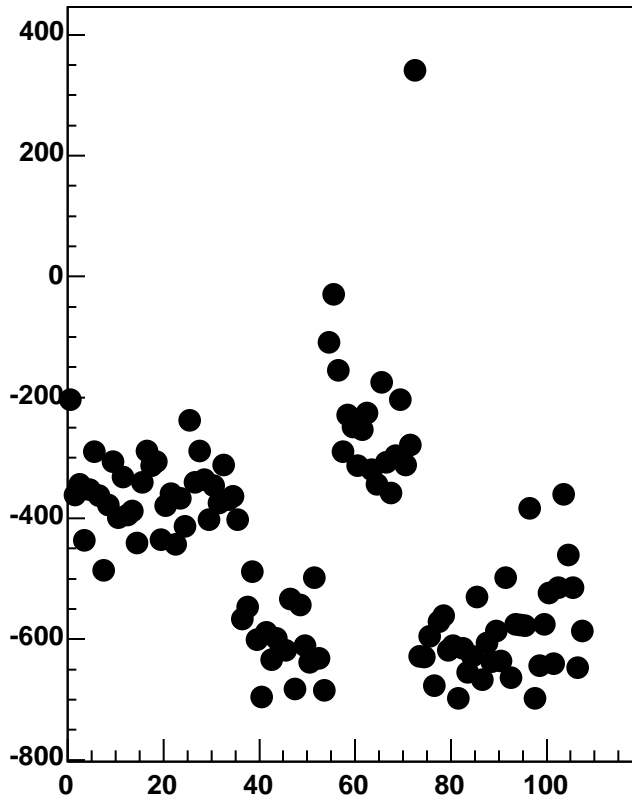
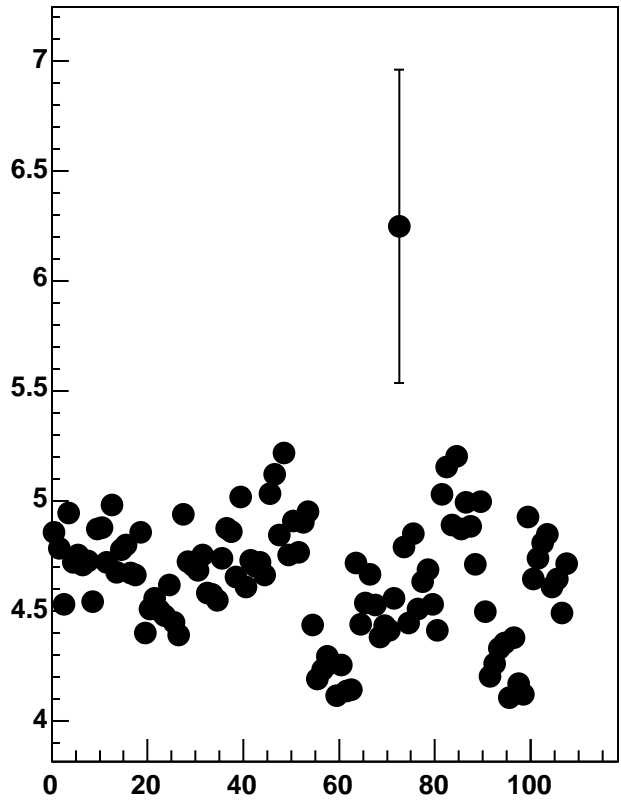


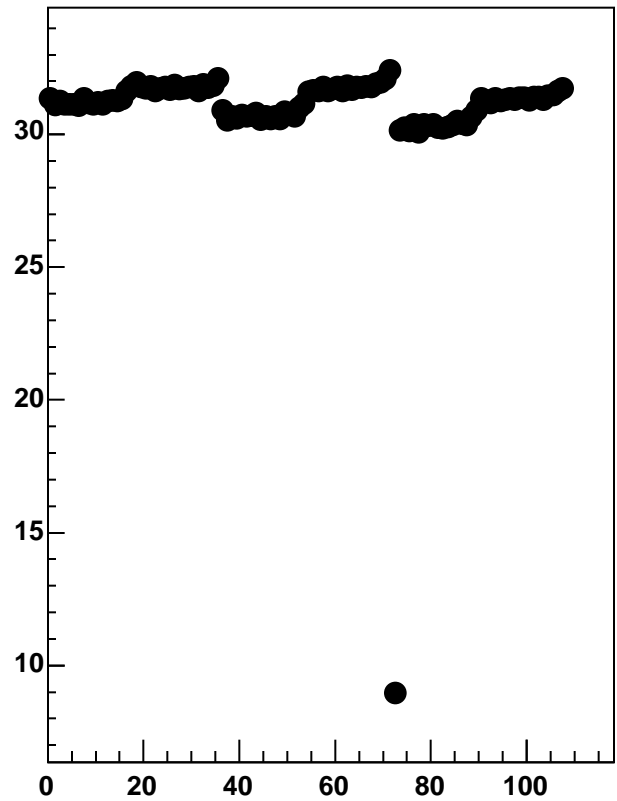
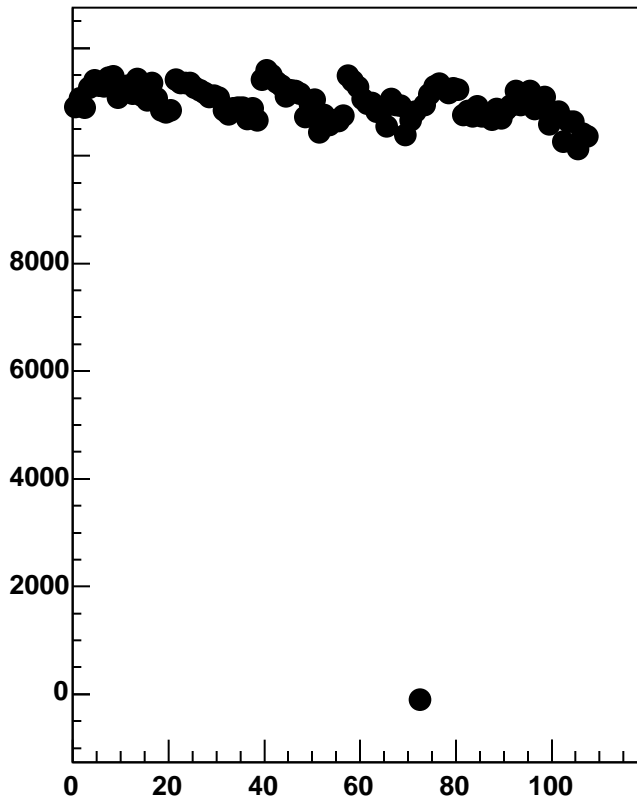
Channel Enabled, DAC=4000, Fit Baseline vs 18°Chip+Chan



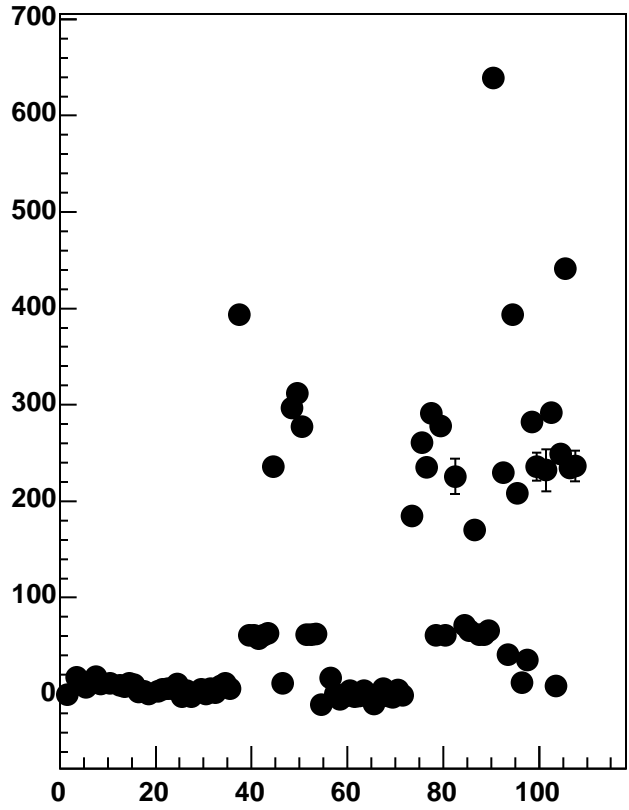
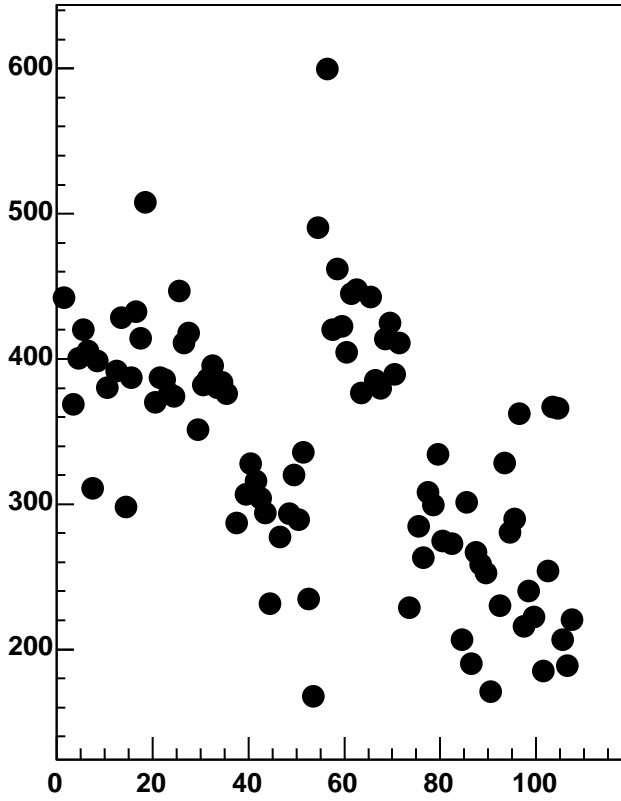
Channel Enabled, DAC=4000, Fit Start Time vs 18°Chip+Chan



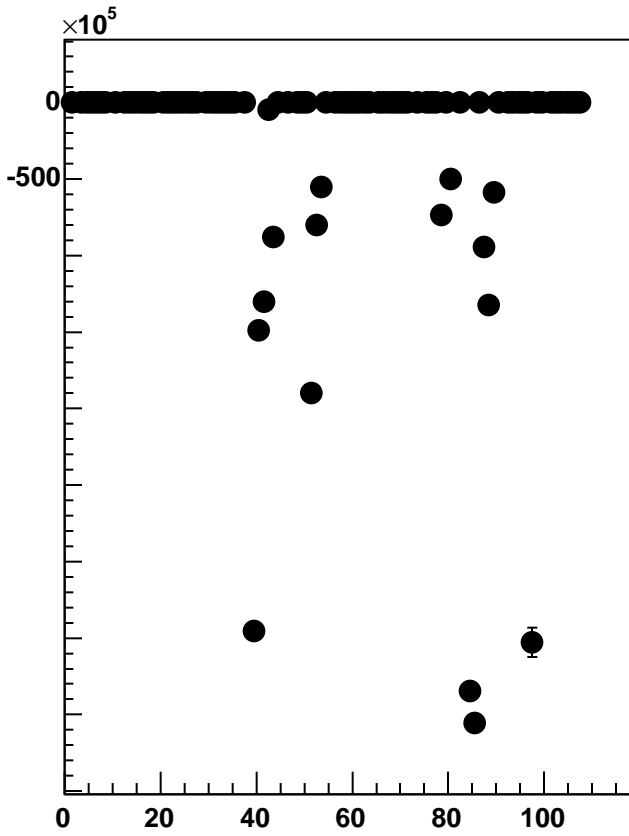
Channel Enabled, DAC=4000, Fit Normalisation vs 18°Chip+Chan



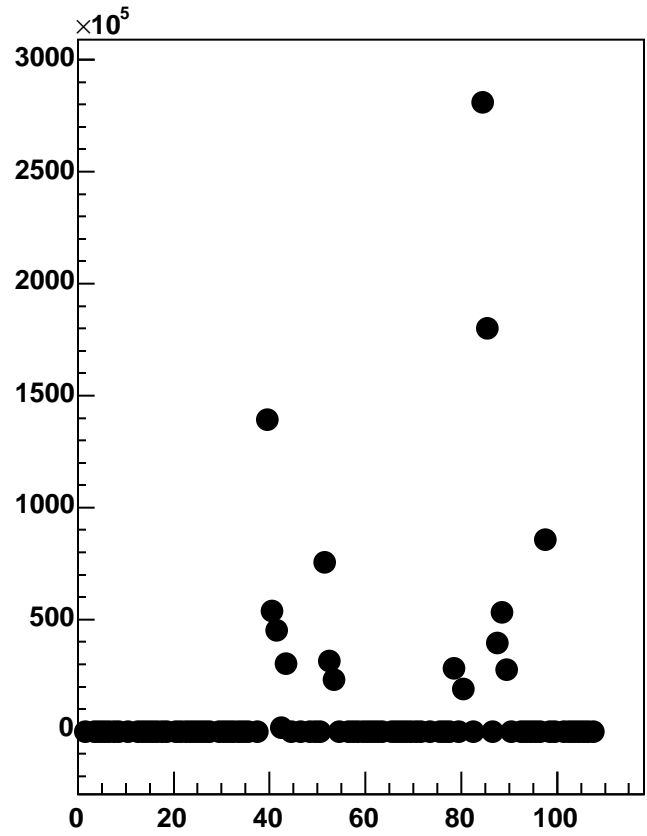
Disabled 0, DAC=4000, Fit Baseline vs 18\*Chip+Chan



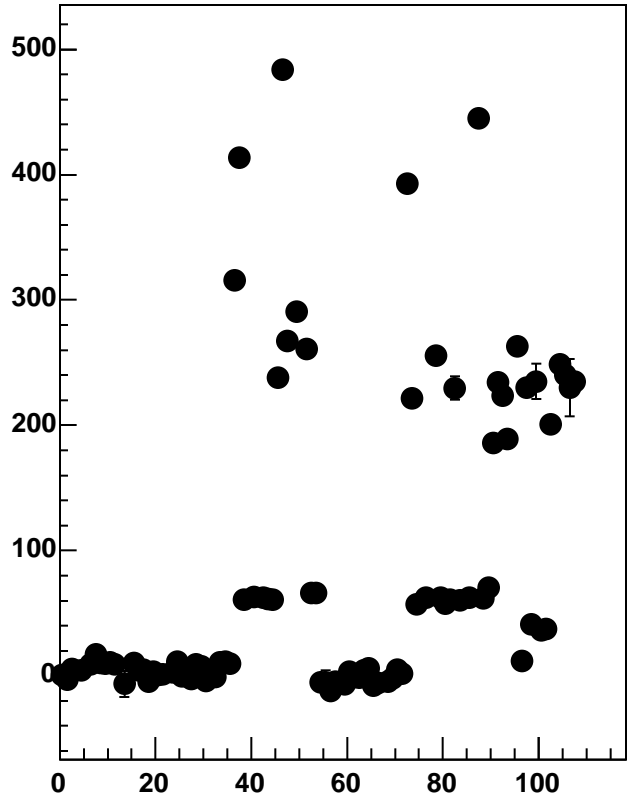
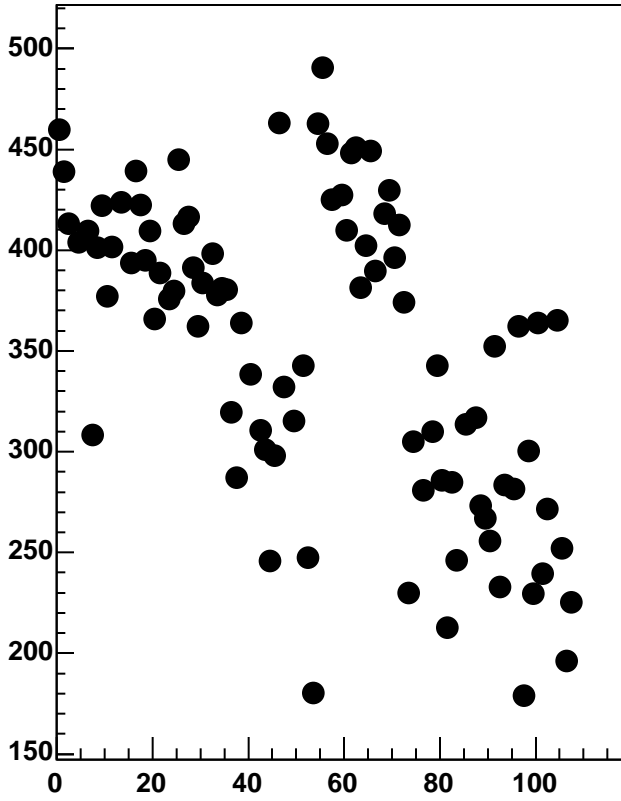
Disabled 0, DAC=4000, Fit Normalisation vs 18\*Chip+Chan



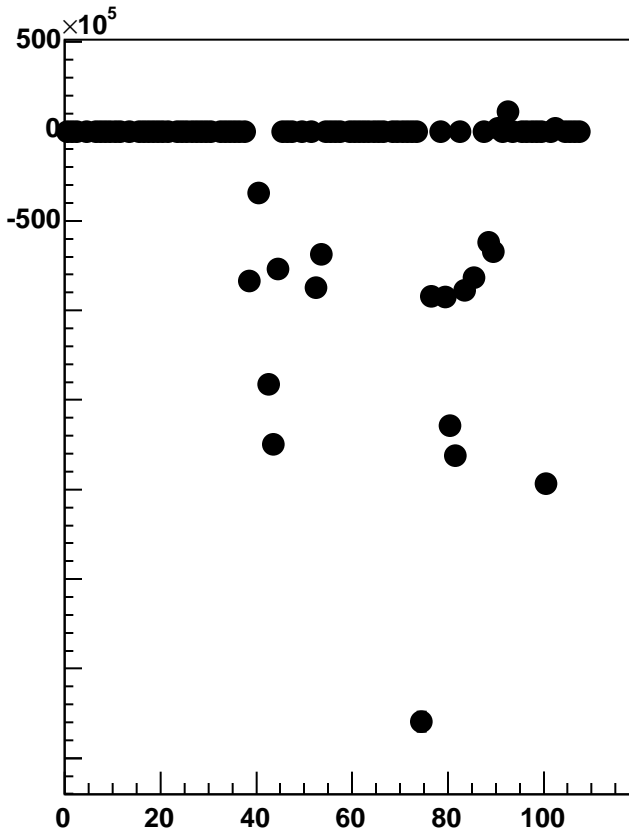
Disabled 0, DAC=4000, Fit Shaping Time vs 18\*Chip+Chan



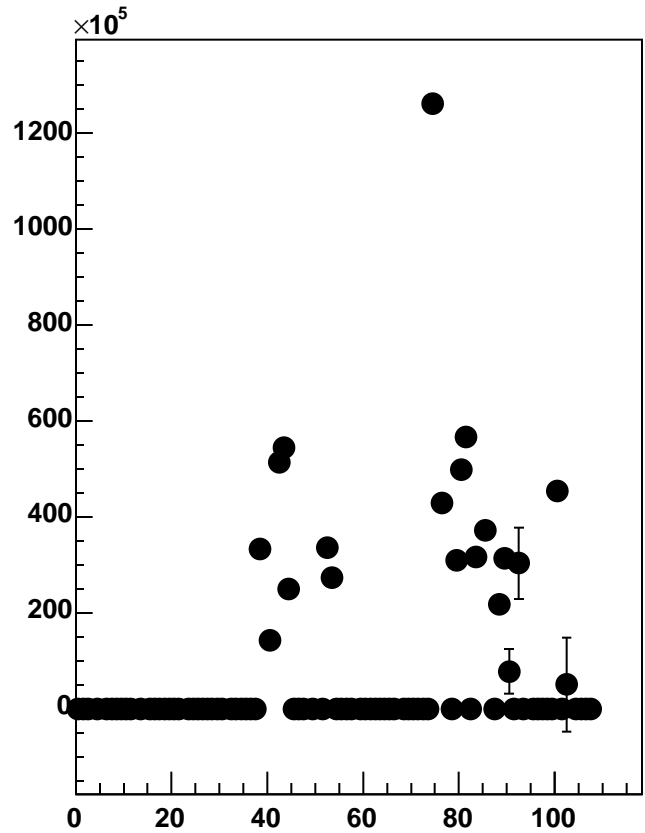
Disabled 1, DAC=4000, Fit Baseline vs 18\*Chip+Chan



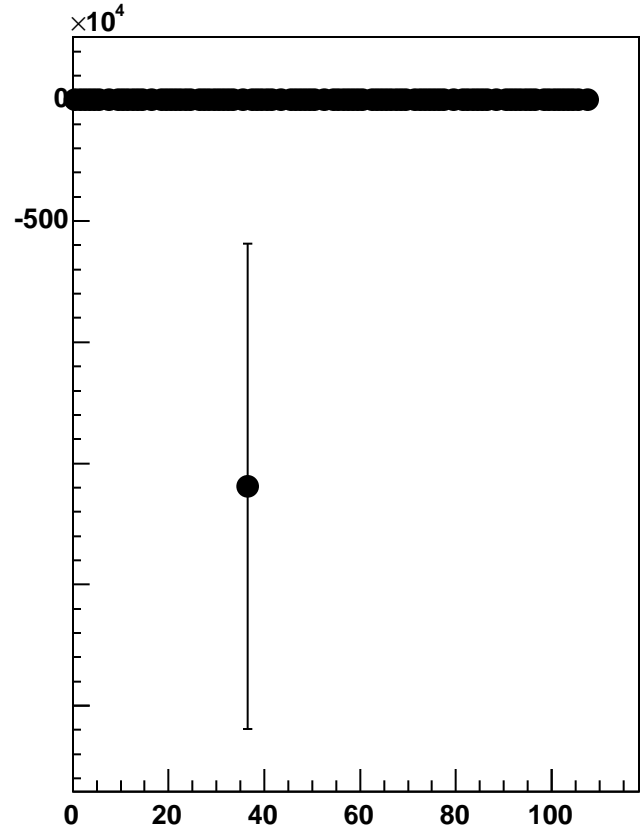
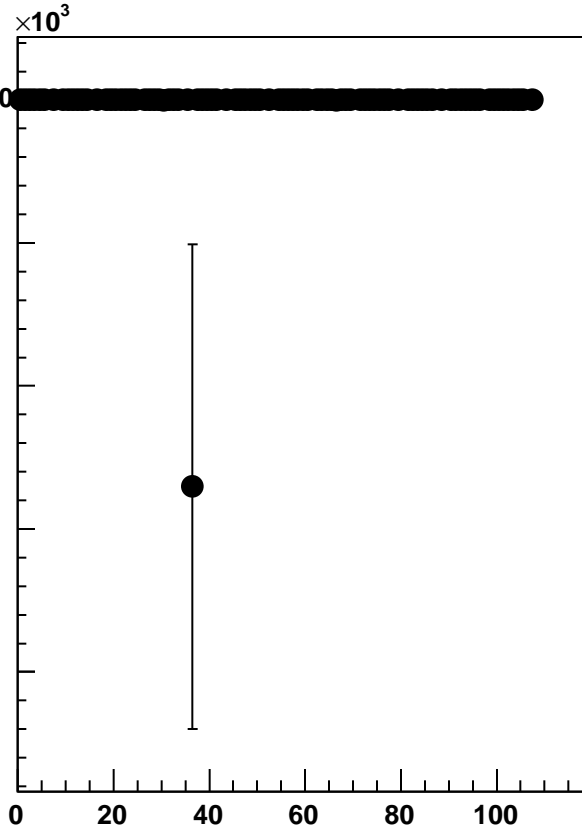
Disabled 1, DAC=4000, Fit Normalisation vs 18\*Chip+Chan



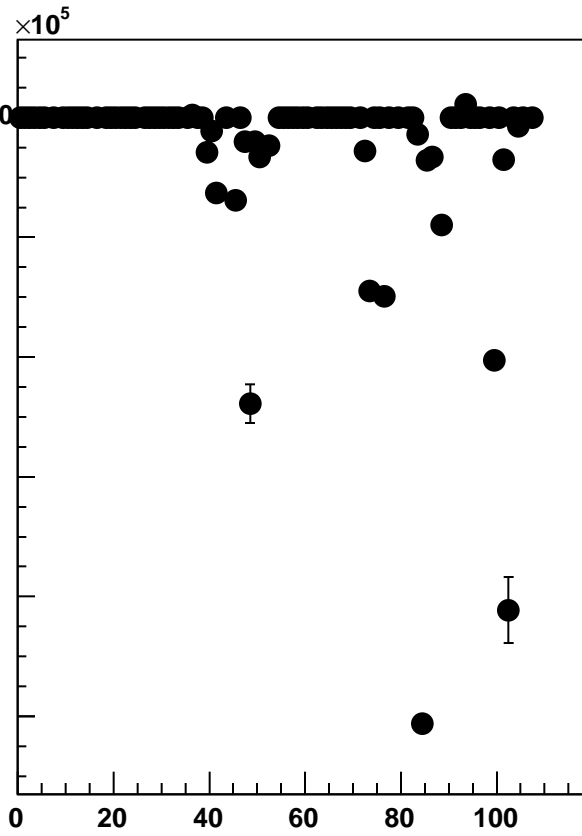
Disabled 1, DAC=4000, Fit Shaping Time vs 18\*Chip+Chan



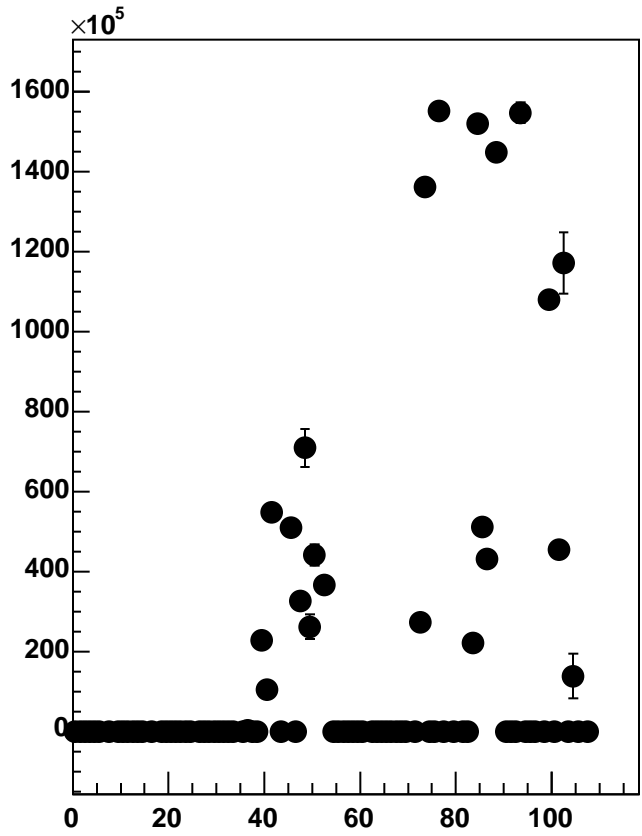
Disabled 2, DAC=4000, Fit Baseline vs 18°Chip+Chan



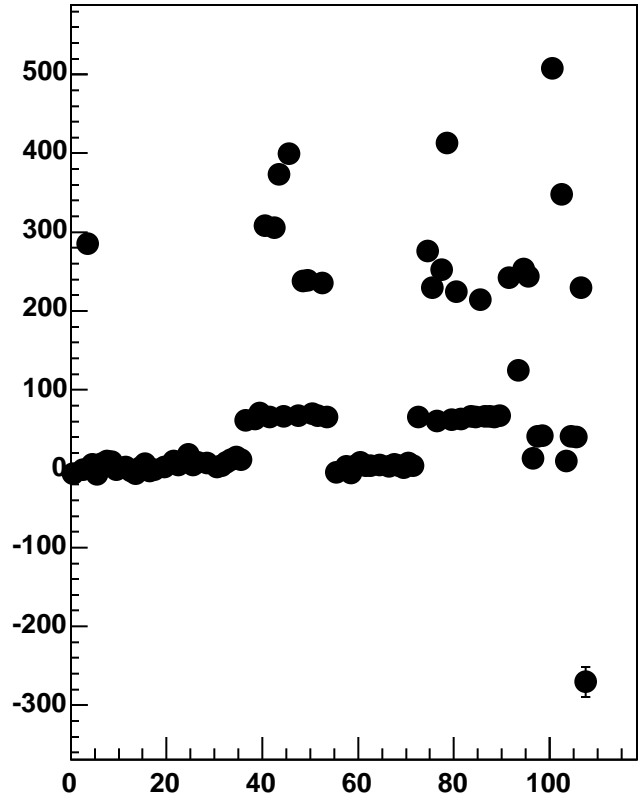
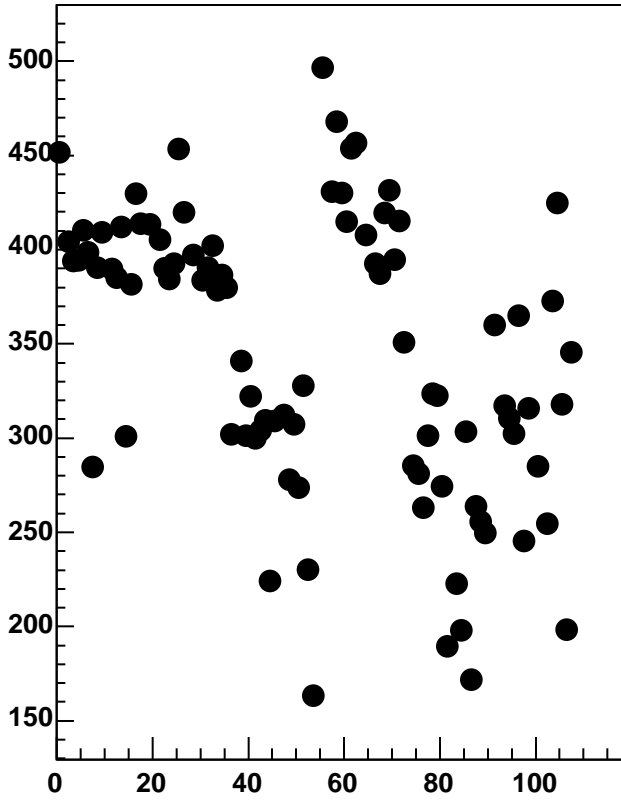
Disabled 2, DAC=4000, Fit Normalisation vs 18°Chip+Chan



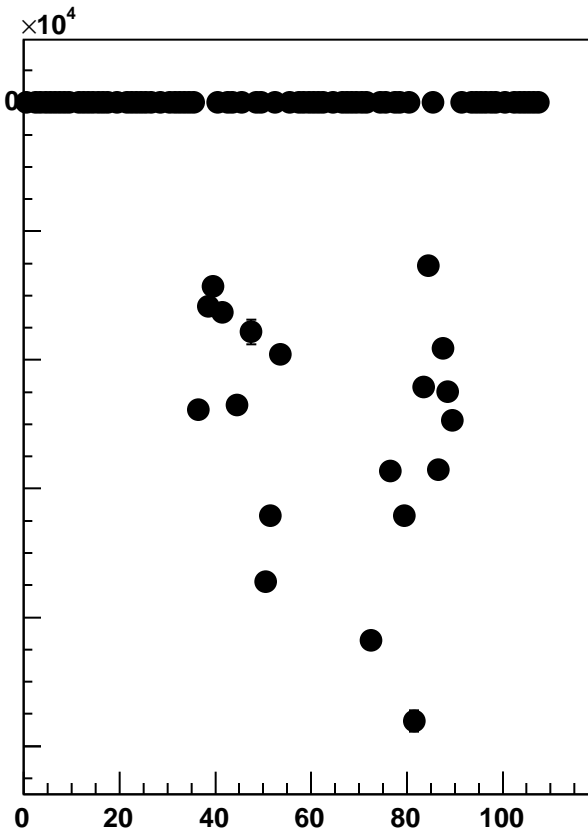
Disabled 2, DAC=4000, Fit Shaping Time vs 18°Chip+Chan



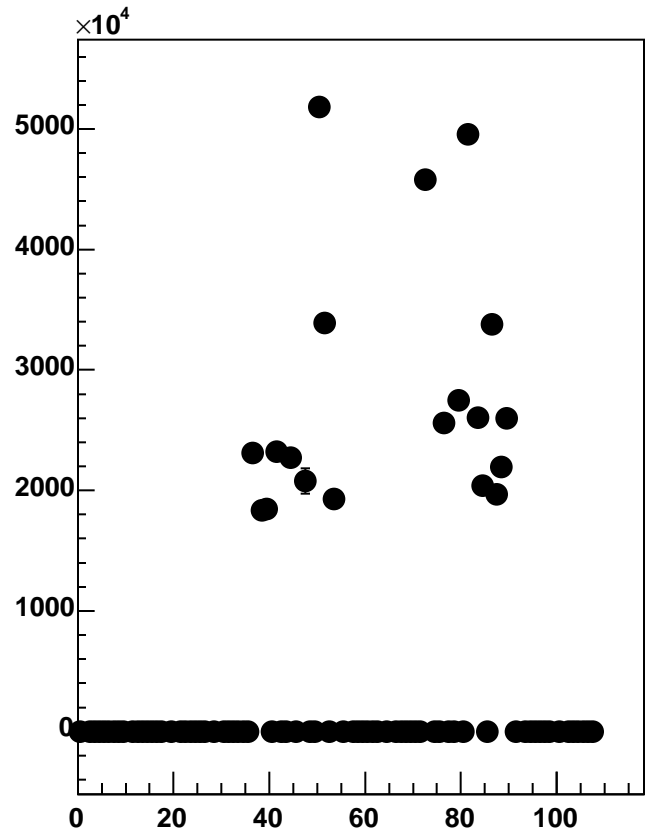
Disabled 3, DAC=4000, Fit Baseline vs 18\*Chip+Chan



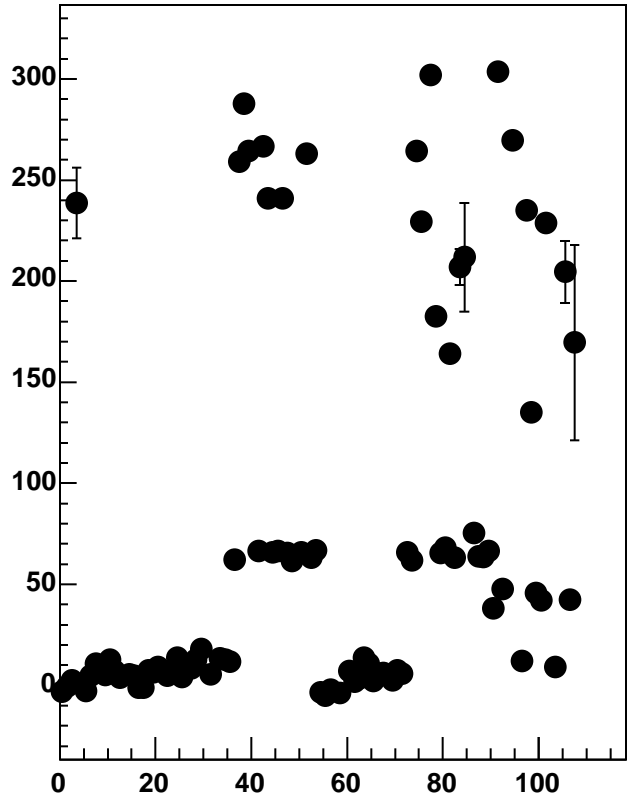
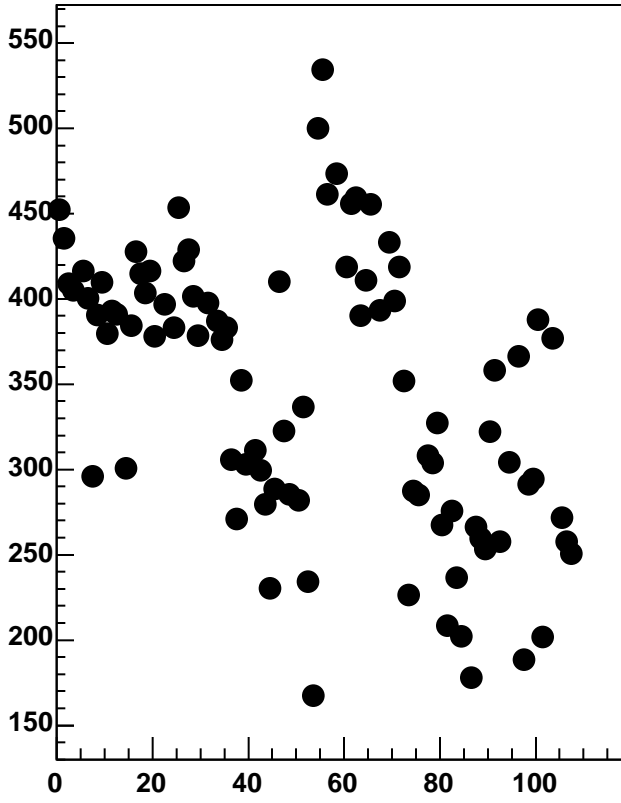
Disabled 3, DAC=4000, Fit Normalisation vs 18\*Chip+Chan



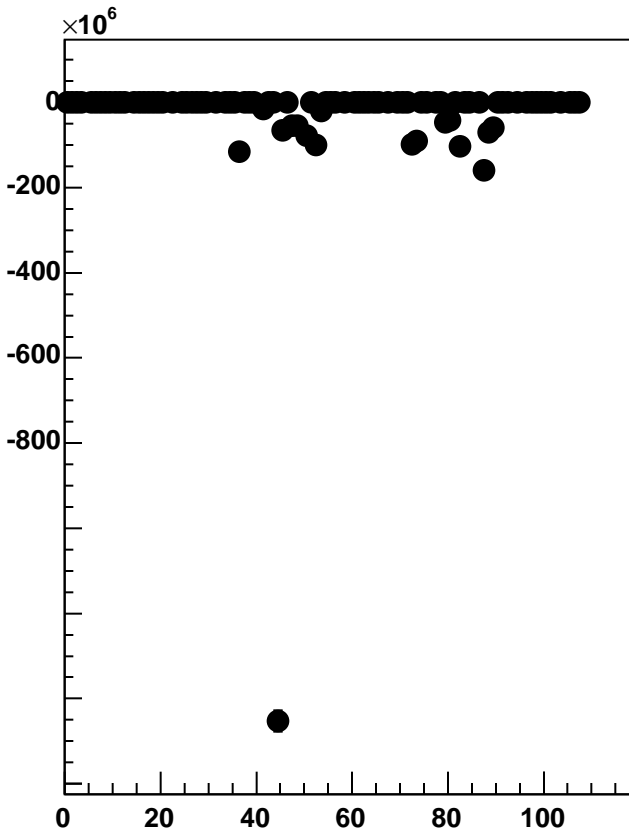
Disabled 3, DAC=4000, Fit Shaping Time vs 18\*Chip+Chan



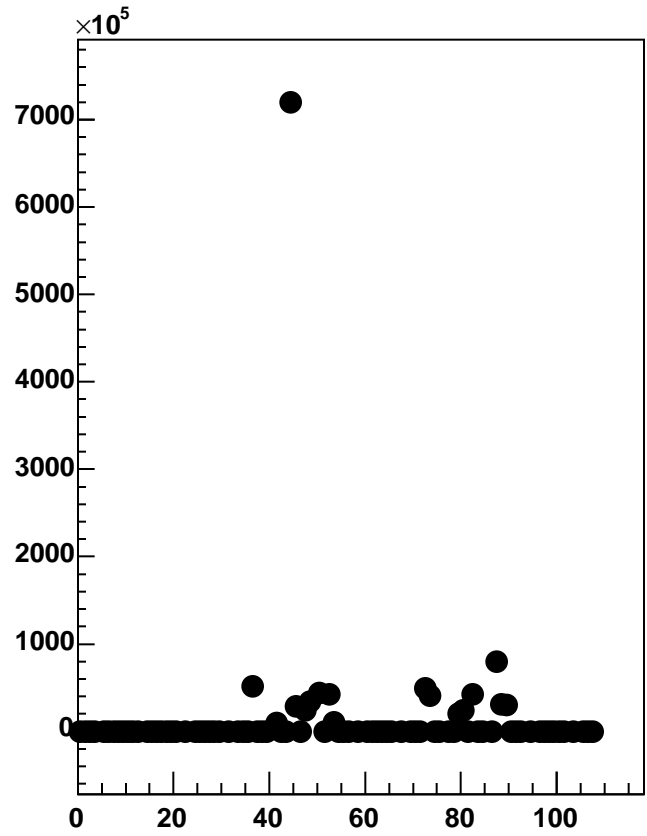
Disabled 4, DAC=4000, Fit Baseline vs 18\*Chip+Chan



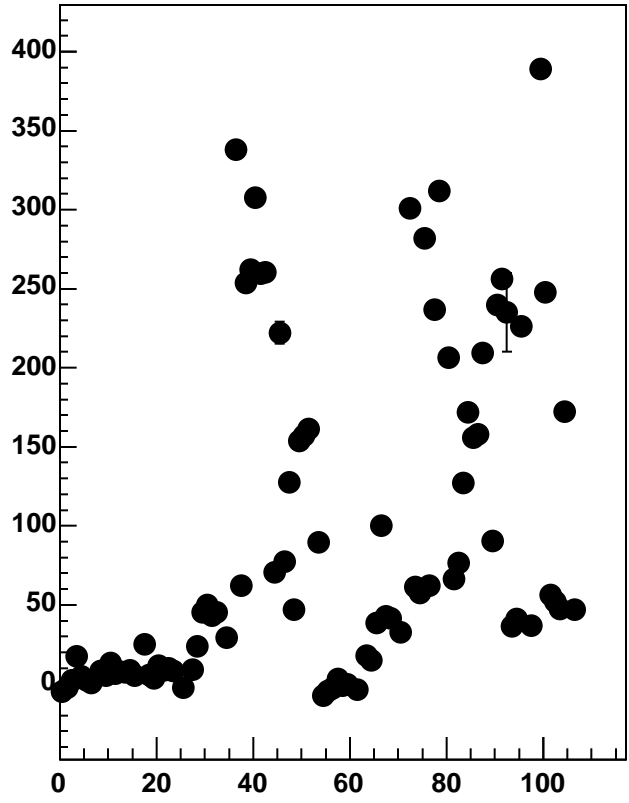
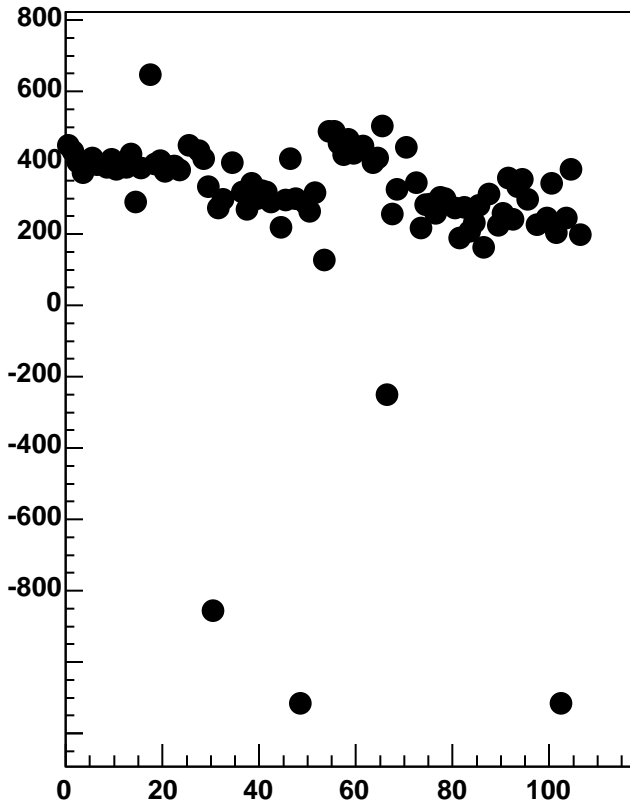
Disabled 4, DAC=4000, Fit Normalisation vs 18\*Chip+Chan



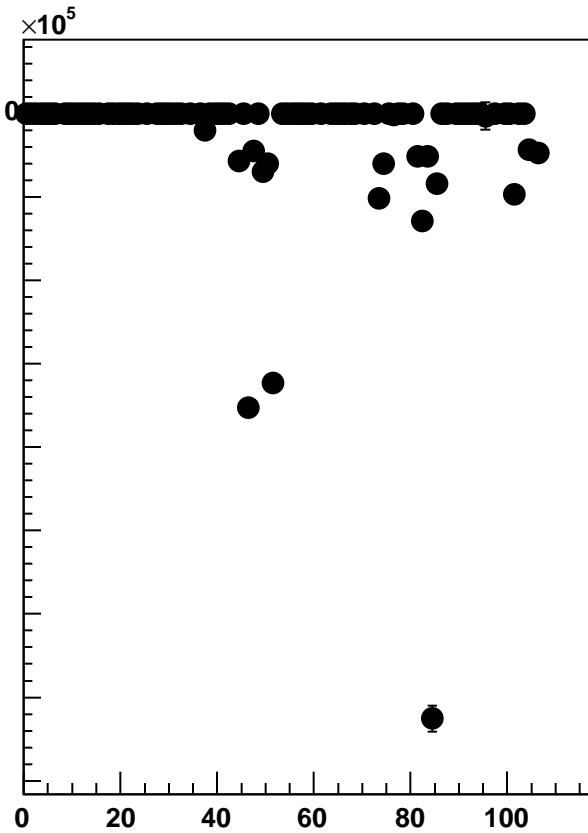
Disabled 4, DAC=4000, Fit Shaping Time vs 18\*Chip+Chan



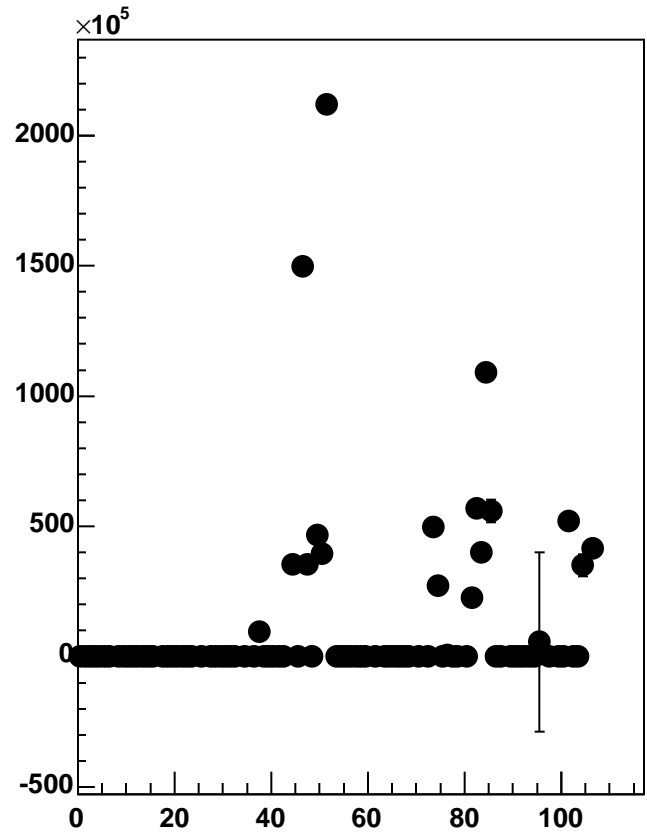
Disabled 5, DAC=4000, Fit Baseline vs 18\*Chip+Chan



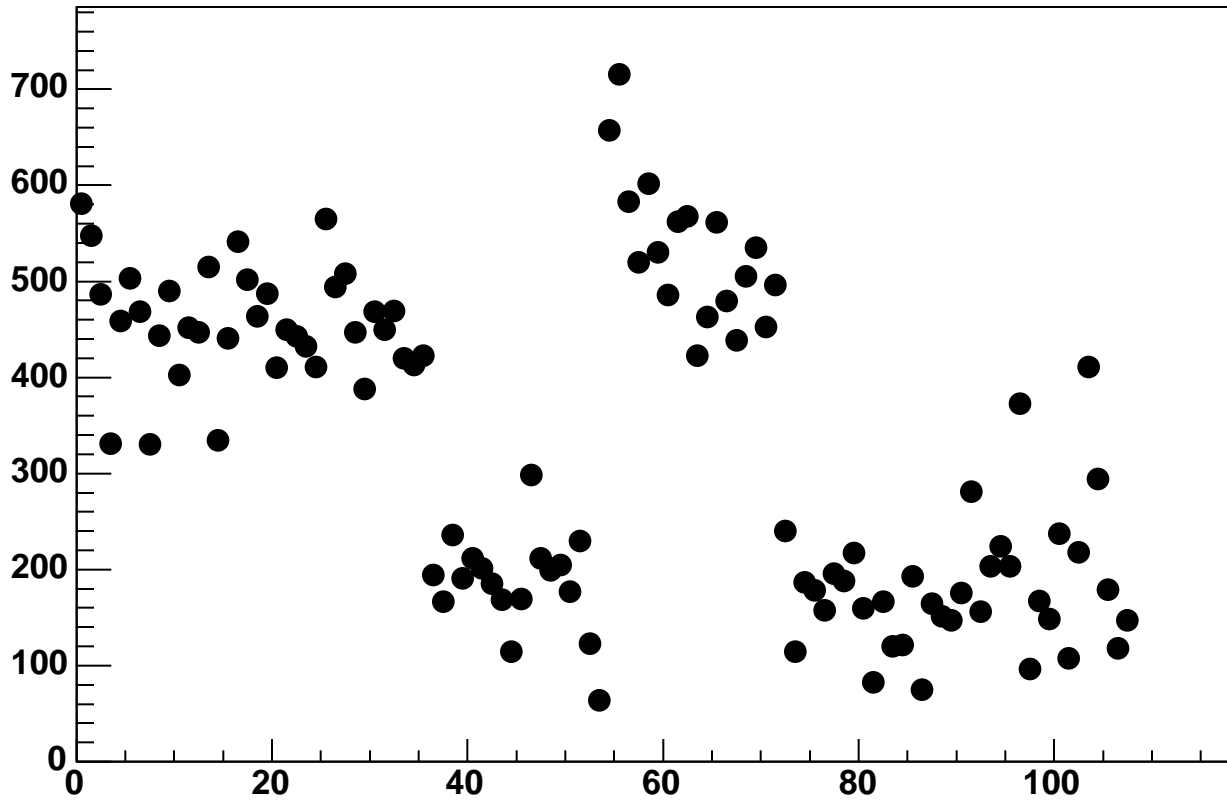
Disabled 5, DAC=4000, Fit Normalisation vs 18\*Chip+Chan



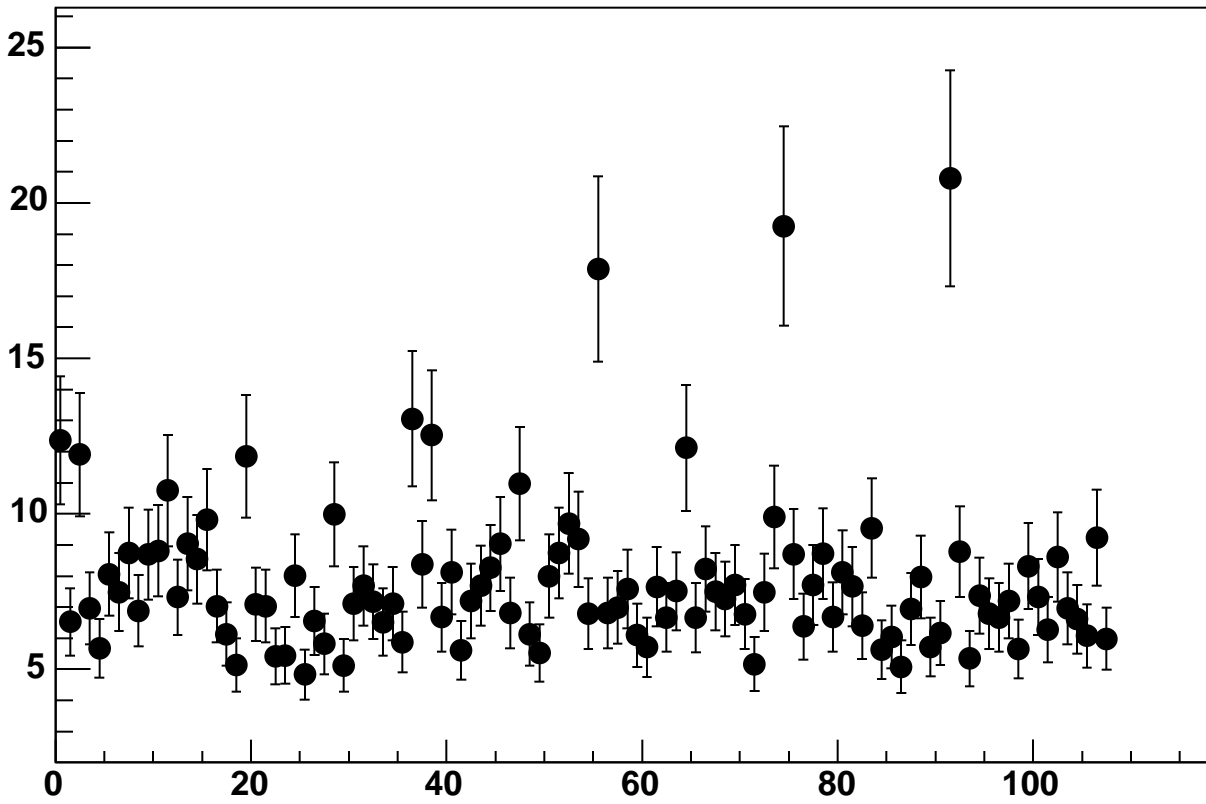
Disabled 5, DAC=4000, Fit Shaping Time vs 18\*Chip+Chan



Enable 0, DAC=4000, Hold=0, ADC Mean vs 18\*Chip+Chan

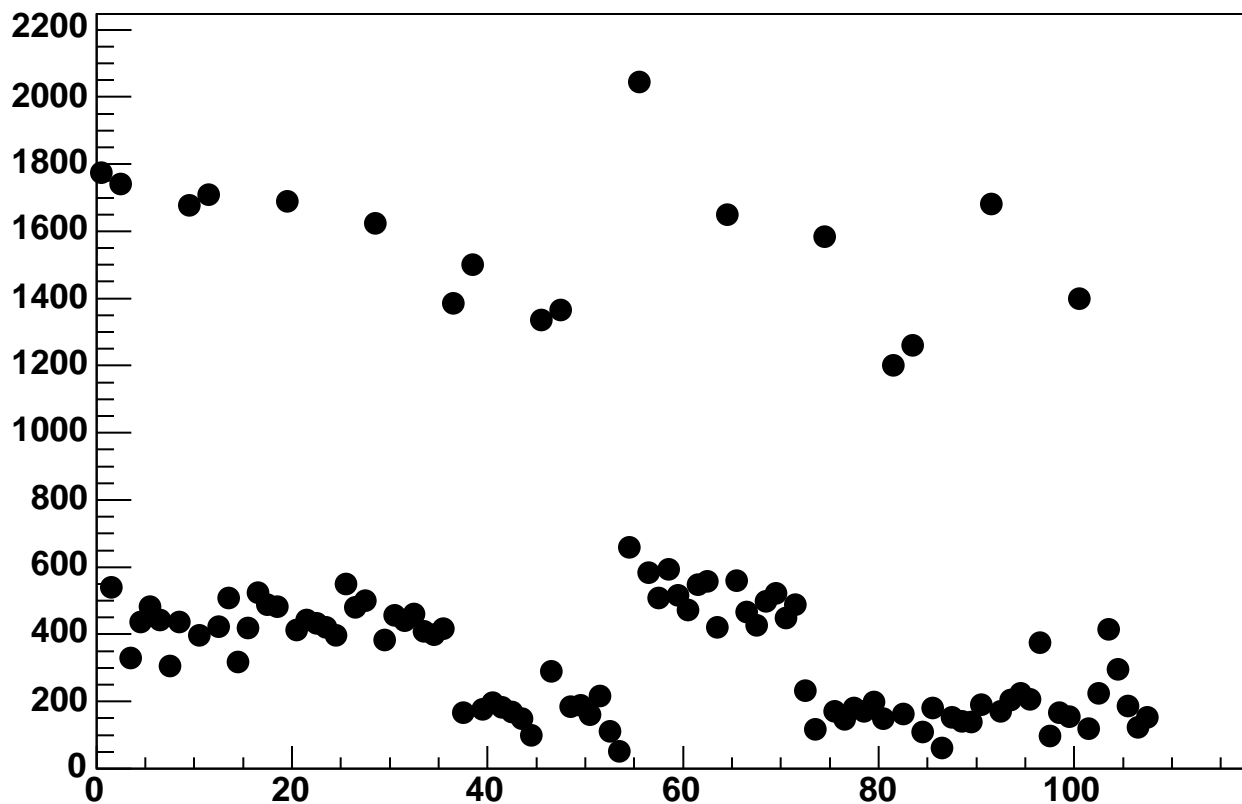


Enable 0, DAC=4000, Hold=0, ADC Noise vs 18\*Chip+Chan

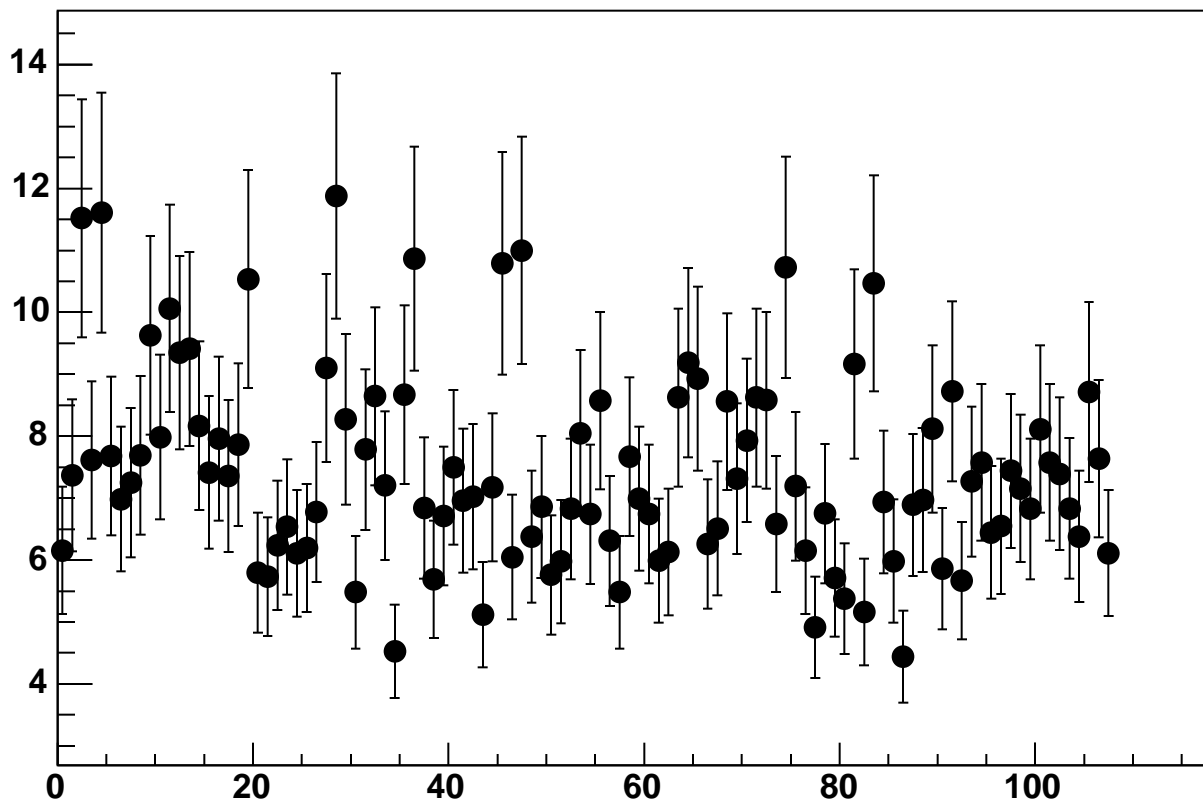




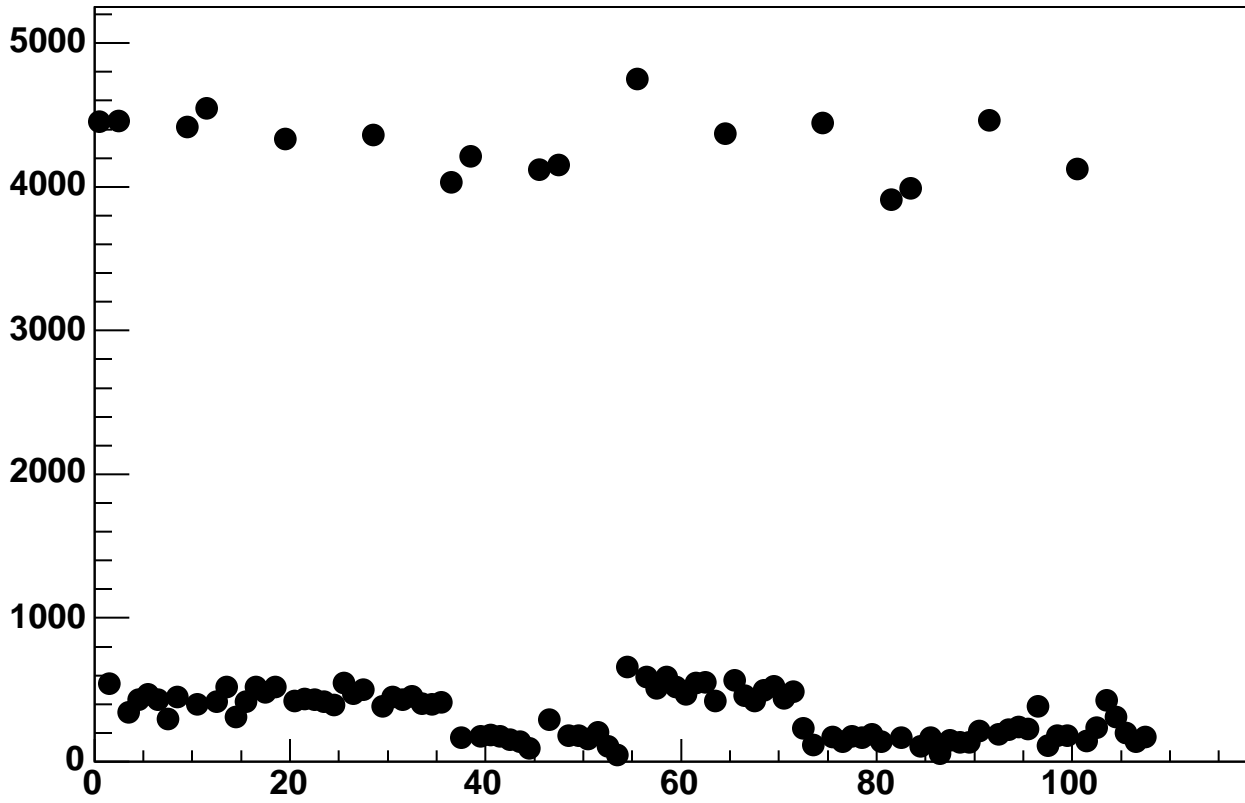
Enable 0, DAC=4000, Hold=5, ADC Mean vs 18\*Chip+Chan



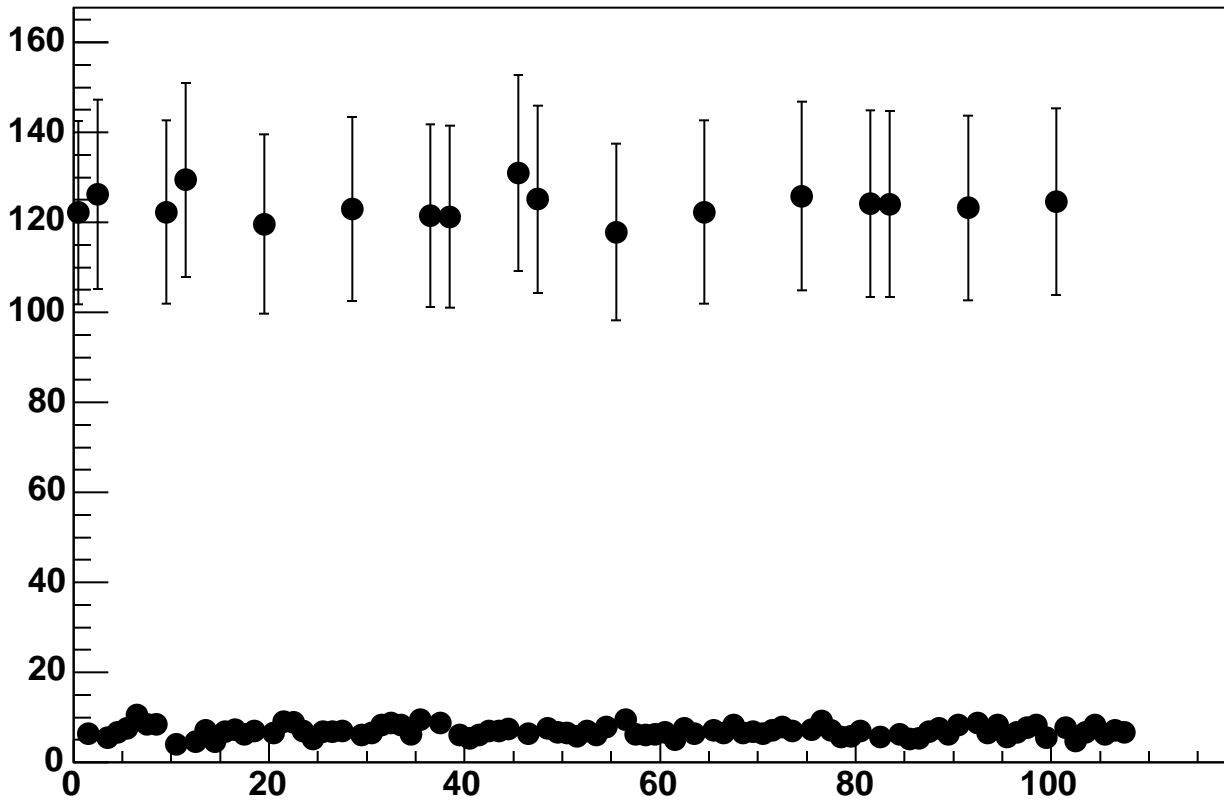
Enable 0, DAC=4000, Hold=5, ADC Noise vs 18\*Chip+Chan



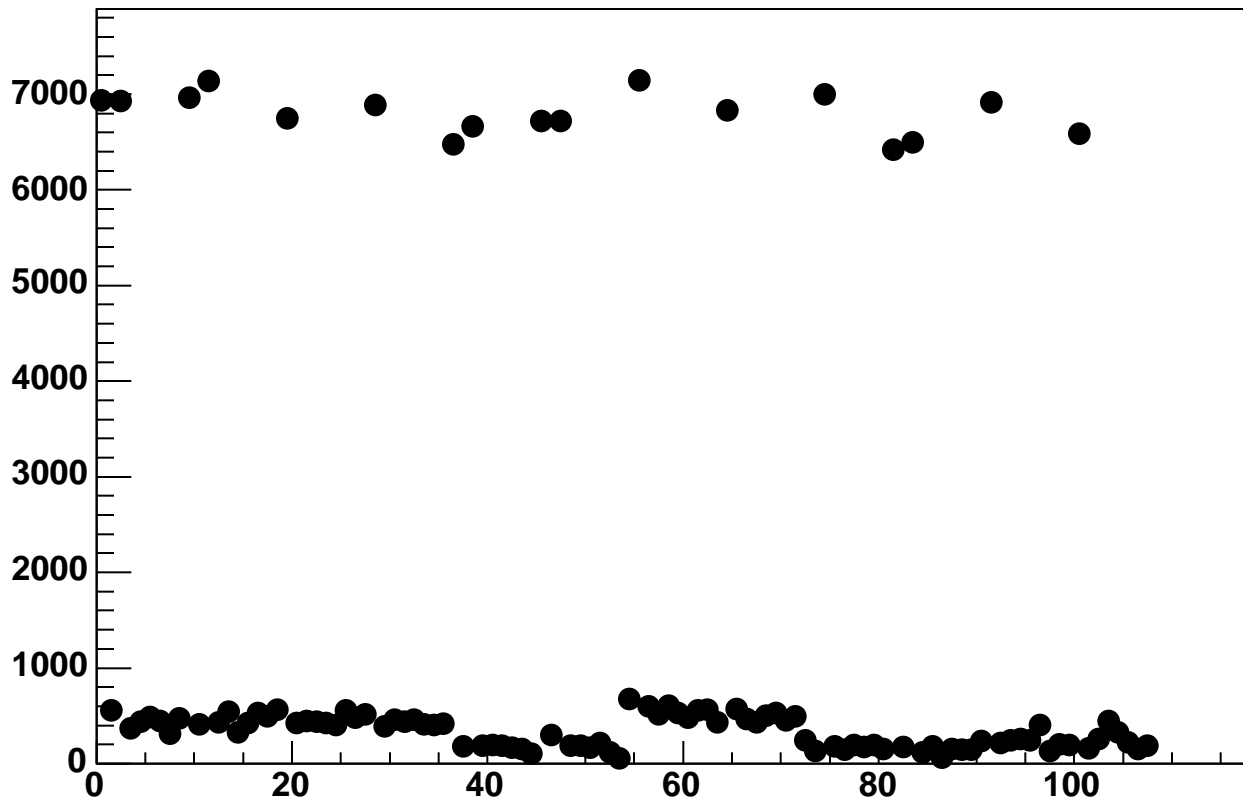
Enable 0, DAC=4000, Hold=10, ADC Mean vs 18\*Chip+Chan



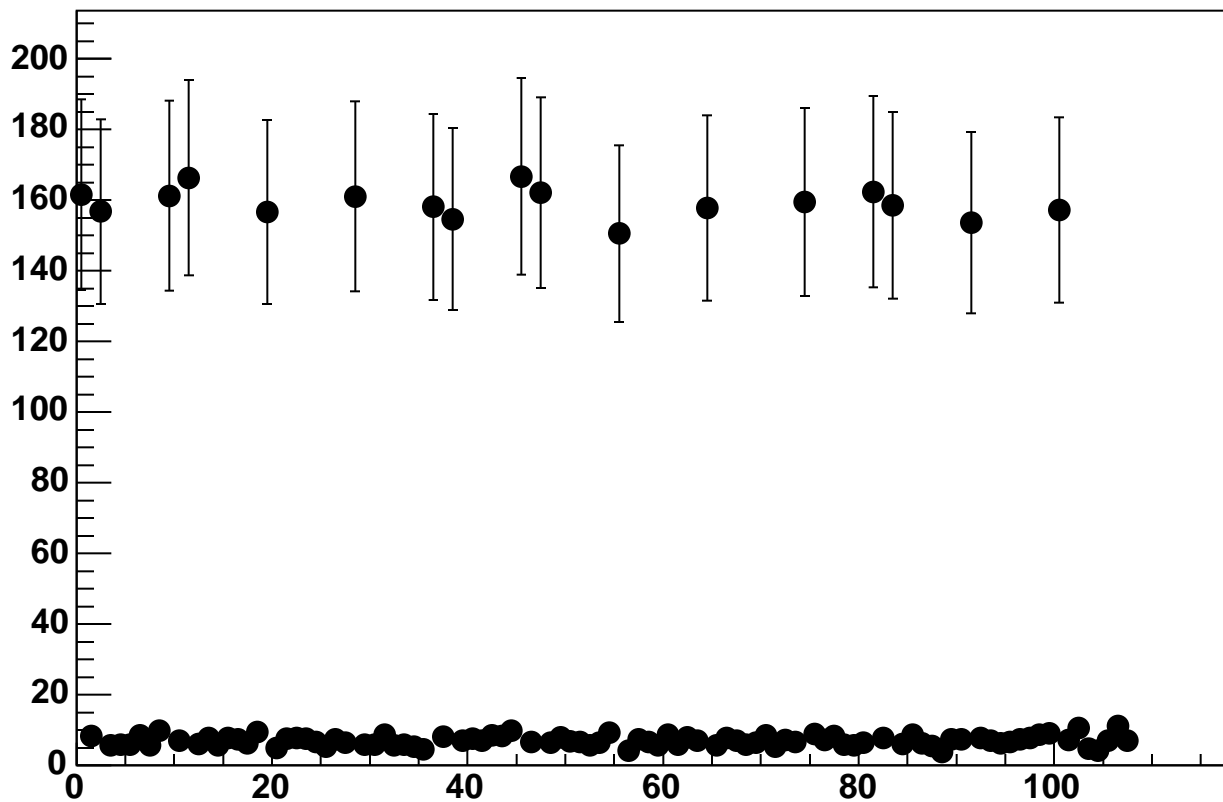
Enable 0, DAC=4000, Hold=10, ADC Noise vs 18\*Chip+Chan



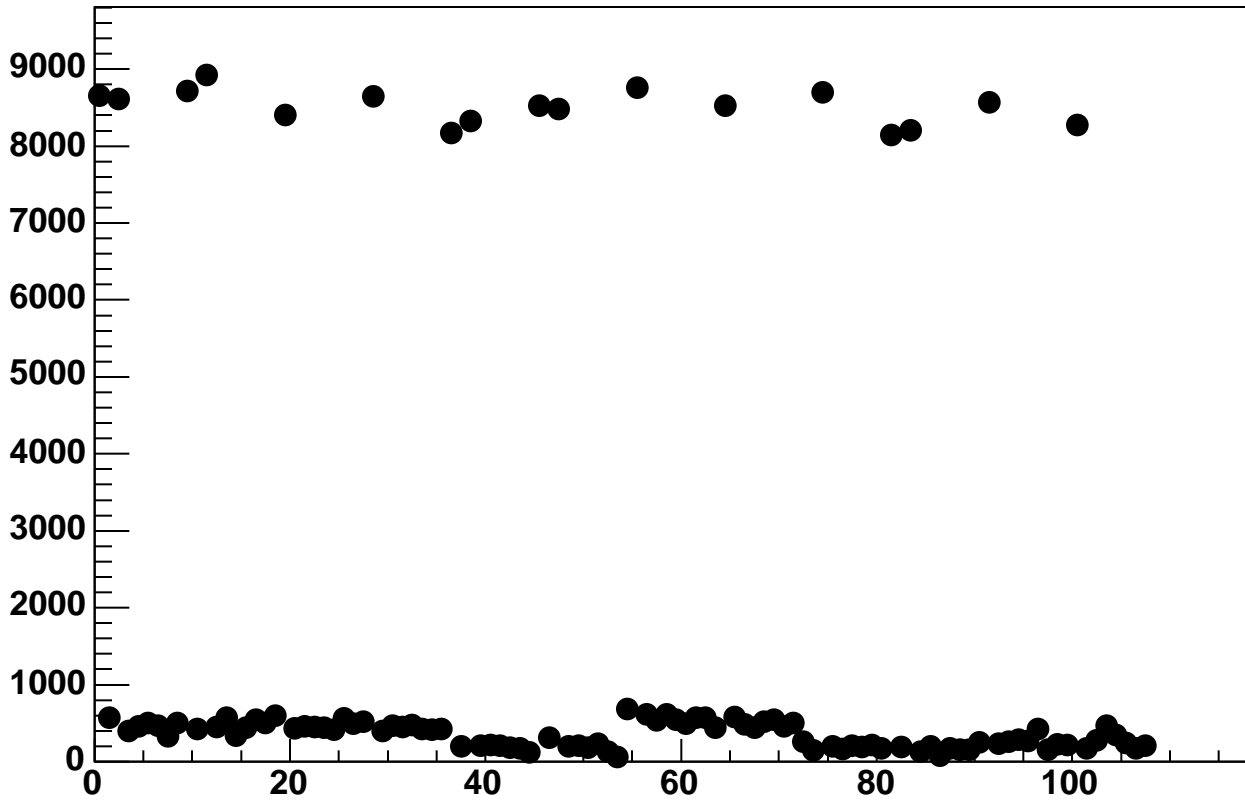
Enable 0, DAC=4000, Hold=15, ADC Mean vs 18\*Chip+Chan



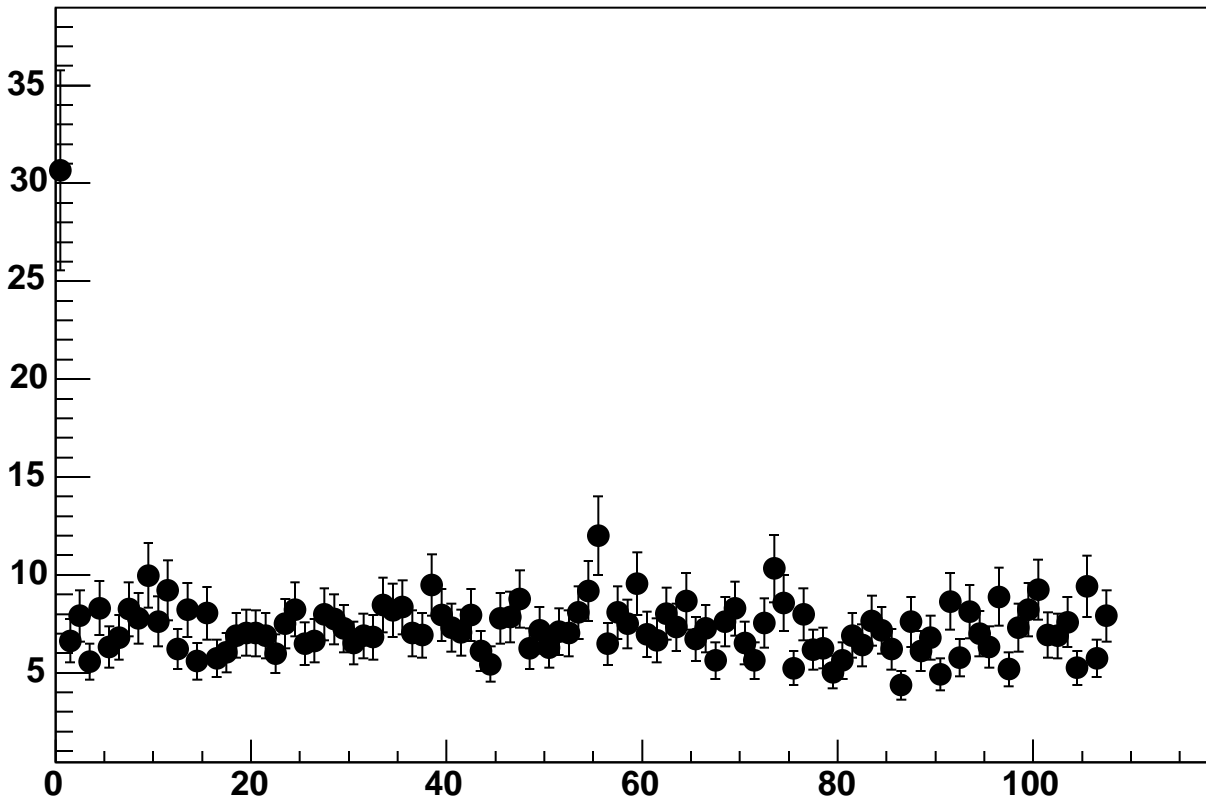
Enable 0, DAC=4000, Hold=15, ADC Noise vs 18\*Chip+Chan



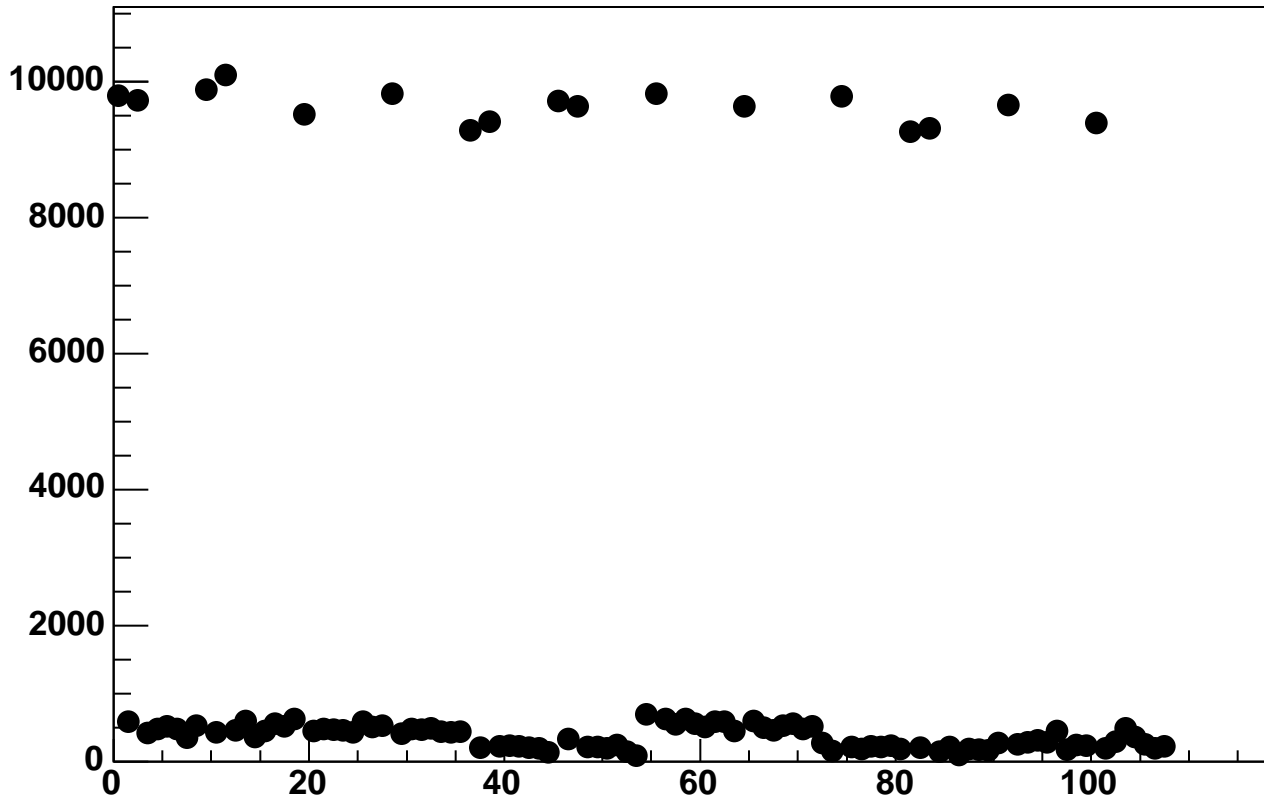
Enable 0, DAC=4000, Hold=20, ADC Mean vs 18\*Chip+Chan



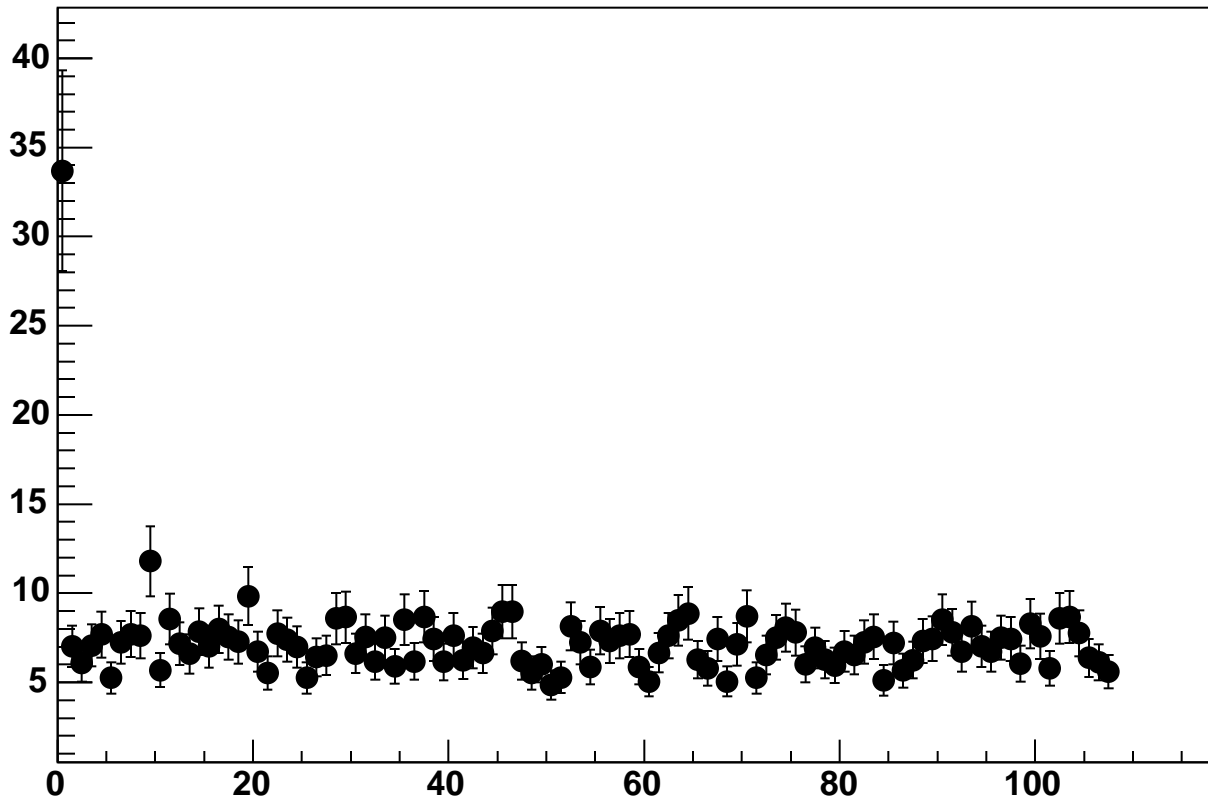
Enable 0, DAC=4000, Hold=20, ADC Noise vs 18\*Chip+Chan



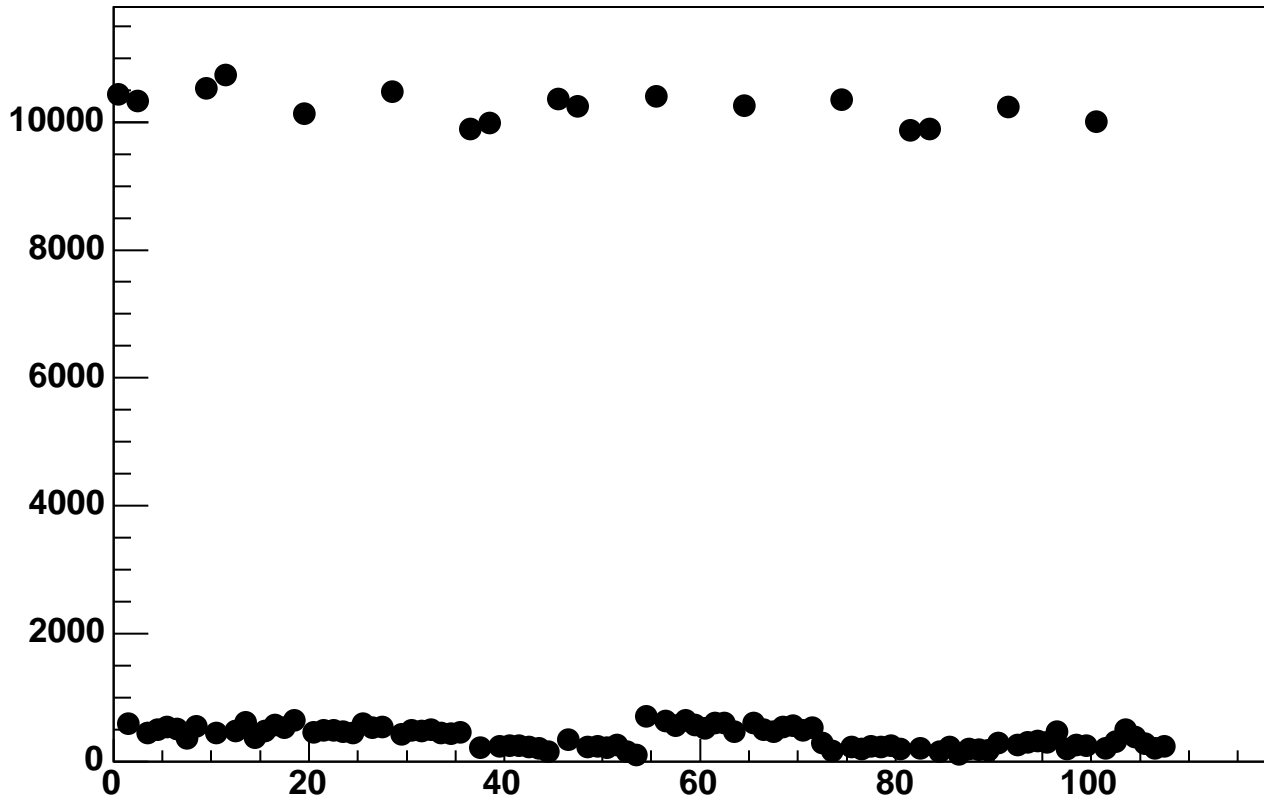
Enable 0, DAC=4000, Hold=25, ADC Mean vs 18\*Chip+Chan



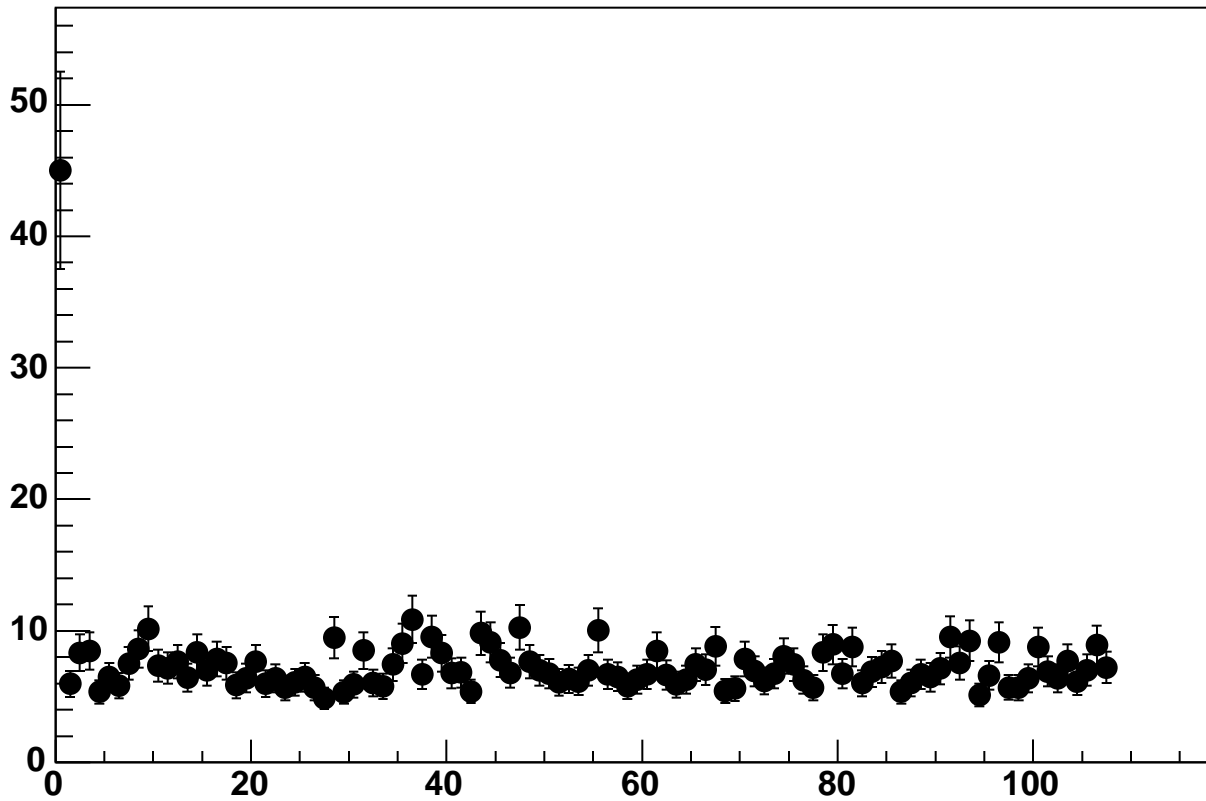
Enable 0, DAC=4000, Hold=25, ADC Noise vs 18\*Chip+Chan



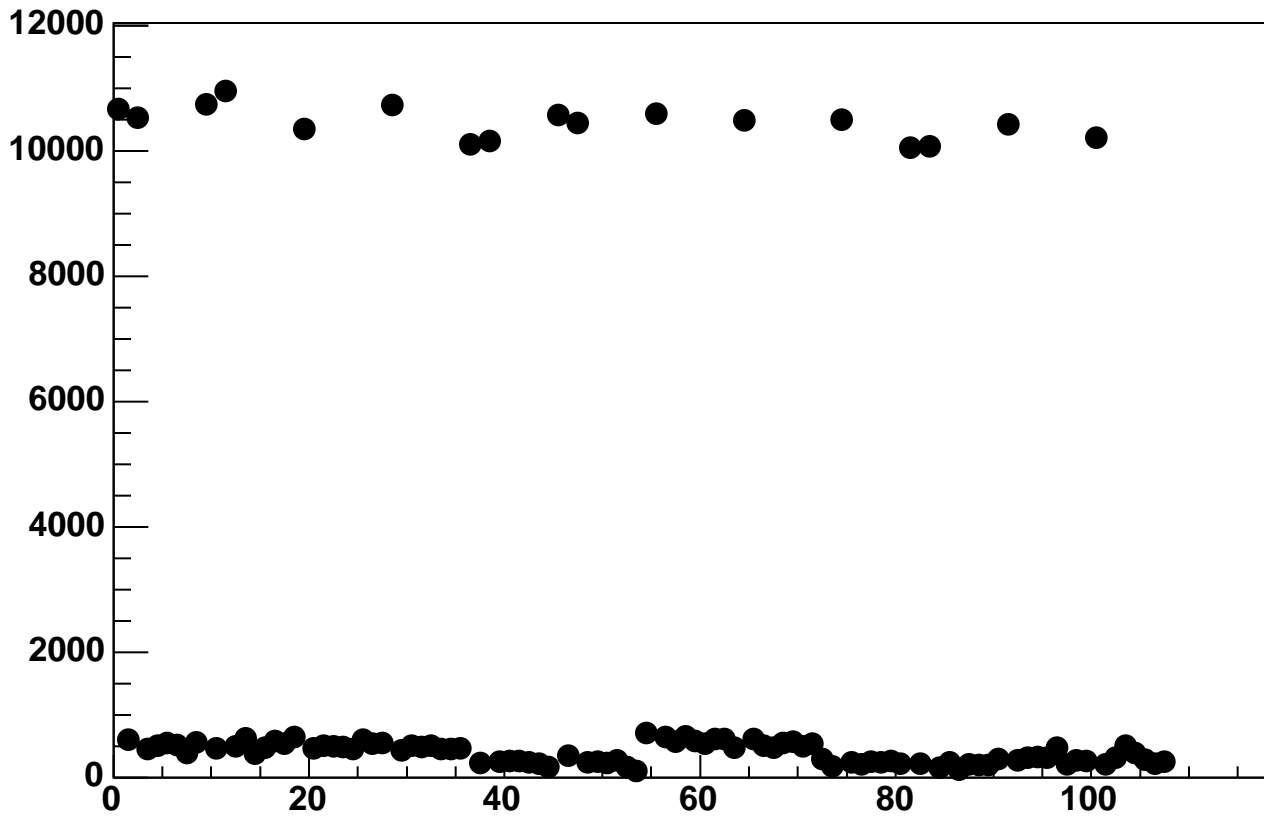
Enable 0, DAC=4000, Hold=30, ADC Mean vs 18\*Chip+Chan



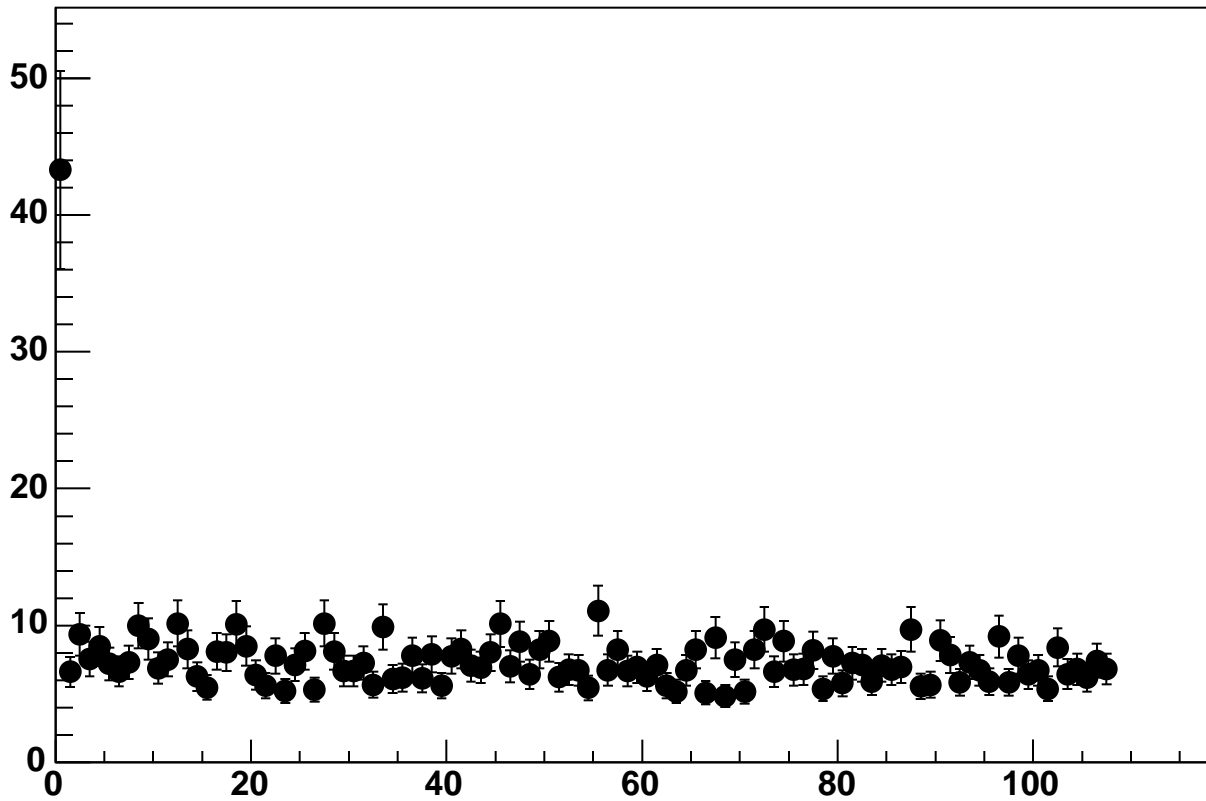
Enable 0, DAC=4000, Hold=30, ADC Noise vs 18\*Chip+Chan



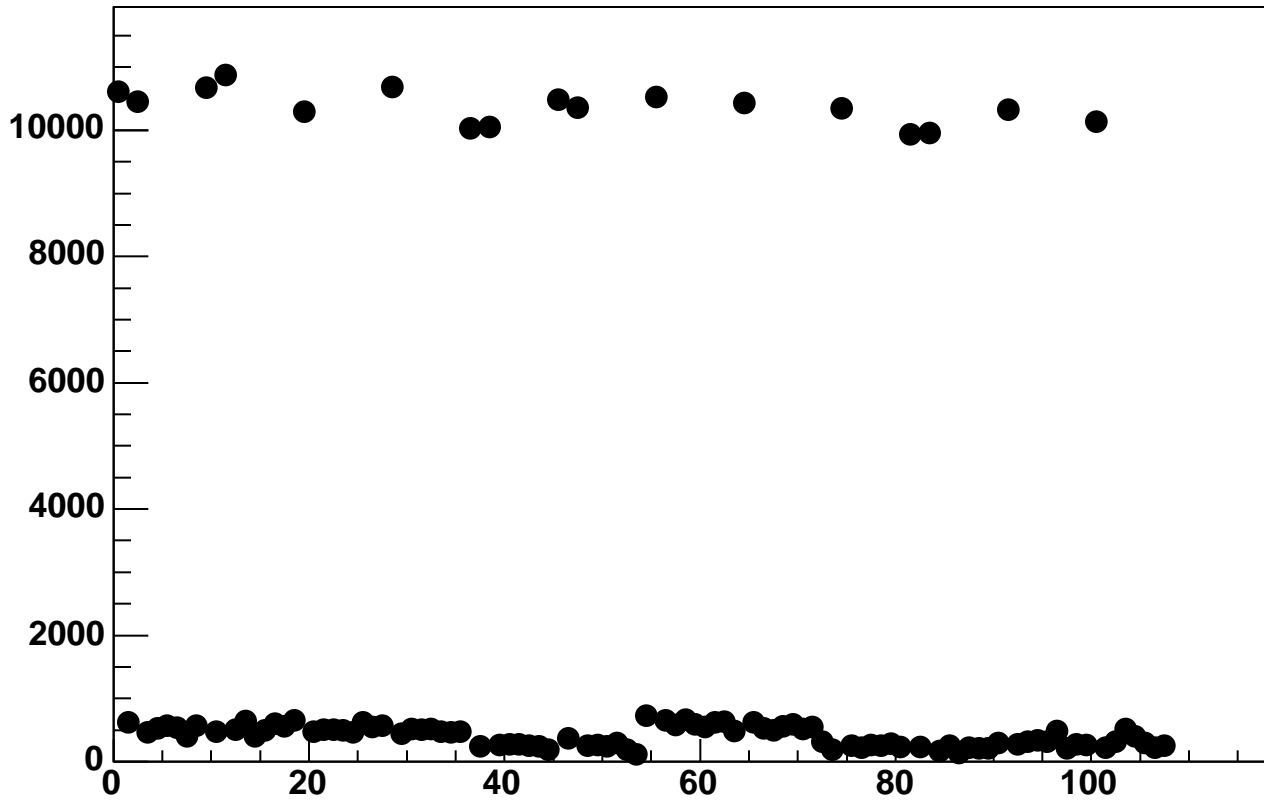
Enable 0, DAC=4000, Hold=35, ADC Mean vs 18\*Chip+Chan



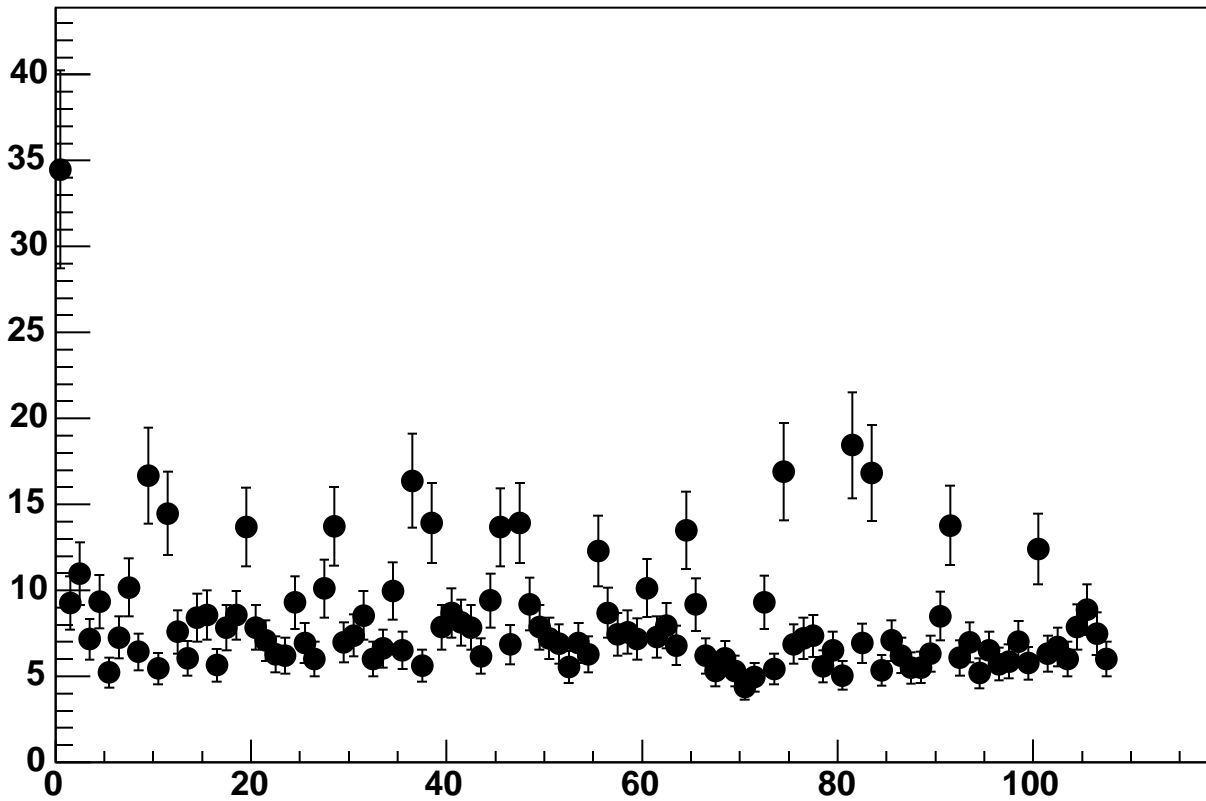
Enable 0, DAC=4000, Hold=35, ADC Noise vs 18\*Chip+Chan



Enable 0, DAC=4000, Hold=40, ADC Mean vs 18\*Chip+Chan

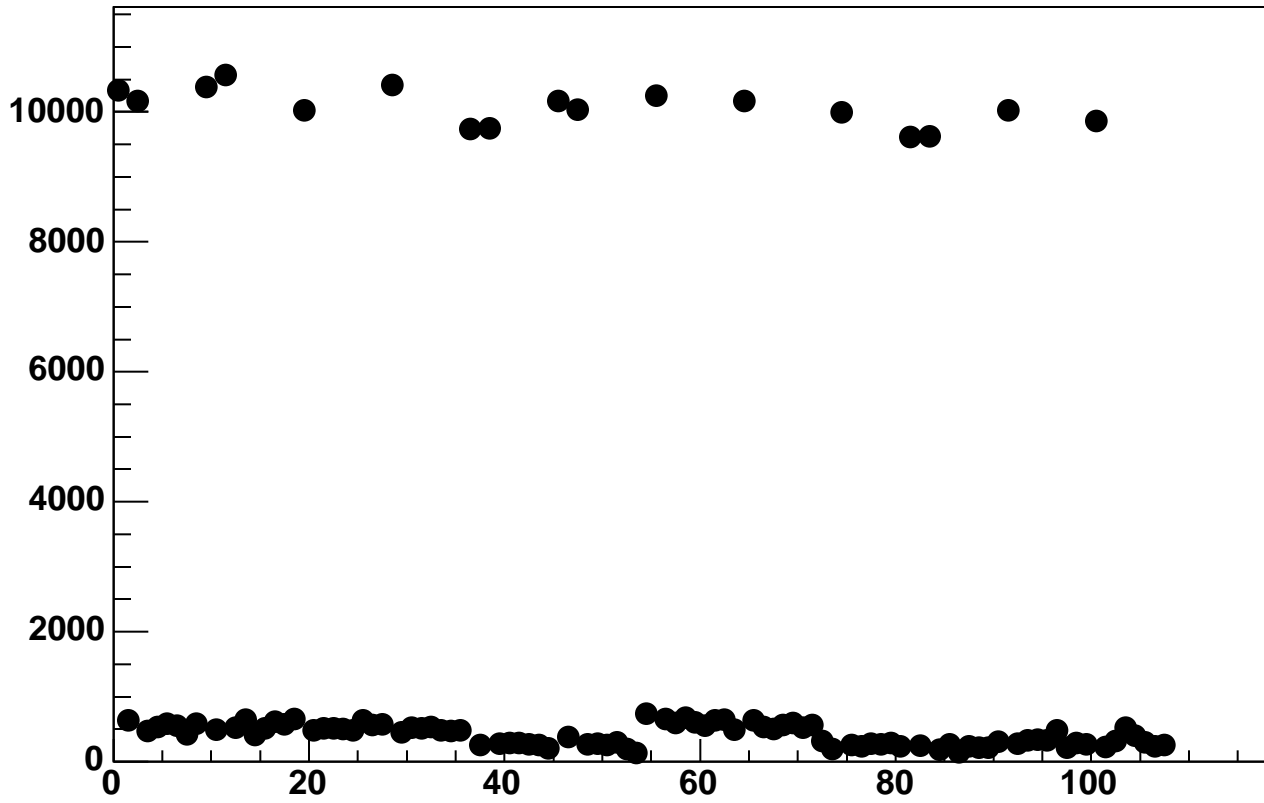


Enable 0, DAC=4000, Hold=40, ADC Noise vs 18\*Chip+Chan

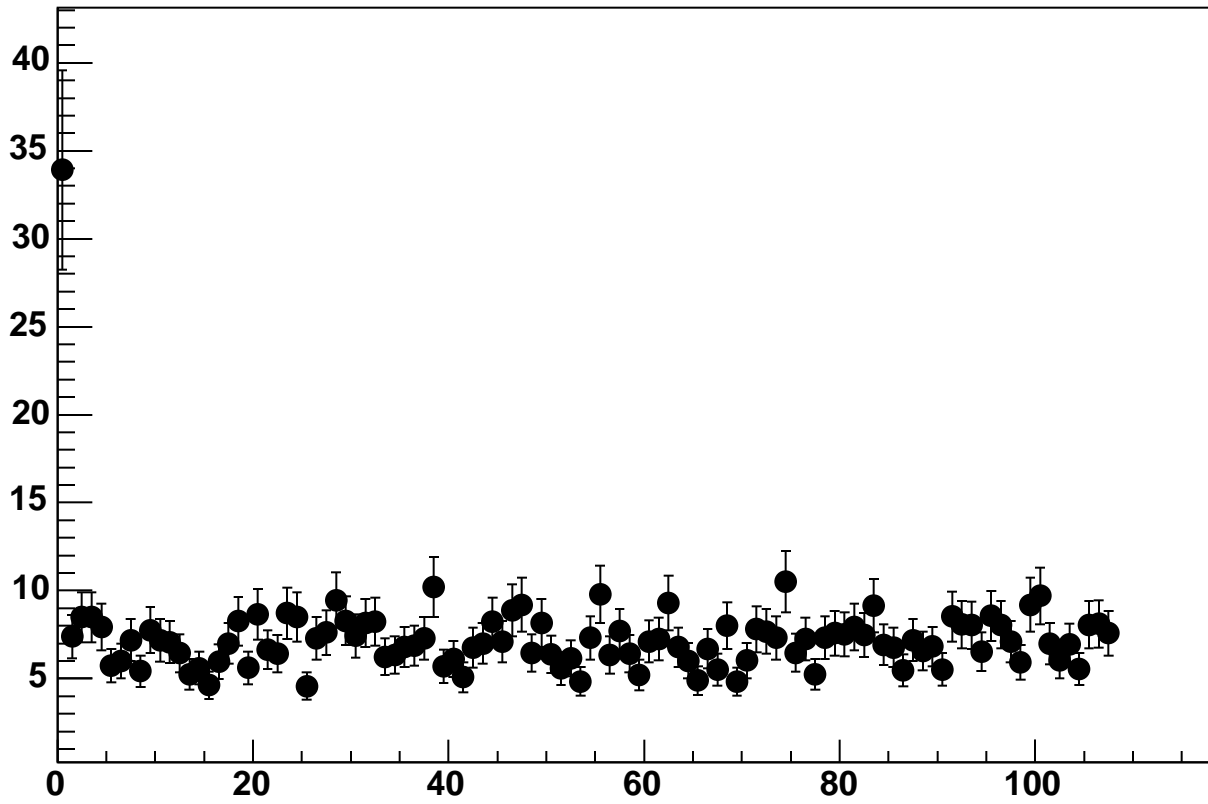




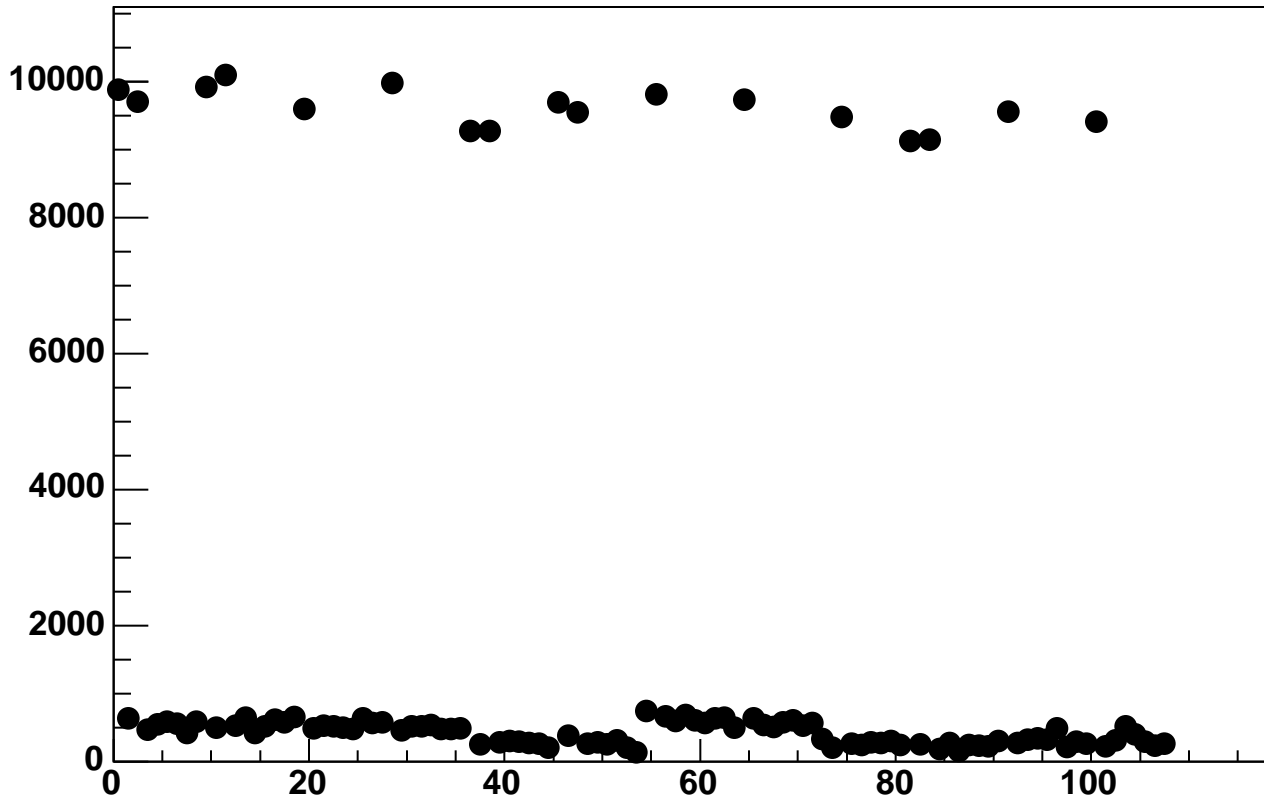
Enable 0, DAC=4000, Hold=45, ADC Mean vs 18\*Chip+Chan



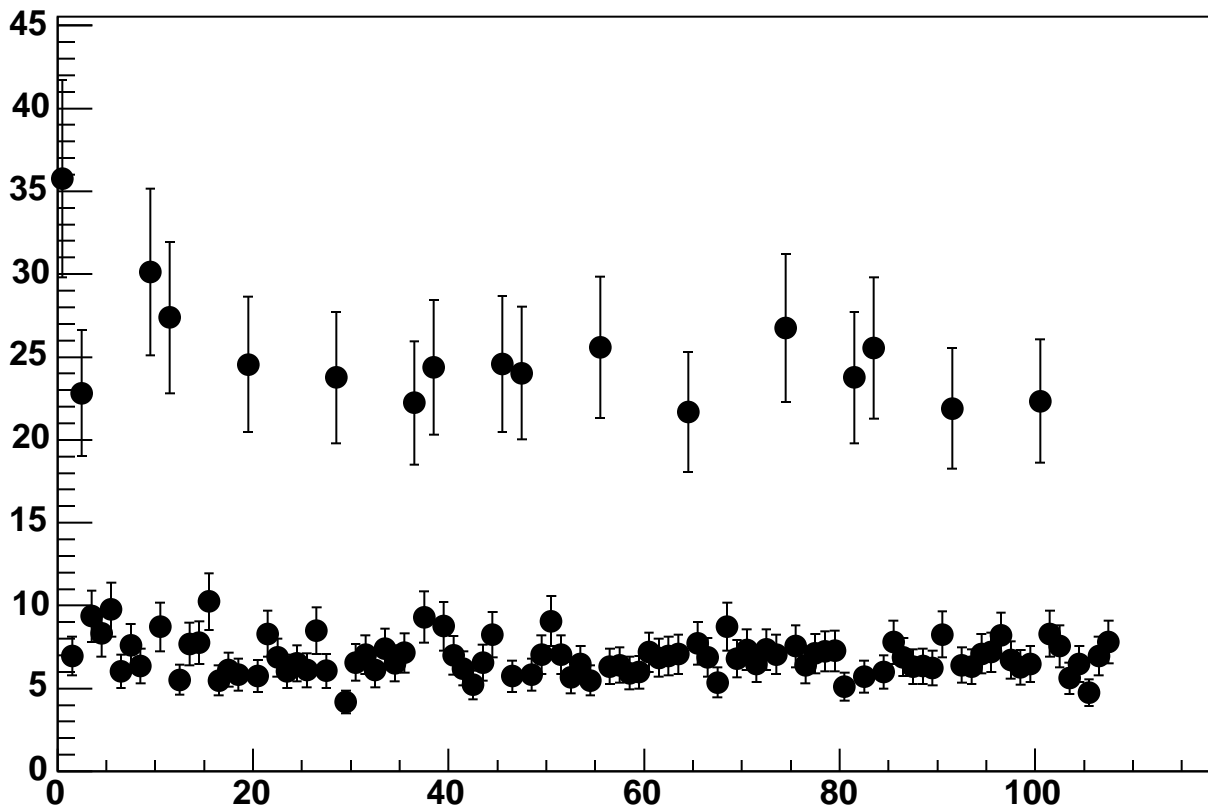
Enable 0, DAC=4000, Hold=45, ADC Noise vs 18\*Chip+Chan



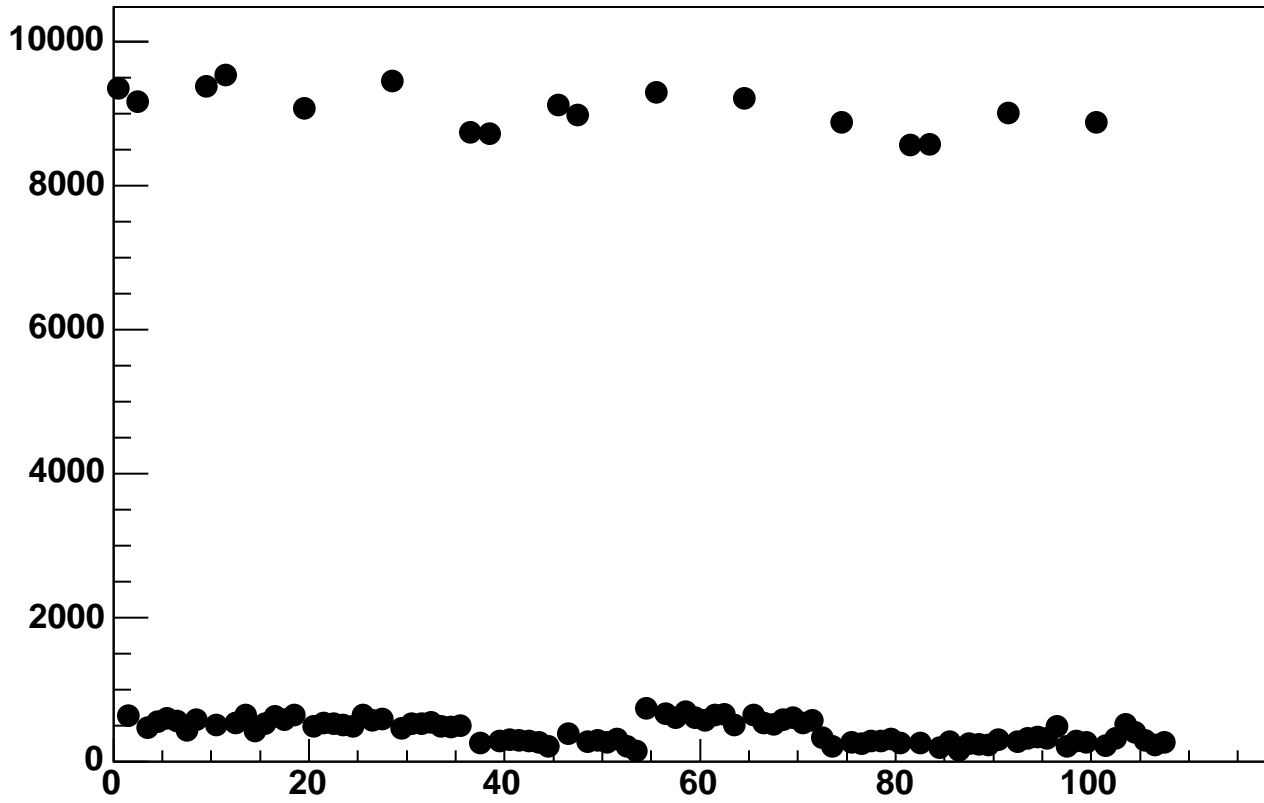
Enable 0, DAC=4000, Hold=50, ADC Mean vs 18\*Chip+Chan



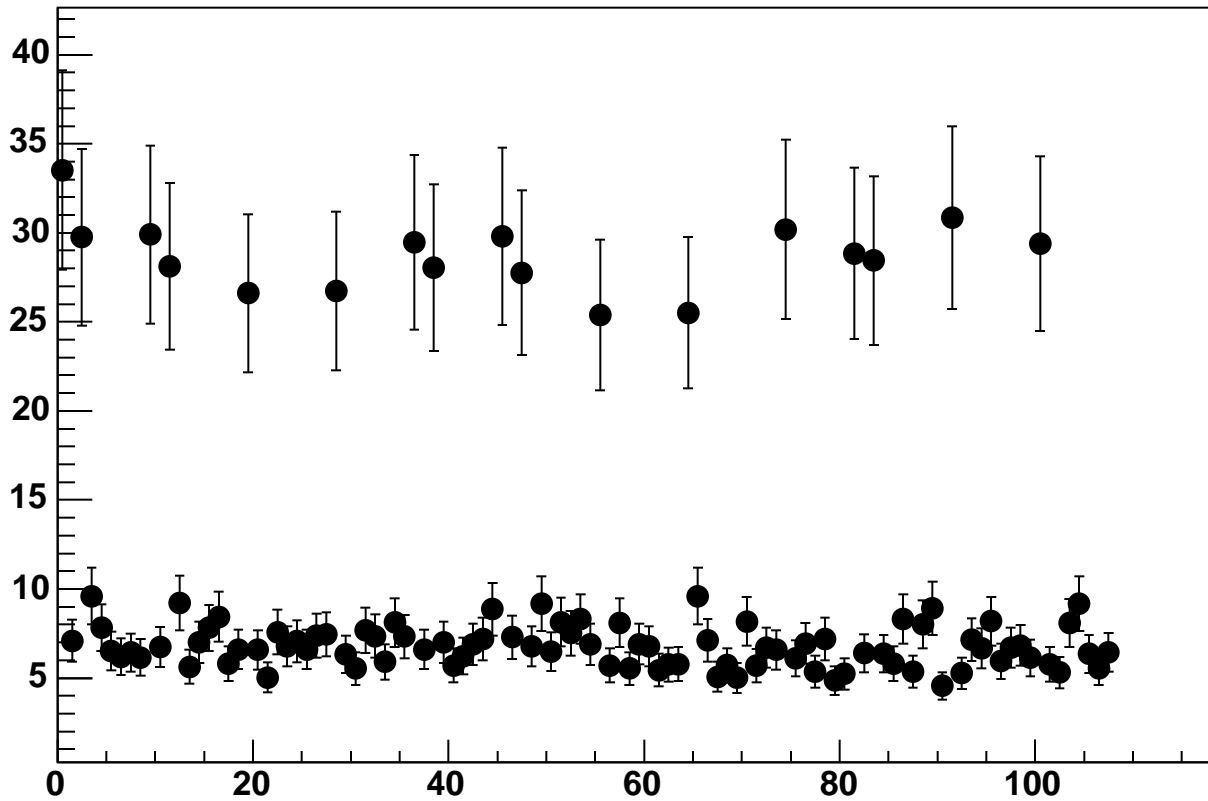
Enable 0, DAC=4000, Hold=50, ADC Noise vs 18\*Chip+Chan



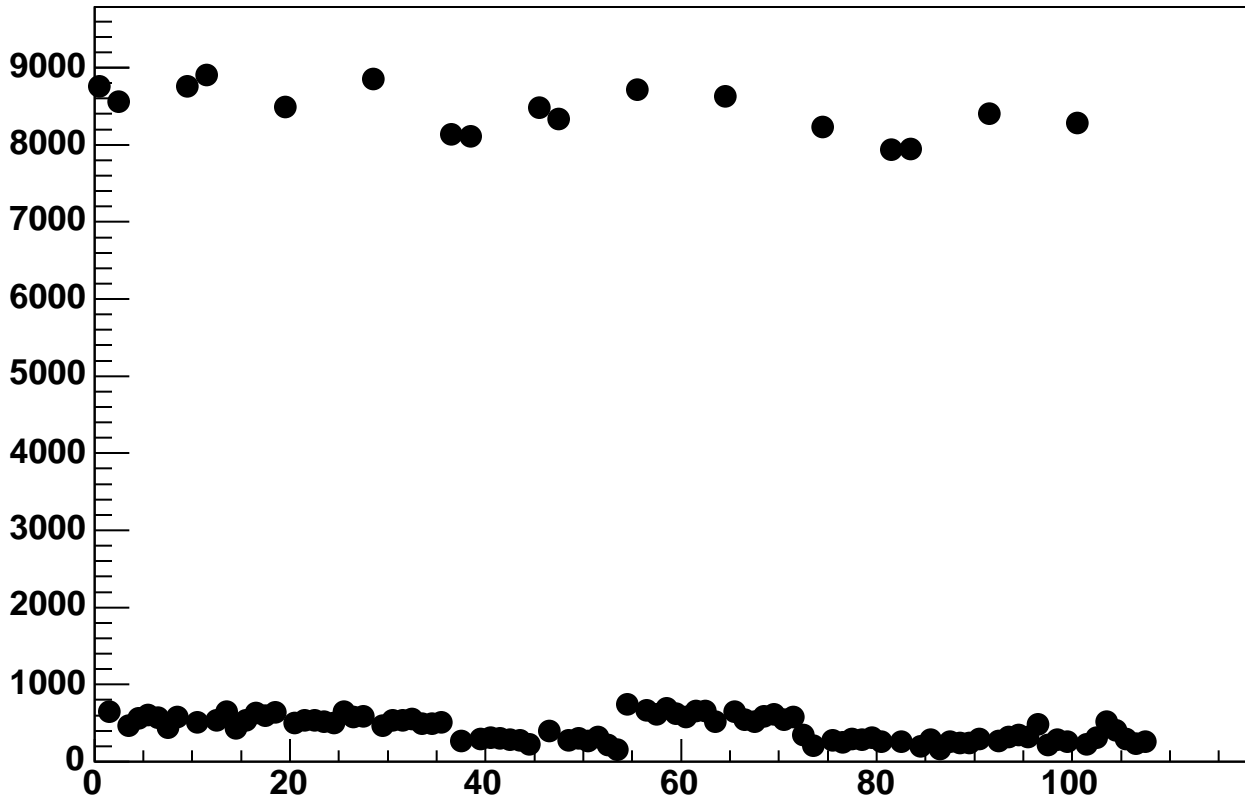
Enable 0, DAC=4000, Hold=55, ADC Mean vs 18\*Chip+Chan



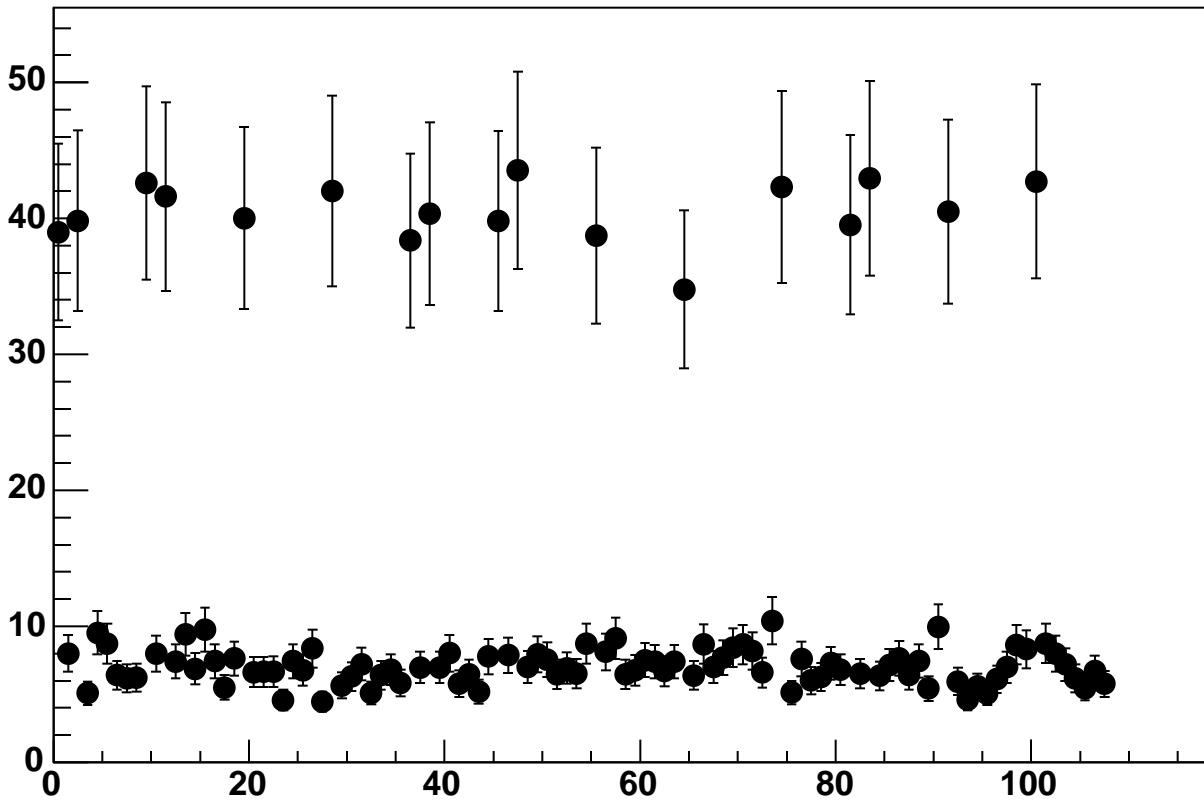
Enable 0, DAC=4000, Hold=55, ADC Noise vs 18\*Chip+Chan



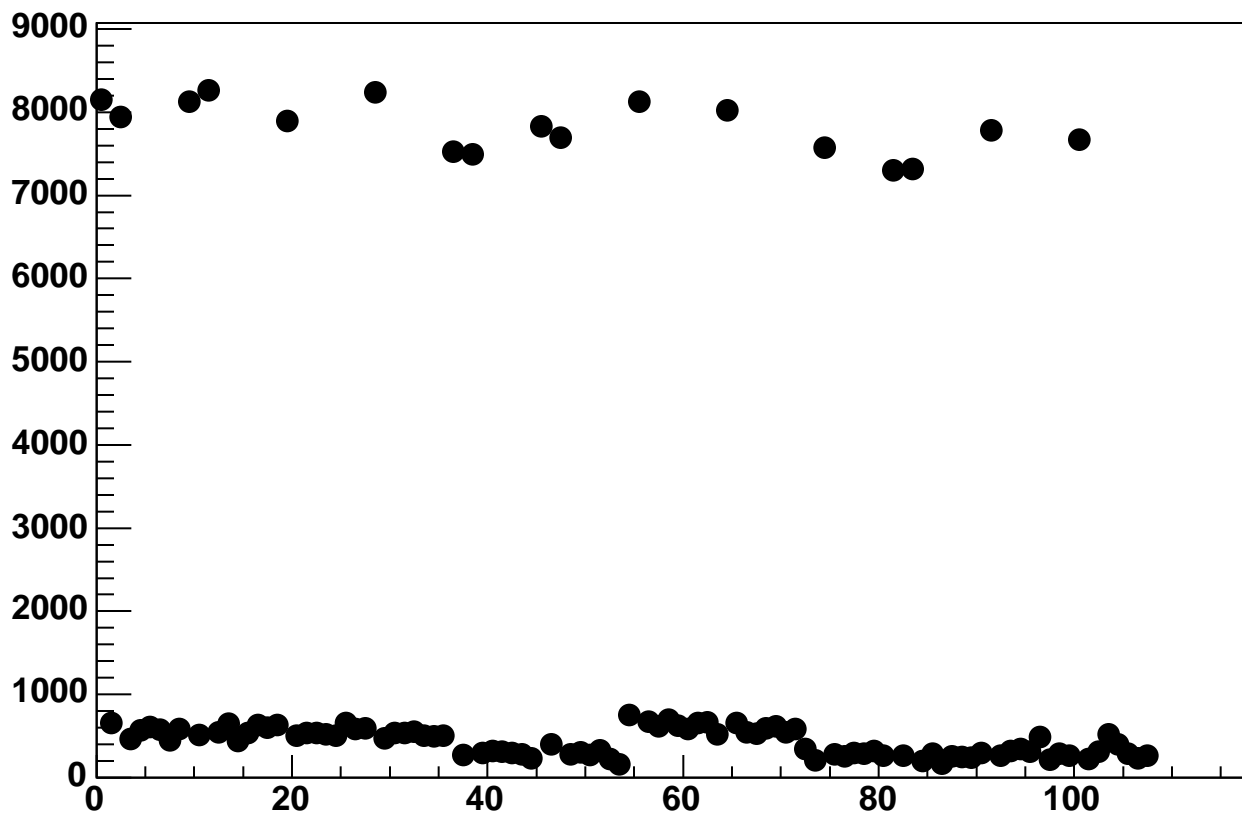
Enable 0, DAC=4000, Hold=60, ADC Mean vs 18\*Chip+Chan



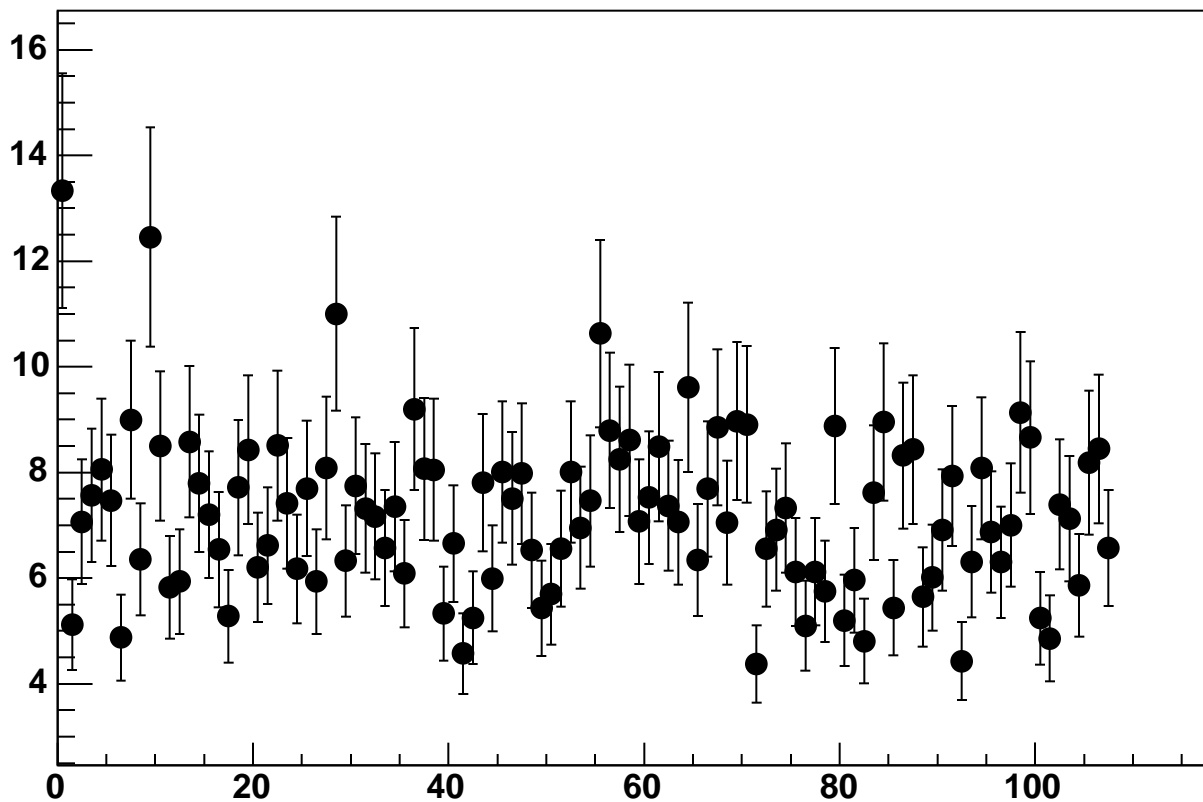
Enable 0, DAC=4000, Hold=60, ADC Noise vs 18\*Chip+Chan



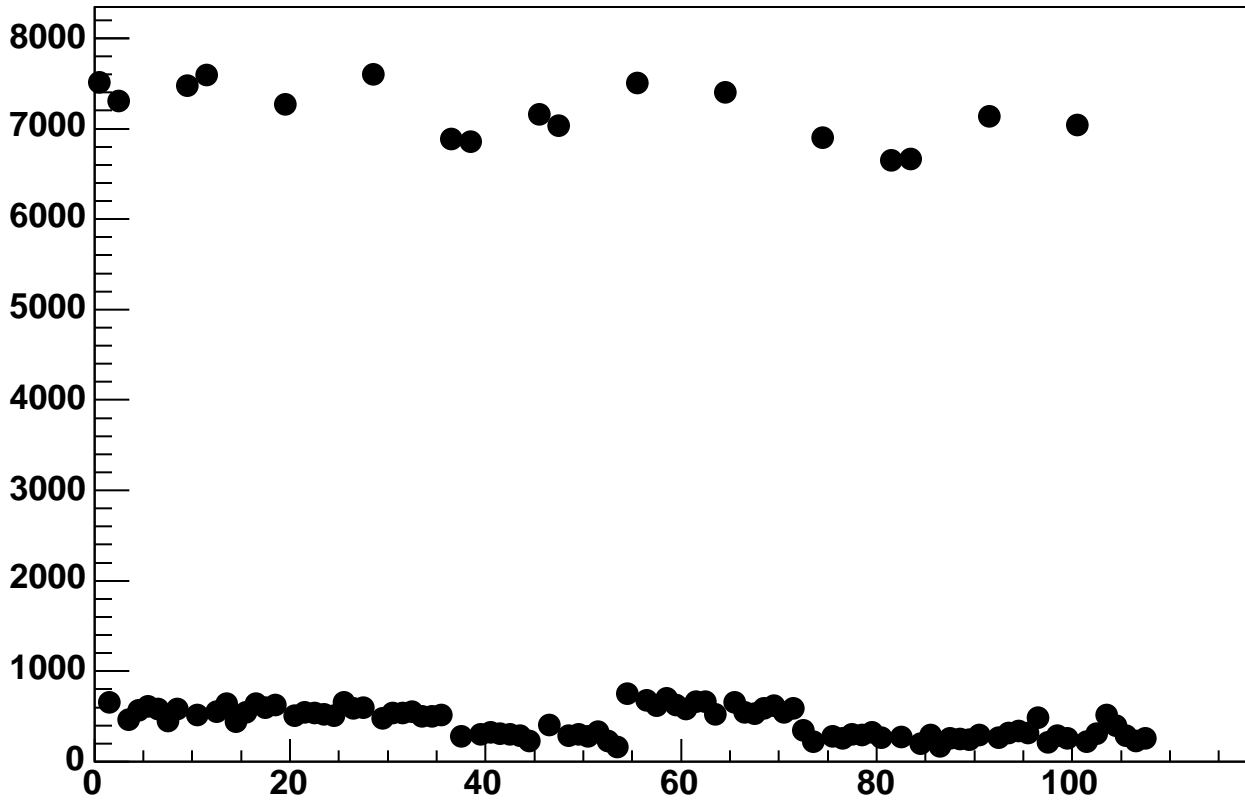
Enable 0, DAC=4000, Hold=65, ADC Mean vs 18\*Chip+Chan



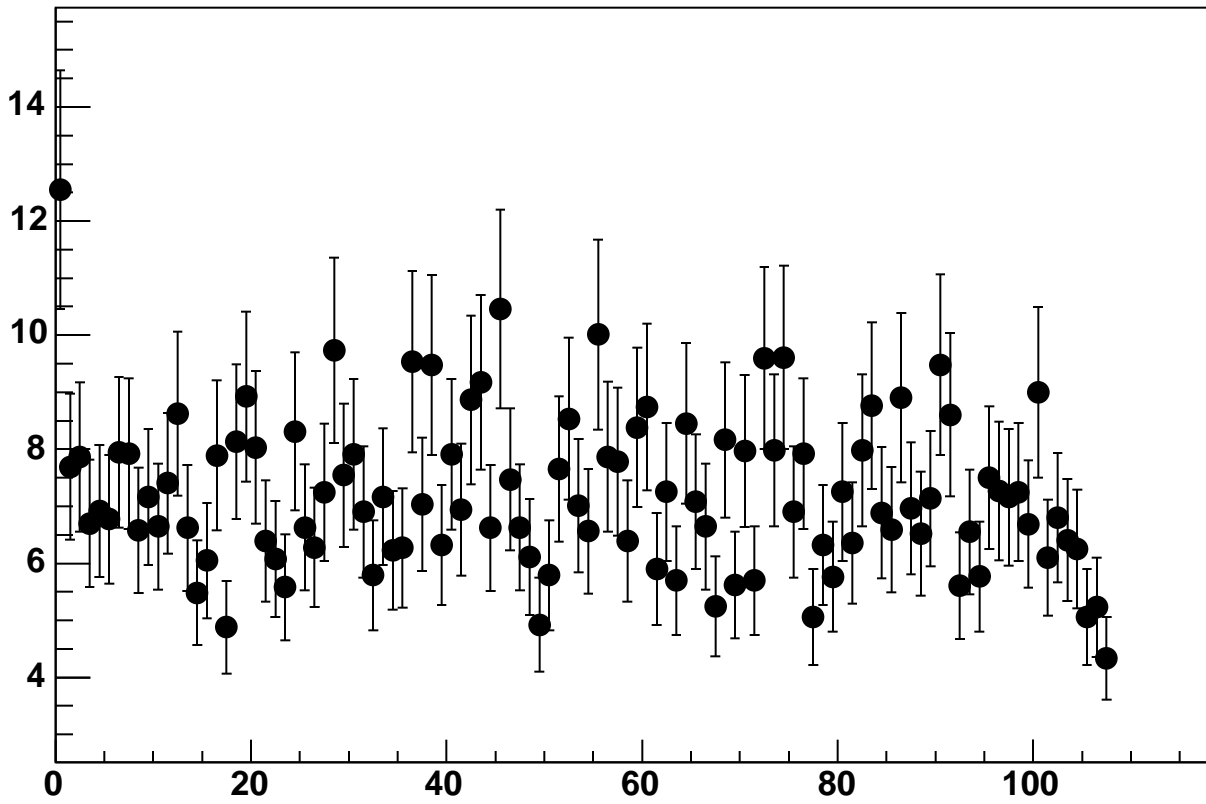
Enable 0, DAC=4000, Hold=65, ADC Noise vs 18\*Chip+Chan



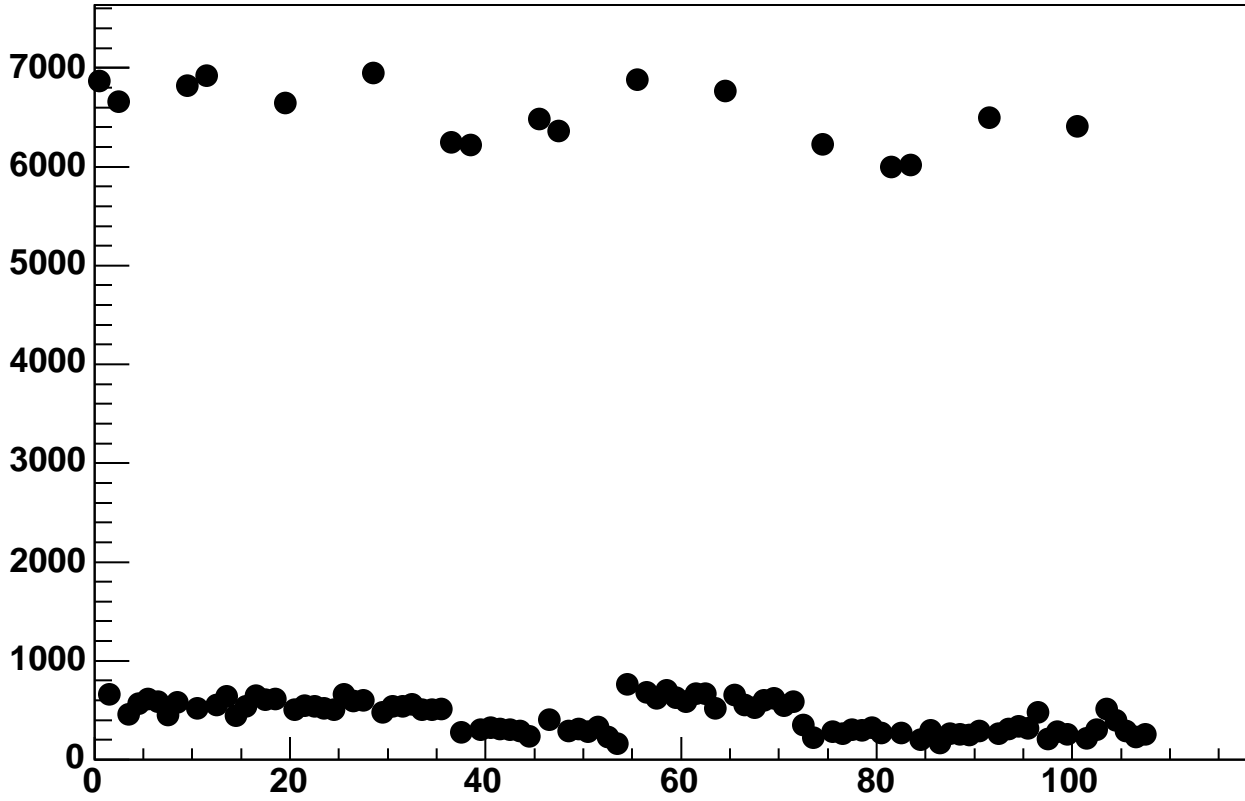
Enable 0, DAC=4000, Hold=70, ADC Mean vs 18\*Chip+Chan



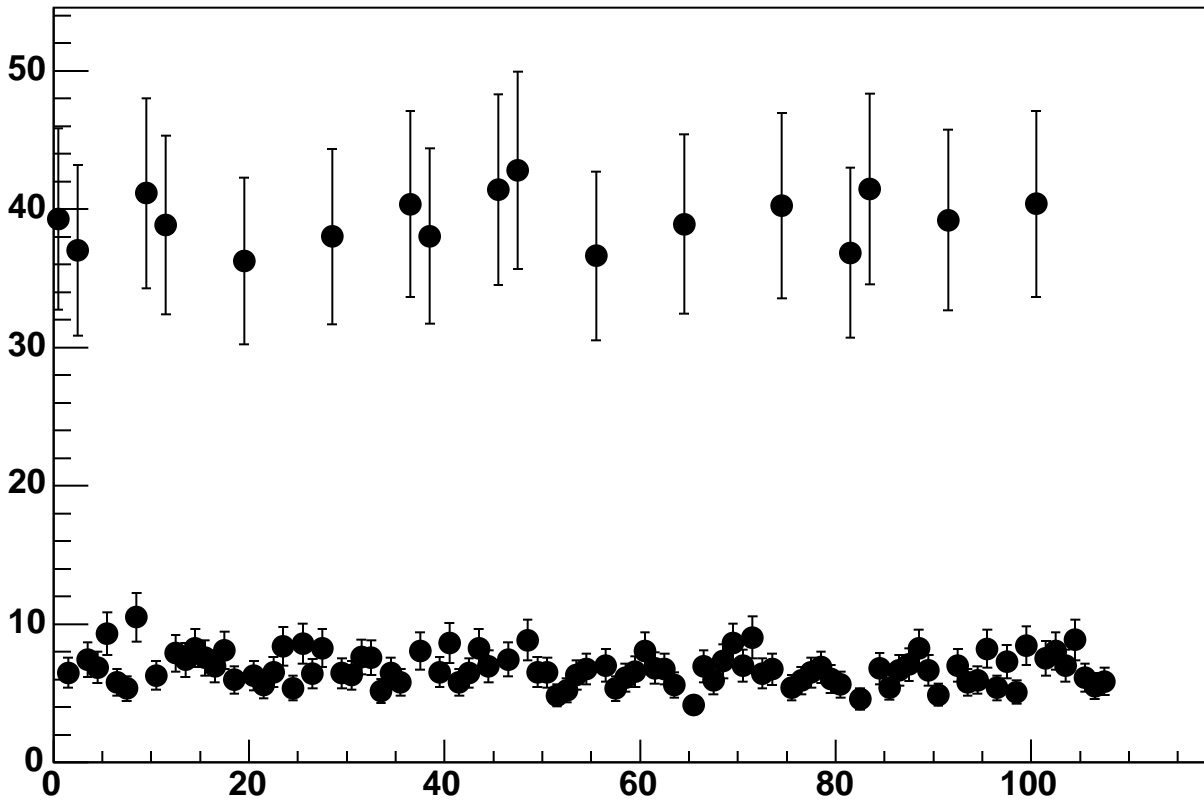
Enable 0, DAC=4000, Hold=70, ADC Noise vs 18\*Chip+Chan



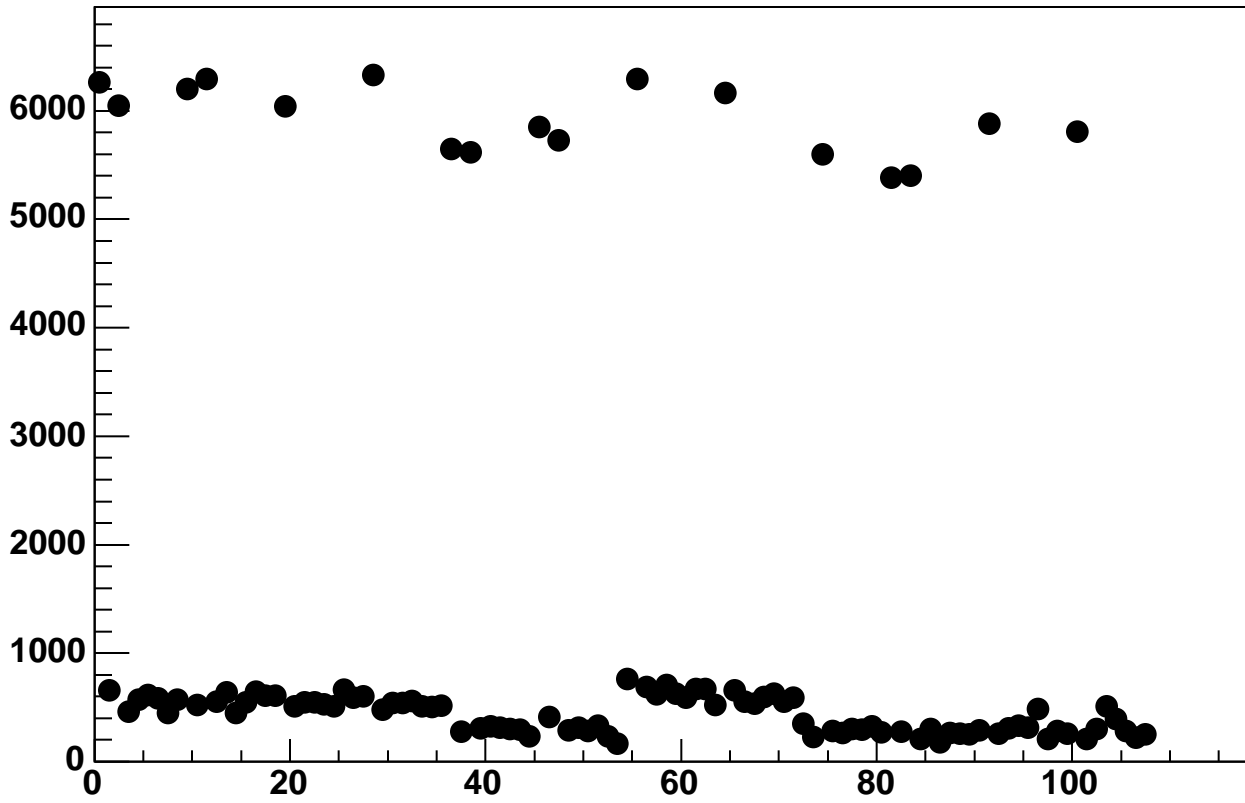
Enable 0, DAC=4000, Hold=75, ADC Mean vs 18\*Chip+Chan



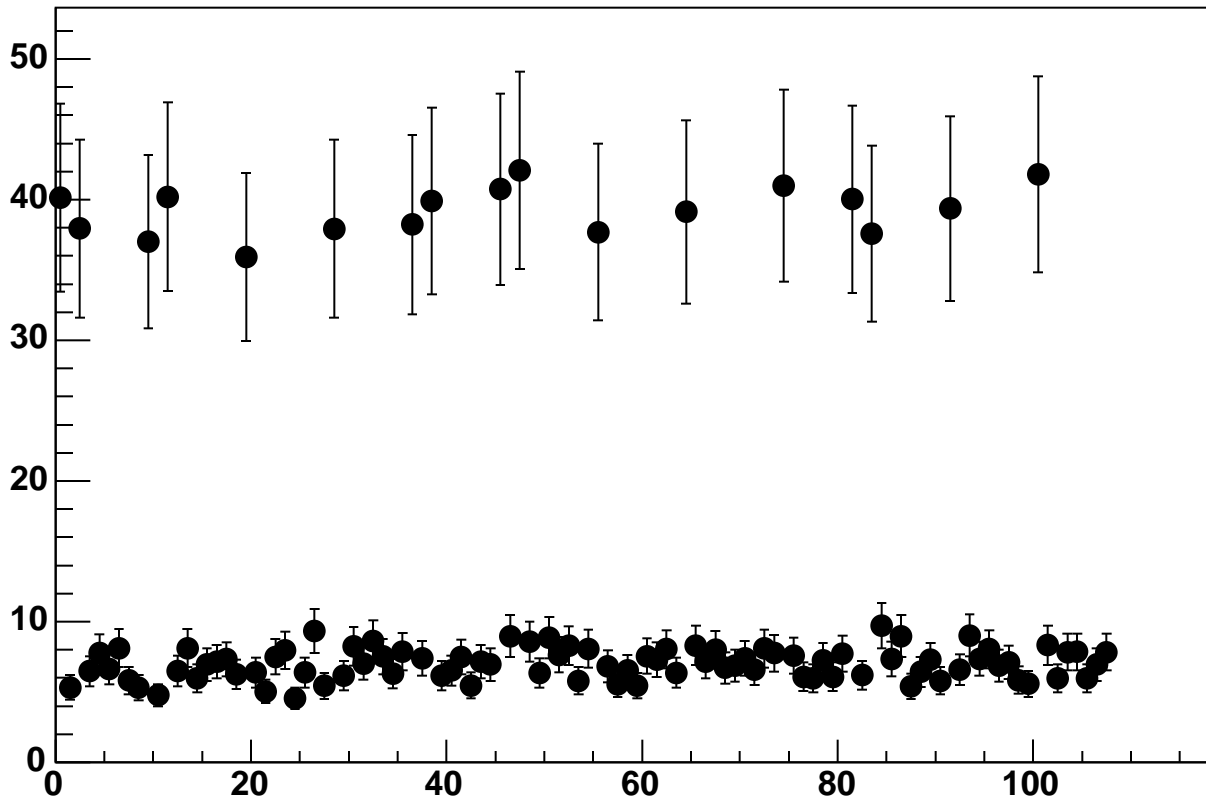
Enable 0, DAC=4000, Hold=75, ADC Noise vs 18\*Chip+Chan



Enable 0, DAC=4000, Hold=80, ADC Mean vs 18\*Chip+Chan

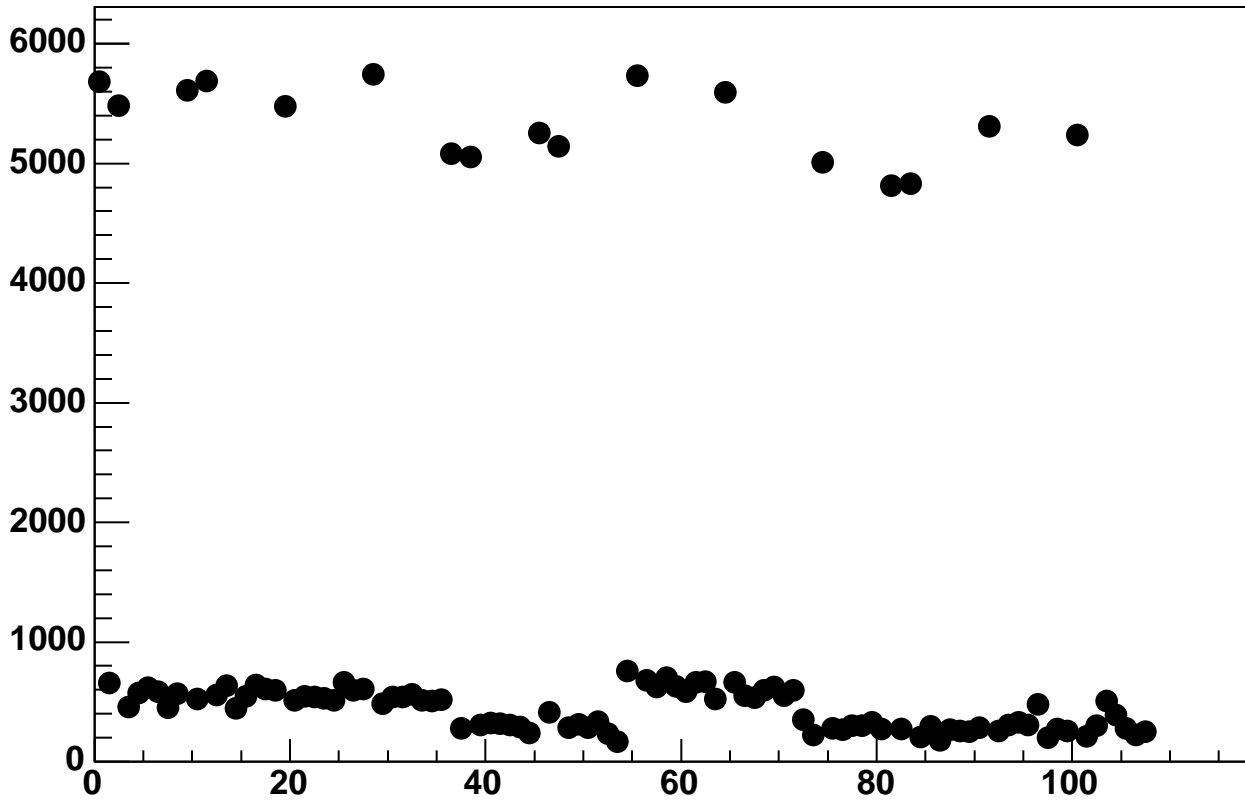


Enable 0, DAC=4000, Hold=80, ADC Noise vs 18\*Chip+Chan

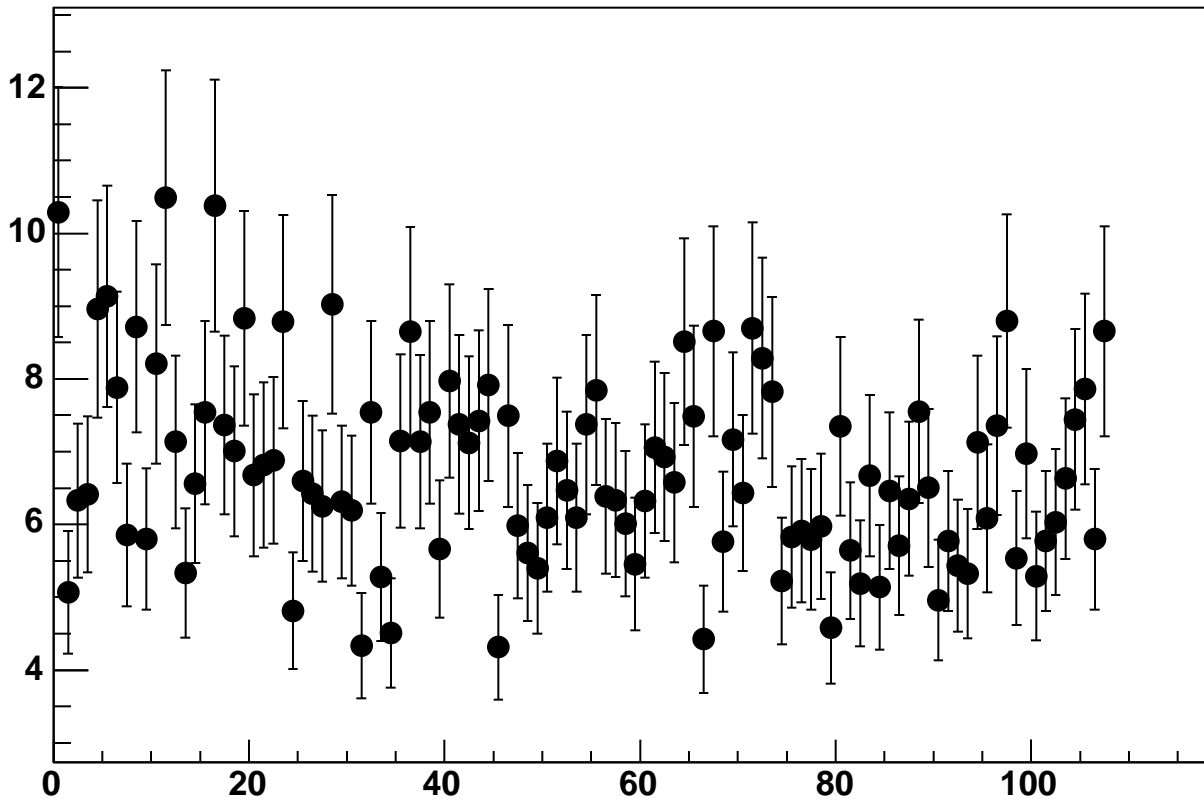




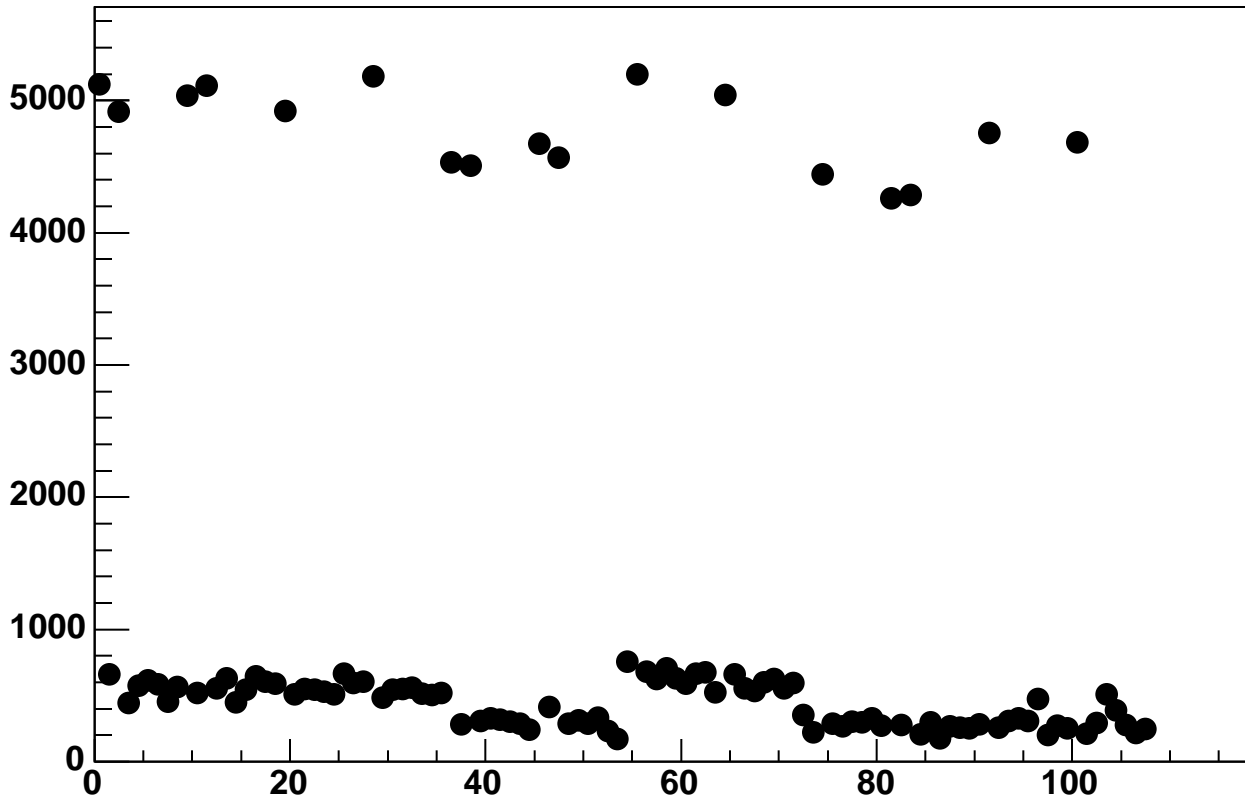
Enable 0, DAC=4000, Hold=85, ADC Mean vs 18\*Chip+Chan



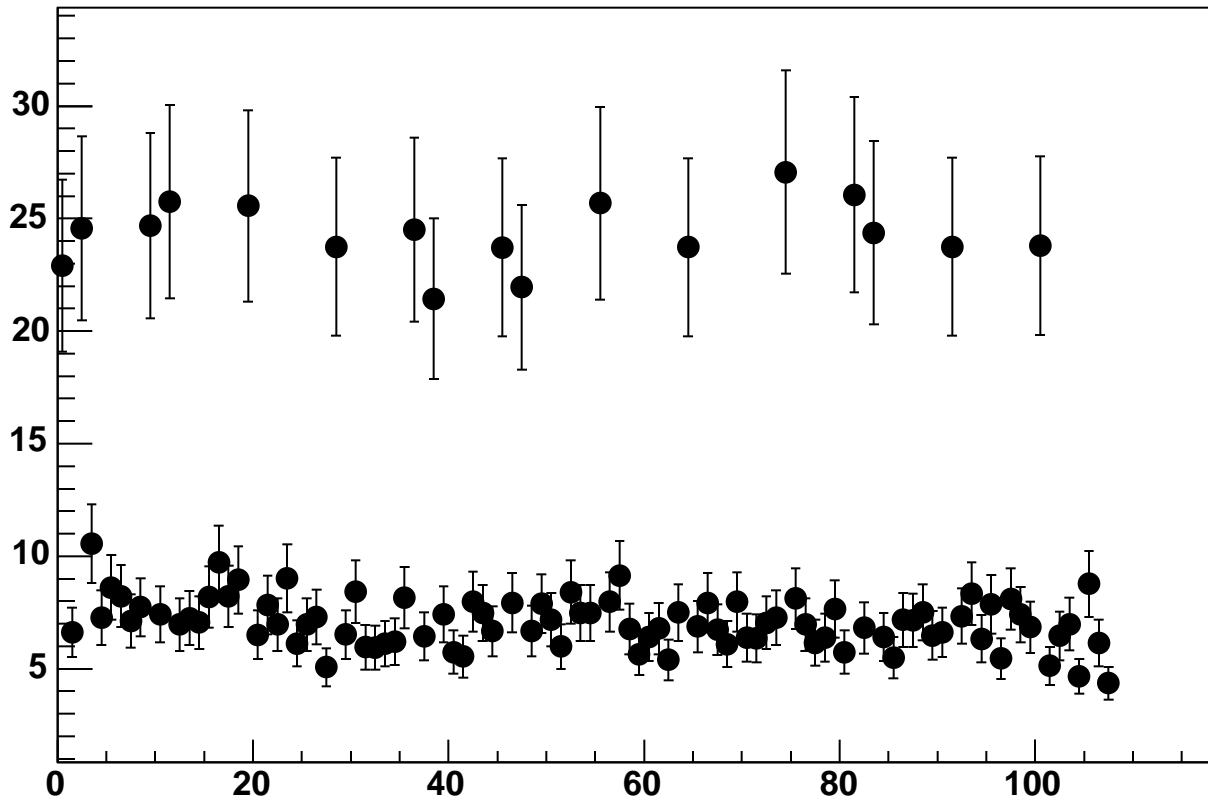
Enable 0, DAC=4000, Hold=85, ADC Noise vs 18\*Chip+Chan



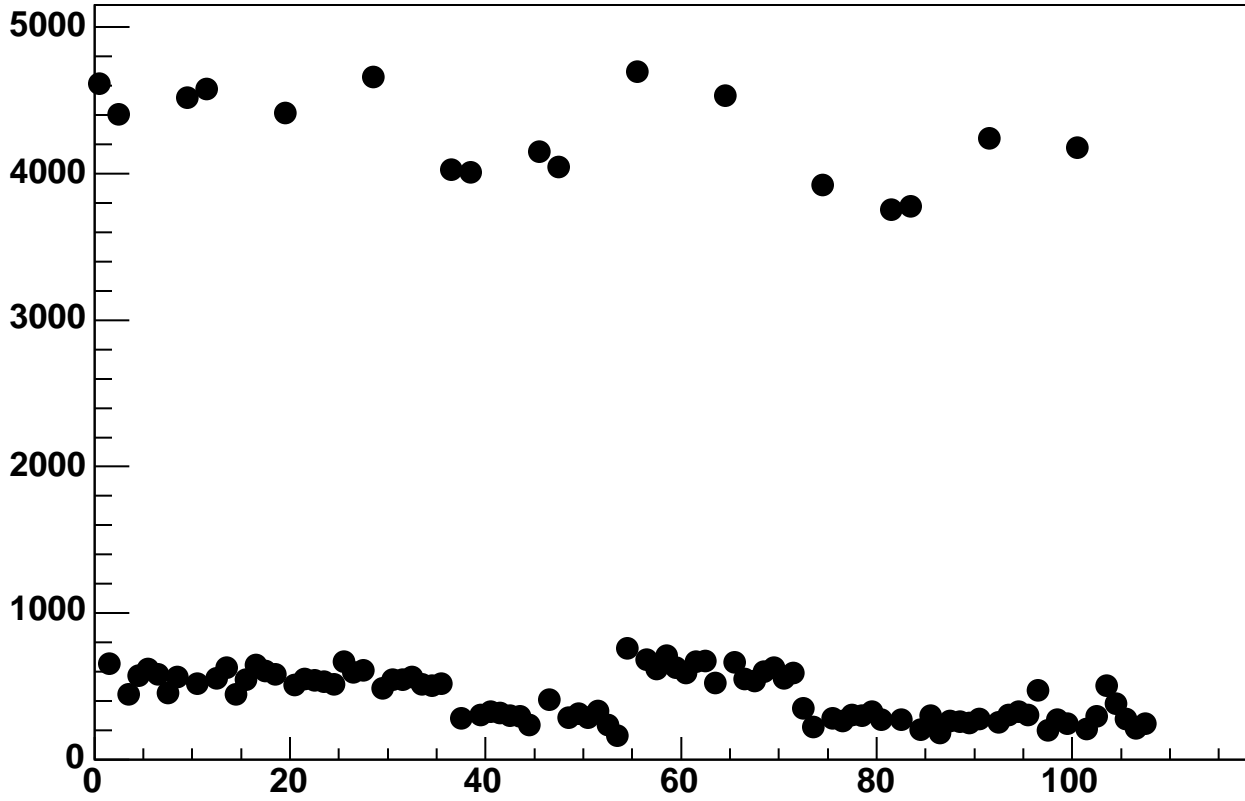
Enable 0, DAC=4000, Hold=90, ADC Mean vs 18\*Chip+Chan



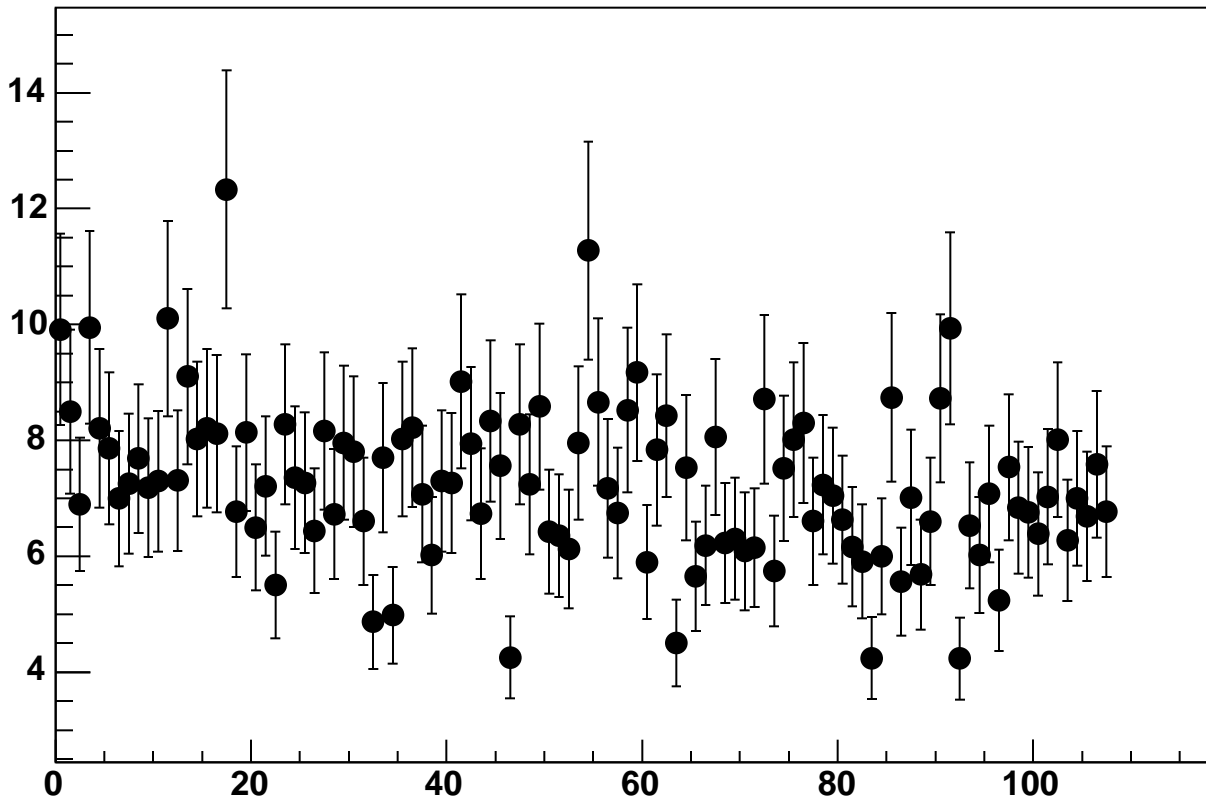
Enable 0, DAC=4000, Hold=90, ADC Noise vs 18\*Chip+Chan



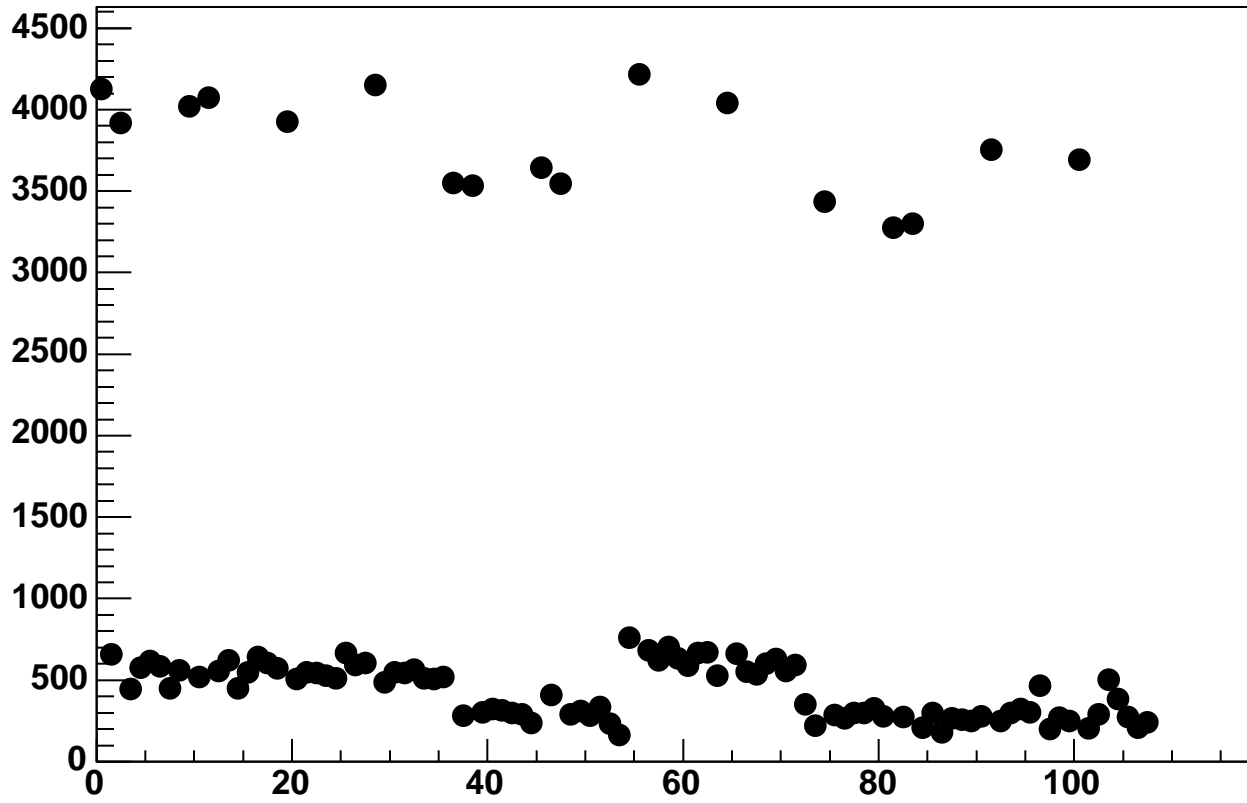
Enable 0, DAC=4000, Hold=95, ADC Mean vs 18\*Chip+Chan



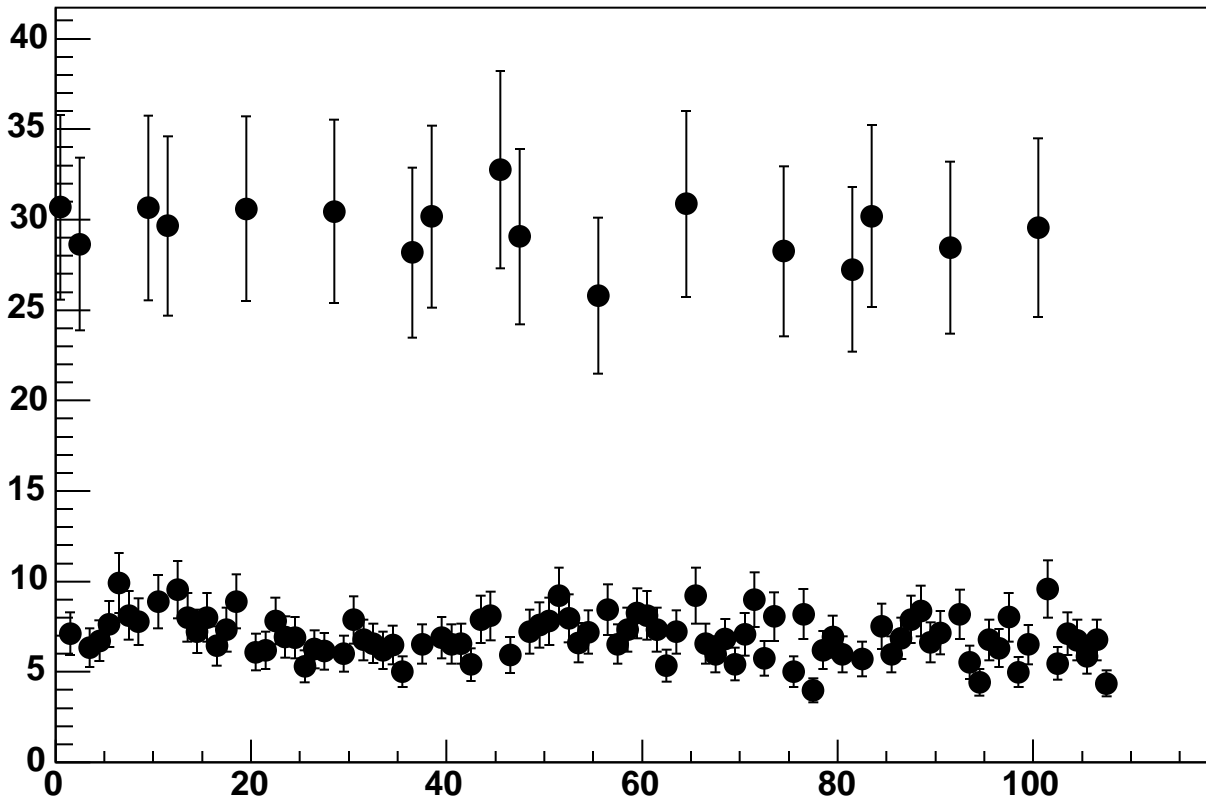
Enable 0, DAC=4000, Hold=95, ADC Noise vs 18\*Chip+Chan



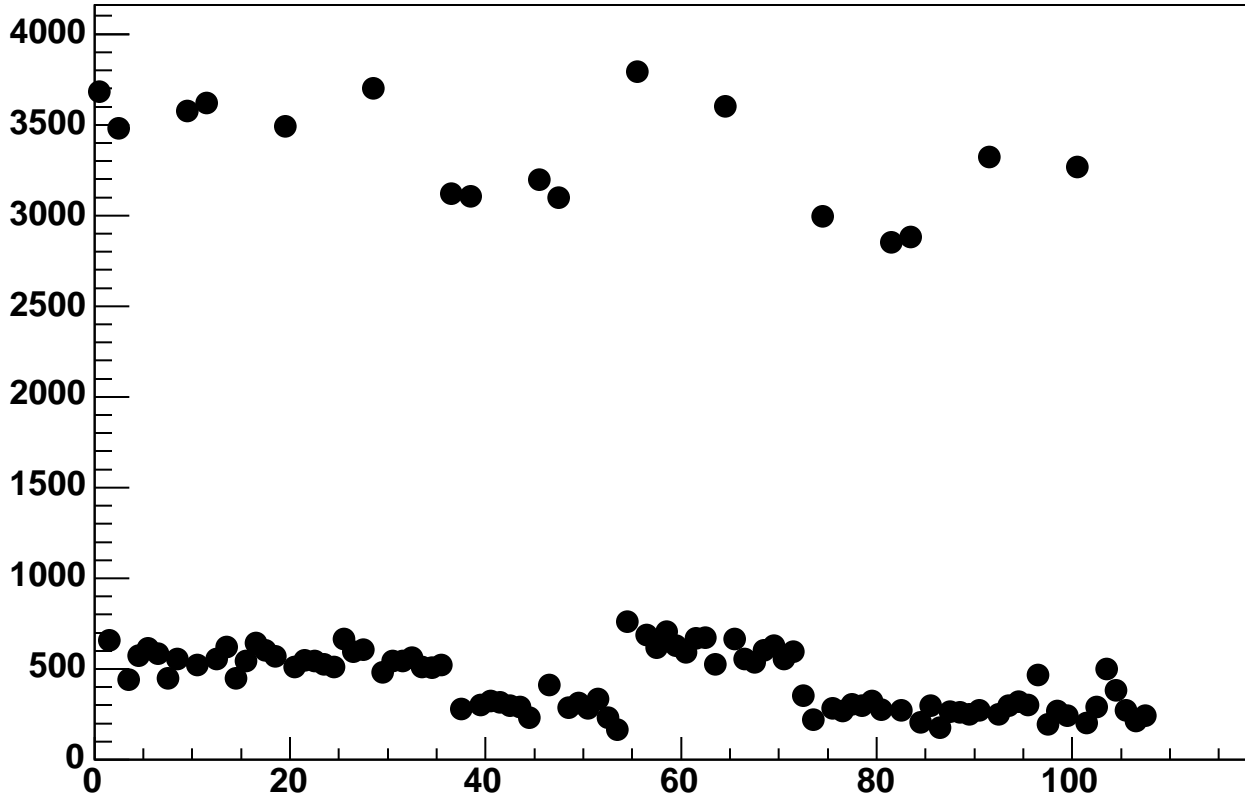
Enable 0, DAC=4000, Hold=100, ADC Mean vs 18\*Chip+Chan



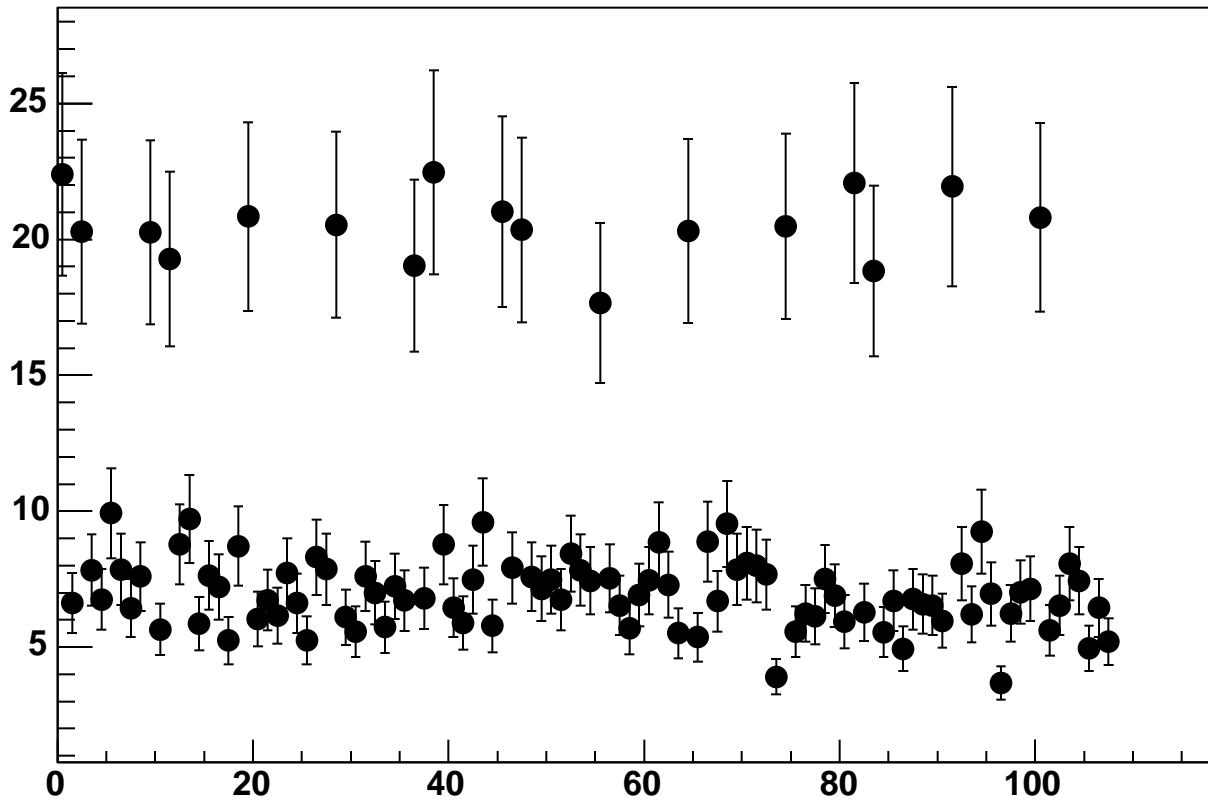
Enable 0, DAC=4000, Hold=100, ADC Noise vs 18\*Chip+Chan



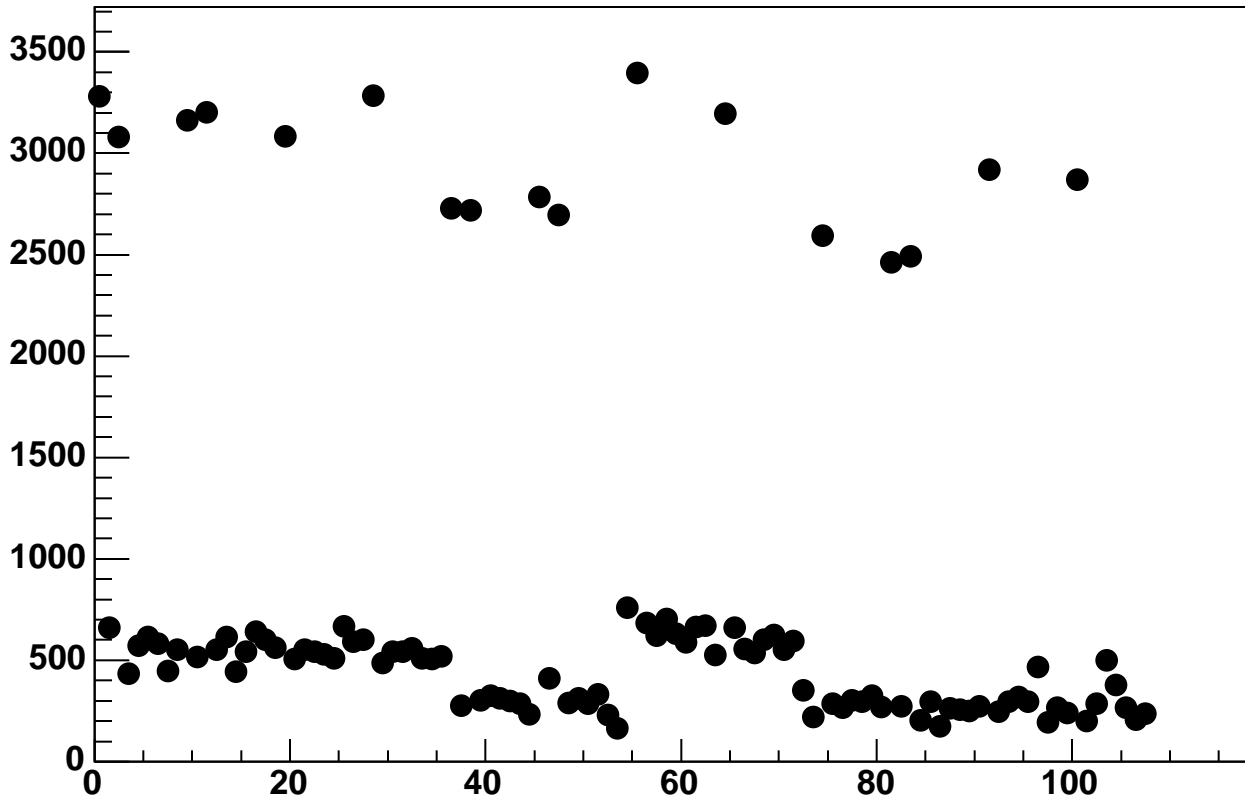
Enable 0, DAC=4000, Hold=105, ADC Mean vs 18\*Chip+Chan



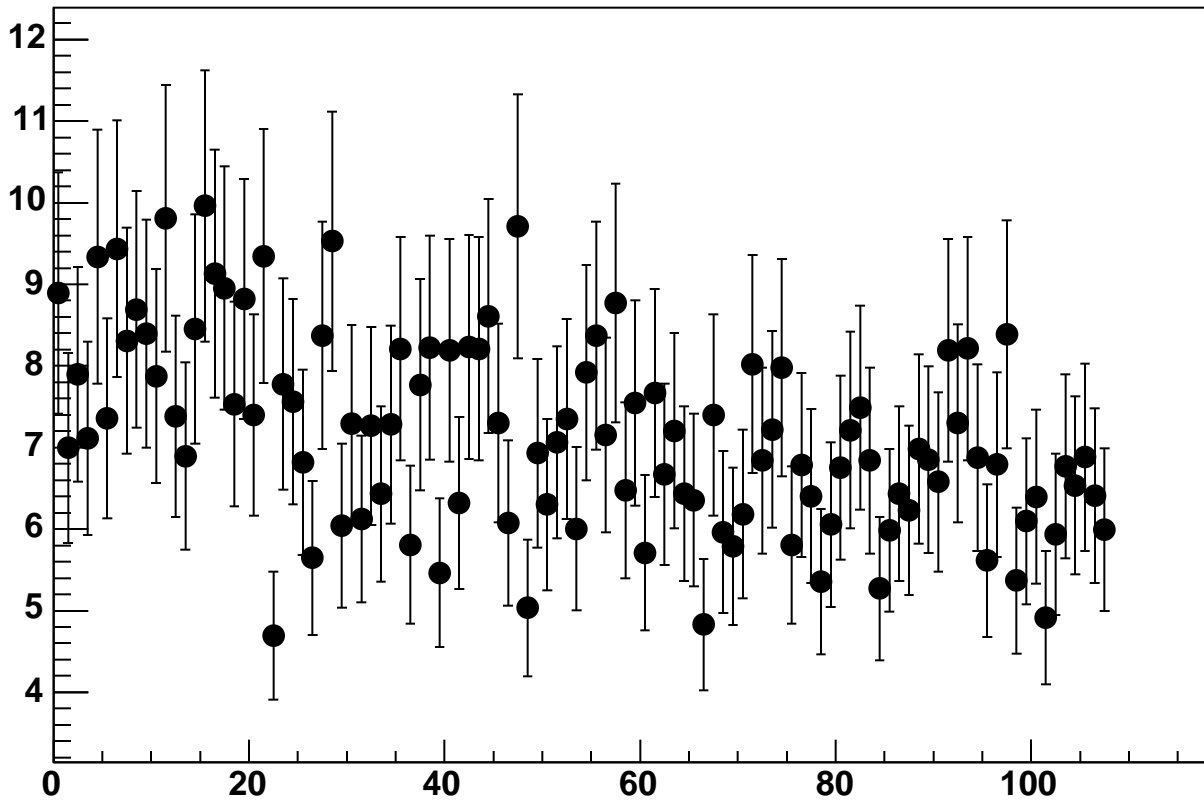
Enable 0, DAC=4000, Hold=105, ADC Noise vs 18\*Chip+Chan



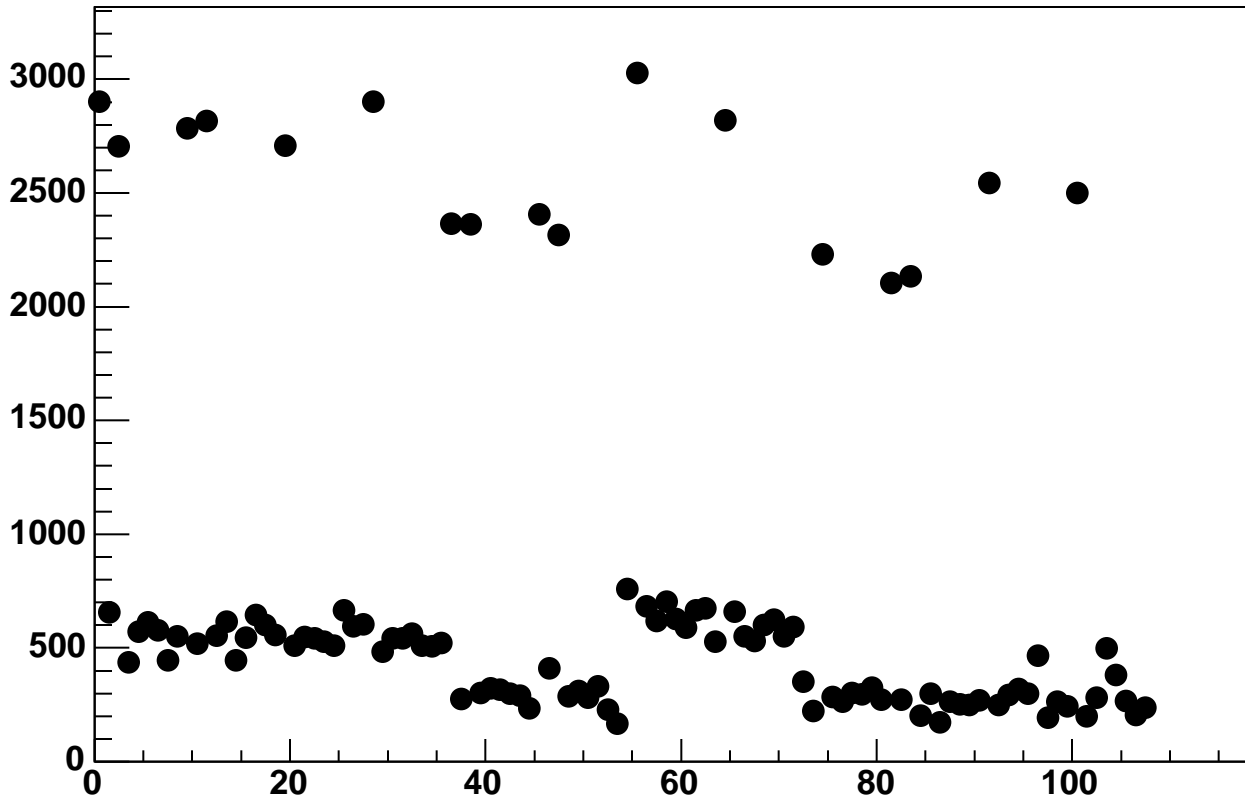
Enable 0, DAC=4000, Hold=110, ADC Mean vs 18\*Chip+Chan



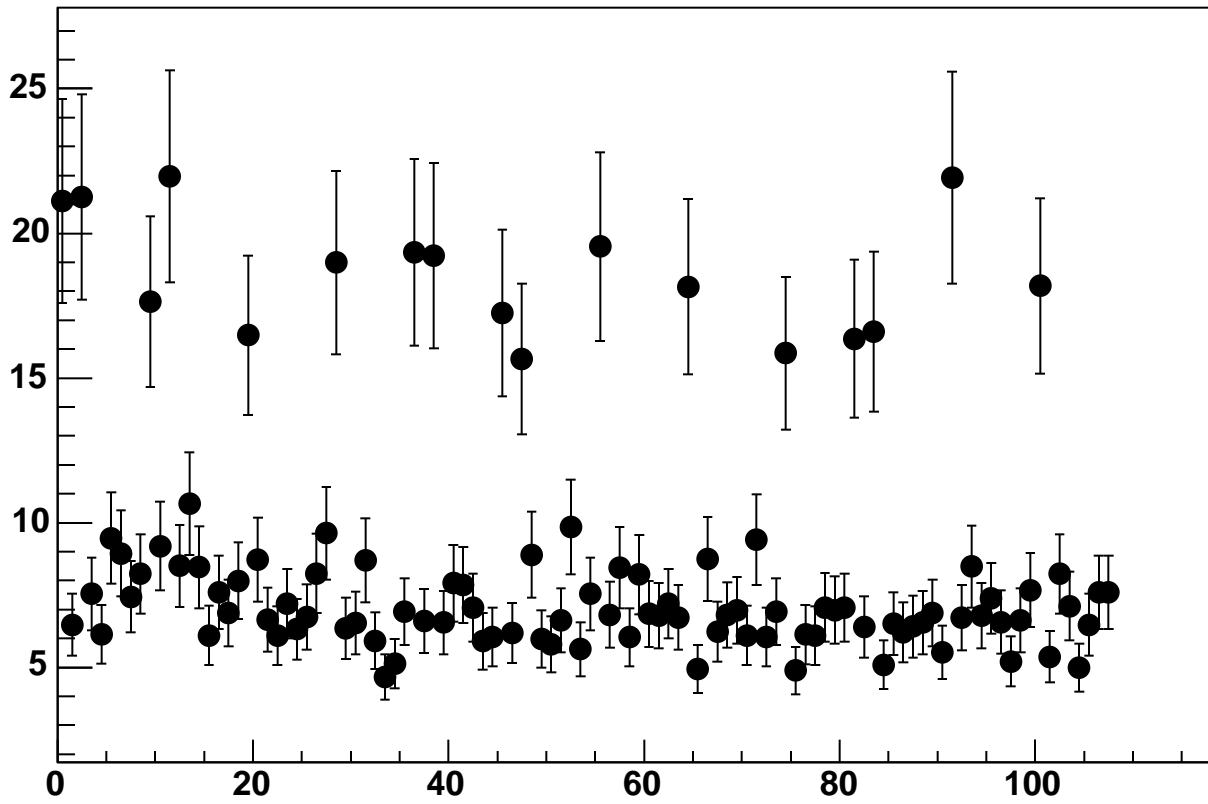
Enable 0, DAC=4000, Hold=110, ADC Noise vs 18\*Chip+Chan



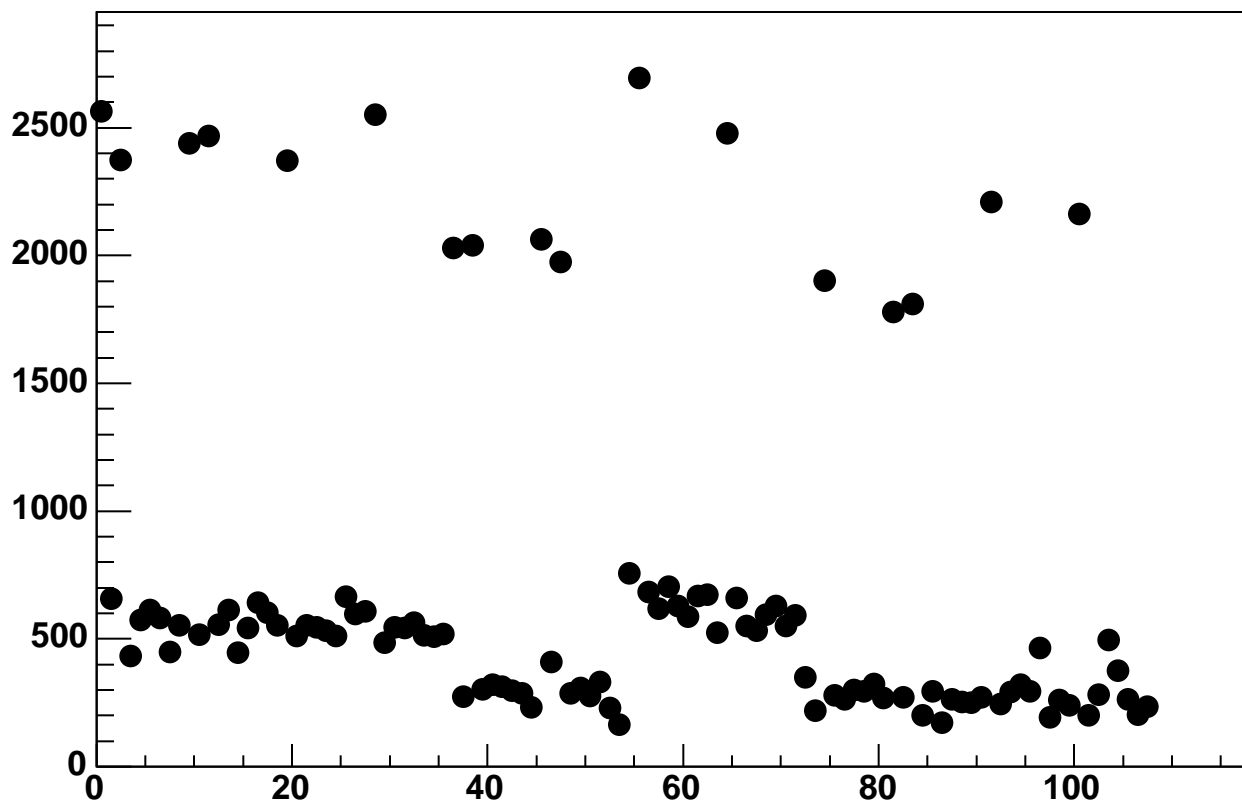
Enable 0, DAC=4000, Hold=115, ADC Mean vs 18\*Chip+Chan



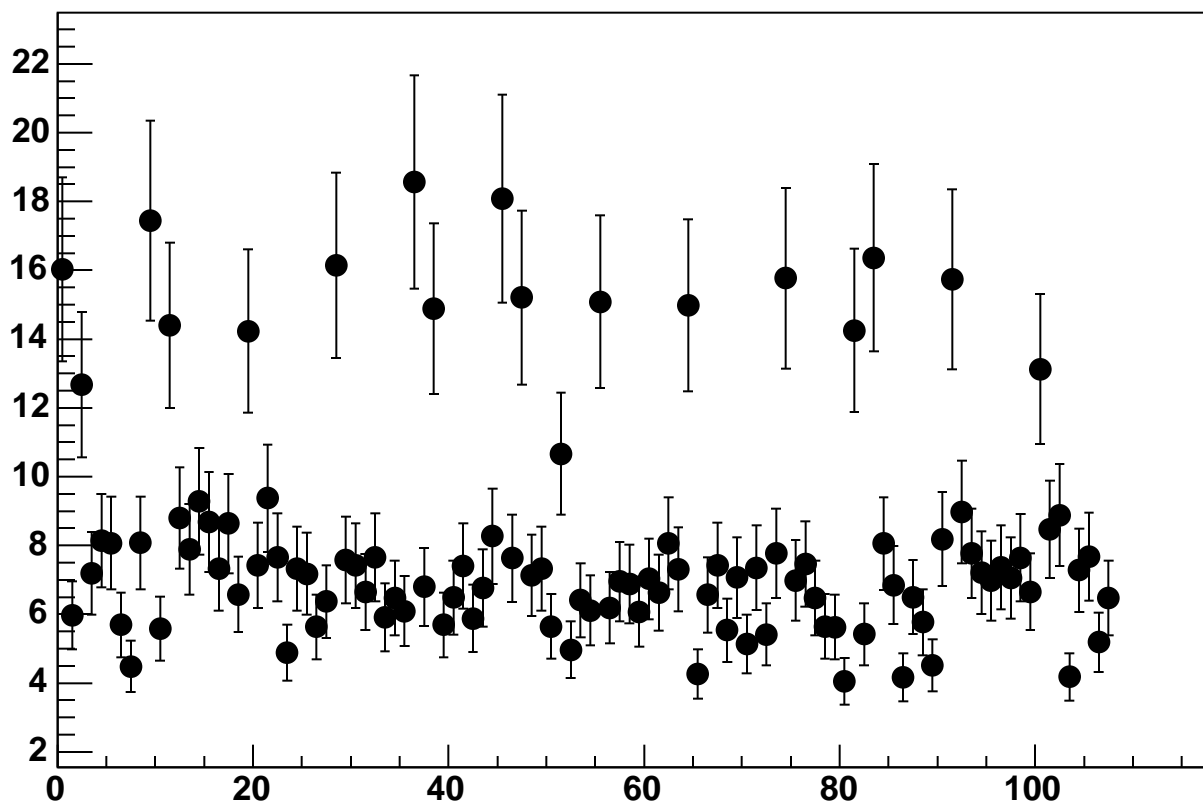
Enable 0, DAC=4000, Hold=115, ADC Noise vs 18\*Chip+Chan



Enable 0, DAC=4000, Hold=120, ADC Mean vs 18\*Chip+Chan

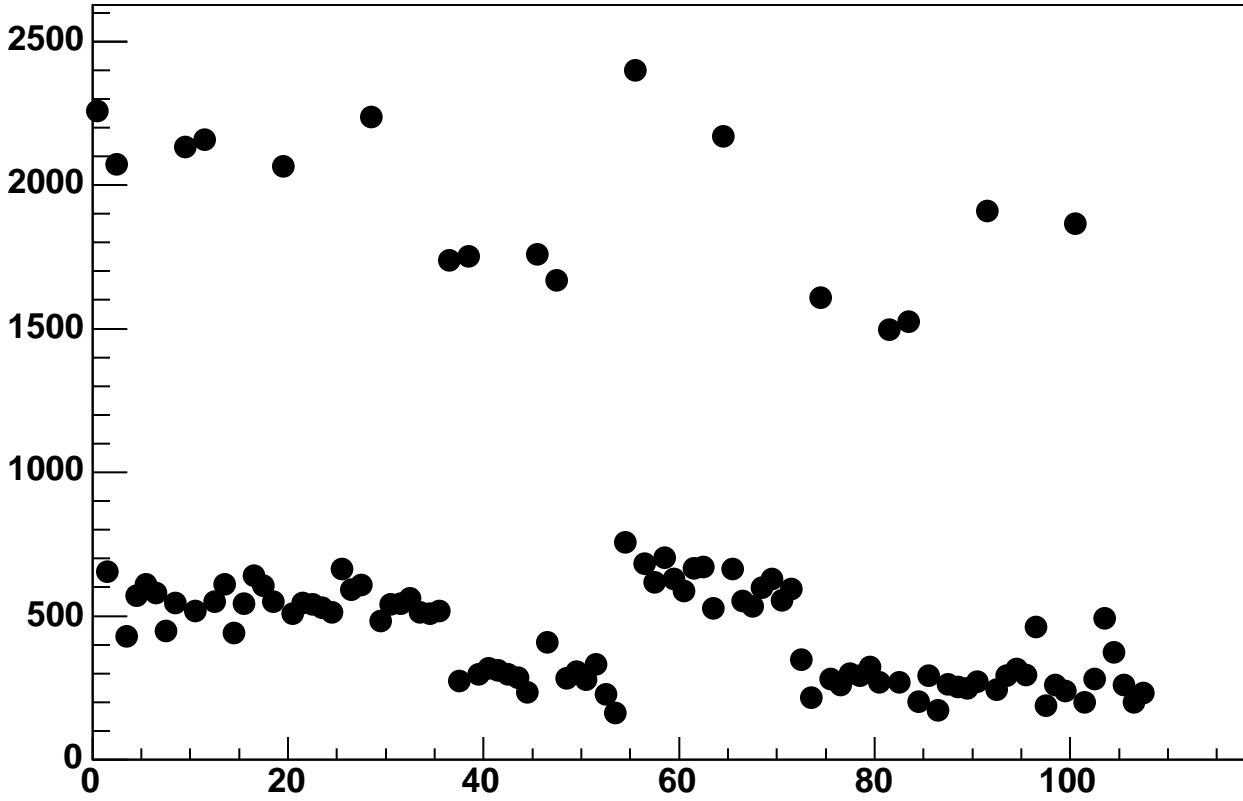


Enable 0, DAC=4000, Hold=120, ADC Noise vs 18\*Chip+Chan

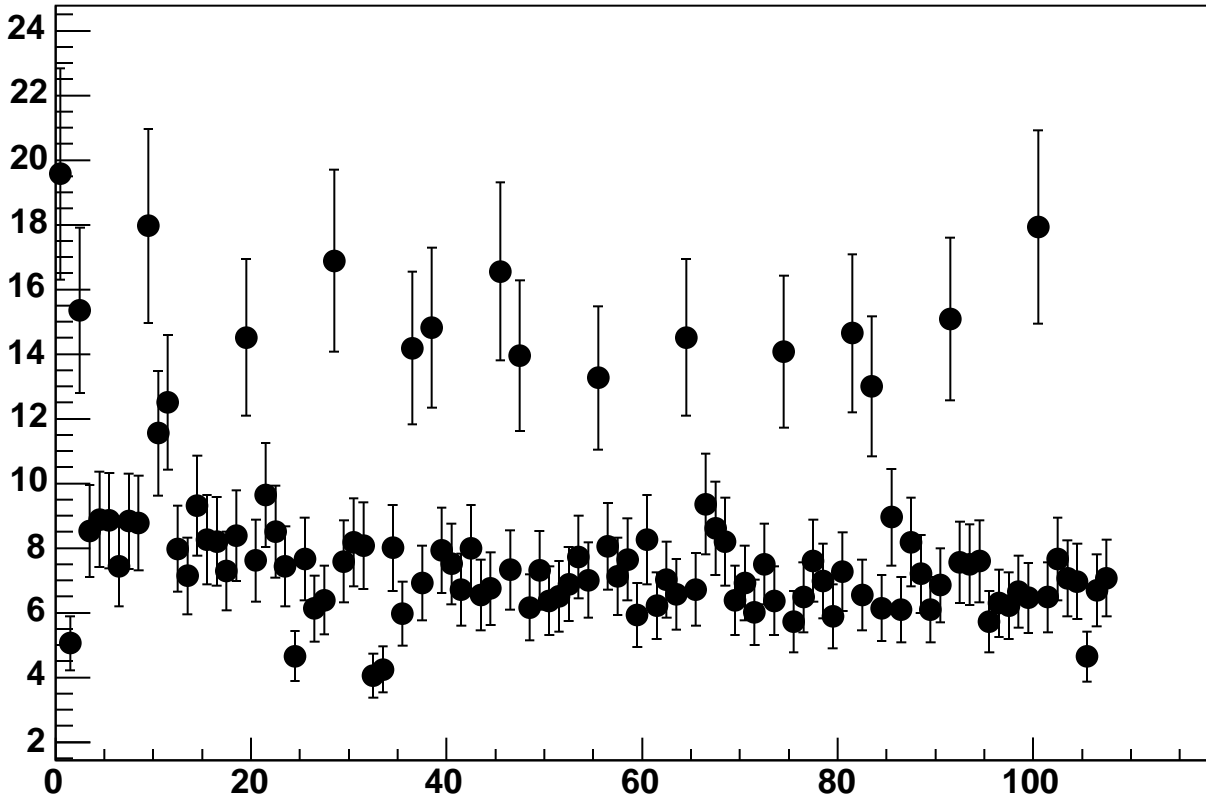




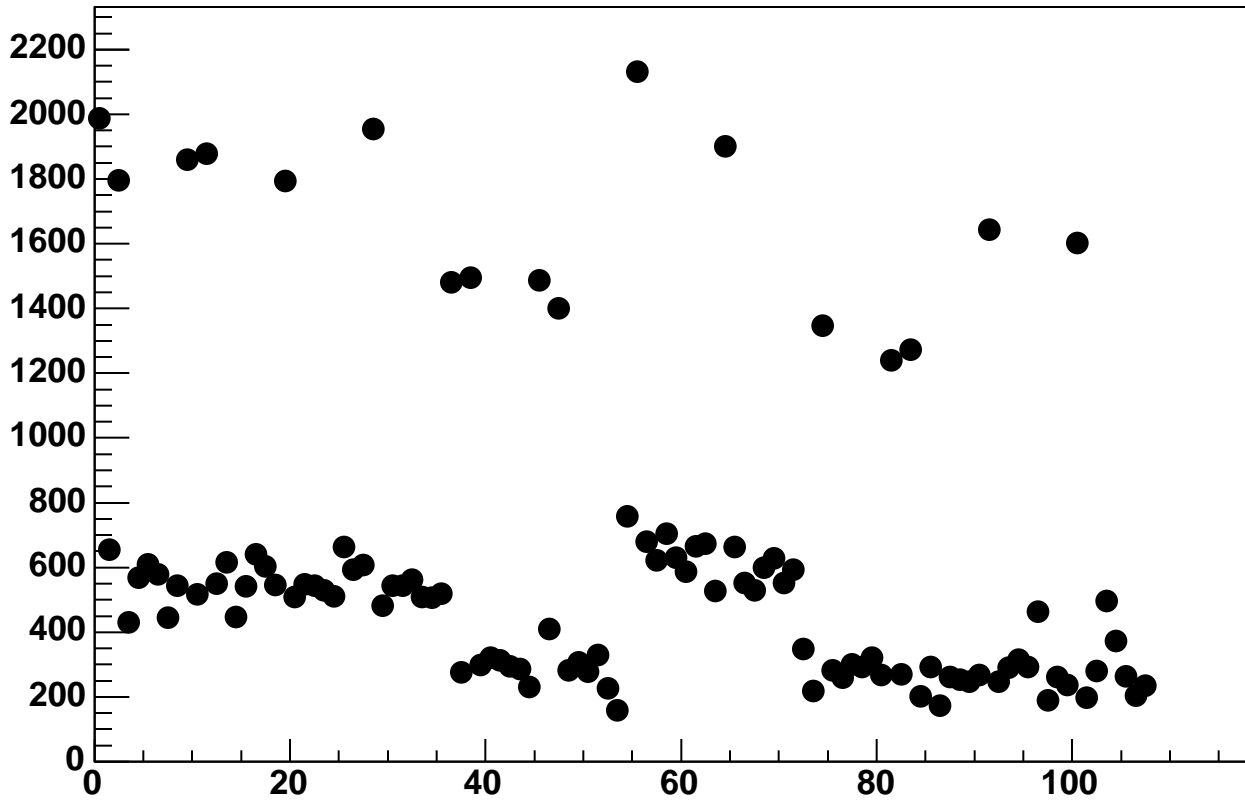
Enable 0, DAC=4000, Hold=125, ADC Mean vs 18\*Chip+Chan



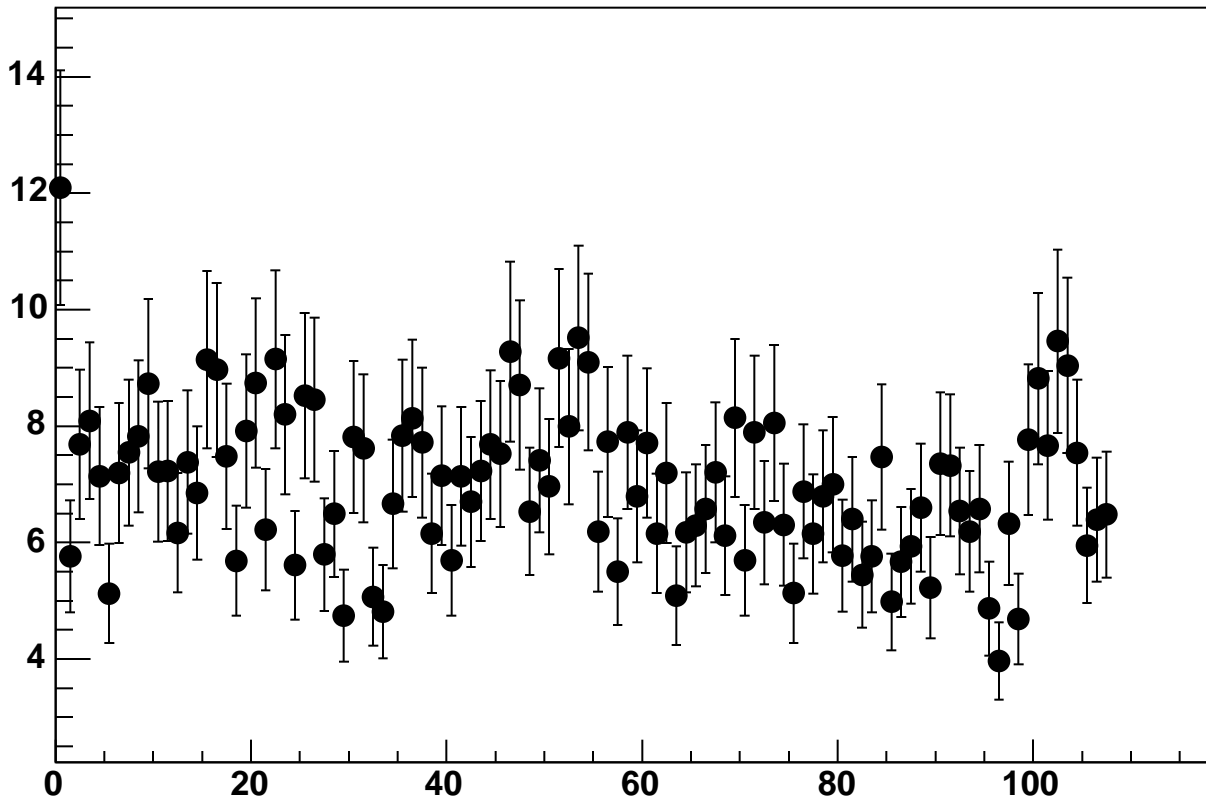
Enable 0, DAC=4000, Hold=125, ADC Noise vs 18\*Chip+Chan



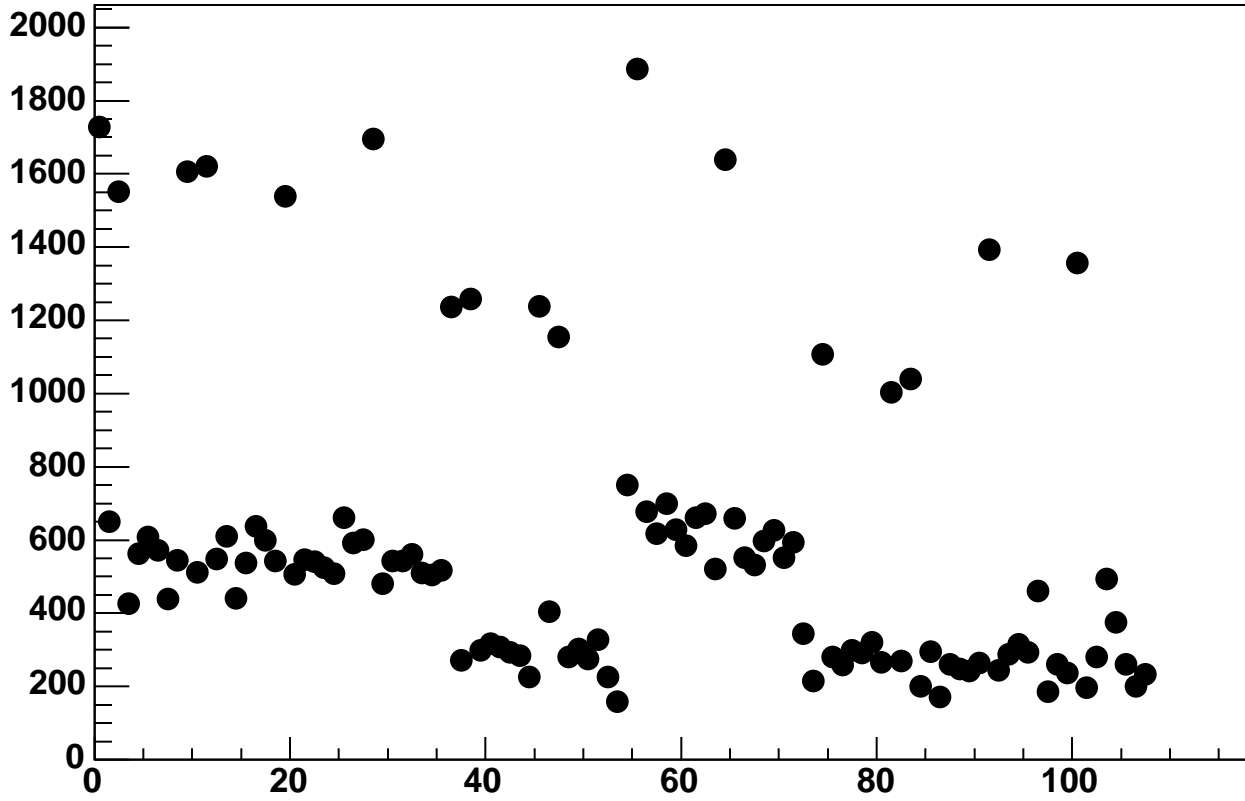
Enable 0, DAC=4000, Hold=130, ADC Mean vs 18\*Chip+Chan



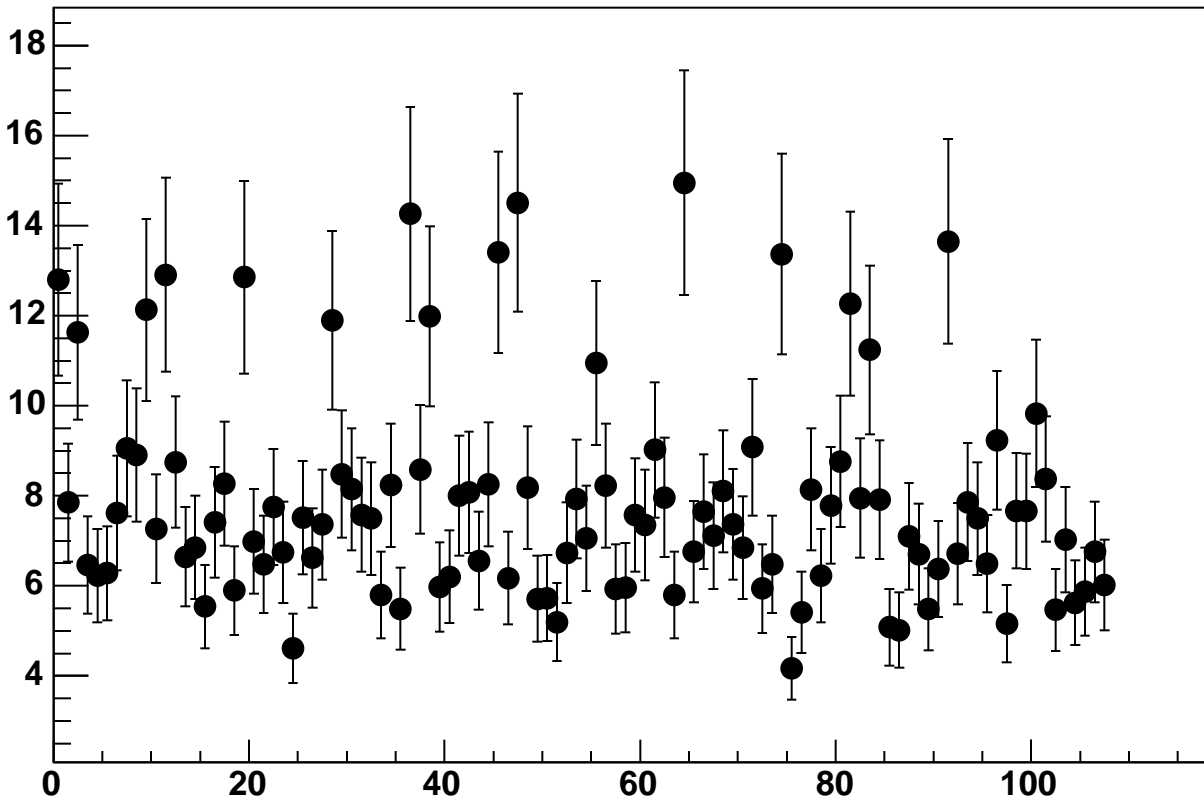
Enable 0, DAC=4000, Hold=130, ADC Noise vs 18\*Chip+Chan



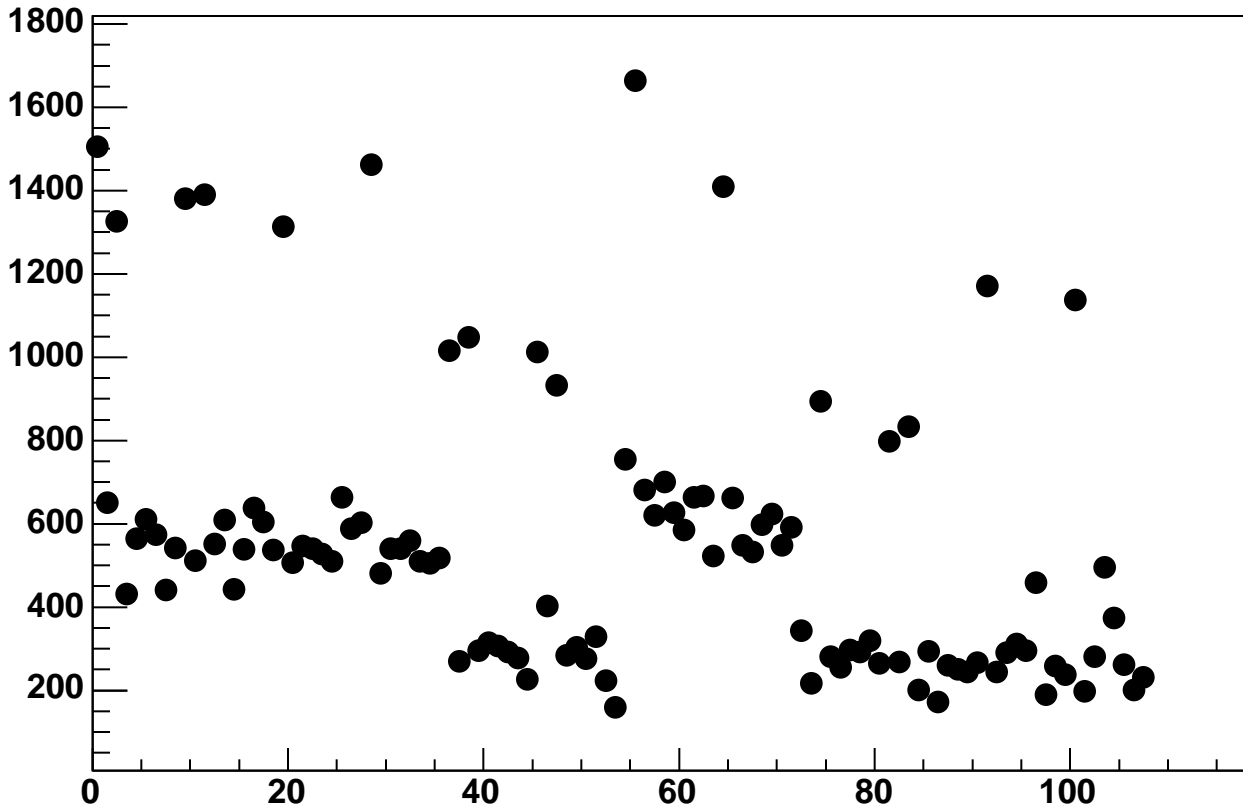
Enable 0, DAC=4000, Hold=135, ADC Mean vs 18\*Chip+Chan



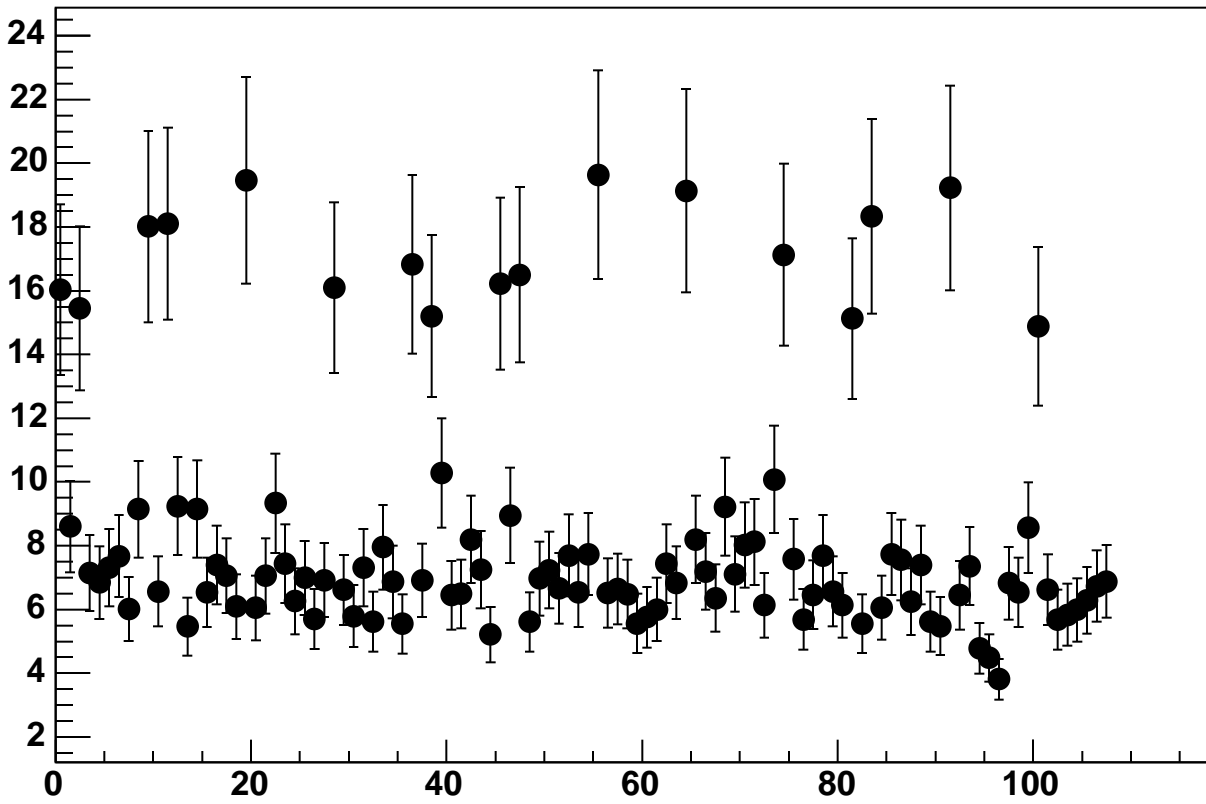
Enable 0, DAC=4000, Hold=135, ADC Noise vs 18\*Chip+Chan



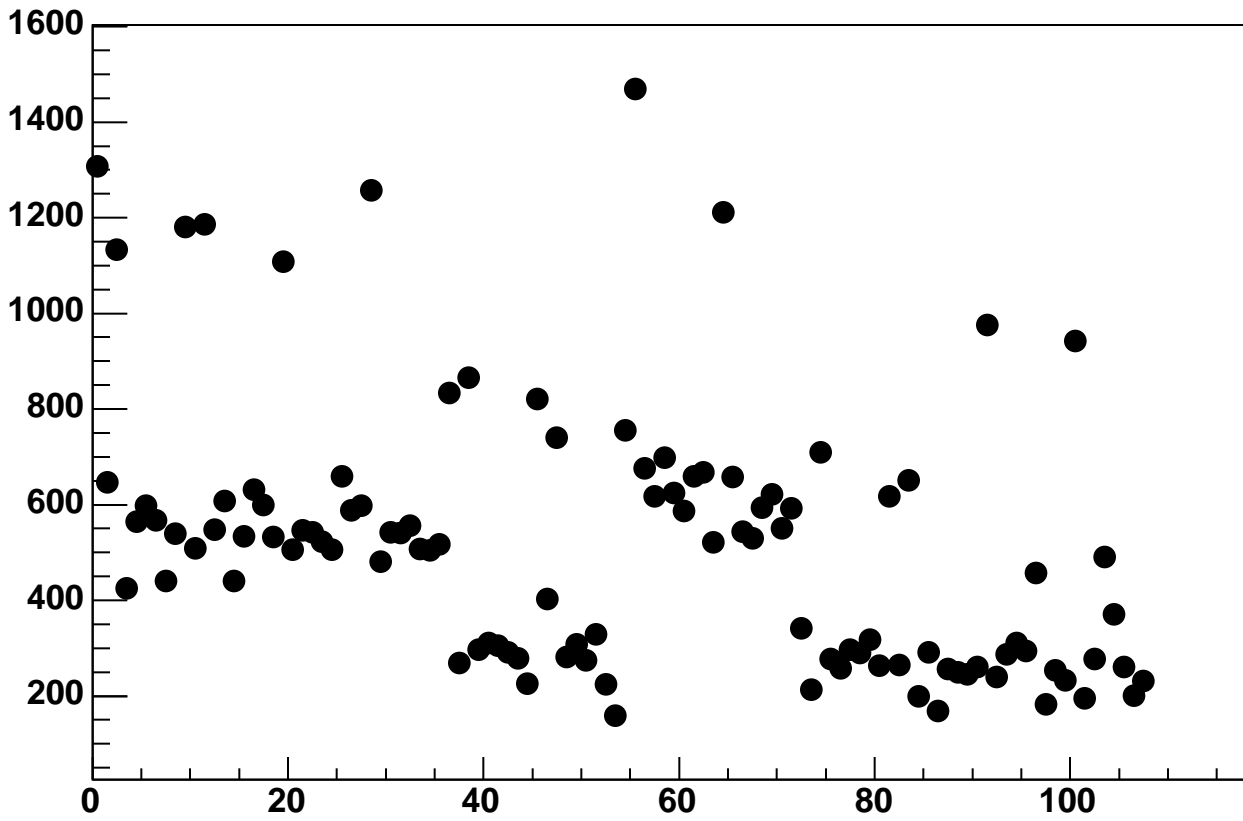
Enable 0, DAC=4000, Hold=140, ADC Mean vs 18\*Chip+Chan



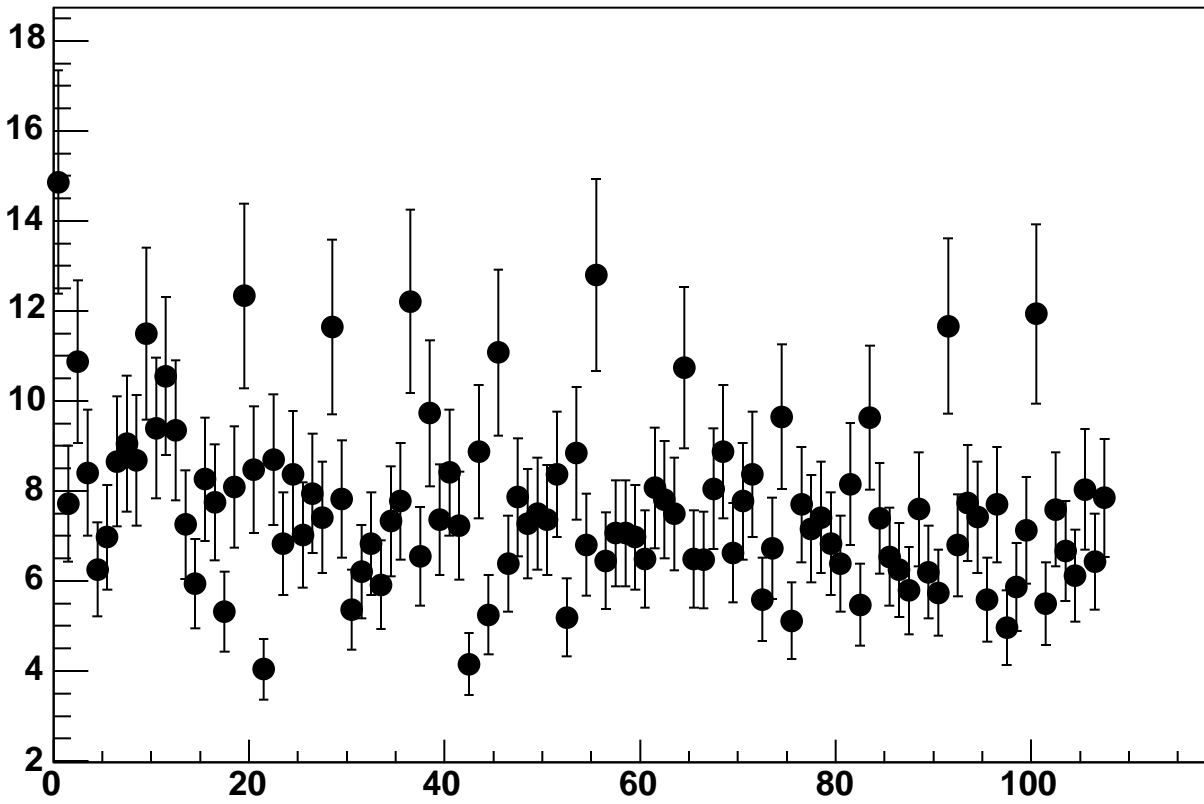
Enable 0, DAC=4000, Hold=140, ADC Noise vs 18\*Chip+Chan



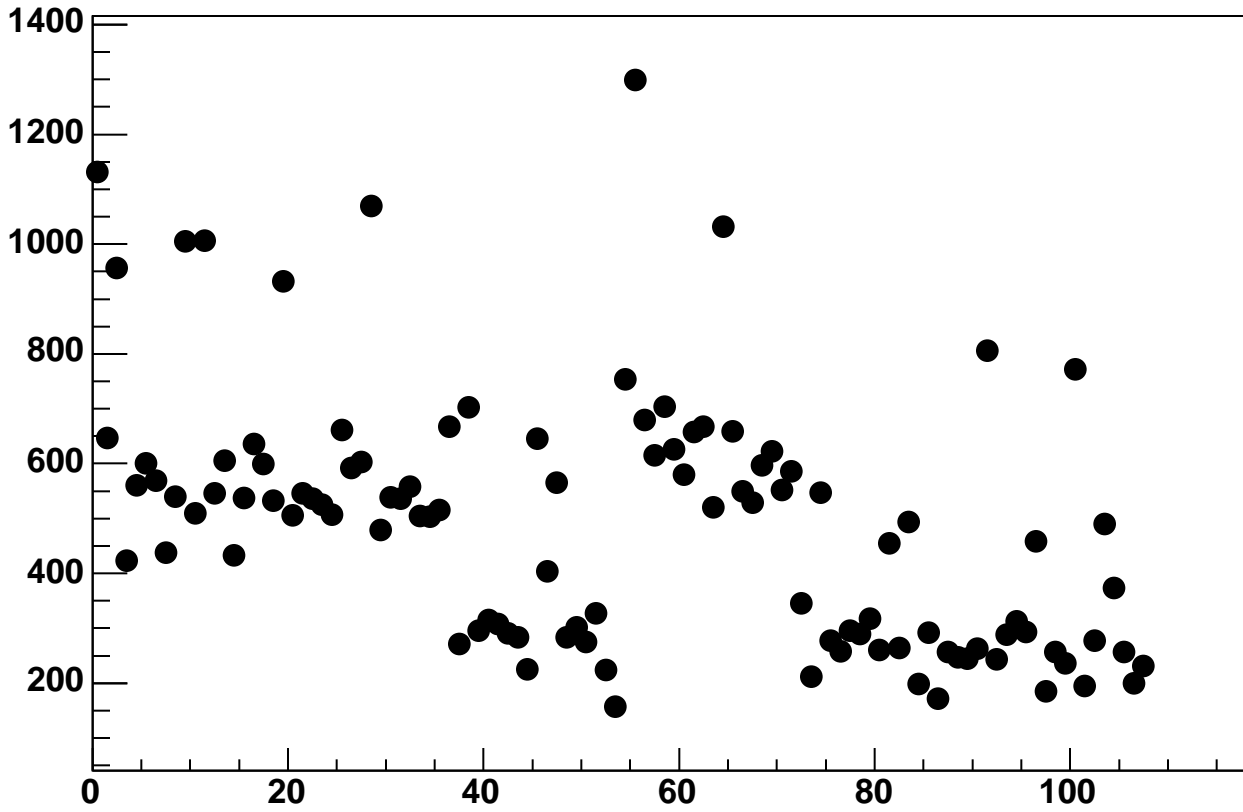
Enable 0, DAC=4000, Hold=145, ADC Mean vs 18\*Chip+Chan



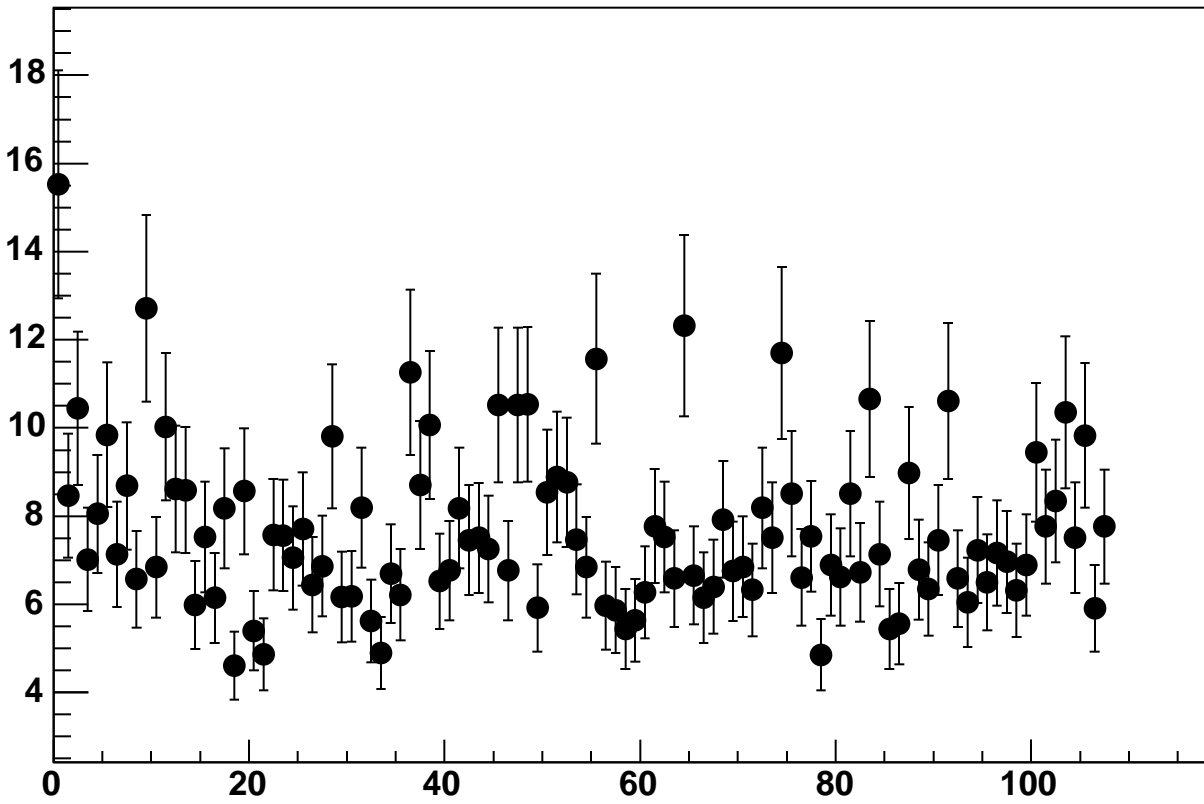
Enable 0, DAC=4000, Hold=145, ADC Noise vs 18\*Chip+Chan



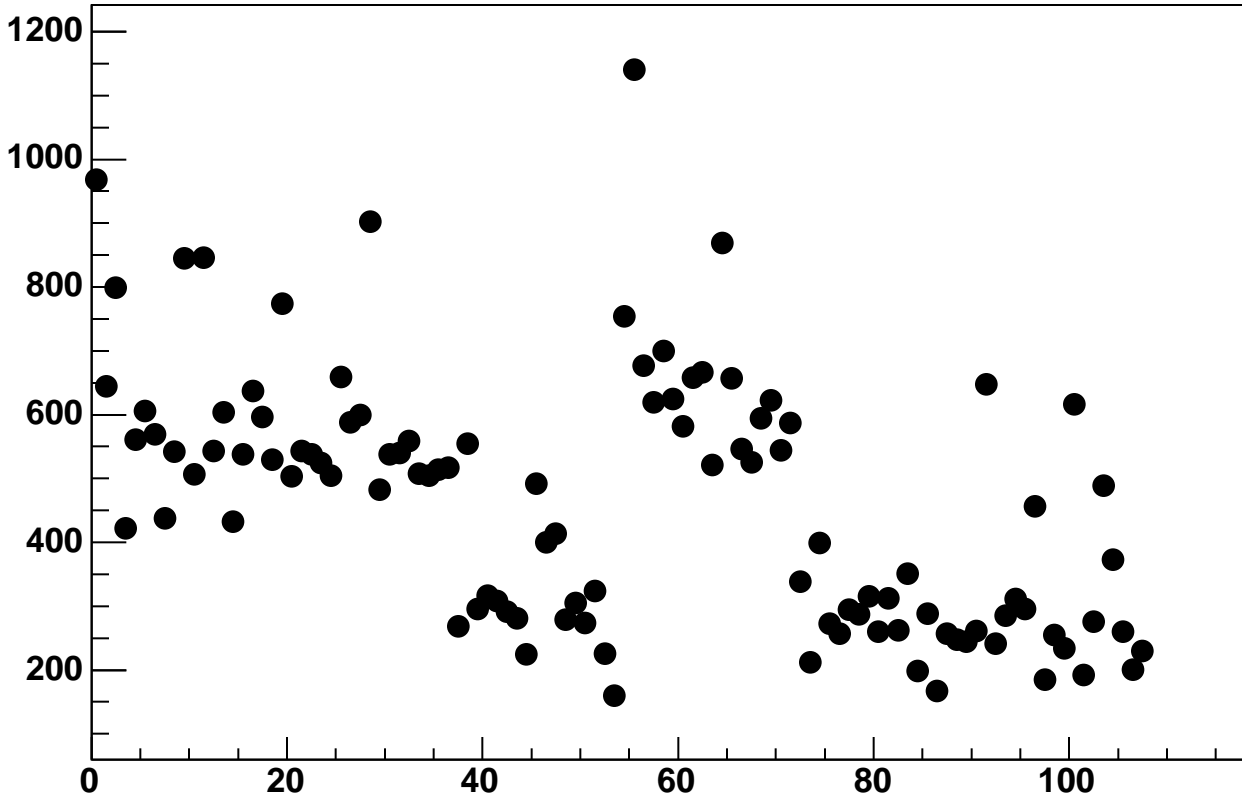
Enable 0, DAC=4000, Hold=150, ADC Mean vs 18\*Chip+Chan



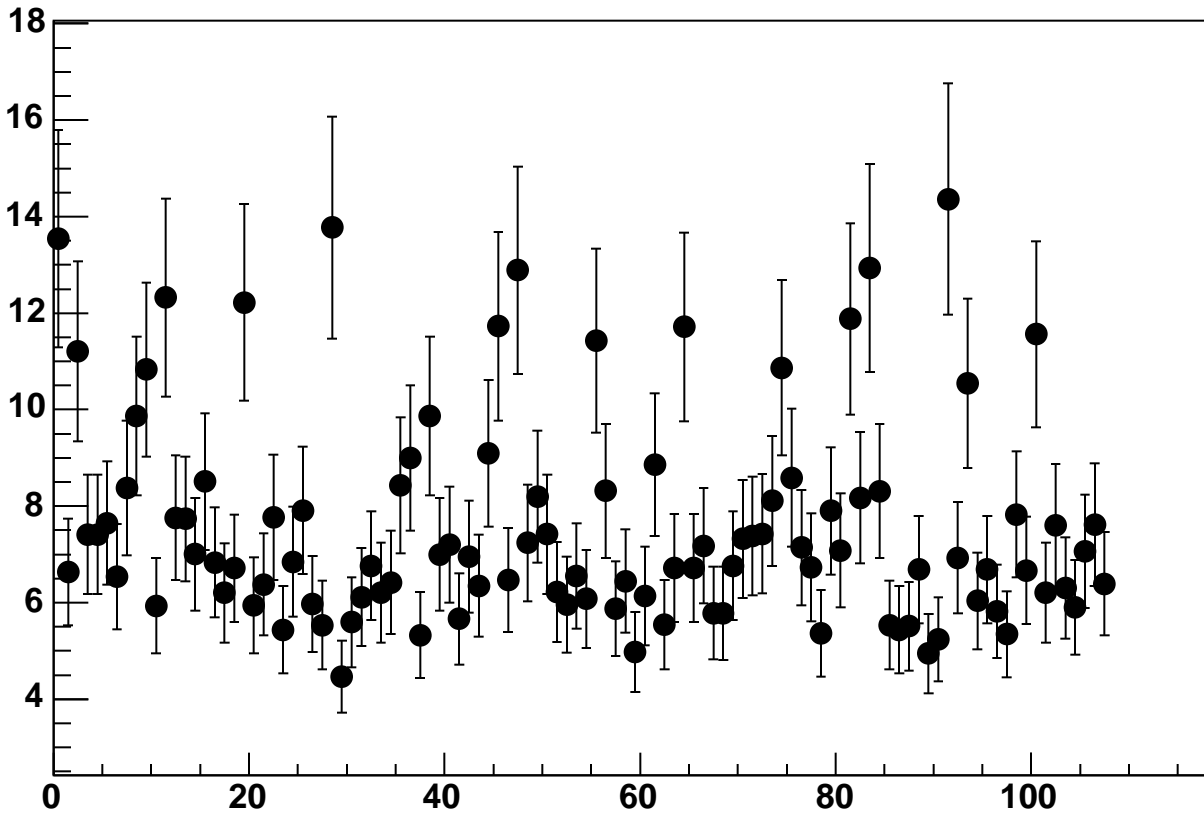
Enable 0, DAC=4000, Hold=150, ADC Noise vs 18\*Chip+Chan



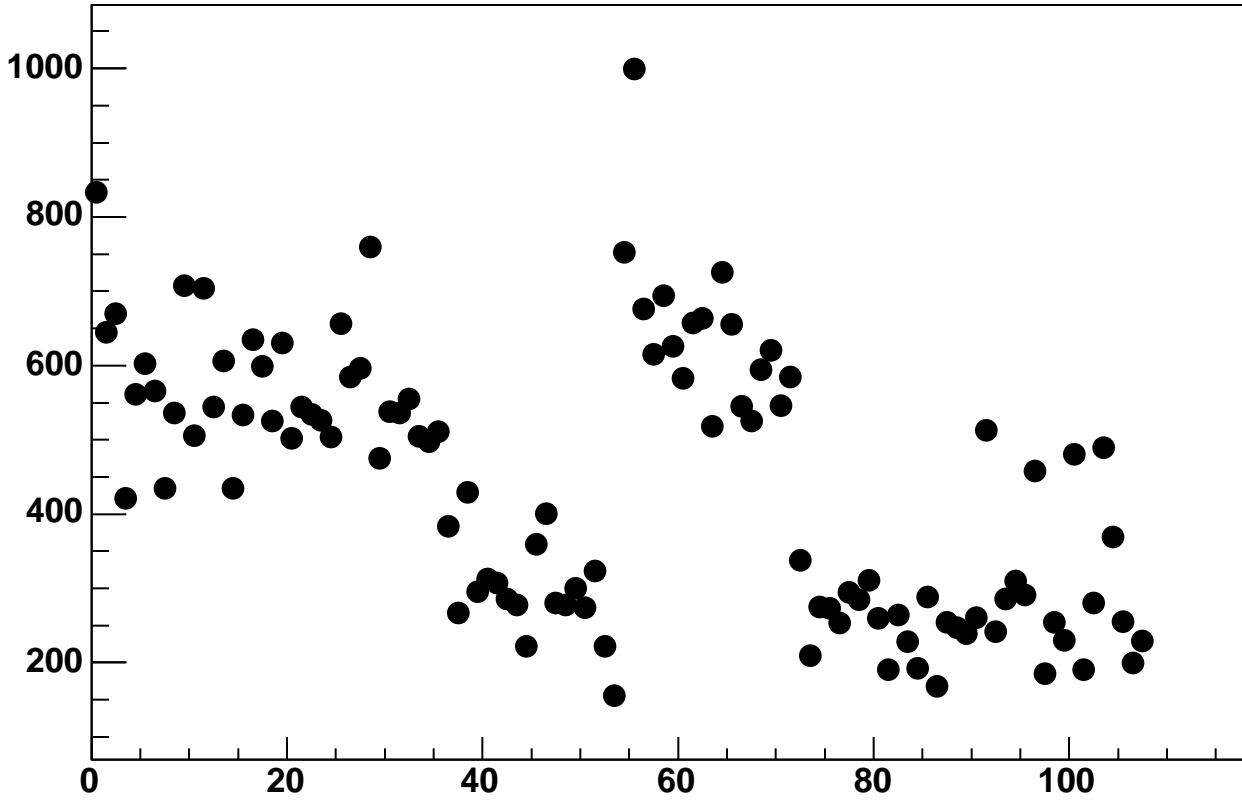
Enable 0, DAC=4000, Hold=155, ADC Mean vs 18\*Chip+Chan



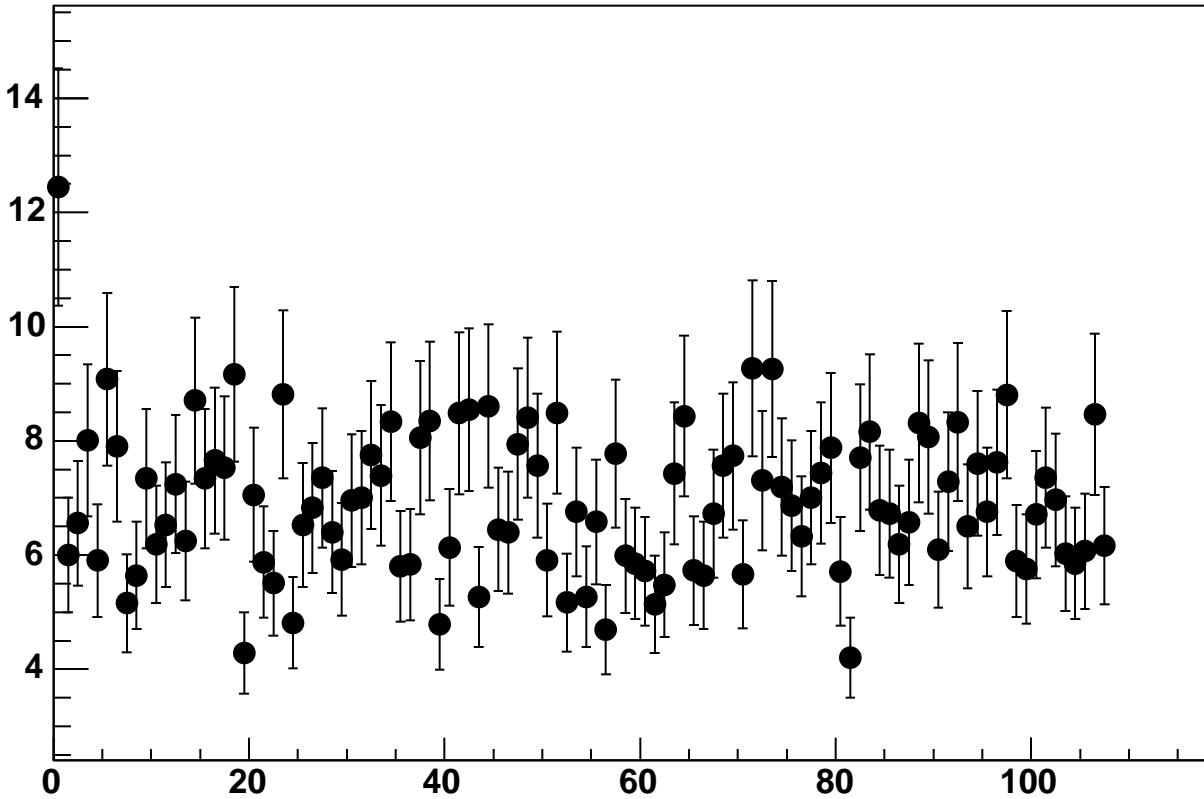
Enable 0, DAC=4000, Hold=155, ADC Noise vs 18\*Chip+Chan



Enable 0, DAC=4000, Hold=160, ADC Mean vs 18\*Chip+Chan

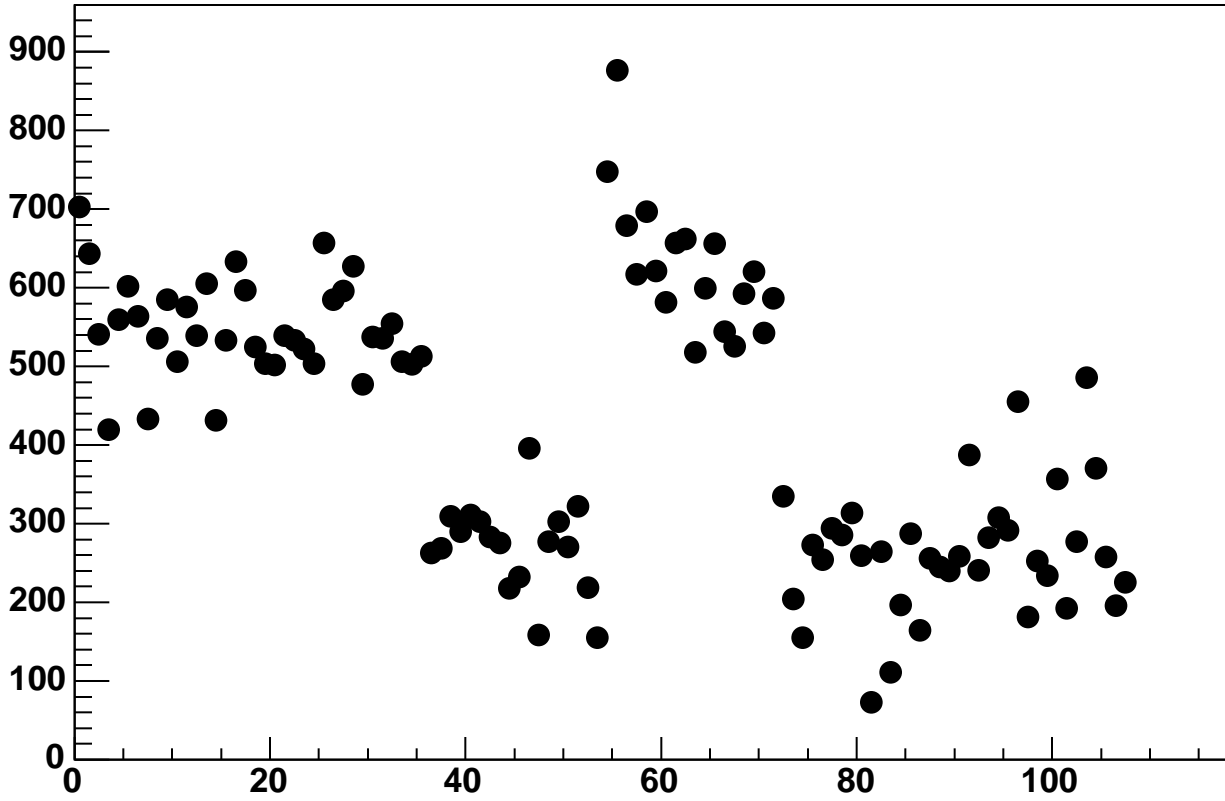


Enable 0, DAC=4000, Hold=160, ADC Noise vs 18\*Chip+Chan

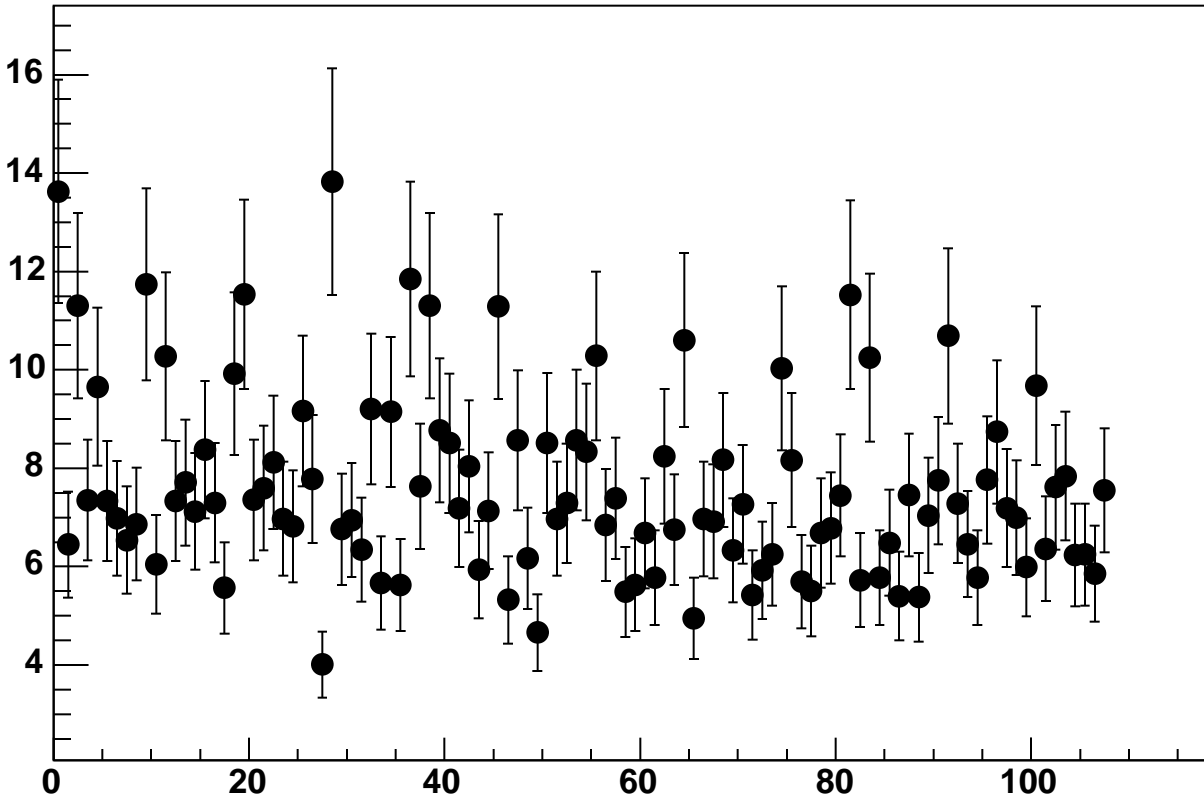




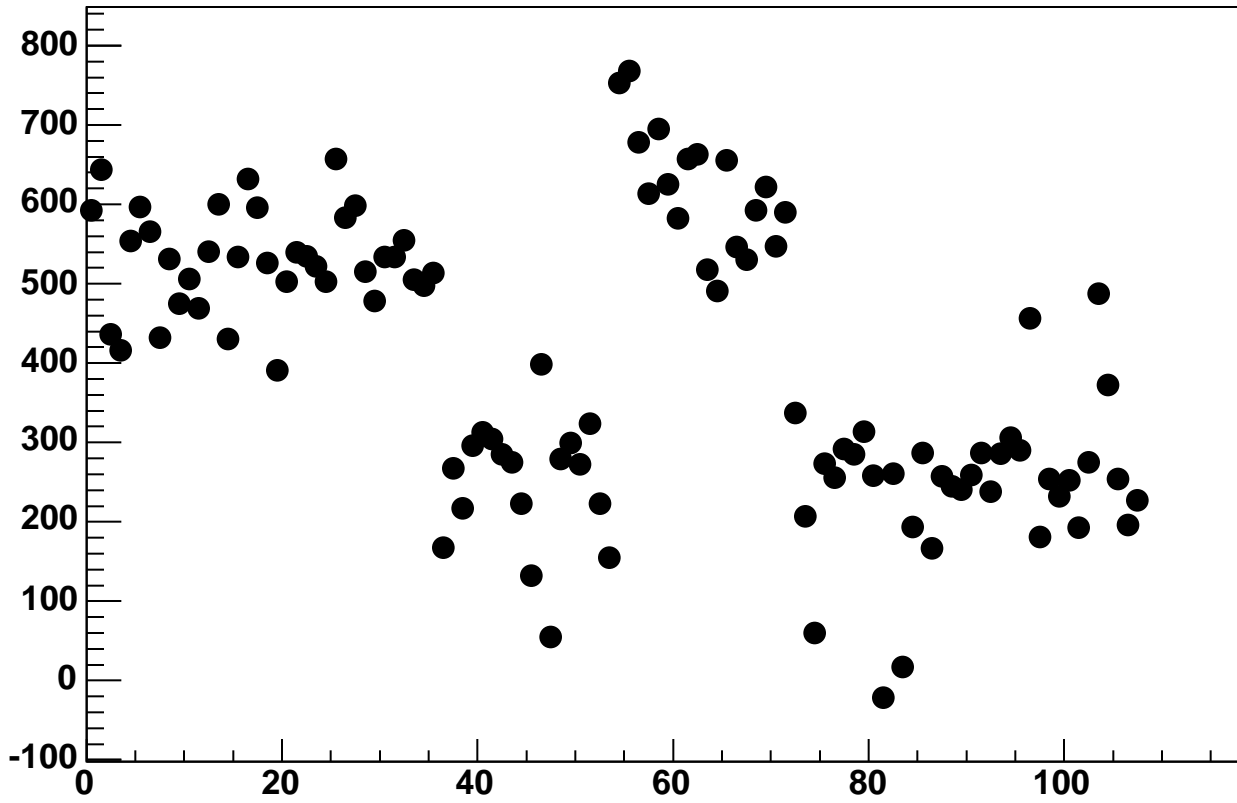
Enable 0, DAC=4000, Hold=165, ADC Mean vs 18\*Chip+Chan



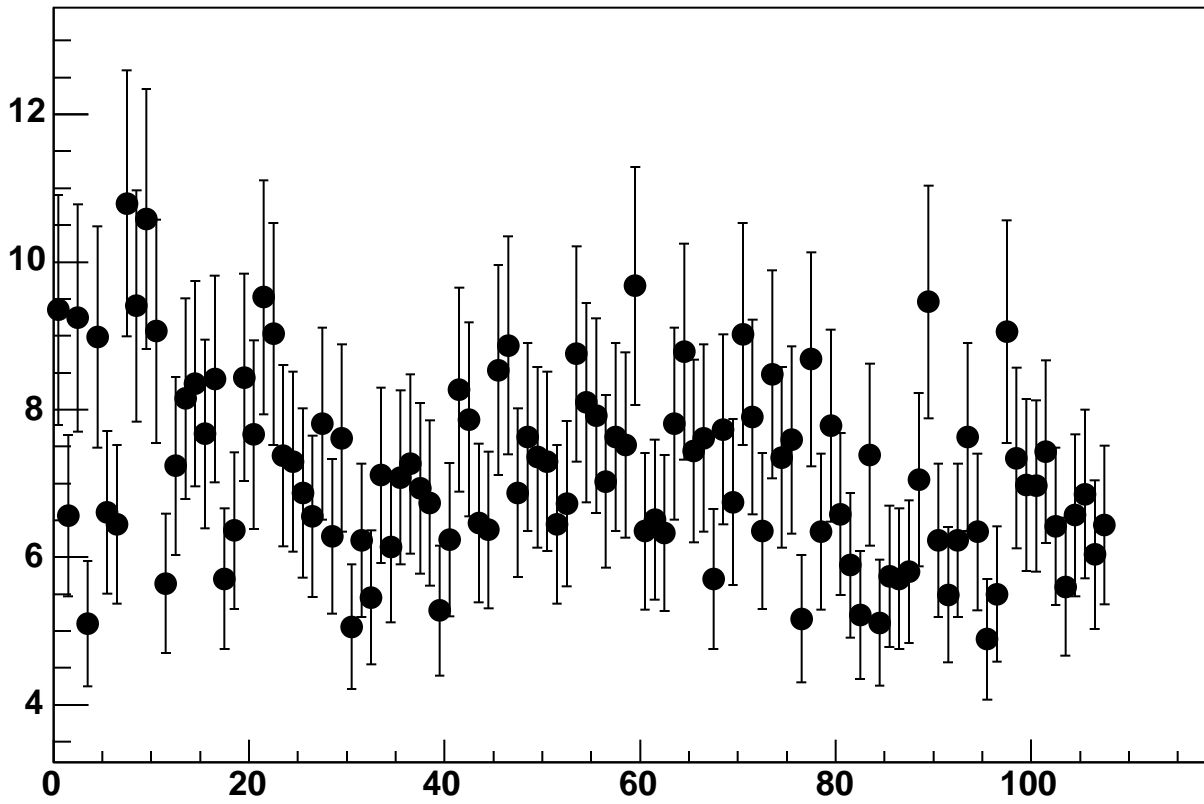
Enable 0, DAC=4000, Hold=165, ADC Noise vs 18\*Chip+Chan



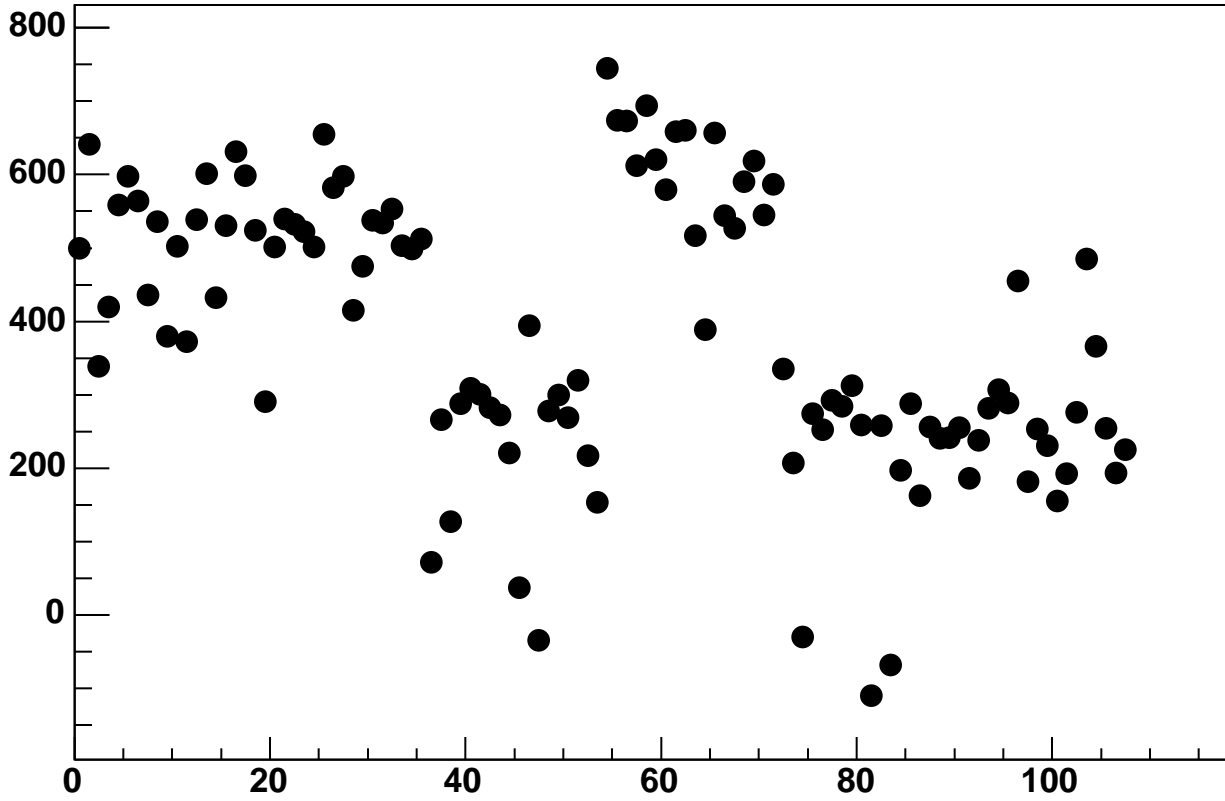
Enable 0, DAC=4000, Hold=170, ADC Mean vs 18\*Chip+Chan



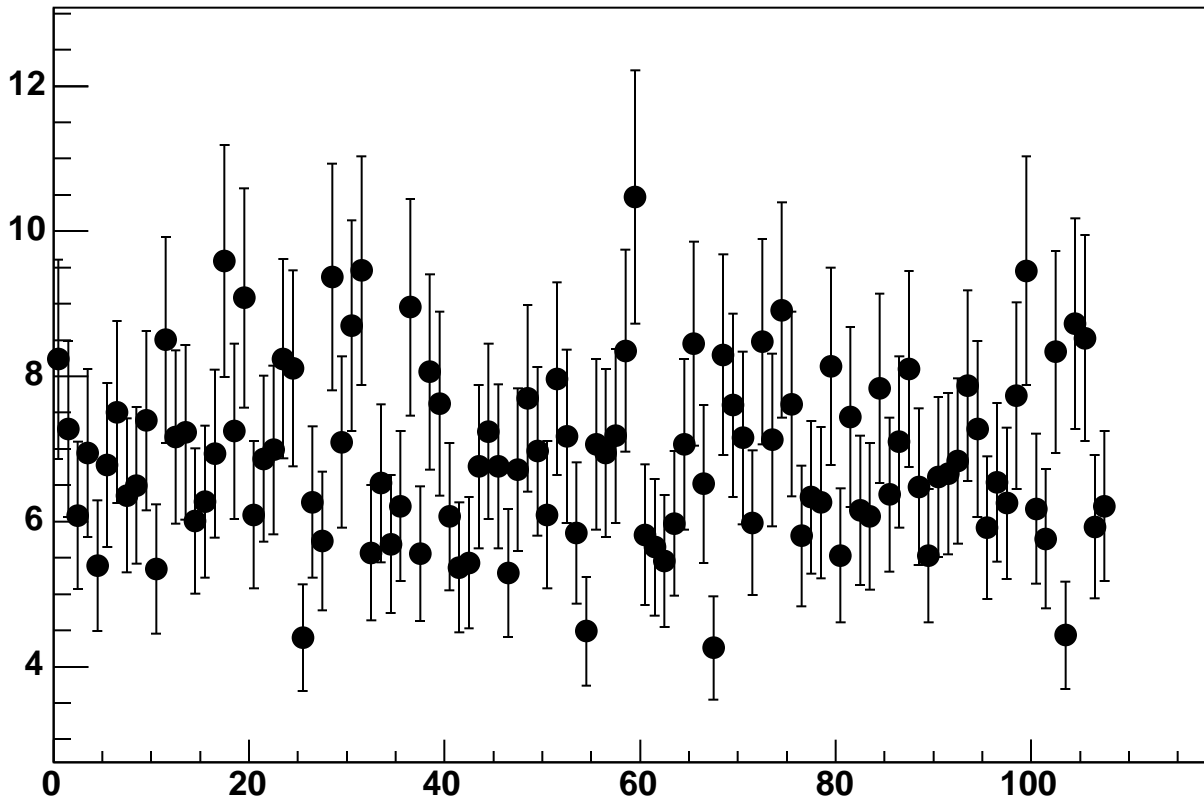
Enable 0, DAC=4000, Hold=170, ADC Noise vs 18\*Chip+Chan



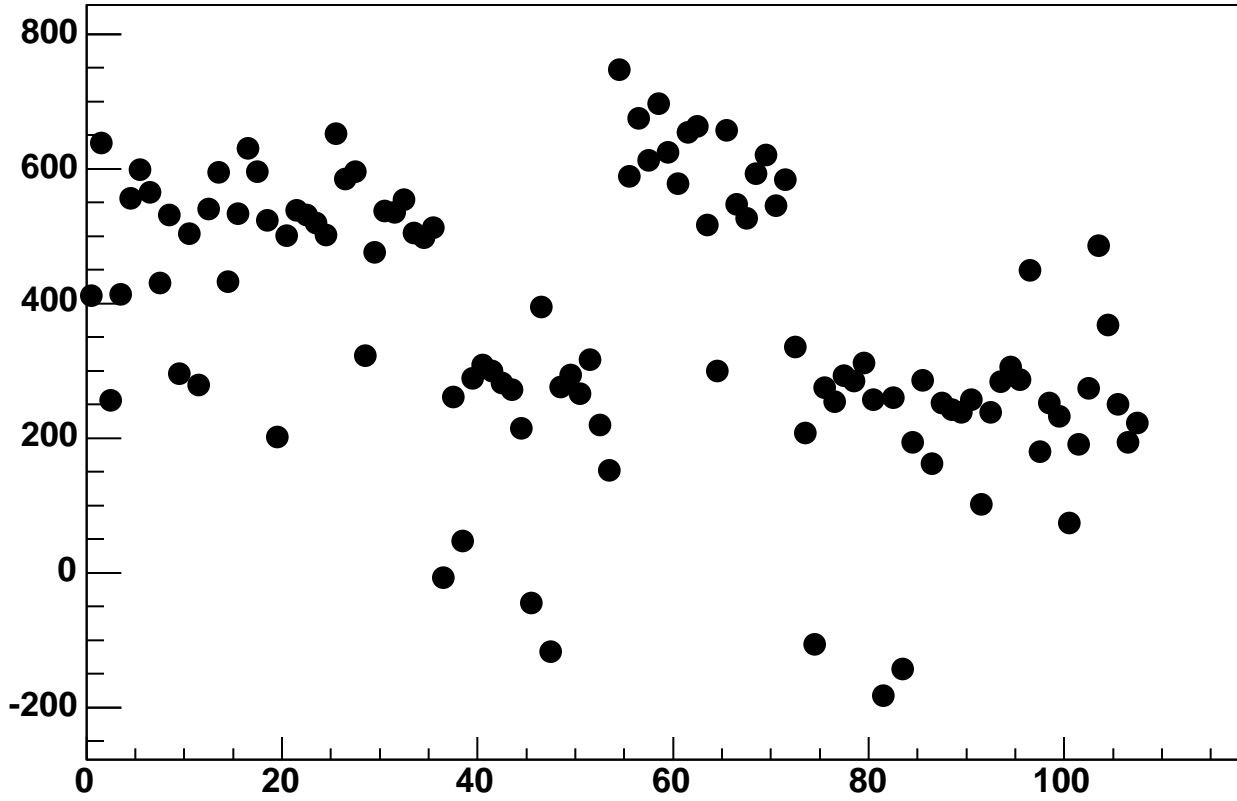
Enable 0, DAC=4000, Hold=175, ADC Mean vs 18\*Chip+Chan



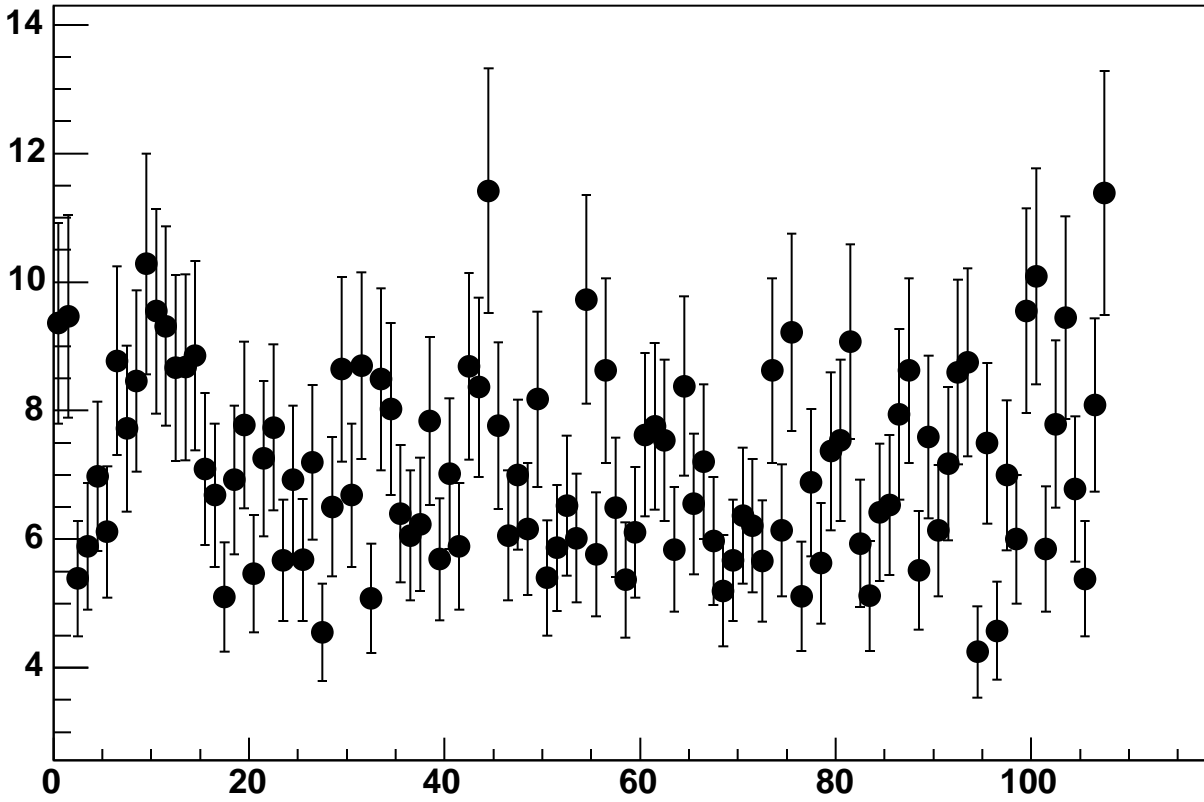
Enable 0, DAC=4000, Hold=175, ADC Noise vs 18\*Chip+Chan



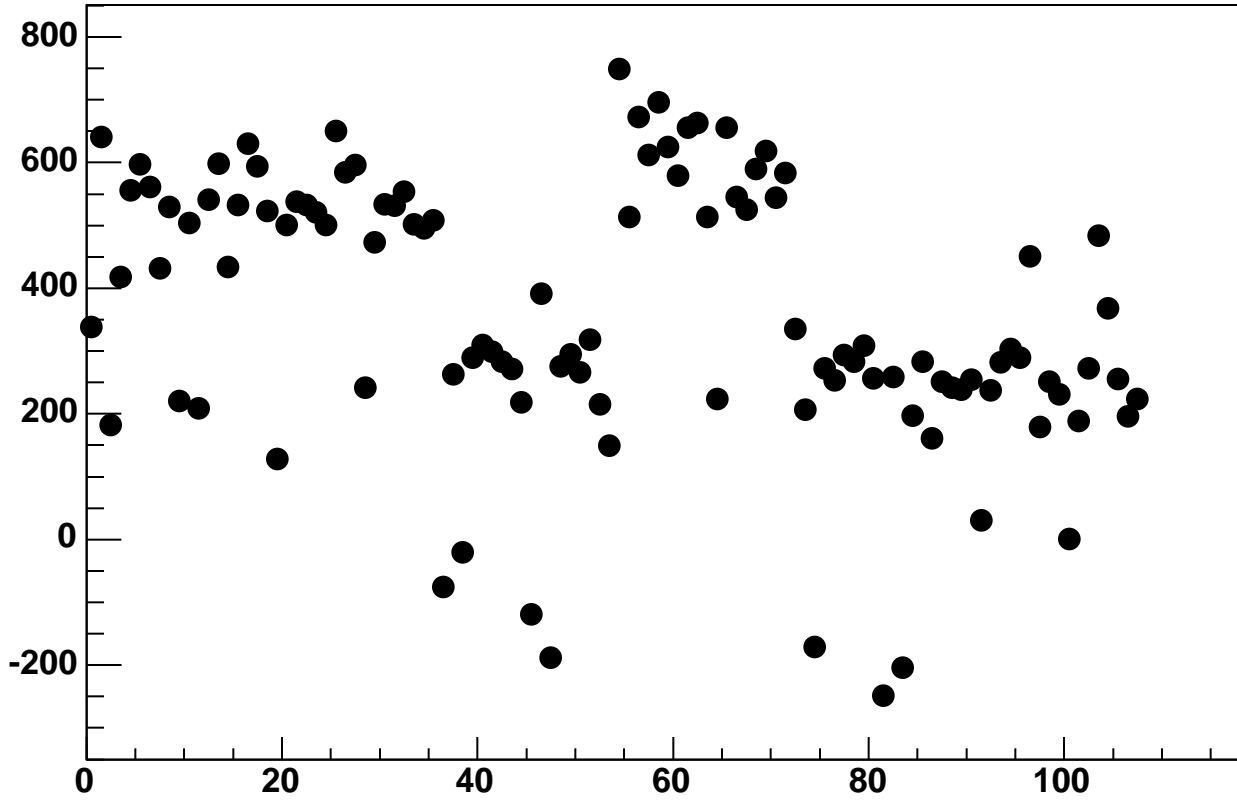
Enable 0, DAC=4000, Hold=180, ADC Mean vs 18\*Chip+Chan



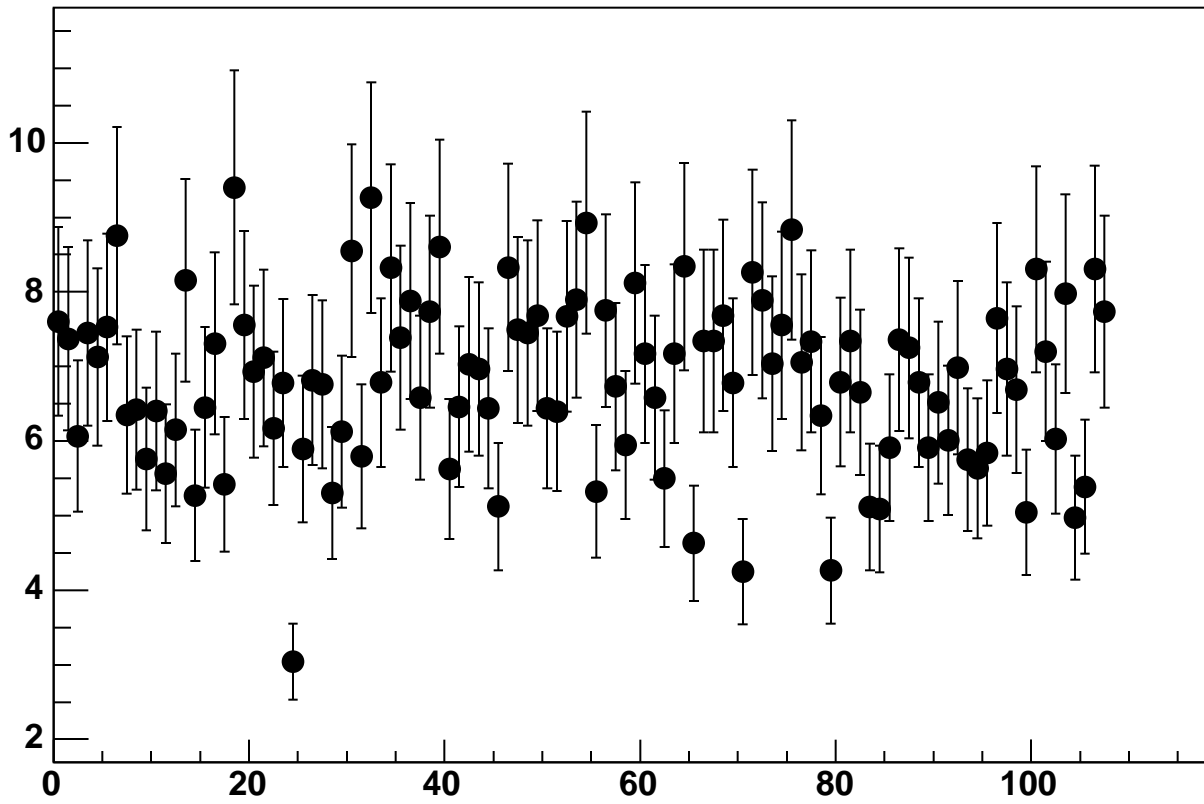
Enable 0, DAC=4000, Hold=180, ADC Noise vs 18\*Chip+Chan



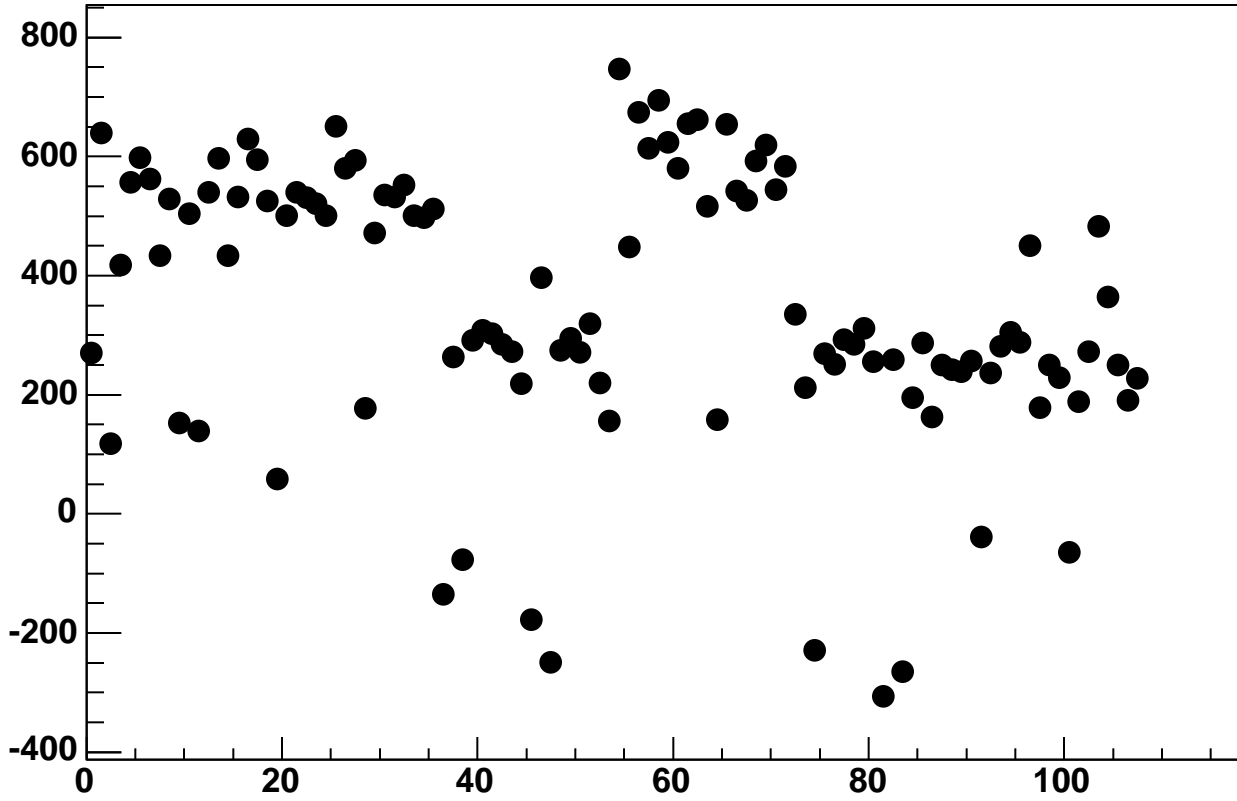
Enable 0, DAC=4000, Hold=185, ADC Mean vs 18\*Chip+Chan



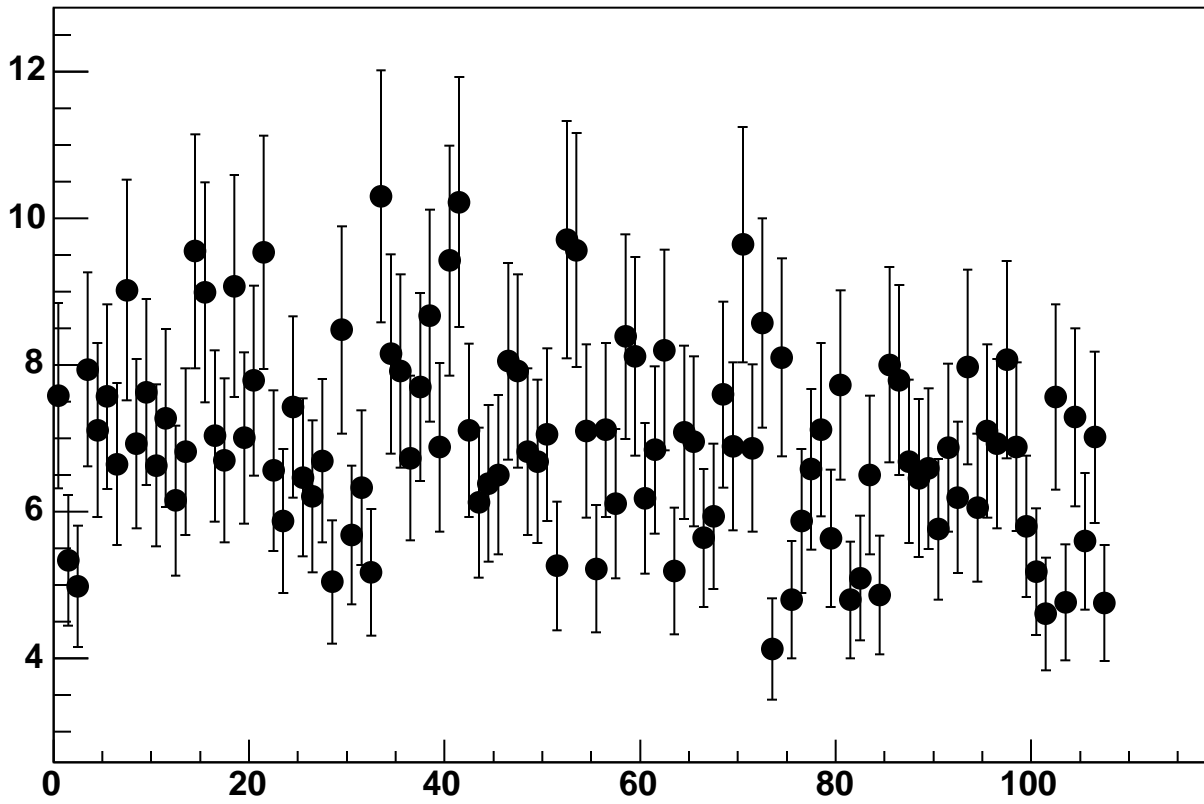
Enable 0, DAC=4000, Hold=185, ADC Noise vs 18\*Chip+Chan



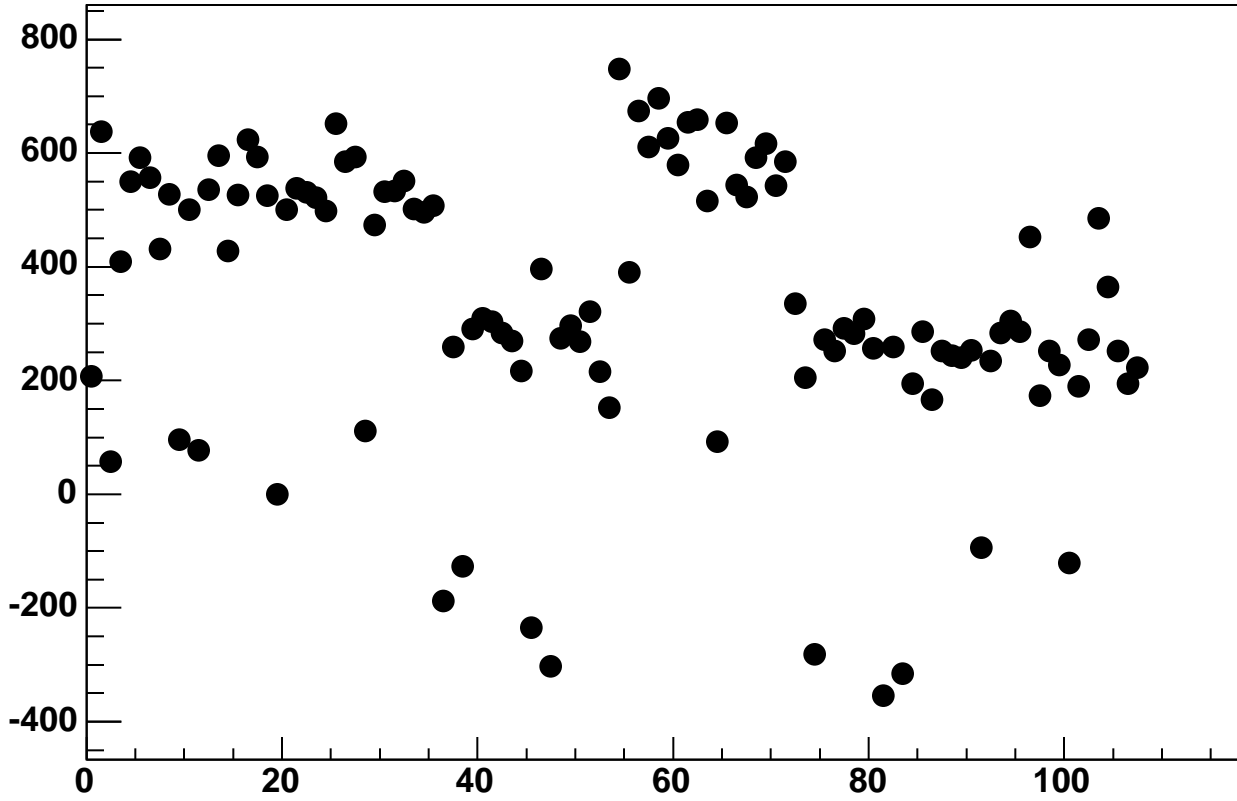
Enable 0, DAC=4000, Hold=190, ADC Mean vs 18\*Chip+Chan



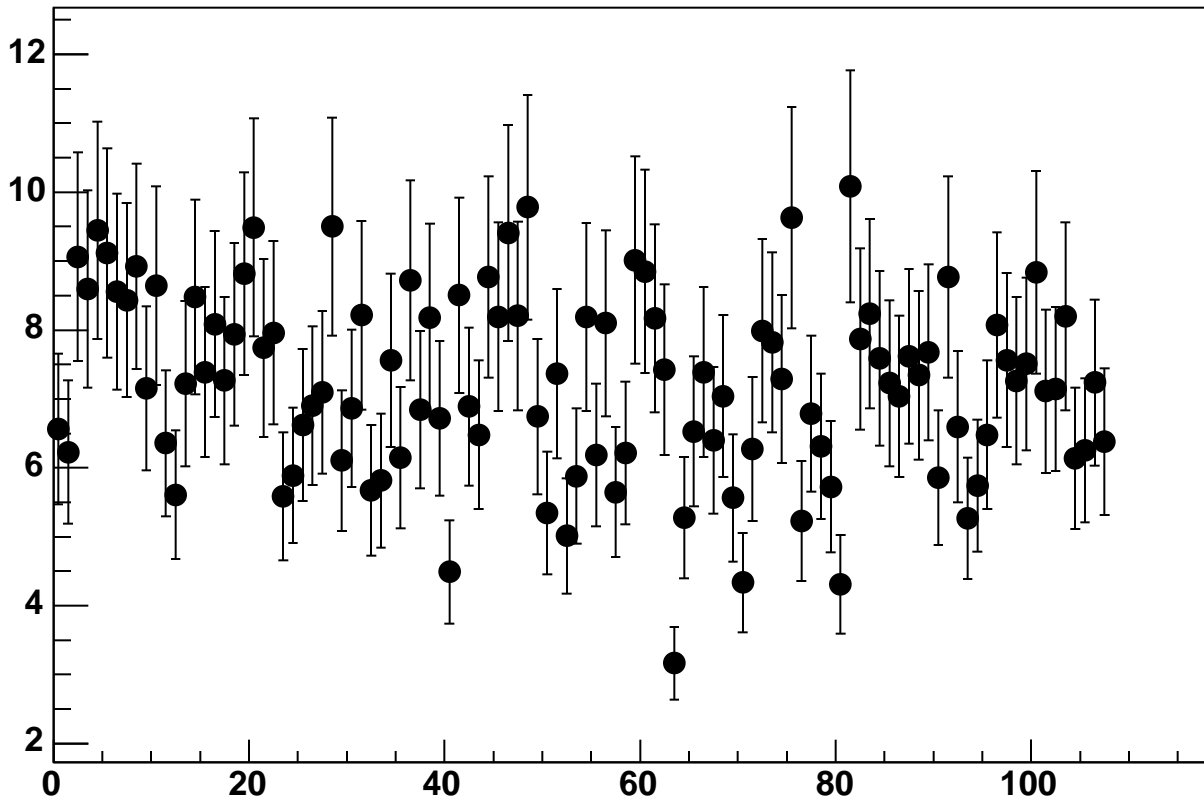
Enable 0, DAC=4000, Hold=190, ADC Noise vs 18\*Chip+Chan



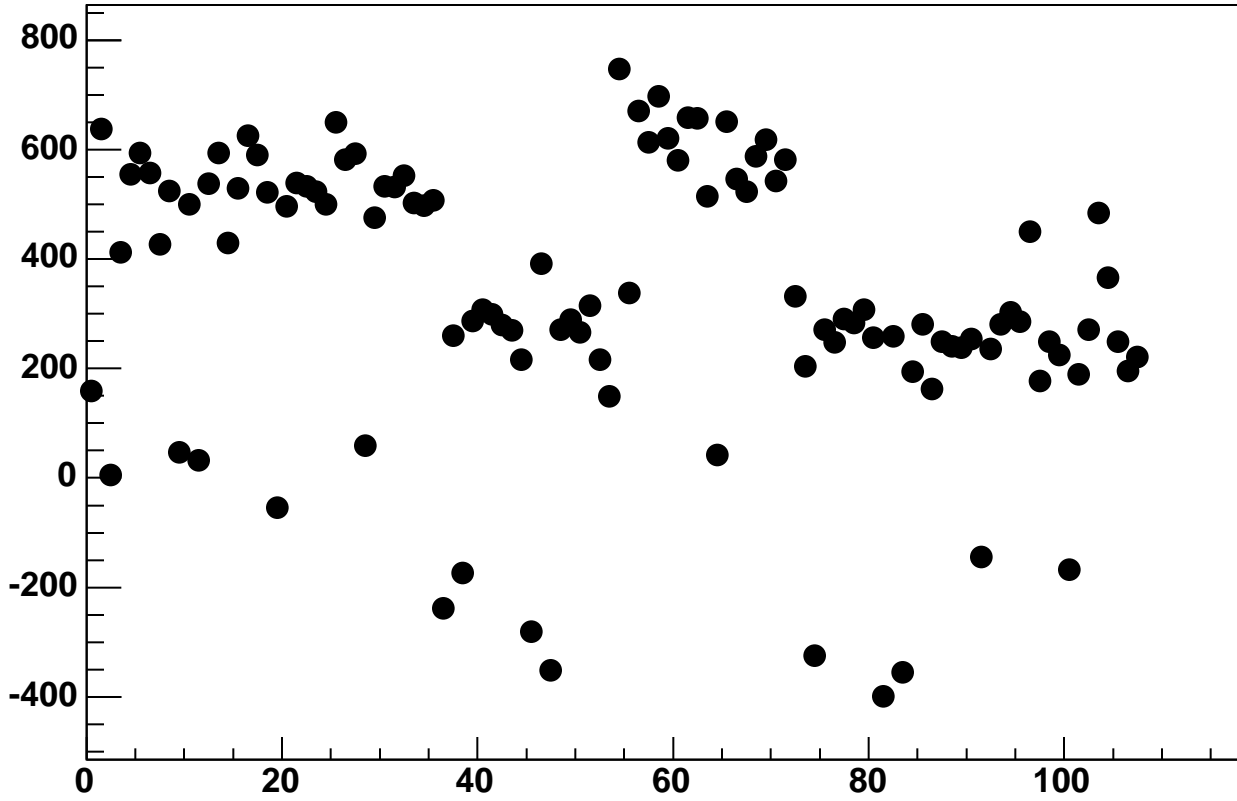
Enable 0, DAC=4000, Hold=195, ADC Mean vs 18\*Chip+Chan



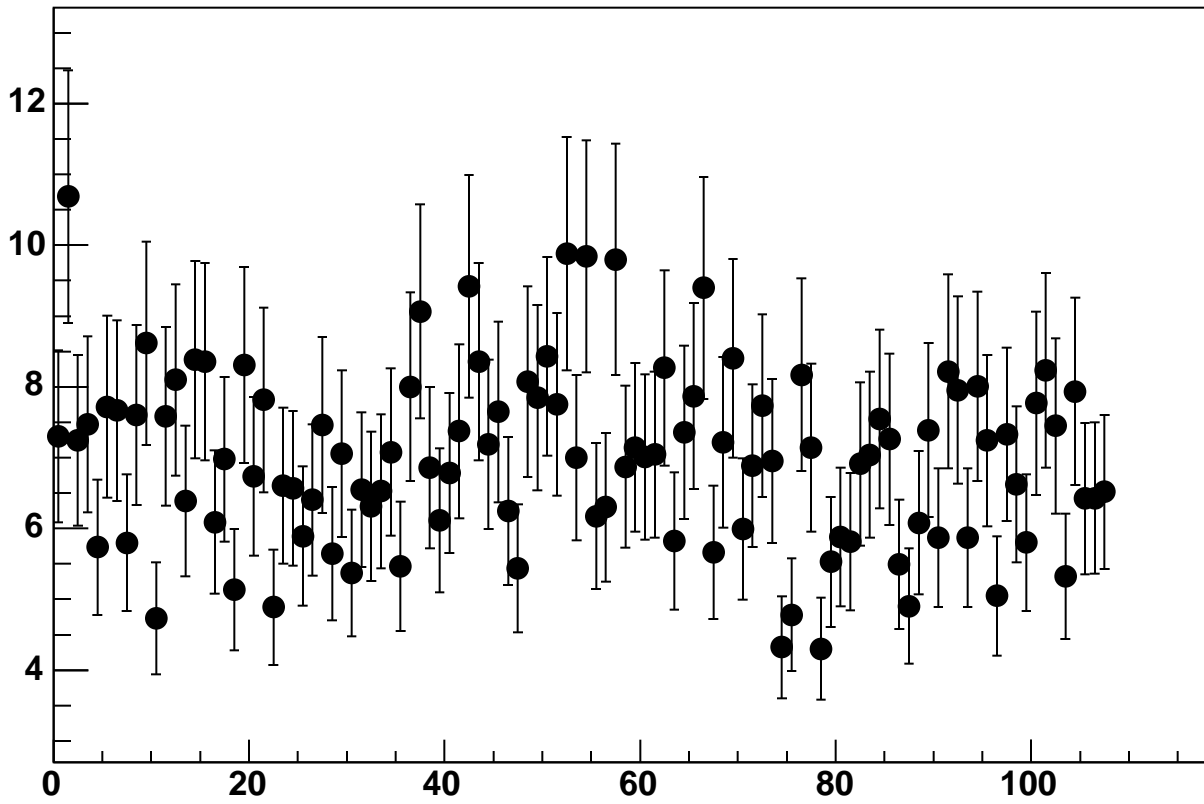
Enable 0, DAC=4000, Hold=195, ADC Noise vs 18\*Chip+Chan



Enable 0, DAC=4000, Hold=200, ADC Mean vs 18\*Chip+Chan

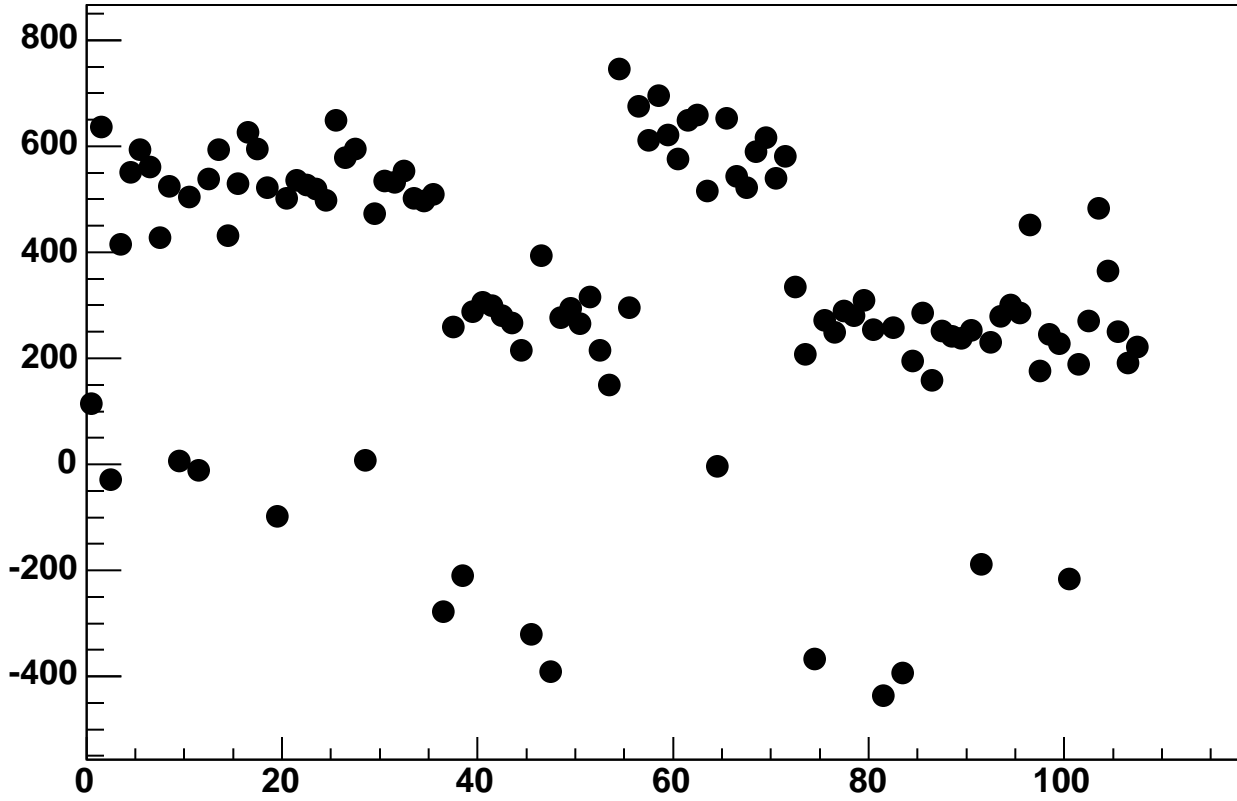


Enable 0, DAC=4000, Hold=200, ADC Noise vs 18\*Chip+Chan

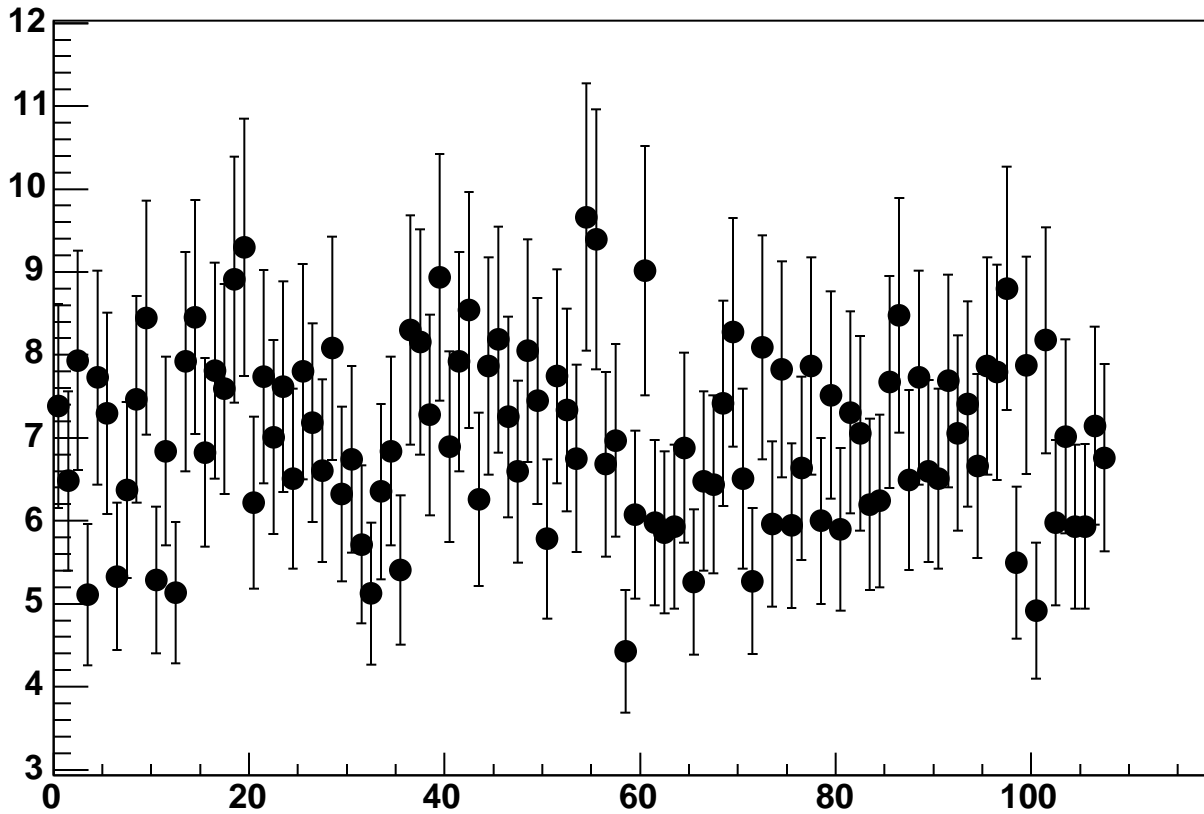




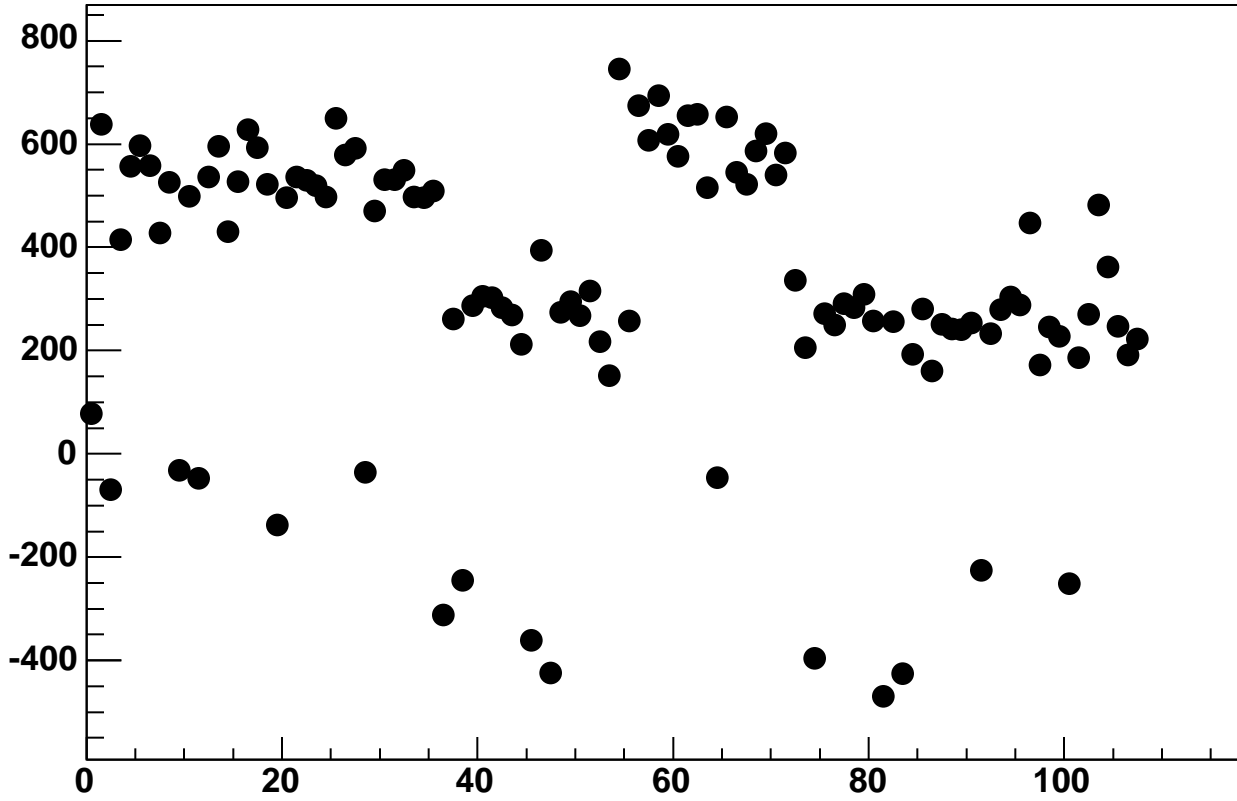
Enable 0, DAC=4000, Hold=205, ADC Mean vs 18\*Chip+Chan



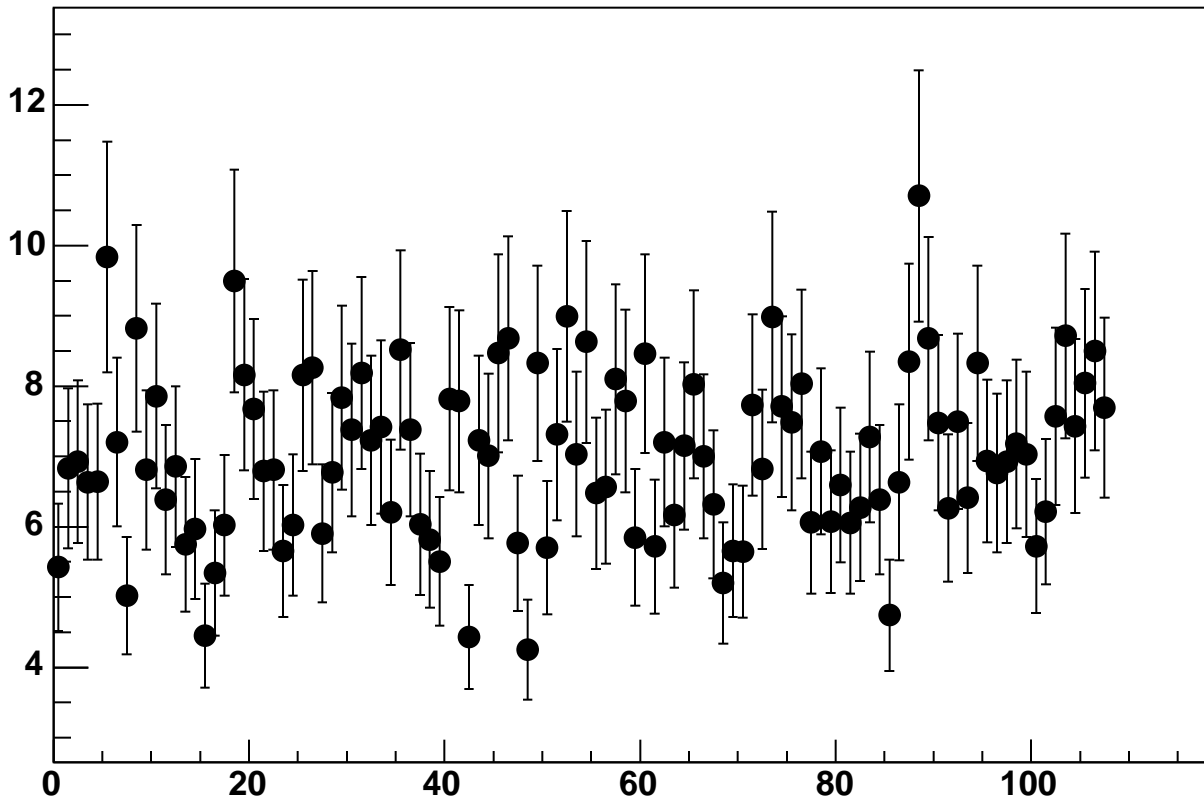
Enable 0, DAC=4000, Hold=205, ADC Noise vs 18\*Chip+Chan



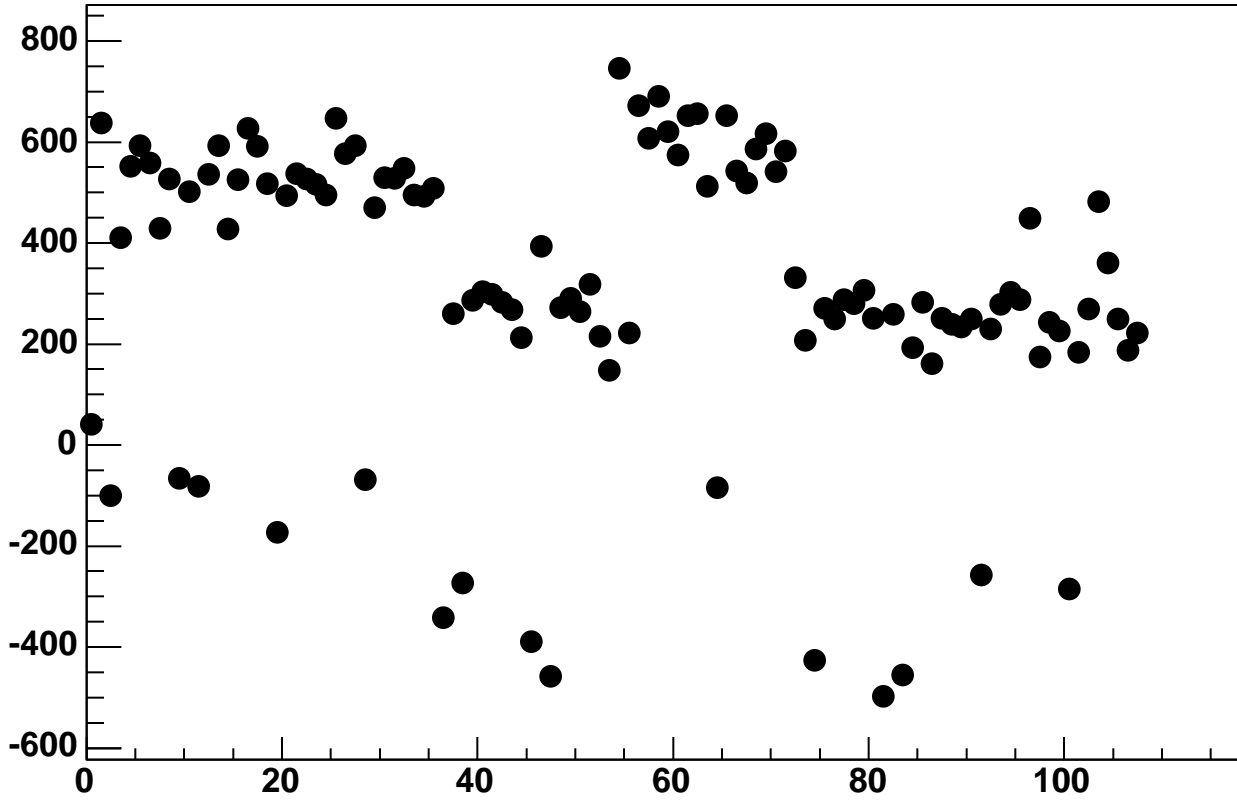
Enable 0, DAC=4000, Hold=210, ADC Mean vs 18\*Chip+Chan



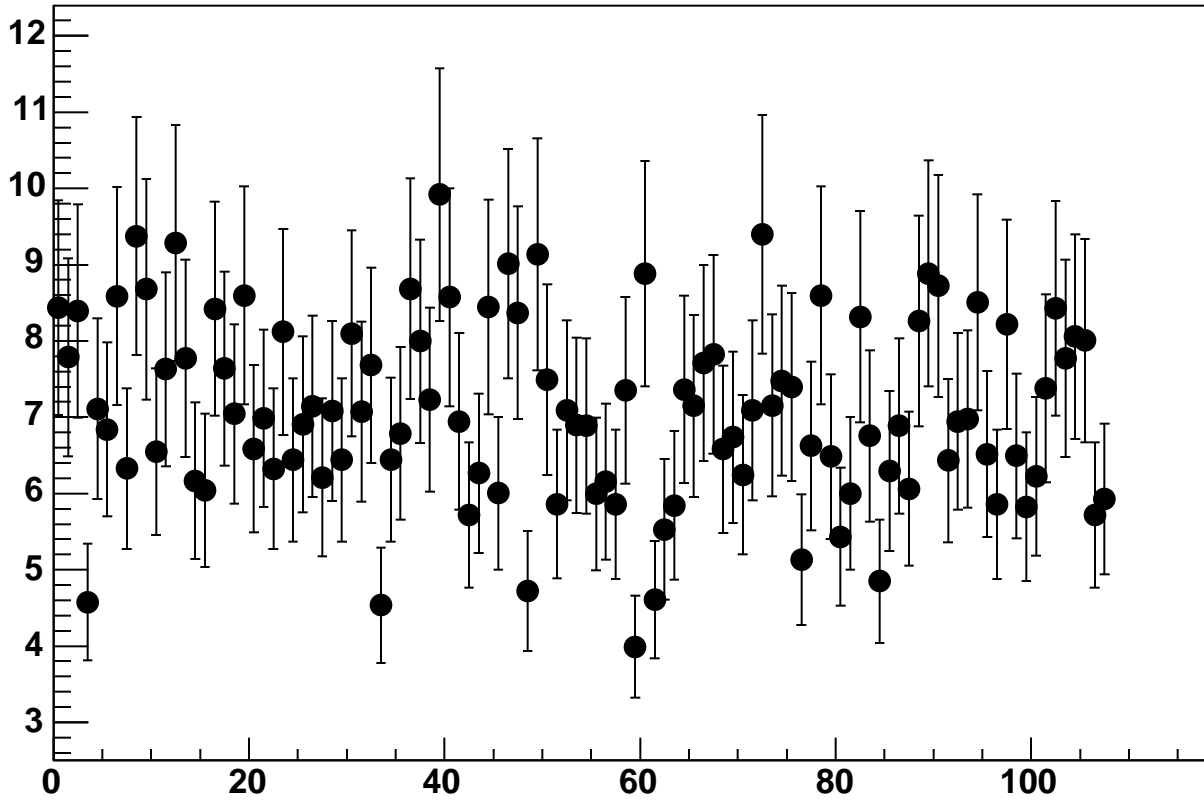
Enable 0, DAC=4000, Hold=210, ADC Noise vs 18\*Chip+Chan



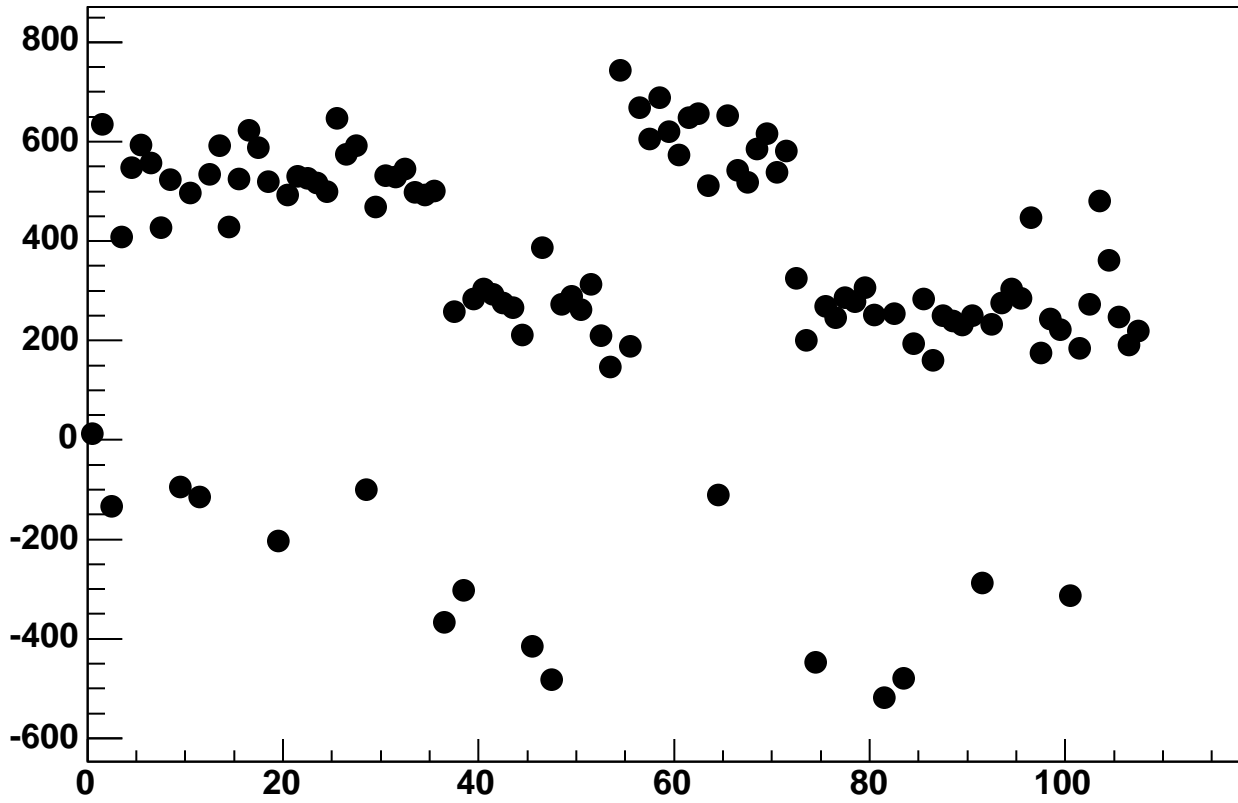
Enable 0, DAC=4000, Hold=215, ADC Mean vs 18\*Chip+Chan



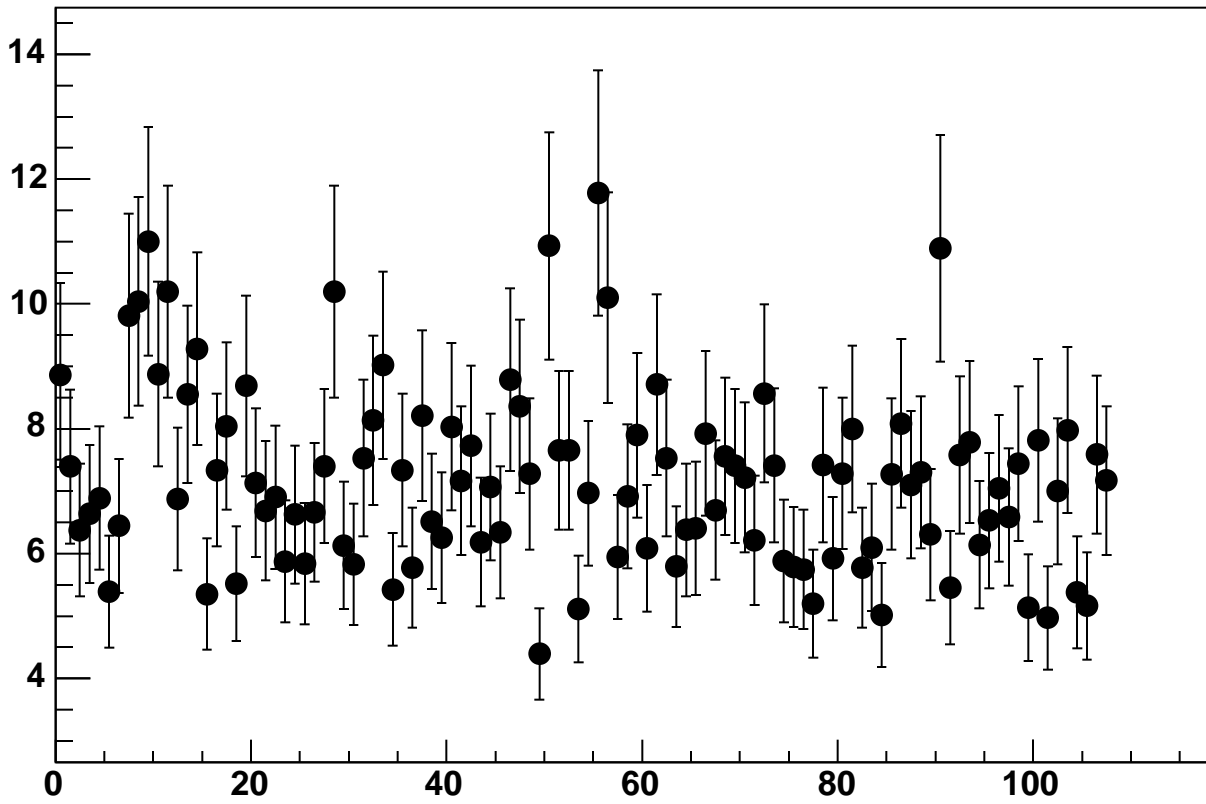
Enable 0, DAC=4000, Hold=215, ADC Noise vs 18\*Chip+Chan



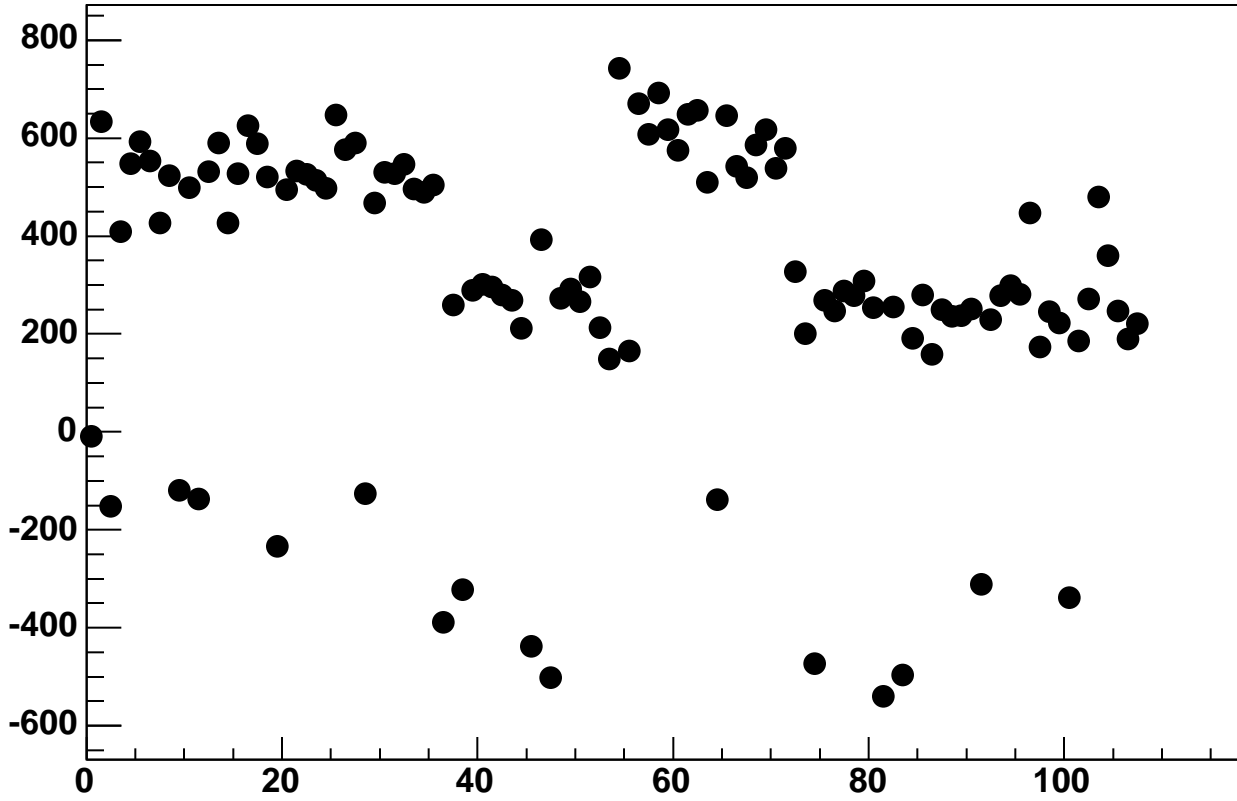
Enable 0, DAC=4000, Hold=220, ADC Mean vs 18\*Chip+Chan



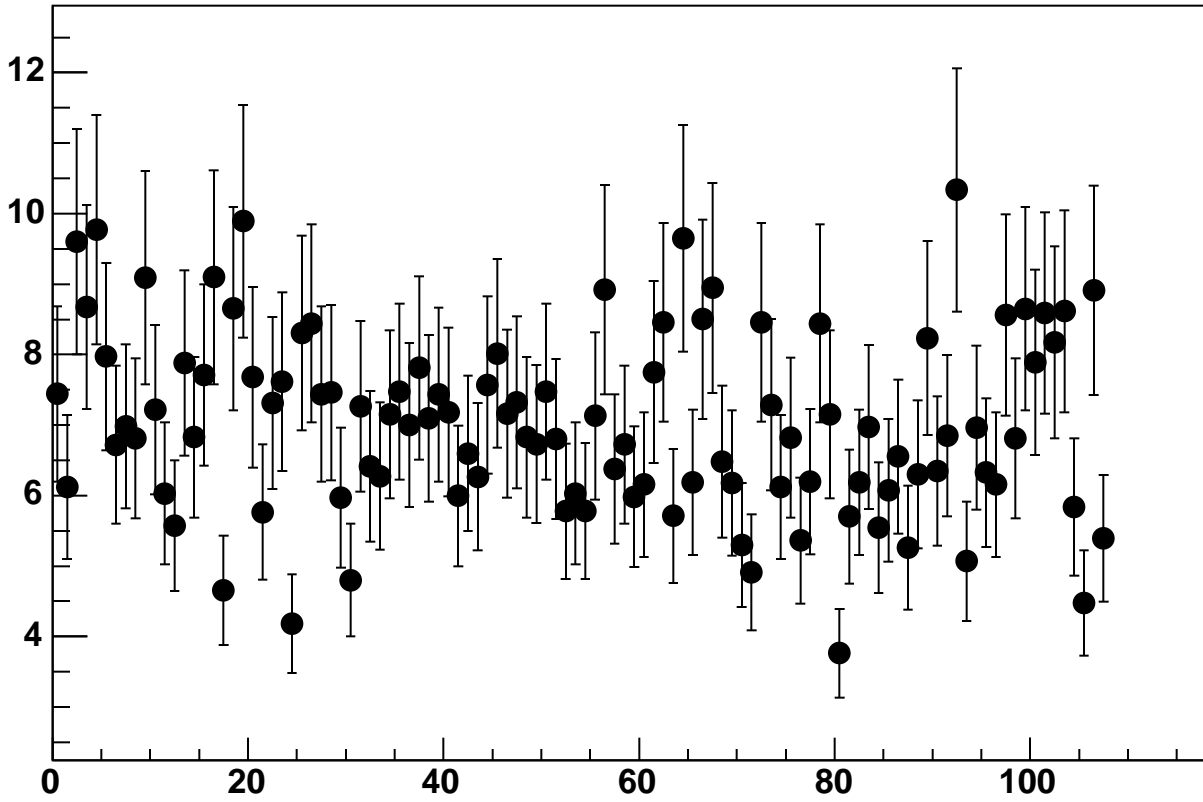
Enable 0, DAC=4000, Hold=220, ADC Noise vs 18\*Chip+Chan



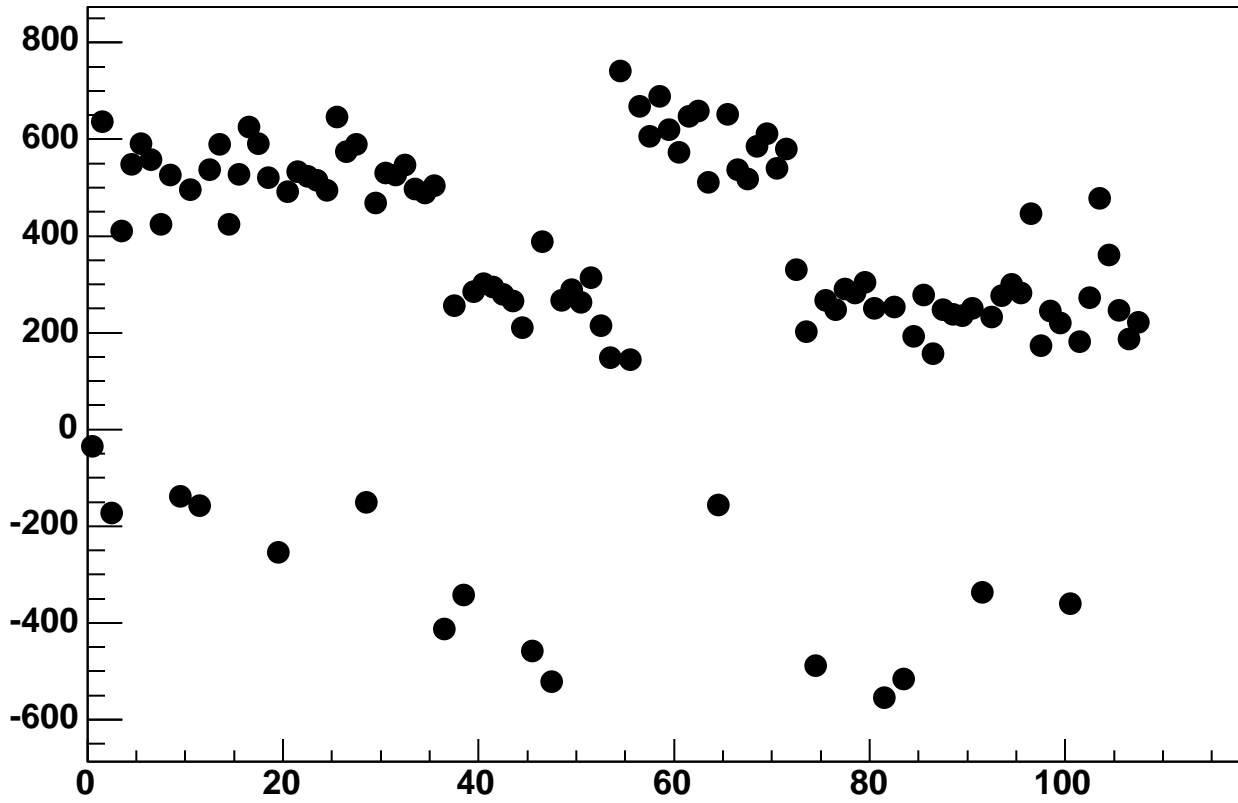
Enable 0, DAC=4000, Hold=225, ADC Mean vs 18\*Chip+Chan



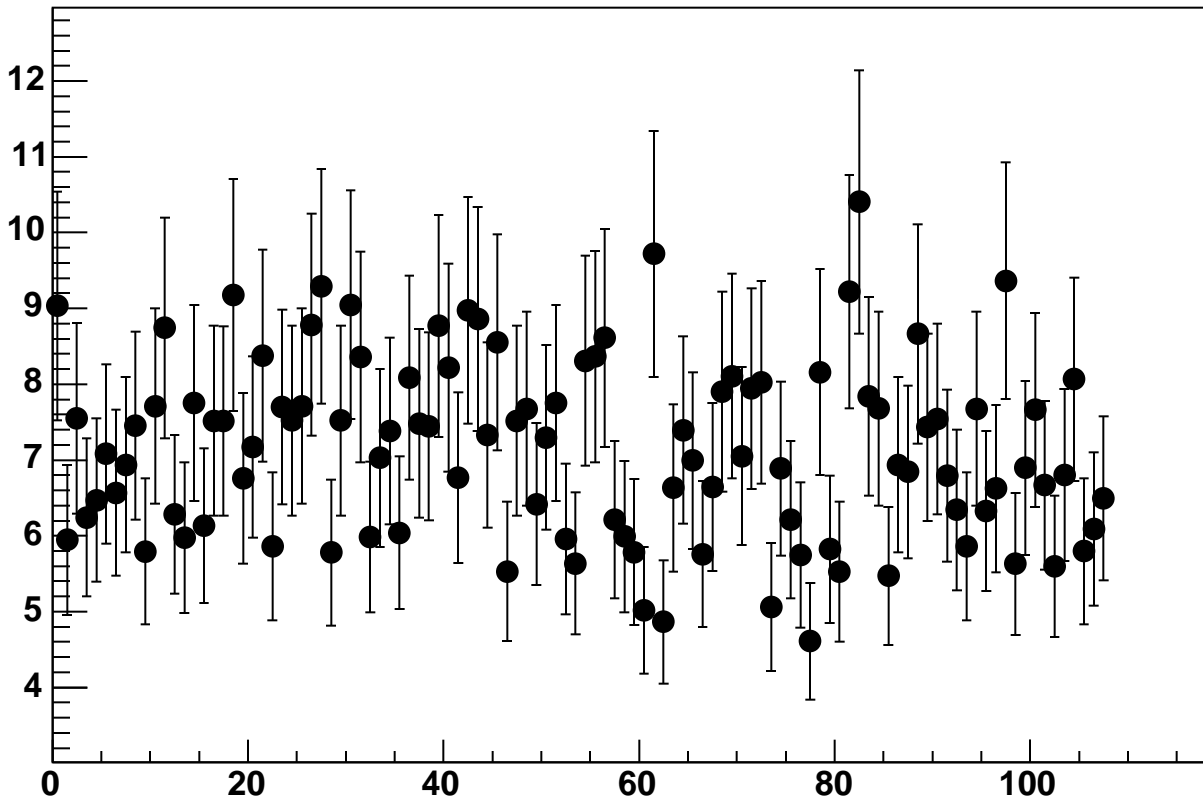
Enable 0, DAC=4000, Hold=225, ADC Noise vs 18\*Chip+Chan



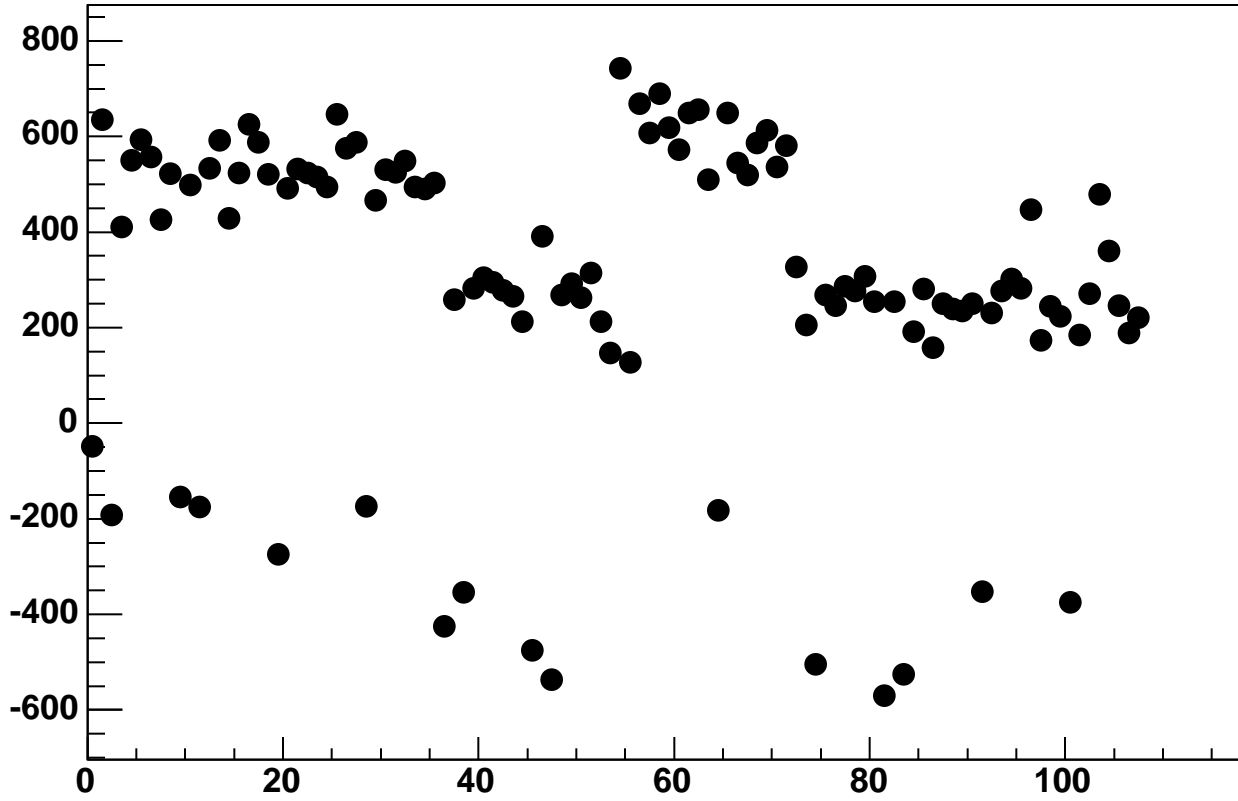
Enable 0, DAC=4000, Hold=230, ADC Mean vs 18\*Chip+Chan



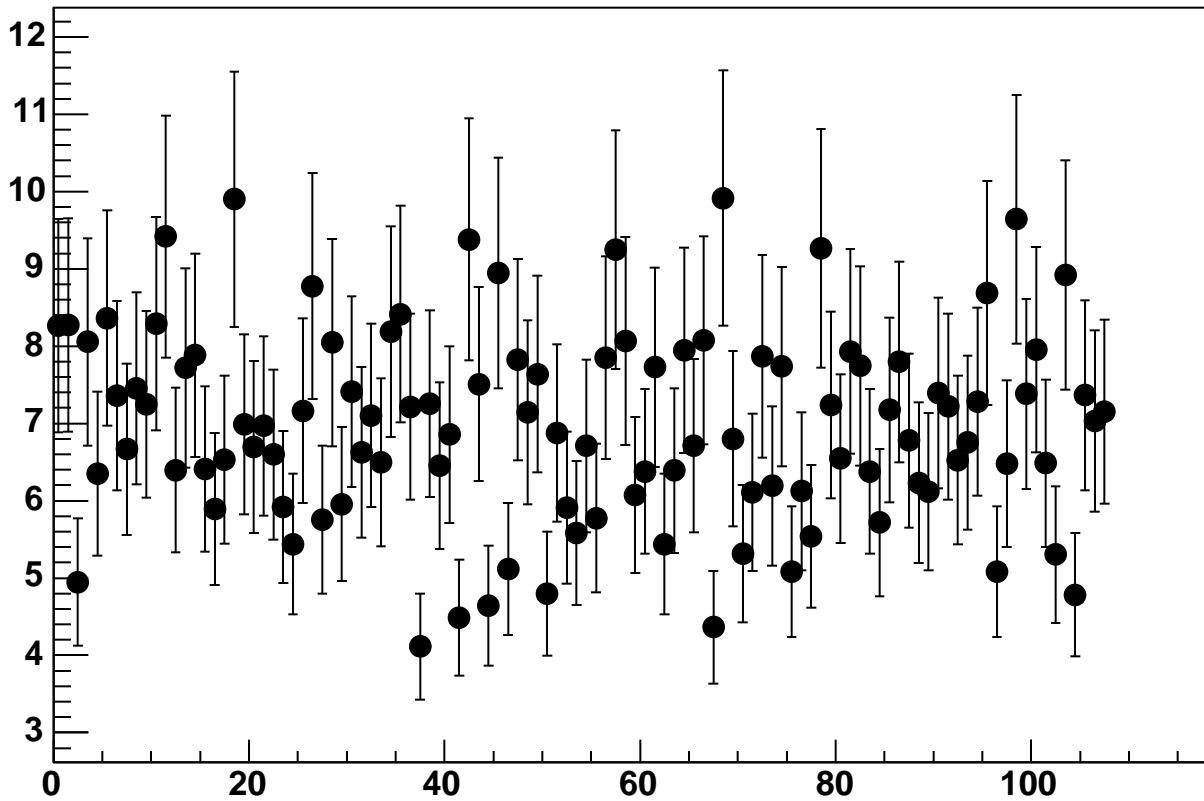
Enable 0, DAC=4000, Hold=230, ADC Noise vs 18\*Chip+Chan



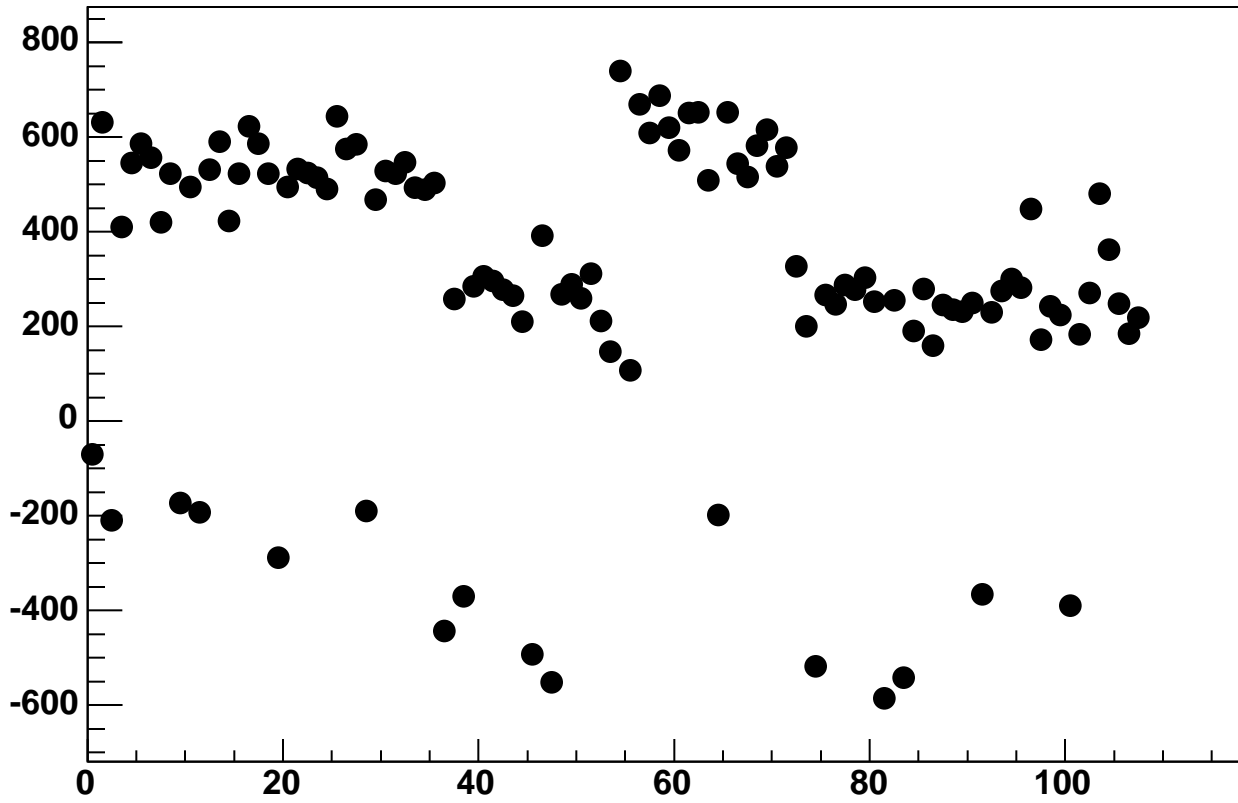
Enable 0, DAC=4000, Hold=235, ADC Mean vs 18\*Chip+Chan



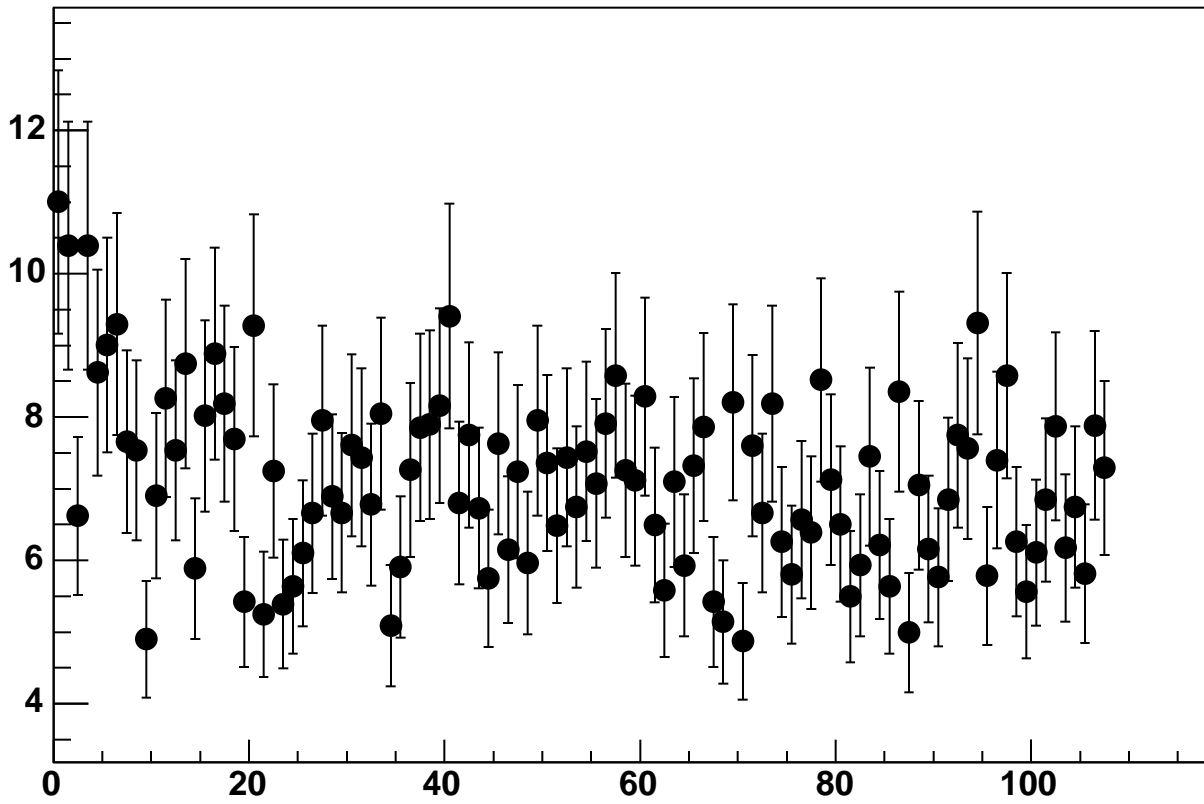
Enable 0, DAC=4000, Hold=235, ADC Noise vs 18\*Chip+Chan



Enable 0, DAC=4000, Hold=240, ADC Mean vs 18\*Chip+Chan

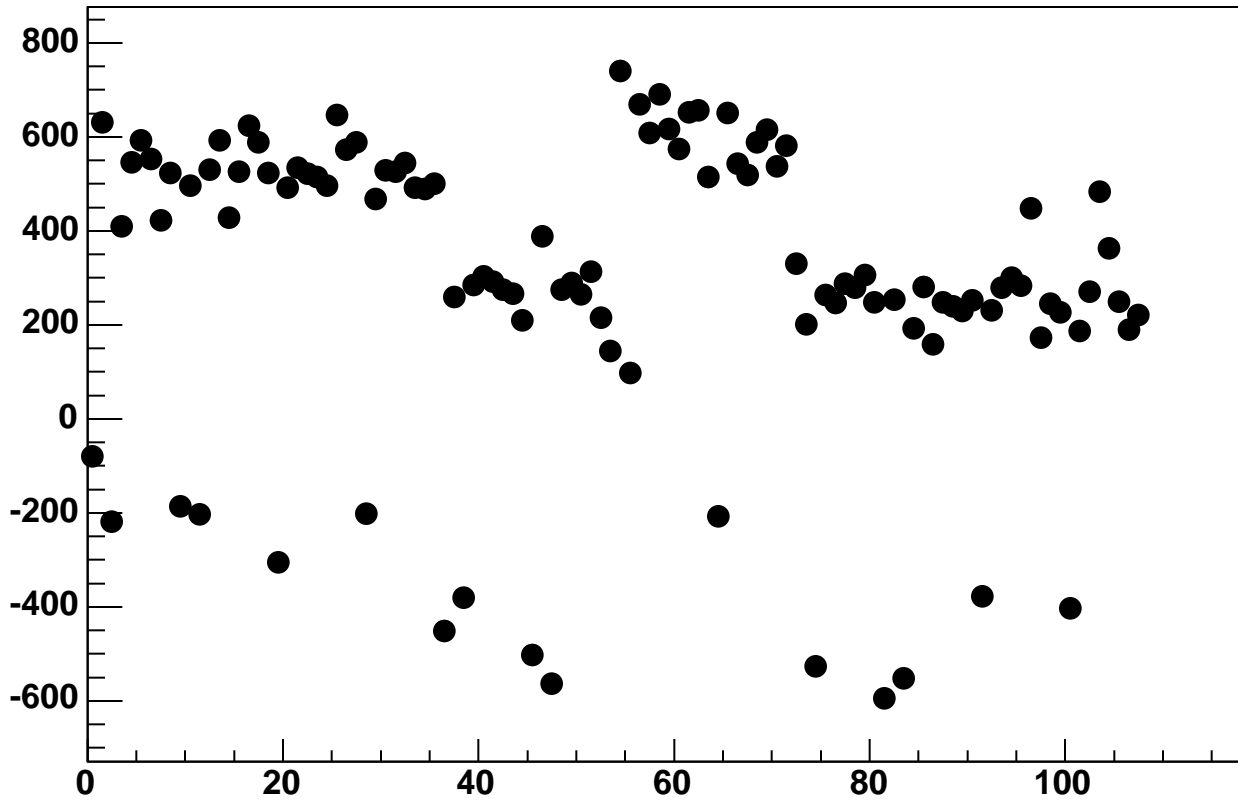


Enable 0, DAC=4000, Hold=240, ADC Noise vs 18\*Chip+Chan

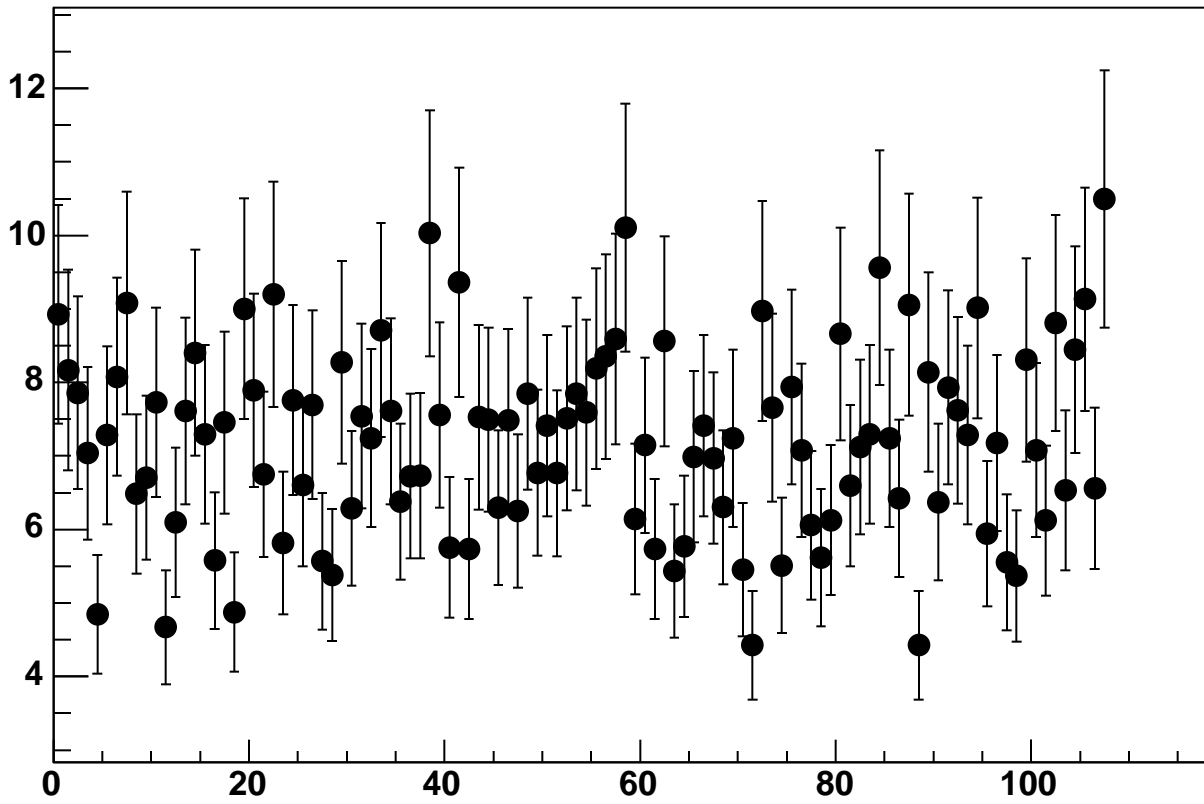




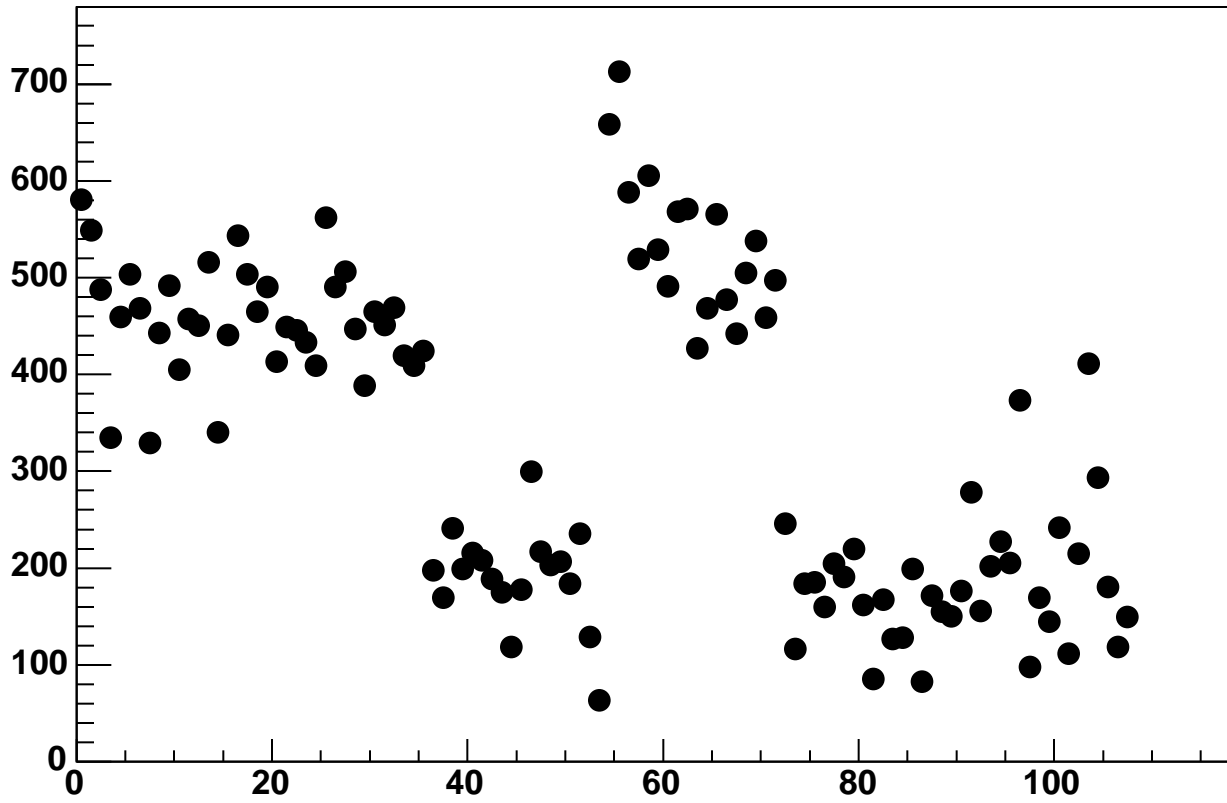
Enable 0, DAC=4000, Hold=245, ADC Mean vs 18\*Chip+Chan



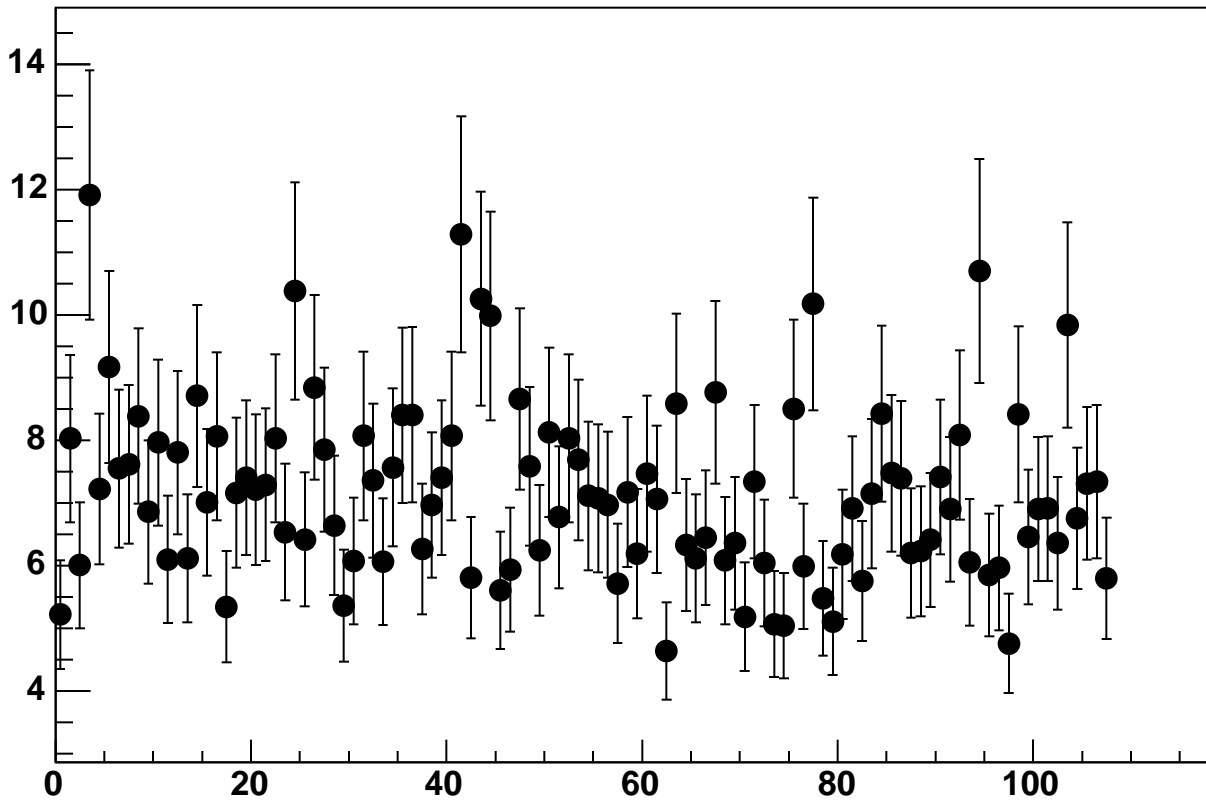
Enable 0, DAC=4000, Hold=245, ADC Noise vs 18\*Chip+Chan



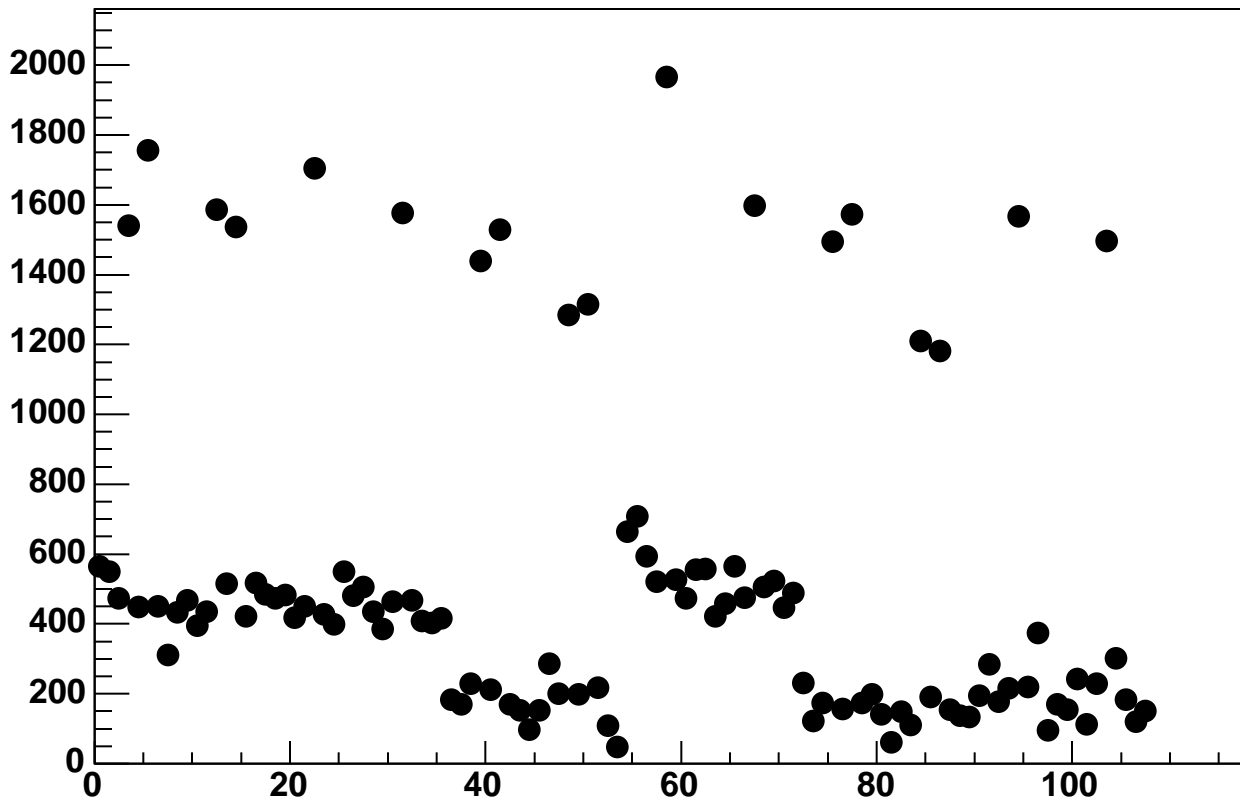
Enable 1, DAC=4000, Hold=0, ADC Mean vs 18\*Chip+Chan



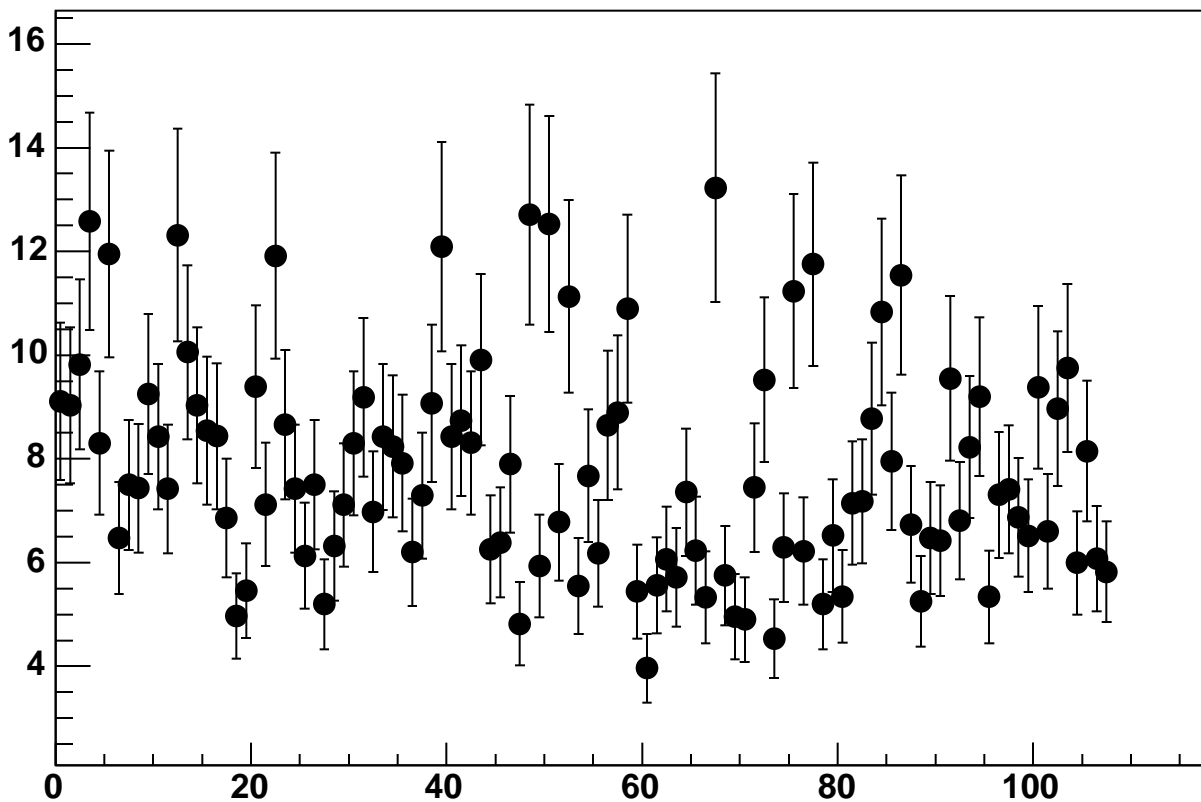
Enable 1, DAC=4000, Hold=0, ADC Noise vs 18\*Chip+Chan



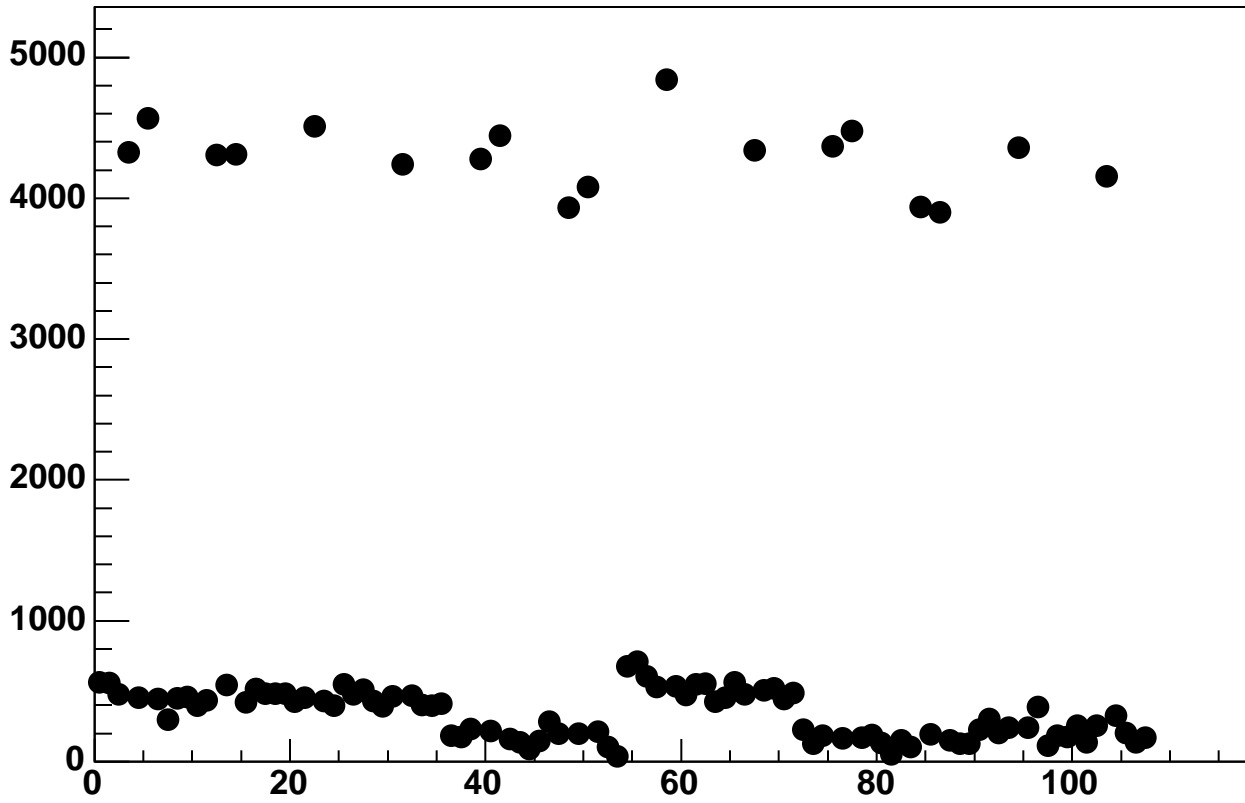
Enable 1, DAC=4000, Hold=5, ADC Mean vs 18\*Chip+Chan



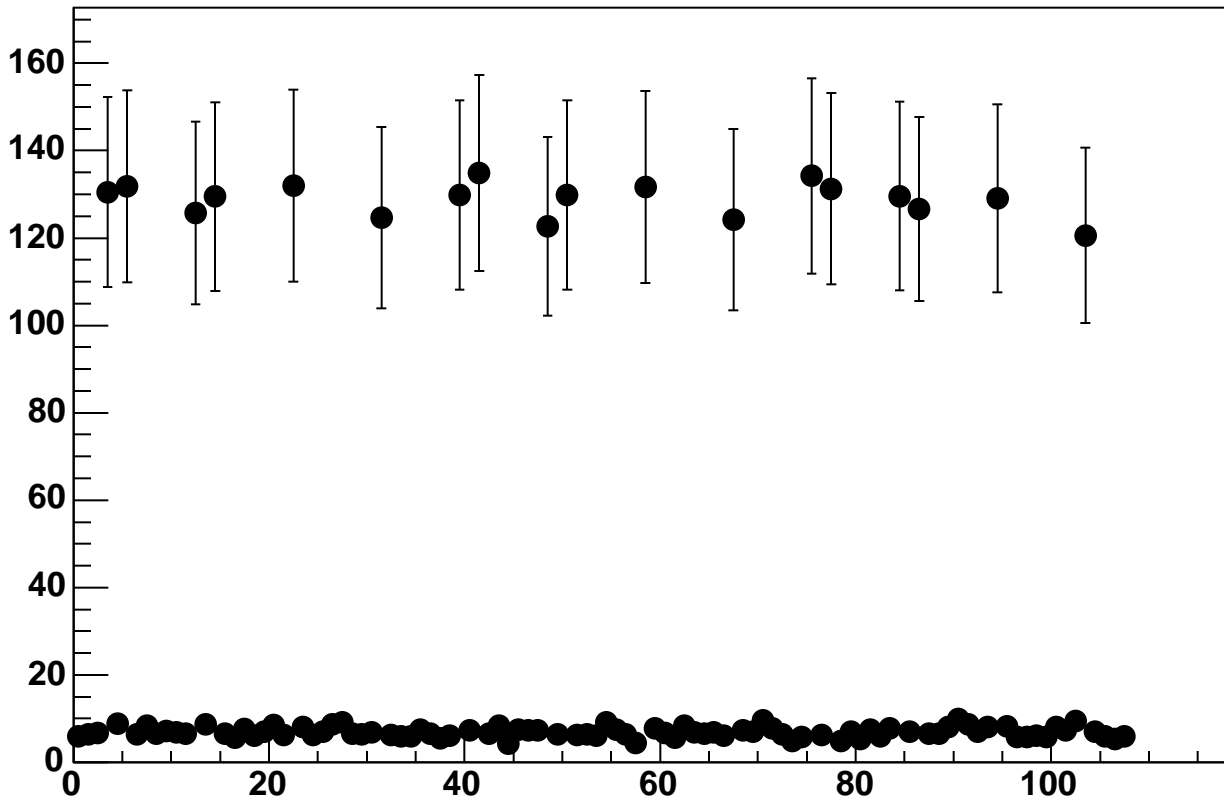
Enable 1, DAC=4000, Hold=5, ADC Noise vs 18\*Chip+Chan



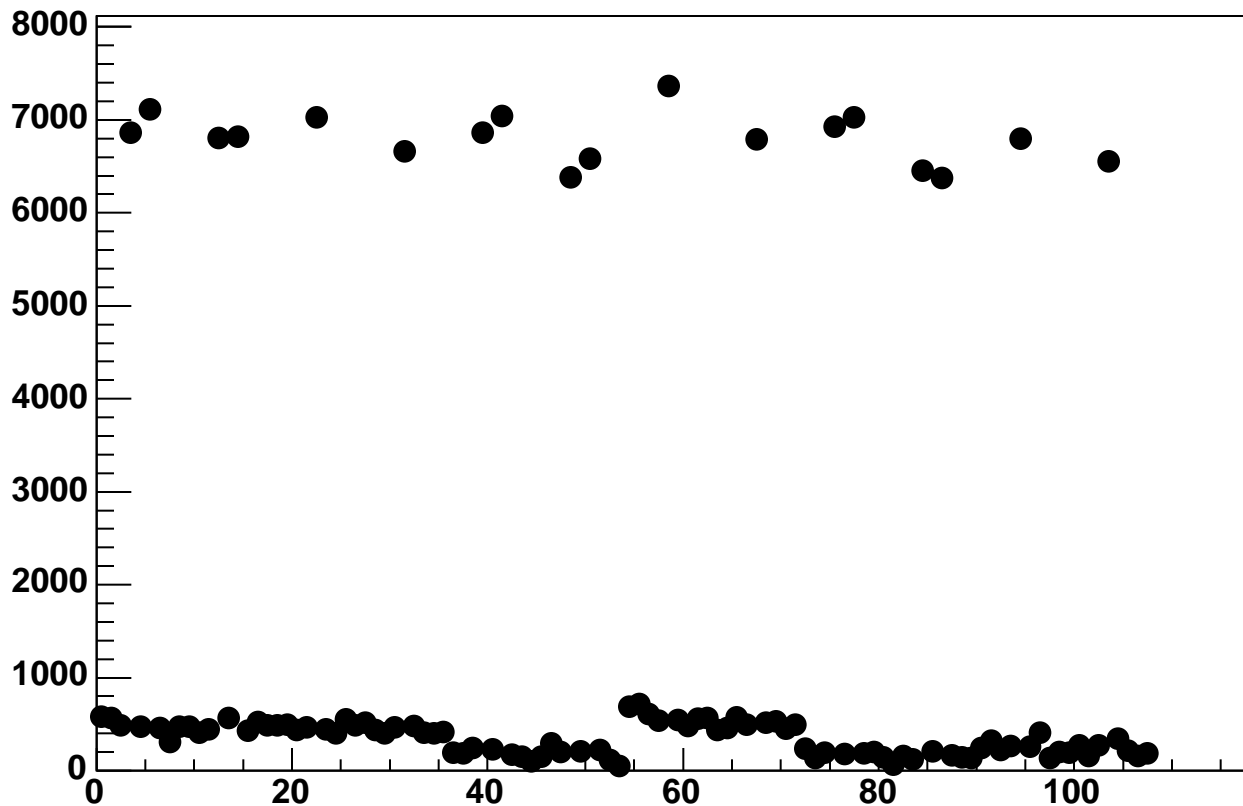
Enable 1, DAC=4000, Hold=10, ADC Mean vs 18\*Chip+Chan



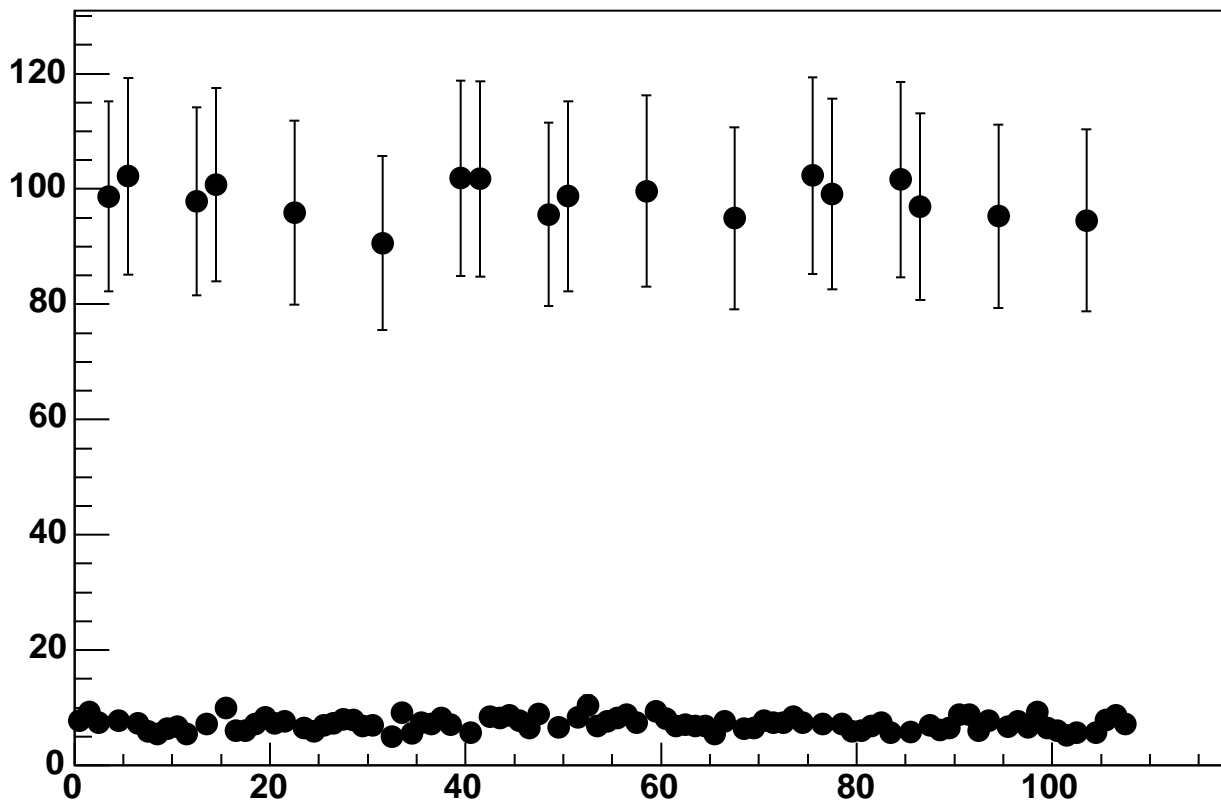
Enable 1, DAC=4000, Hold=10, ADC Noise vs 18\*Chip+Chan



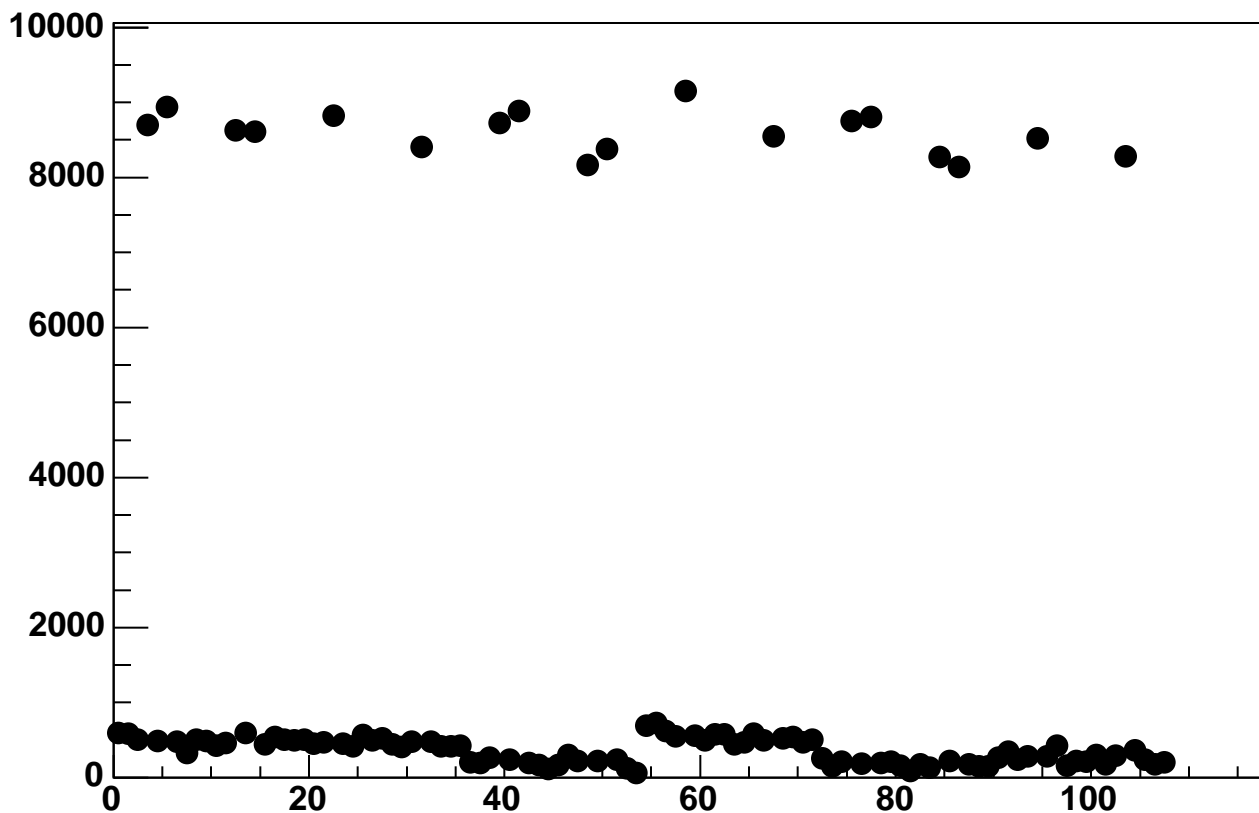
Enable 1, DAC=4000, Hold=15, ADC Mean vs 18\*Chip+Chan



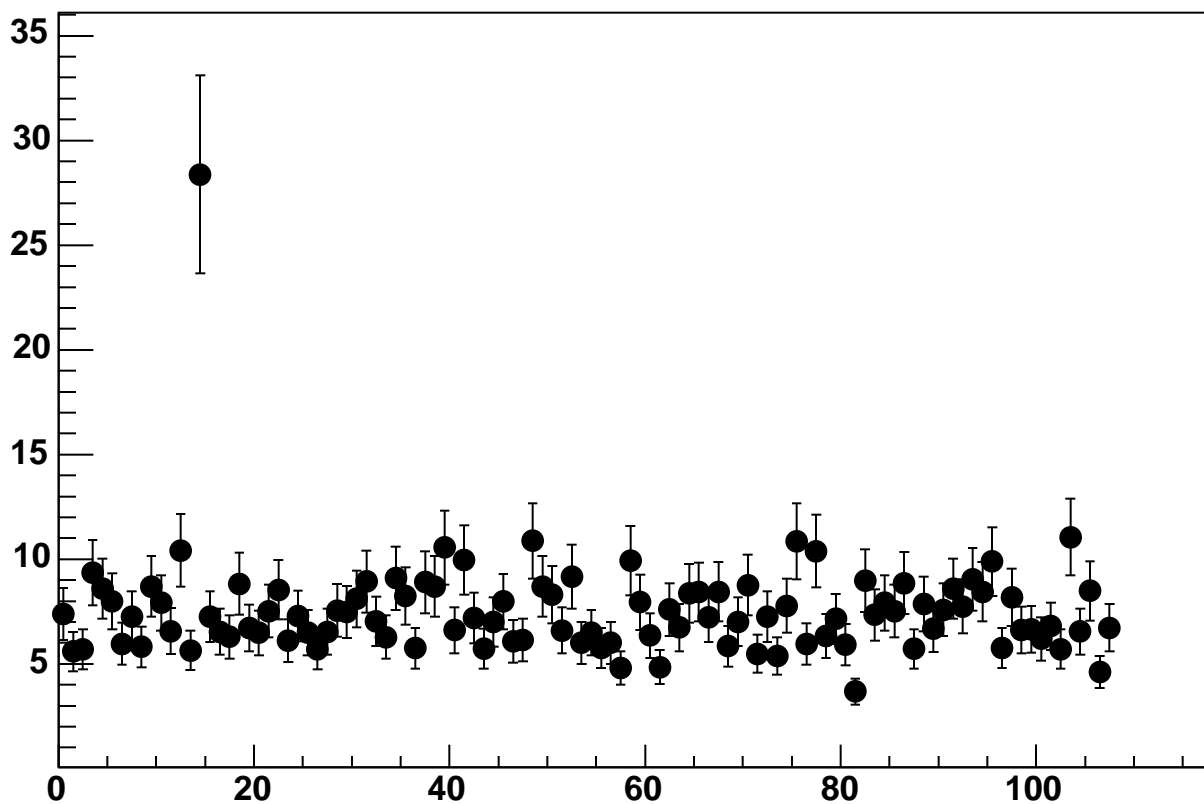
Enable 1, DAC=4000, Hold=15, ADC Noise vs 18\*Chip+Chan



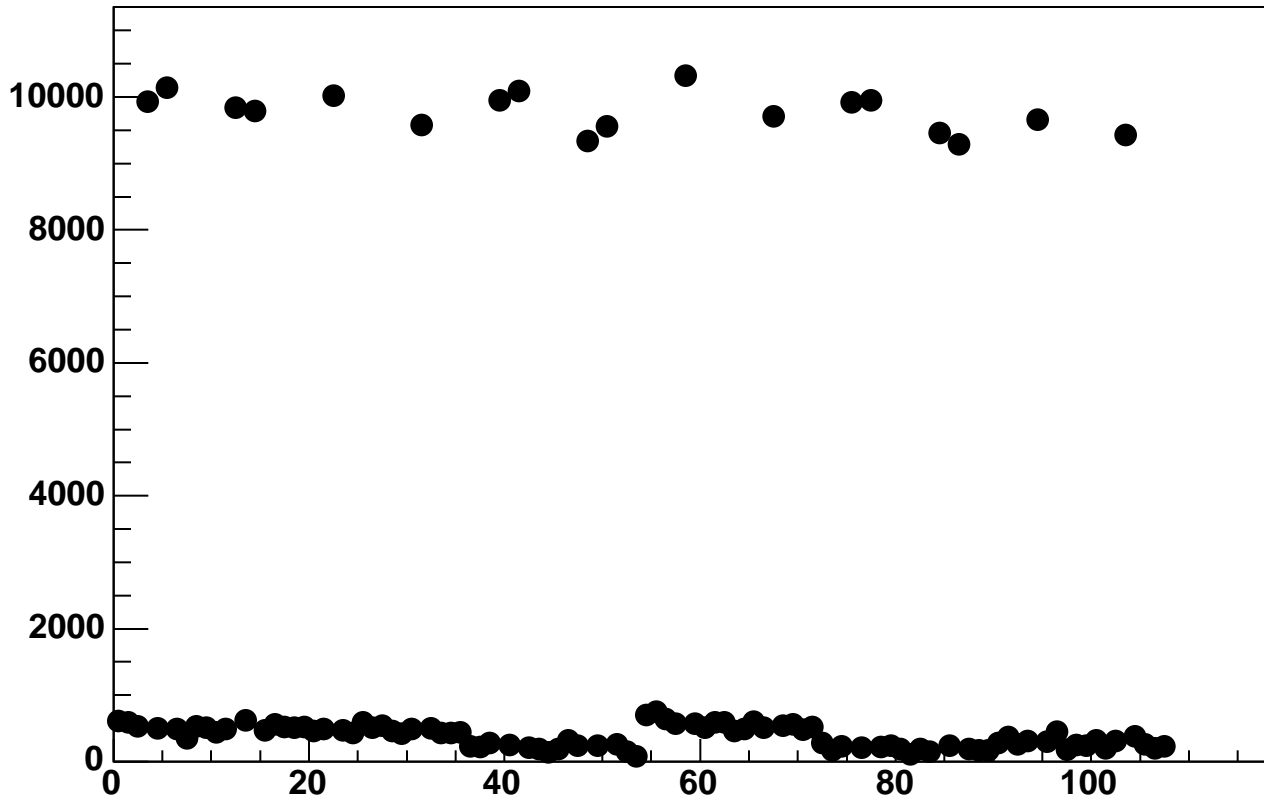
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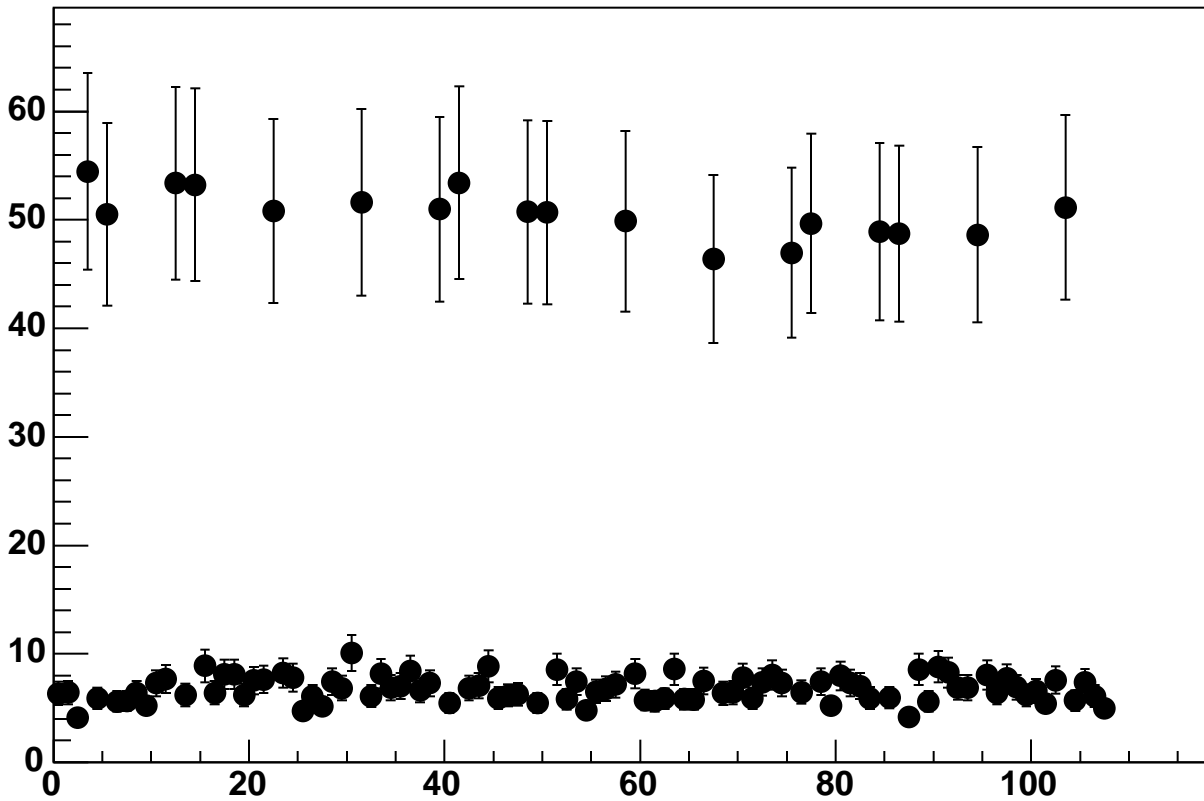
Enable 1, DAC=4000, Hold=20, ADC Noise vs 18\*Chip+Chan



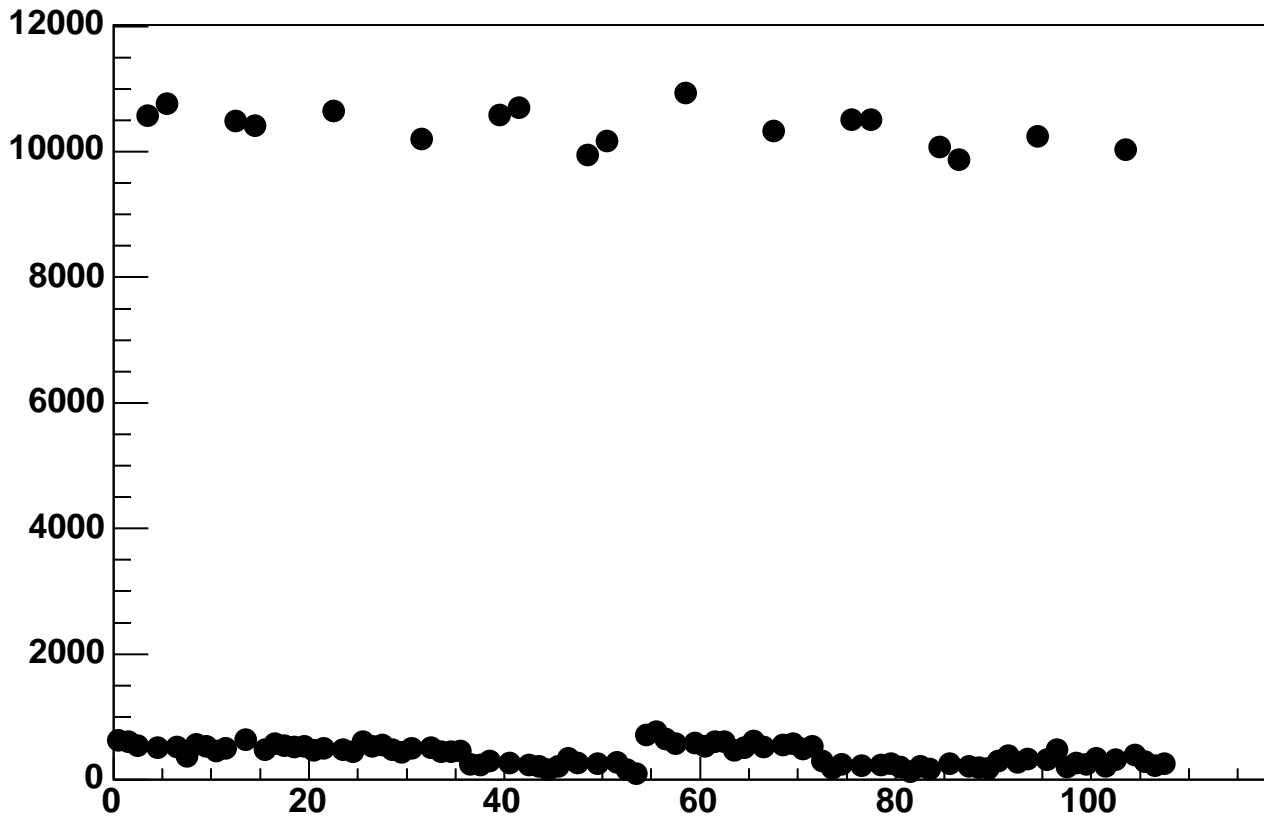
Enable 1, DAC=4000, Hold=25, ADC Mean vs 18\*Chip+Chan



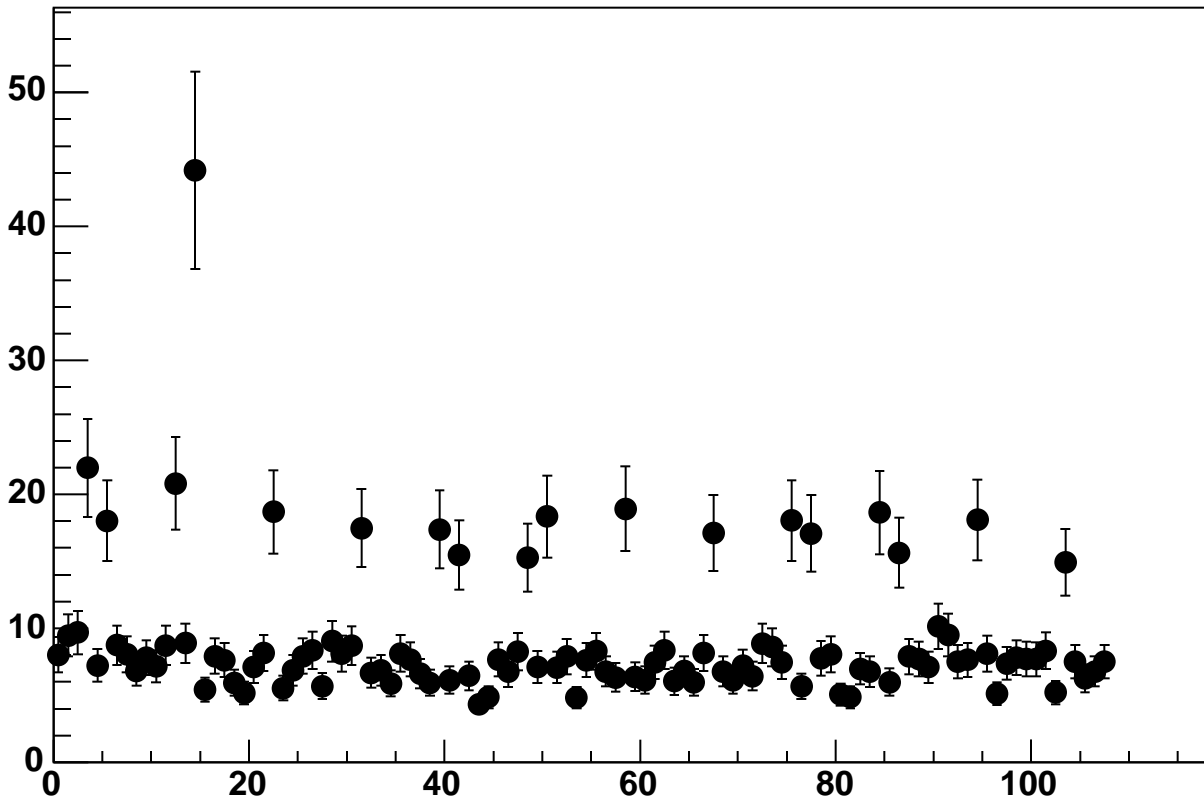
Enable 1, DAC=4000, Hold=25, ADC Noise vs 18\*Chip+Chan



Enable 1, DAC=4000, Hold=30, ADC Mean vs 18\*Chip+Chan

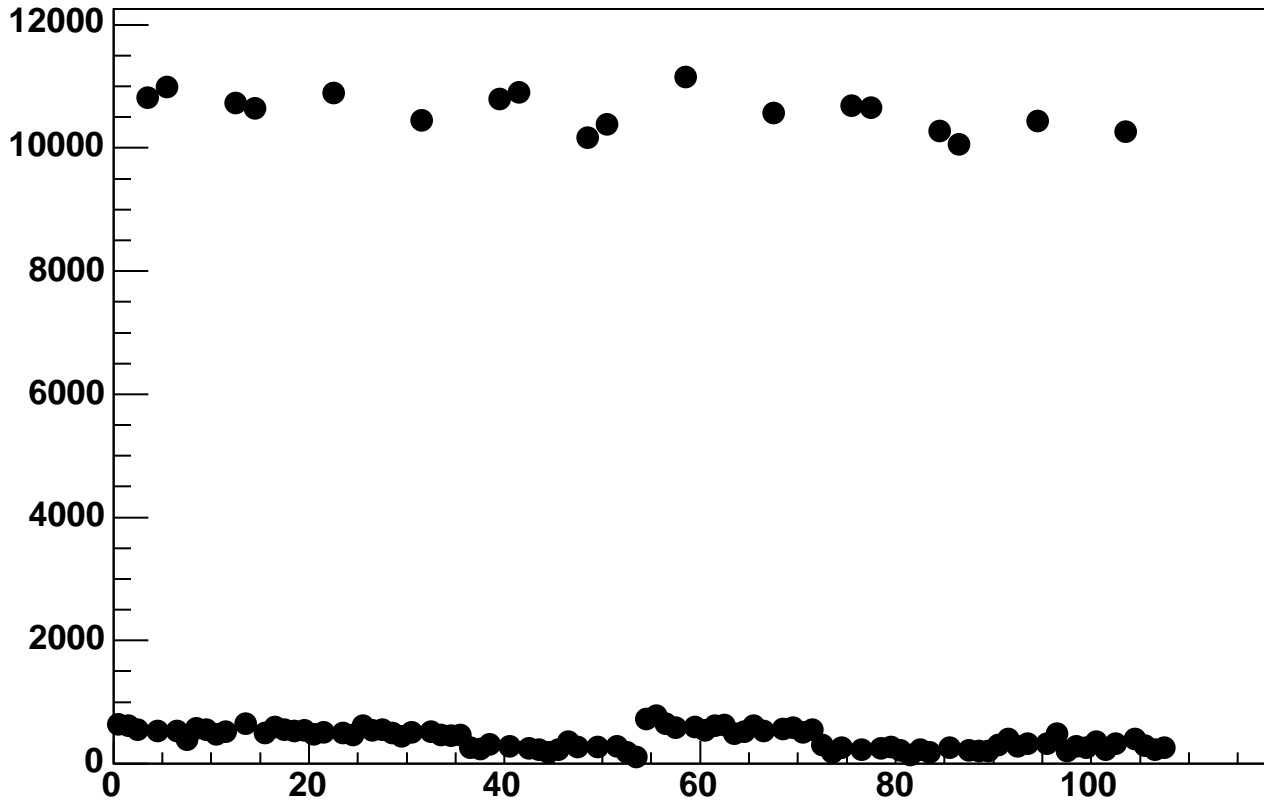


Enable 1, DAC=4000, Hold=30, ADC Noise vs 18\*Chip+Chan

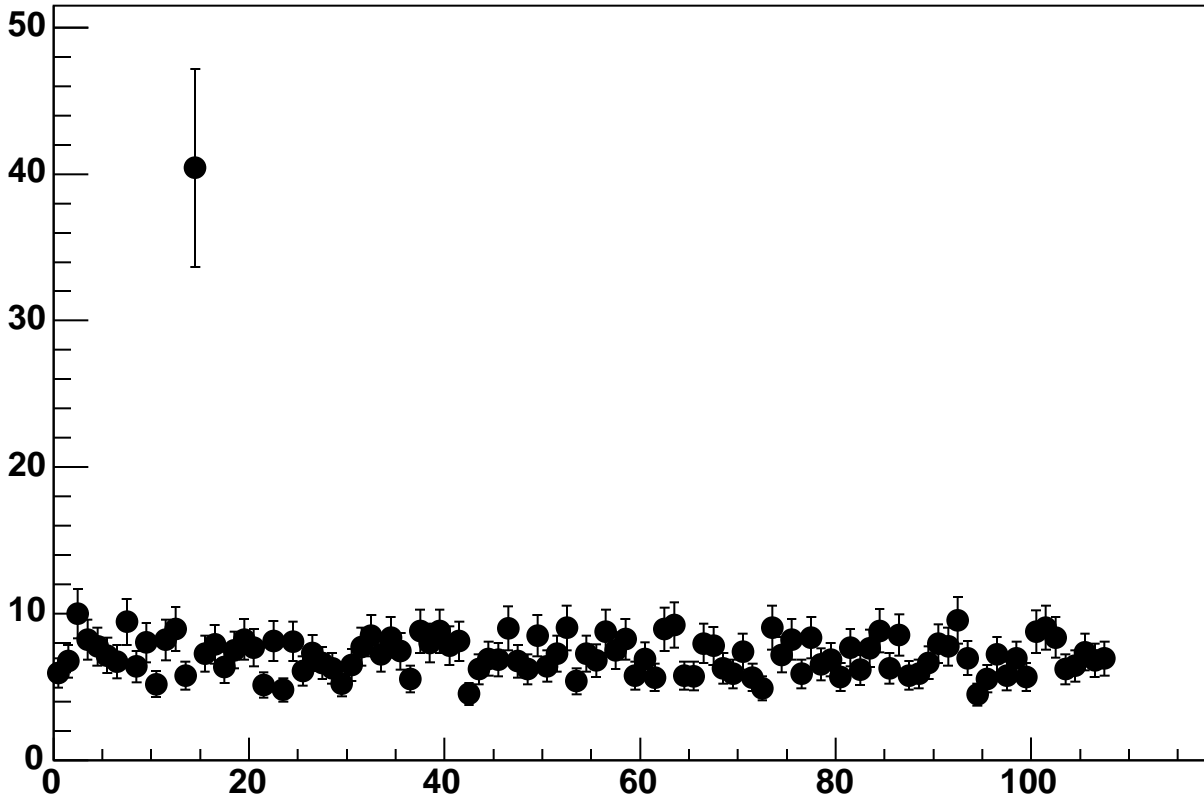




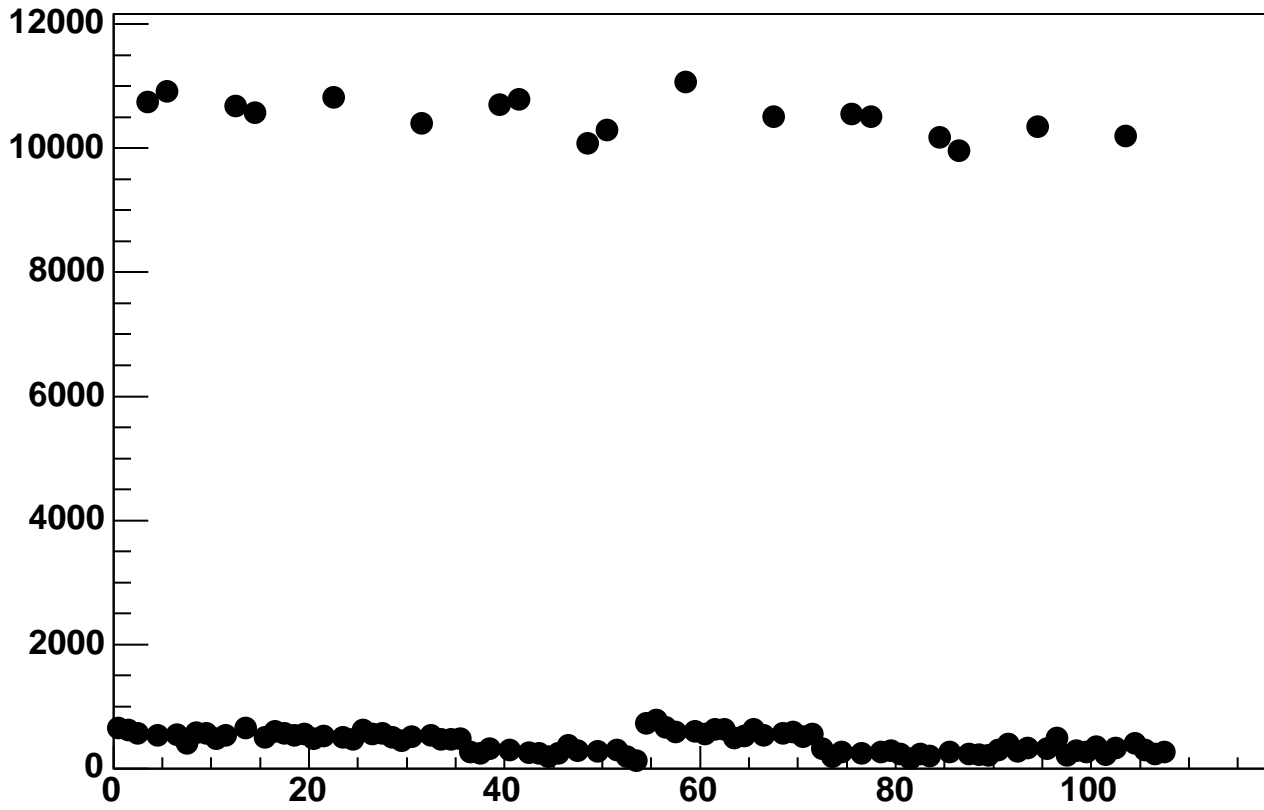
Enable 1, DAC=4000, Hold=35, ADC Mean vs 18\*Chip+Chan



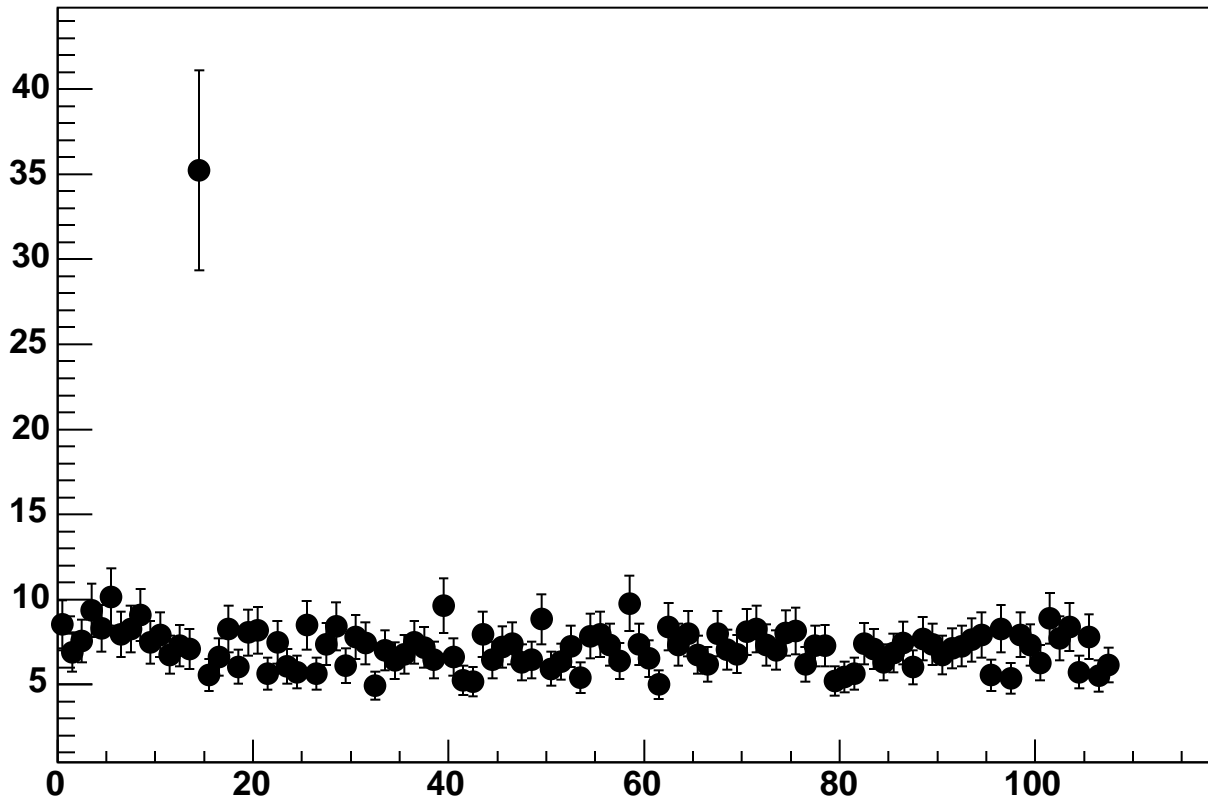
Enable 1, DAC=4000, Hold=35, ADC Noise vs 18\*Chip+Chan



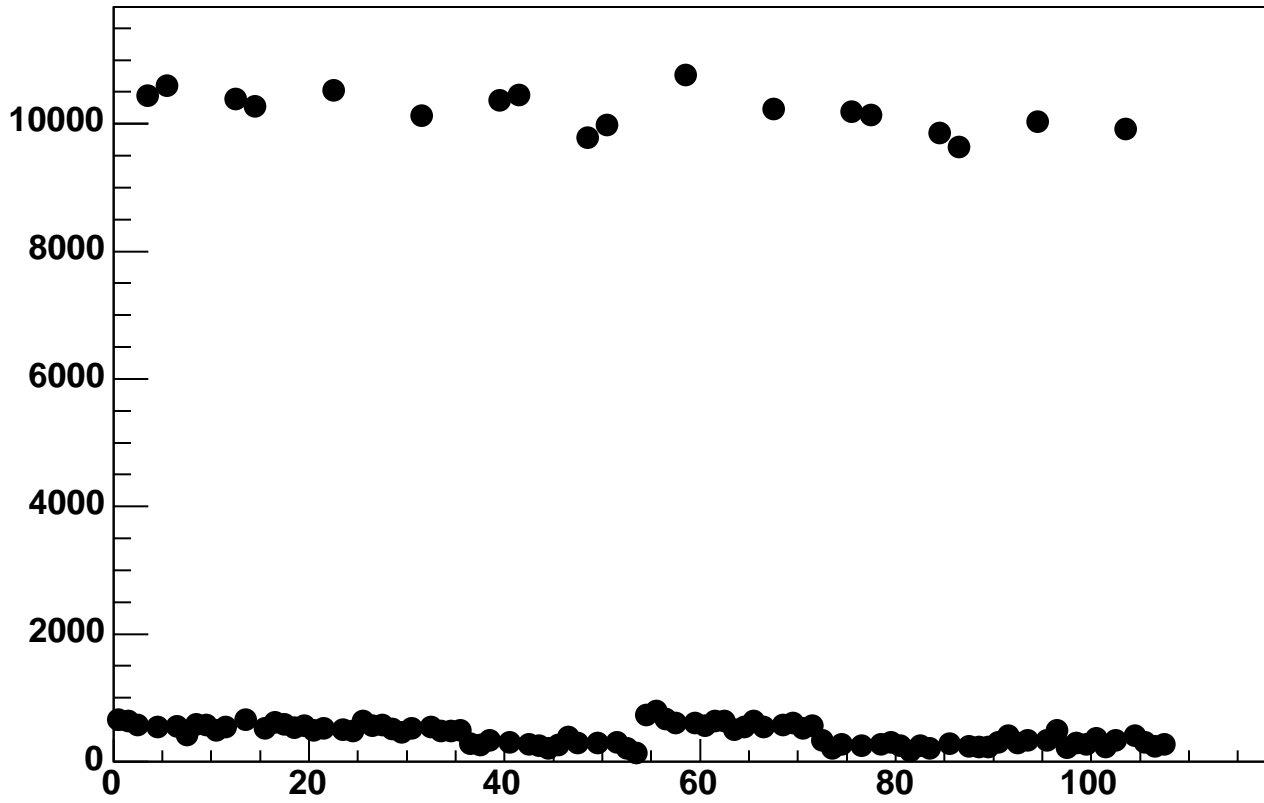
Enable 1, DAC=4000, Hold=40, ADC Mean vs 18\*Chip+Chan



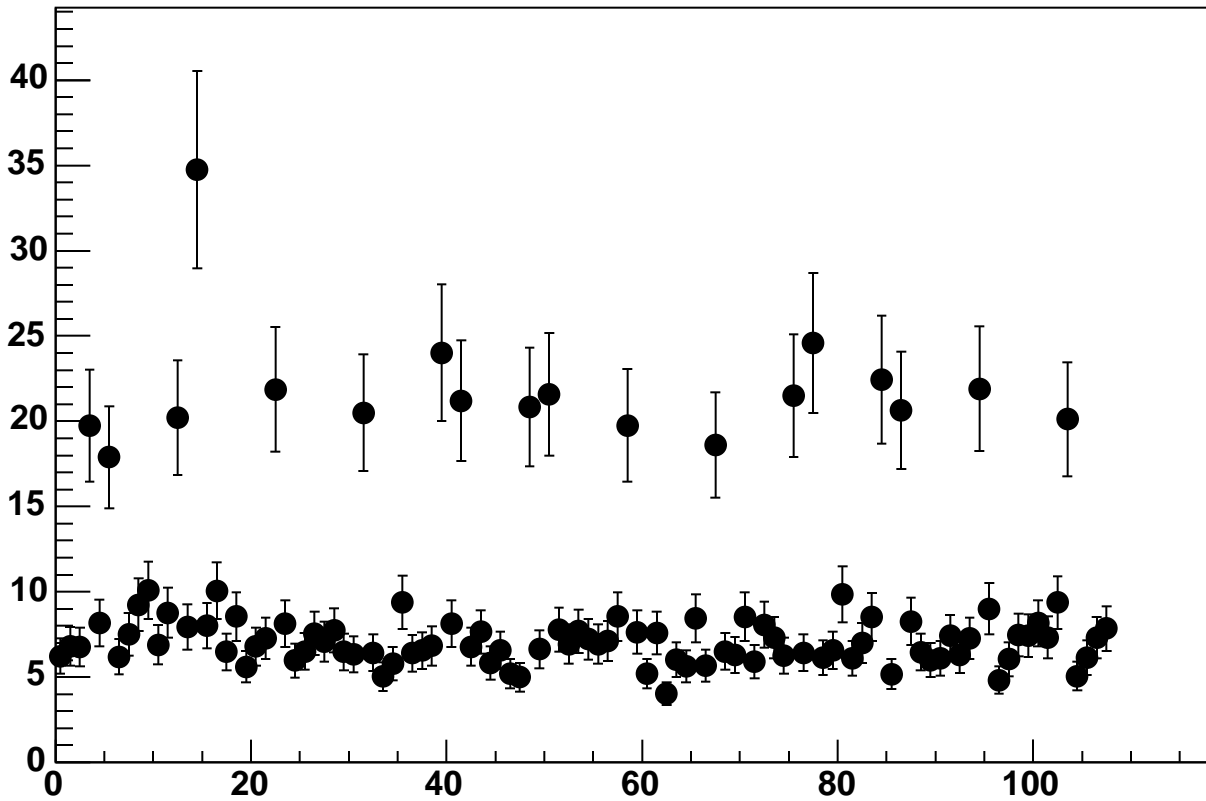
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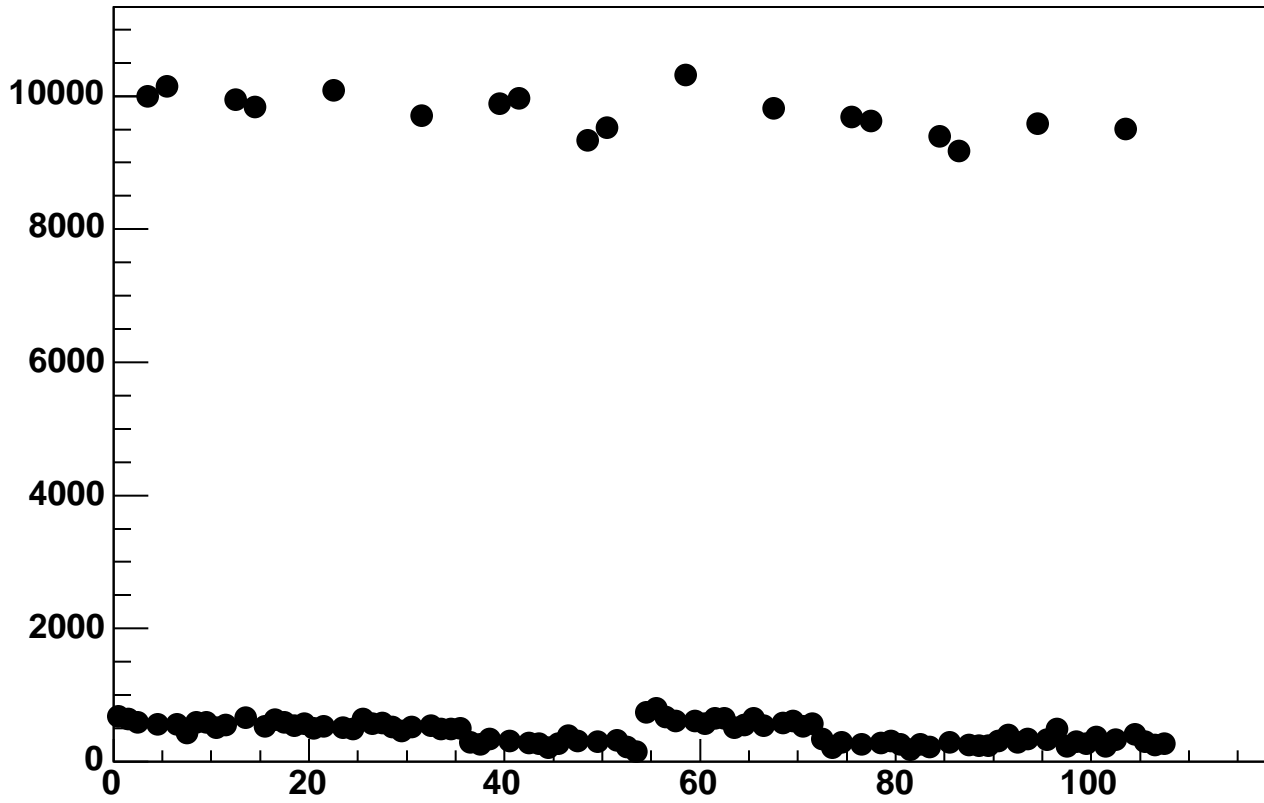
Enable 1, DAC=4000, Hold=45, ADC Mean vs 18\*Chip+Chan



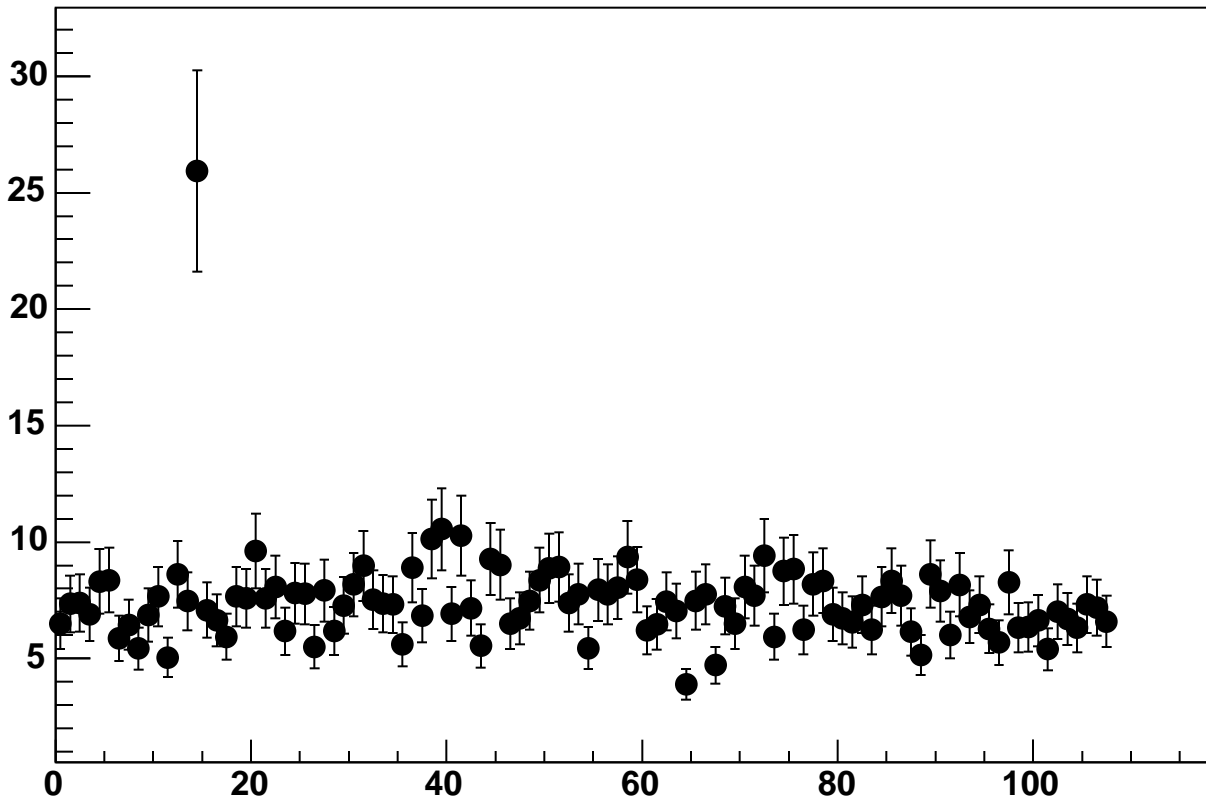
Enable 1, DAC=4000, Hold=45, ADC Noise vs 18\*Chip+Chan



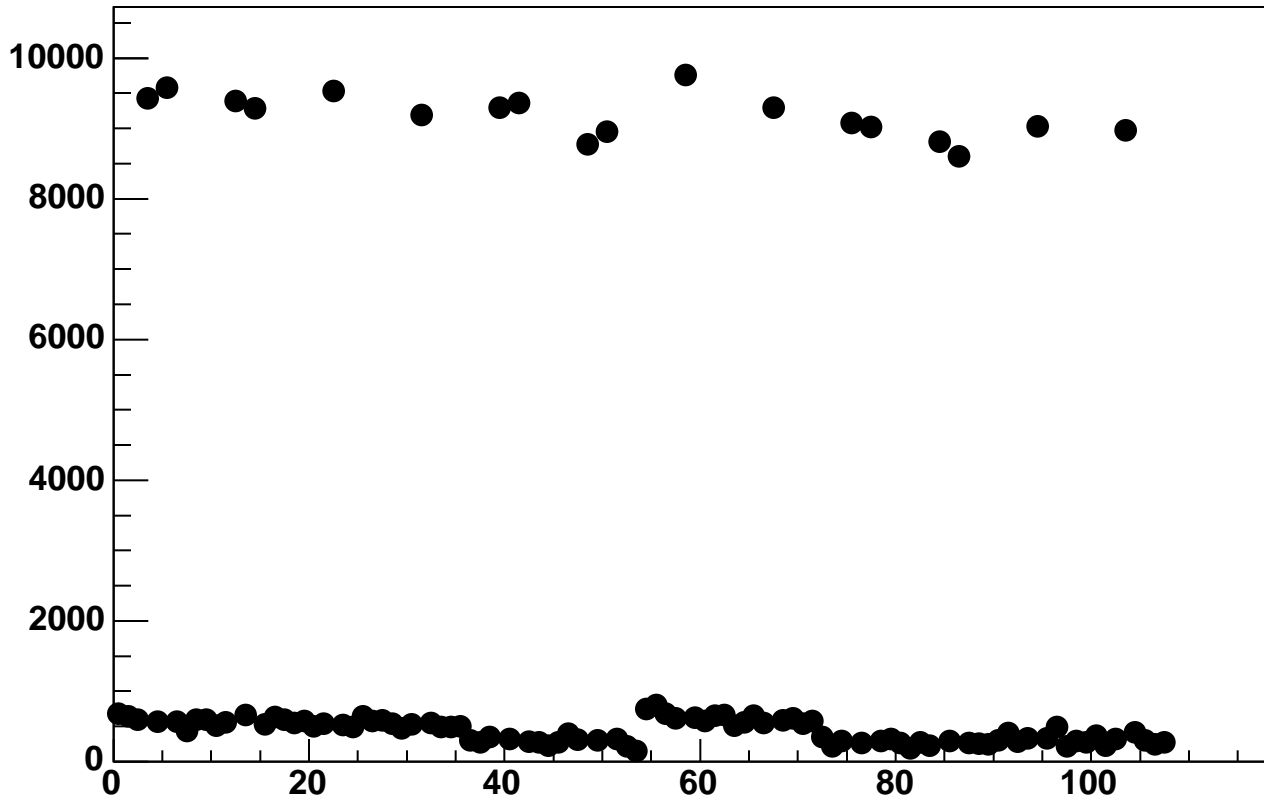
Enable 1, DAC=4000, Hold=50, ADC Mean vs 18\*Chip+Chan



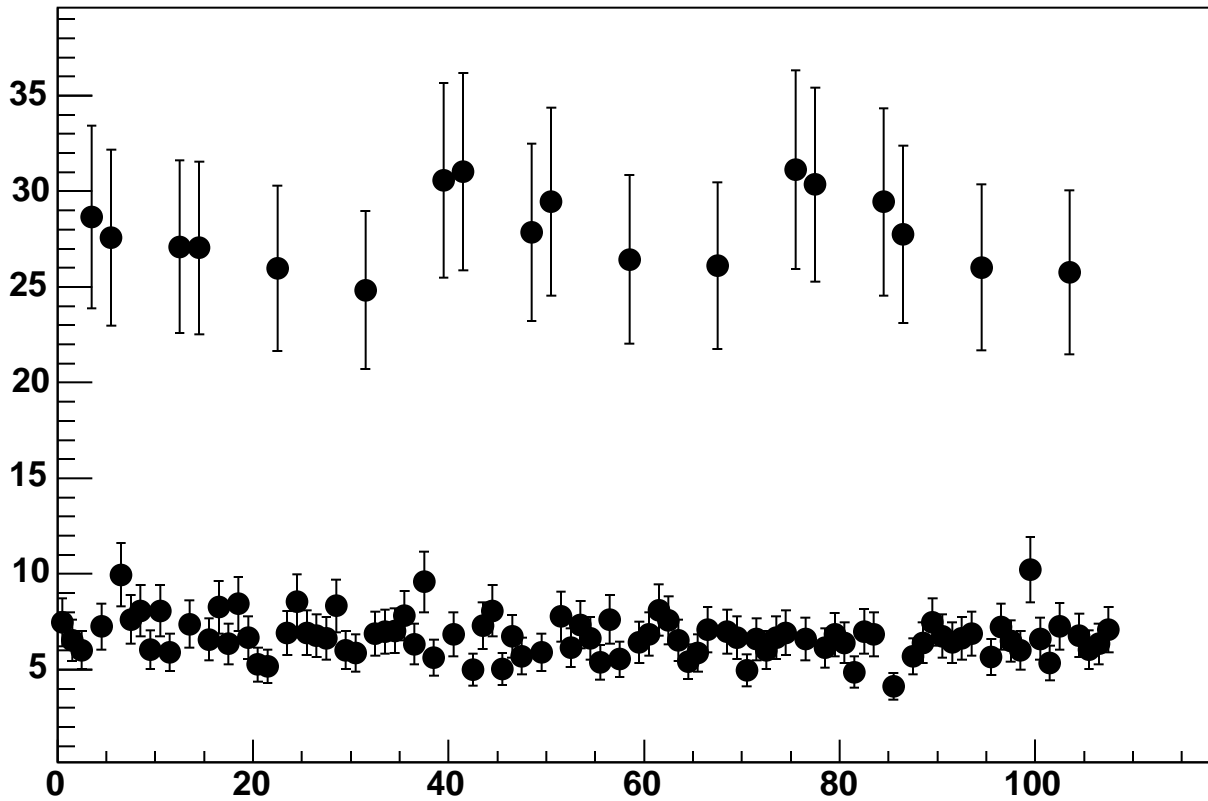
Enable 1, DAC=4000, Hold=50, ADC Noise vs 18\*Chip+Chan



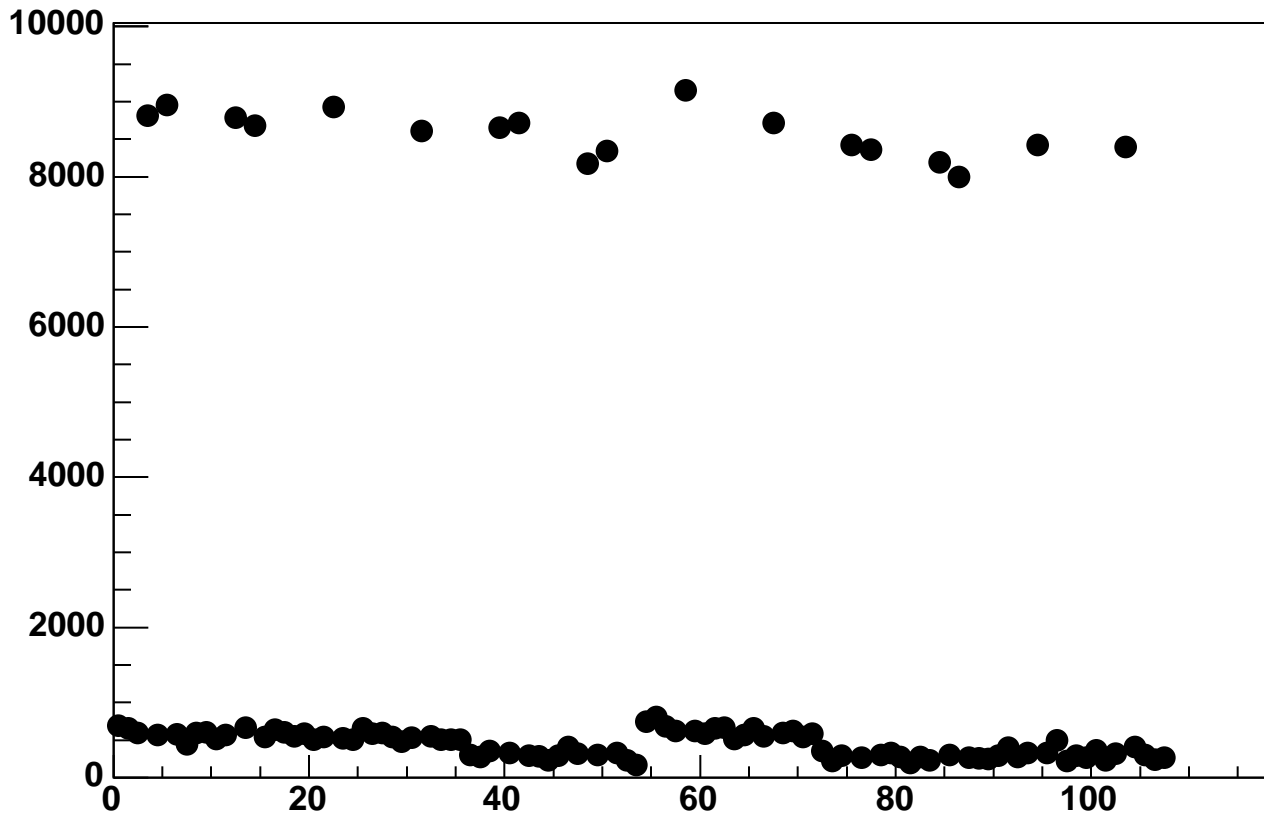
Enable 1, DAC=4000, Hold=55, ADC Mean vs 18\*Chip+Chan



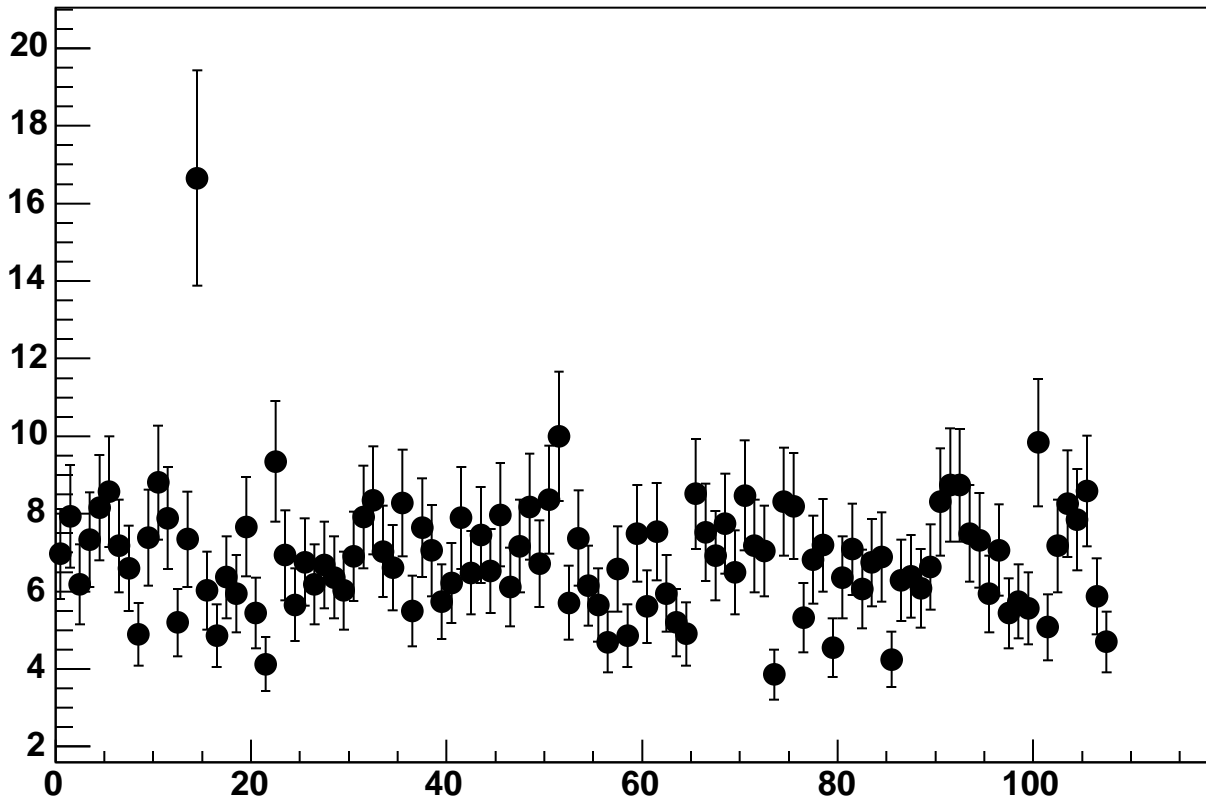
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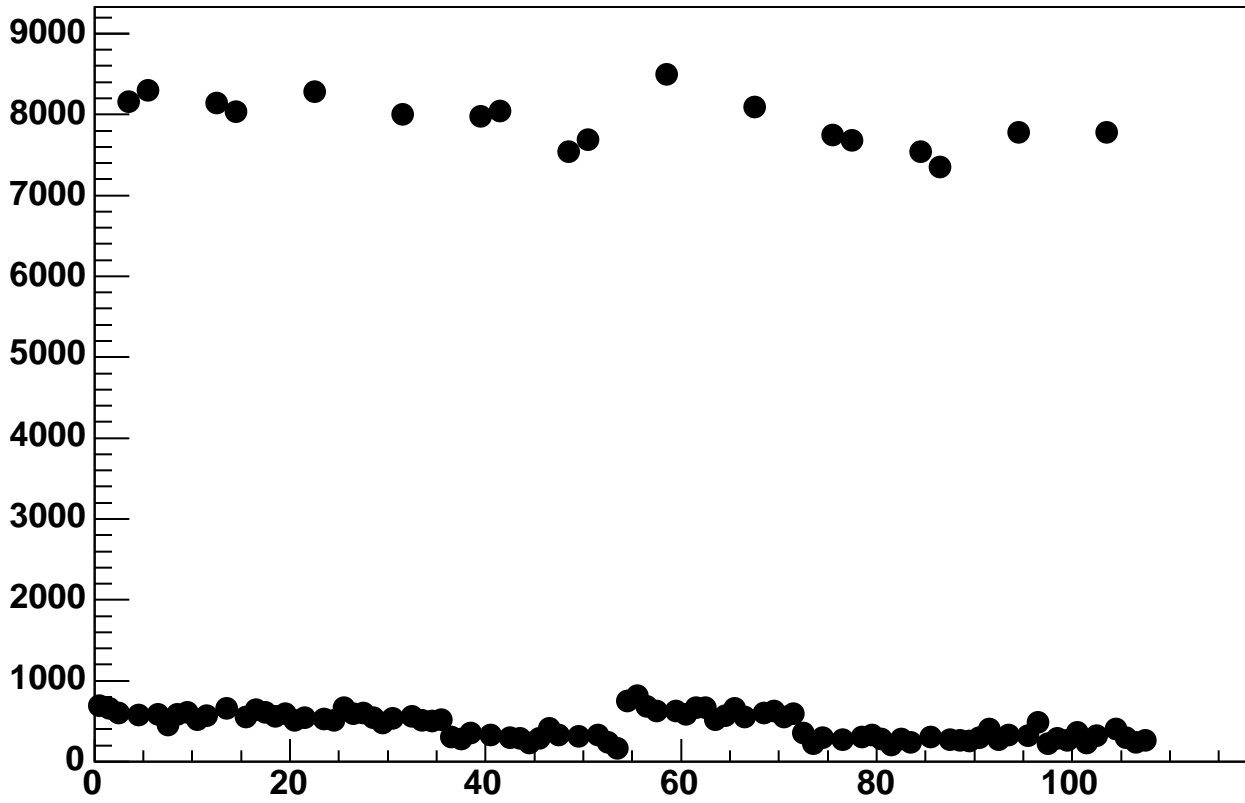
Enable 1, DAC=4000, Hold=60, ADC Mean vs 18\*Chip+Chan



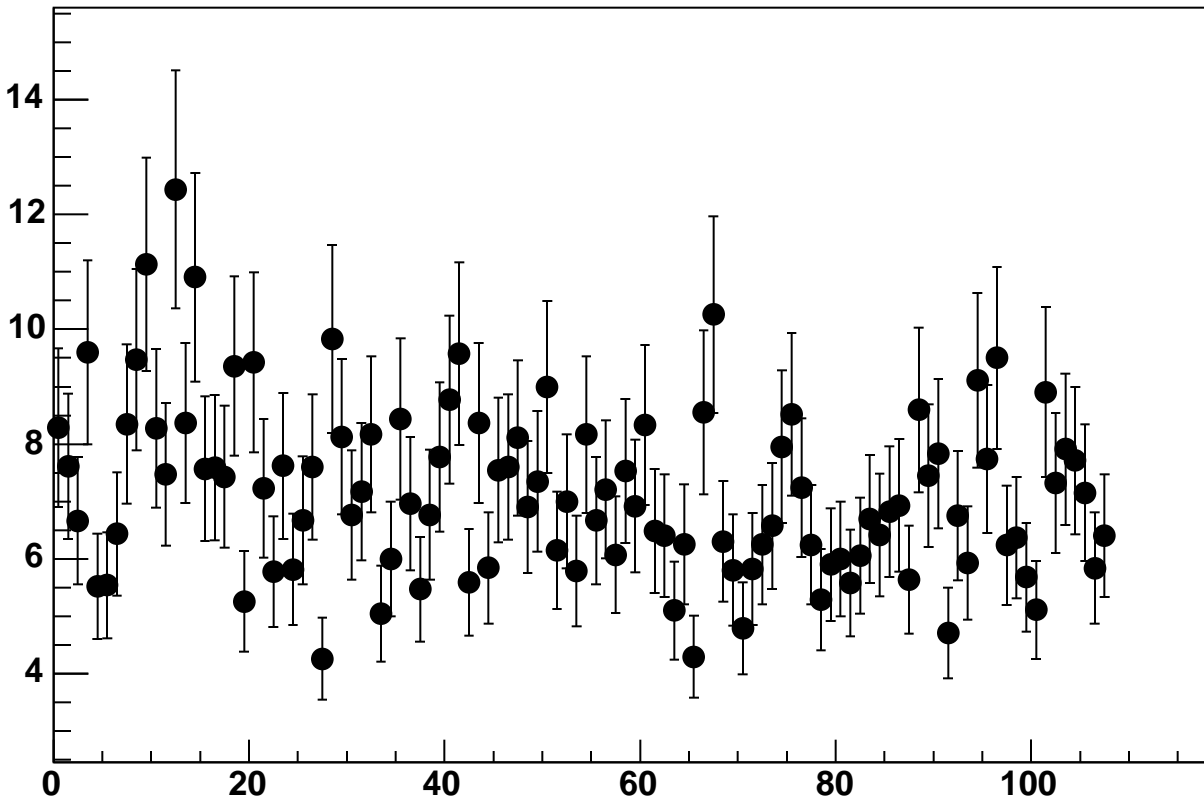
Enable 1, DAC=4000, Hold=60, ADC Noise vs 18\*Chip+Chan



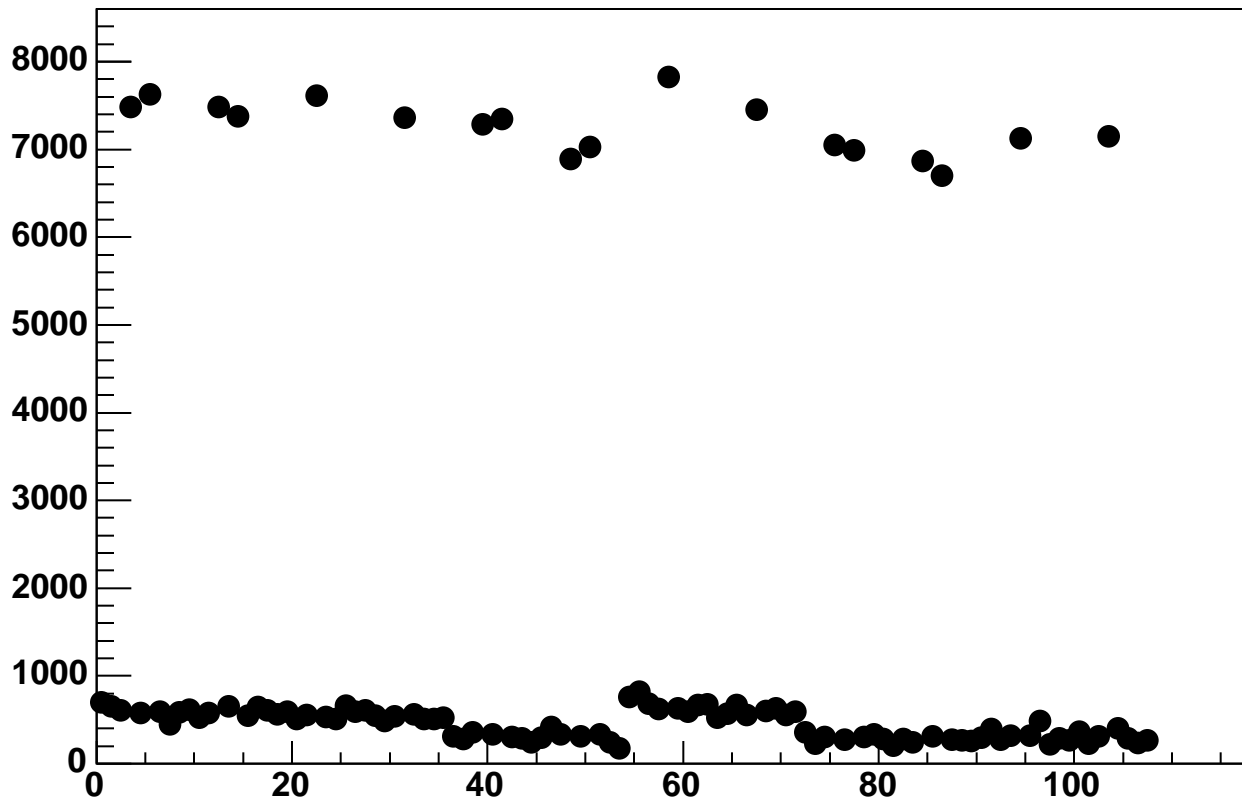
Enable 1, DAC=4000, Hold=65, ADC Mean vs 18\*Chip+Chan



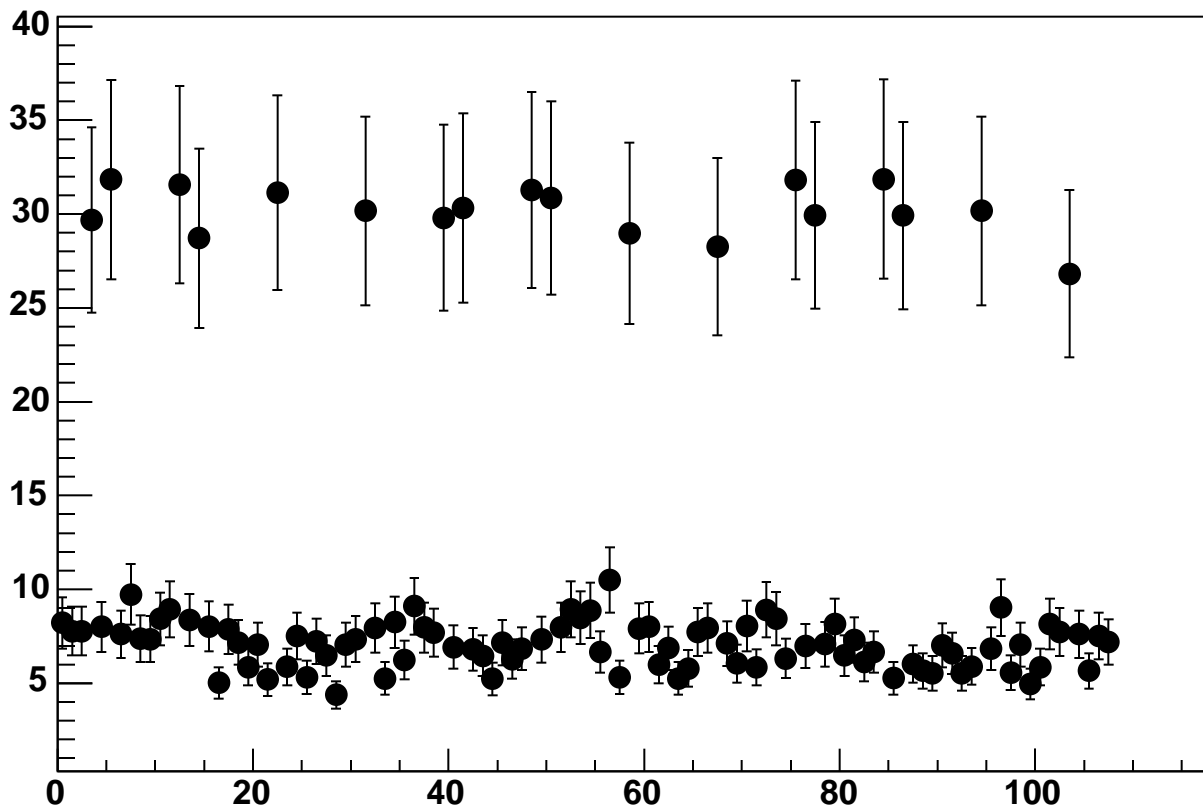
Enable 1, DAC=4000, Hold=65, ADC Noise vs 18\*Chip+Chan



Enable 1, DAC=4000, Hold=70, ADC Mean vs 18\*Chip+Chan

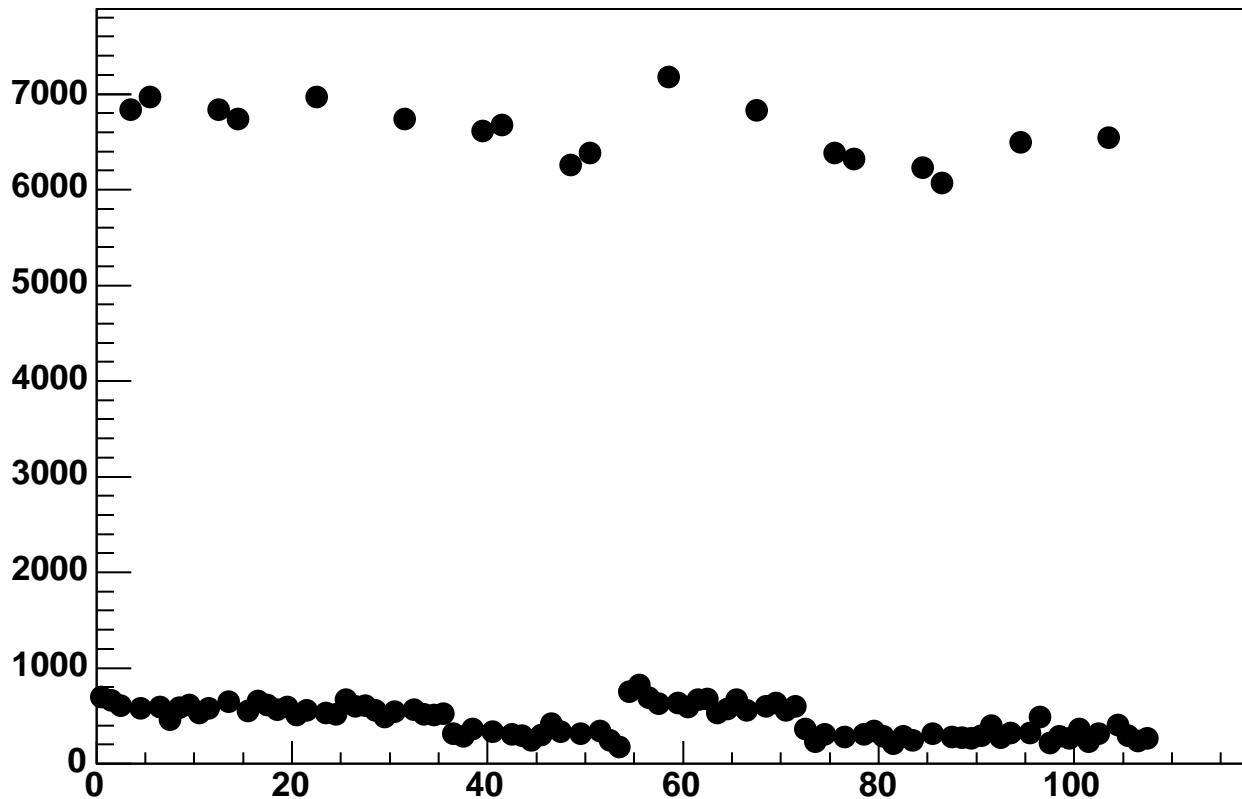


Enable 1, DAC=4000, Hold=70, ADC Noise vs 18\*Chip+Chan

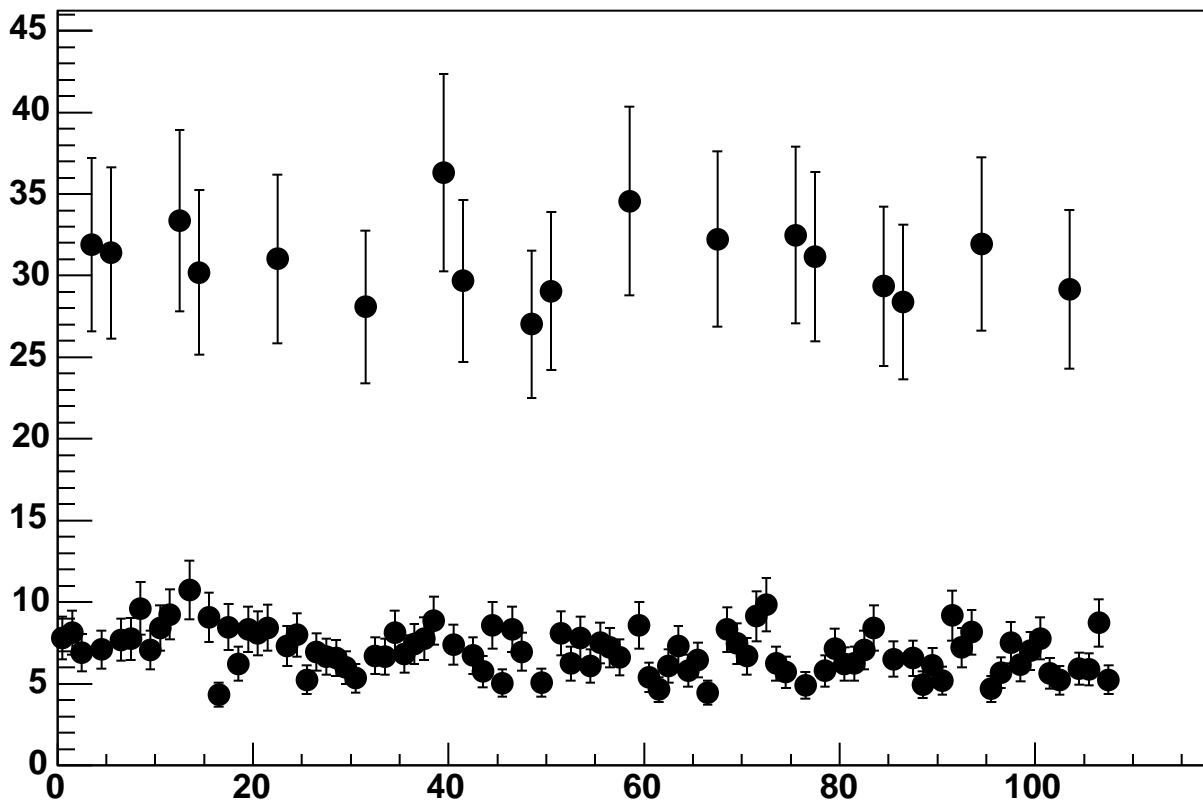




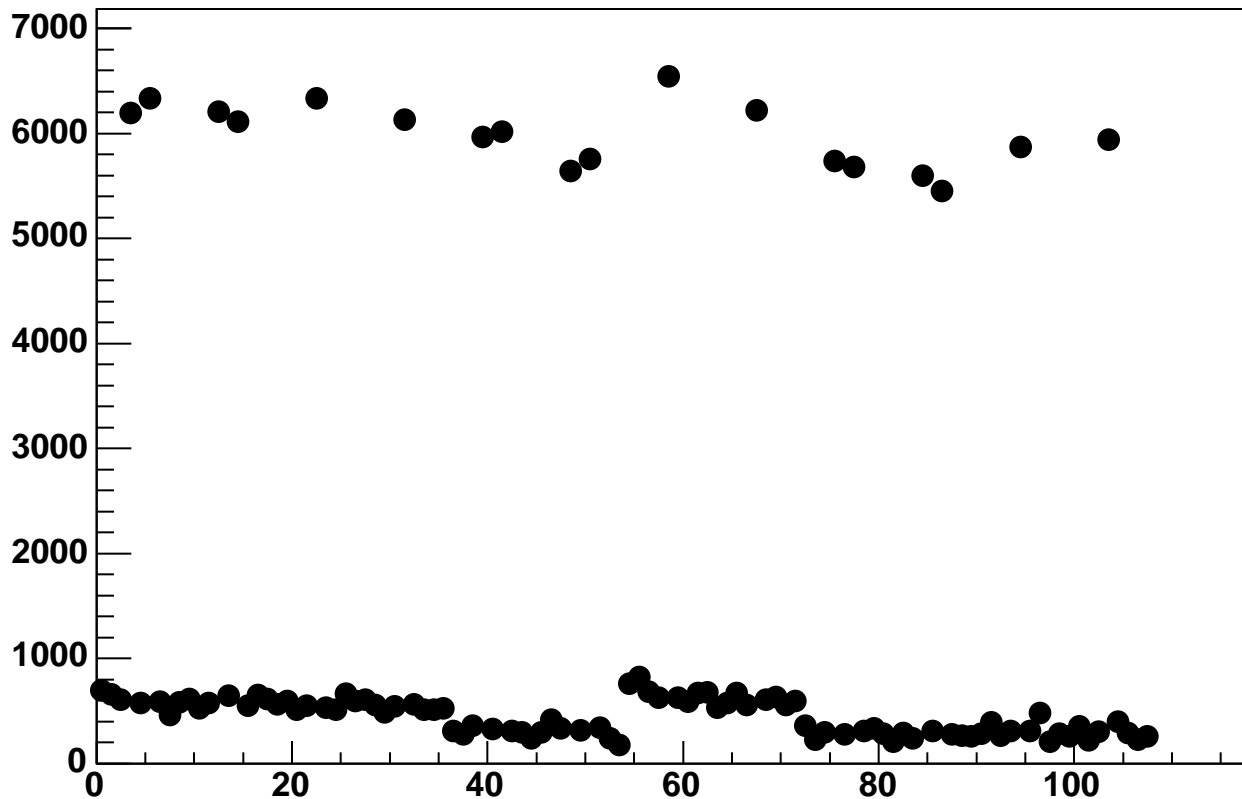
Enable 1, DAC=4000, Hold=75, ADC Mean vs 18\*Chip+Chan



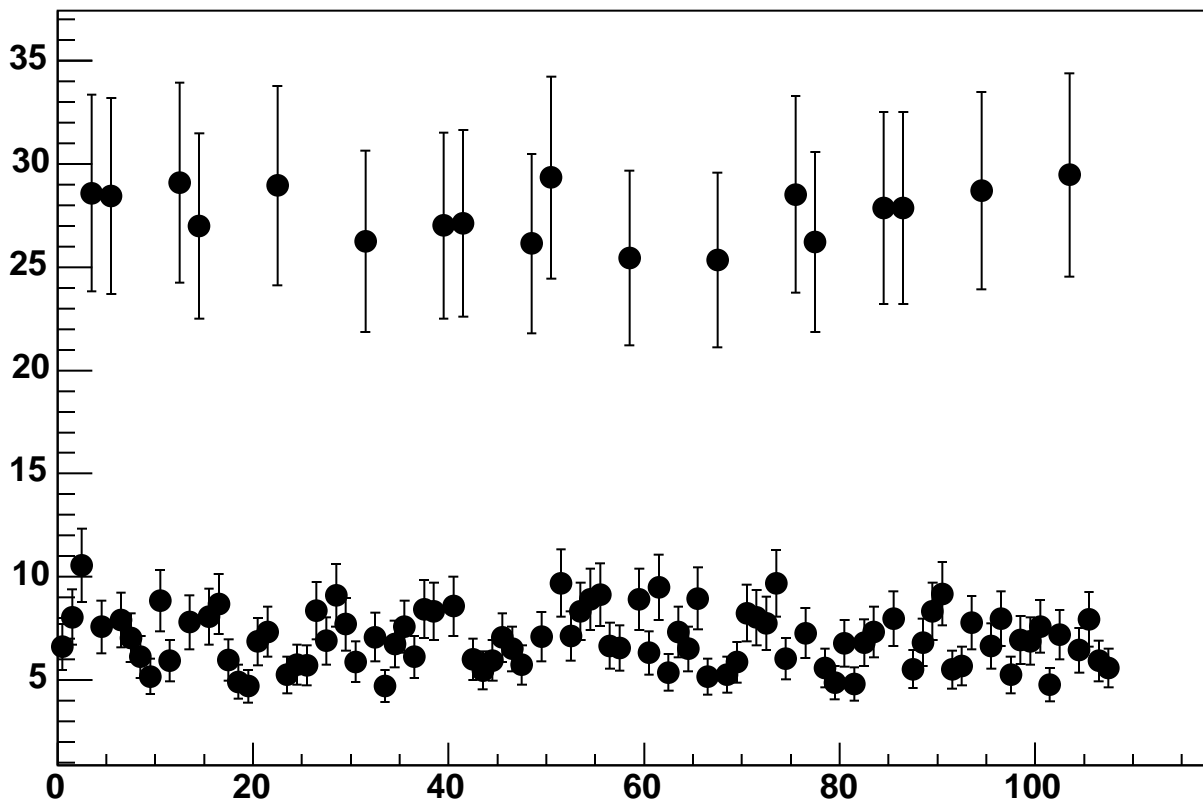
Enable 1, DAC=4000, Hold=75, ADC Noise vs 18\*Chip+Chan



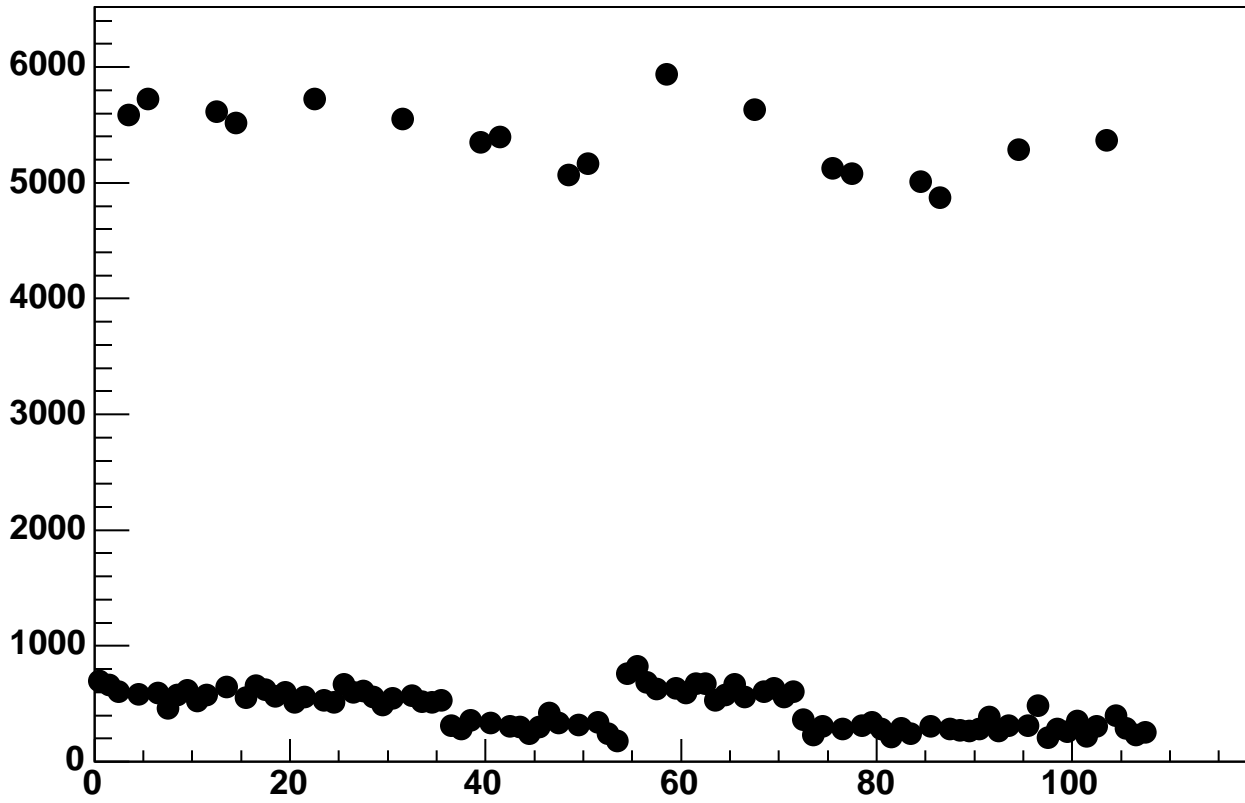
Enable 1, DAC=4000, Hold=80, ADC Mean vs 18\*Chip+Chan



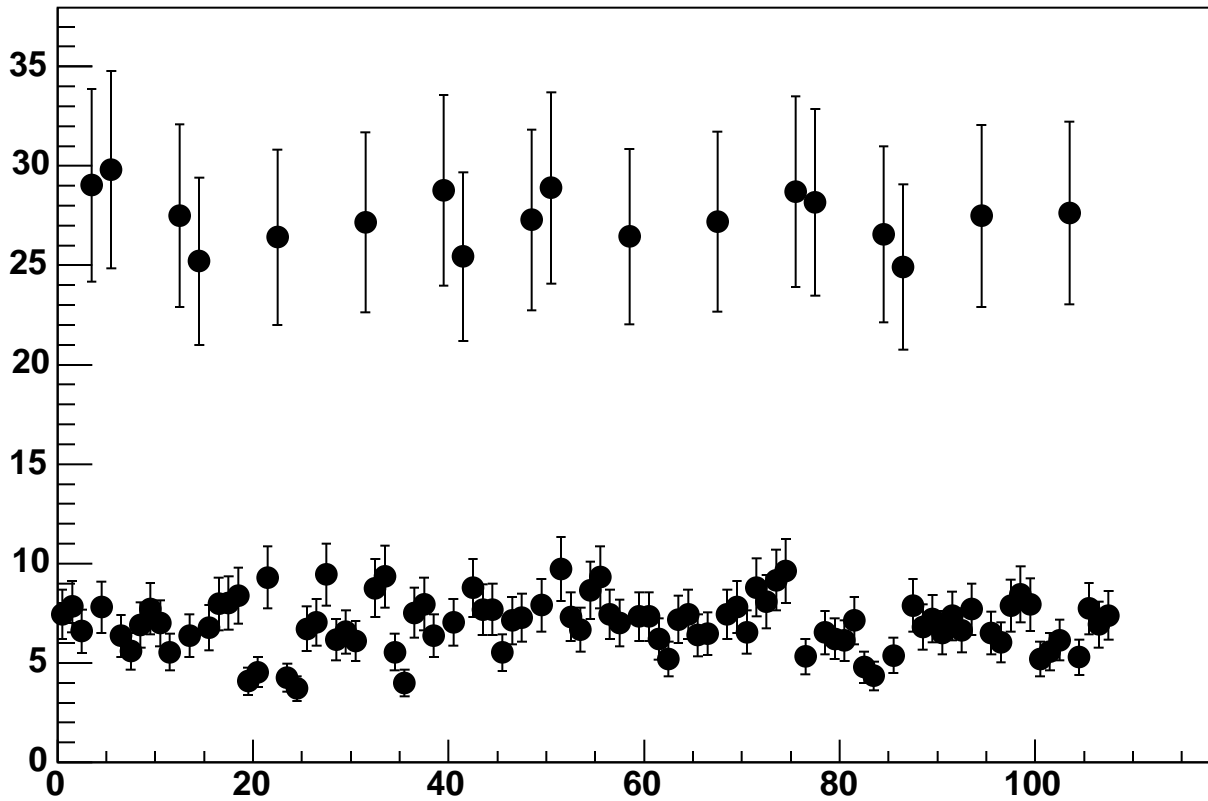
Enable 1, DAC=4000, Hold=80, ADC Noise vs 18\*Chip+Chan



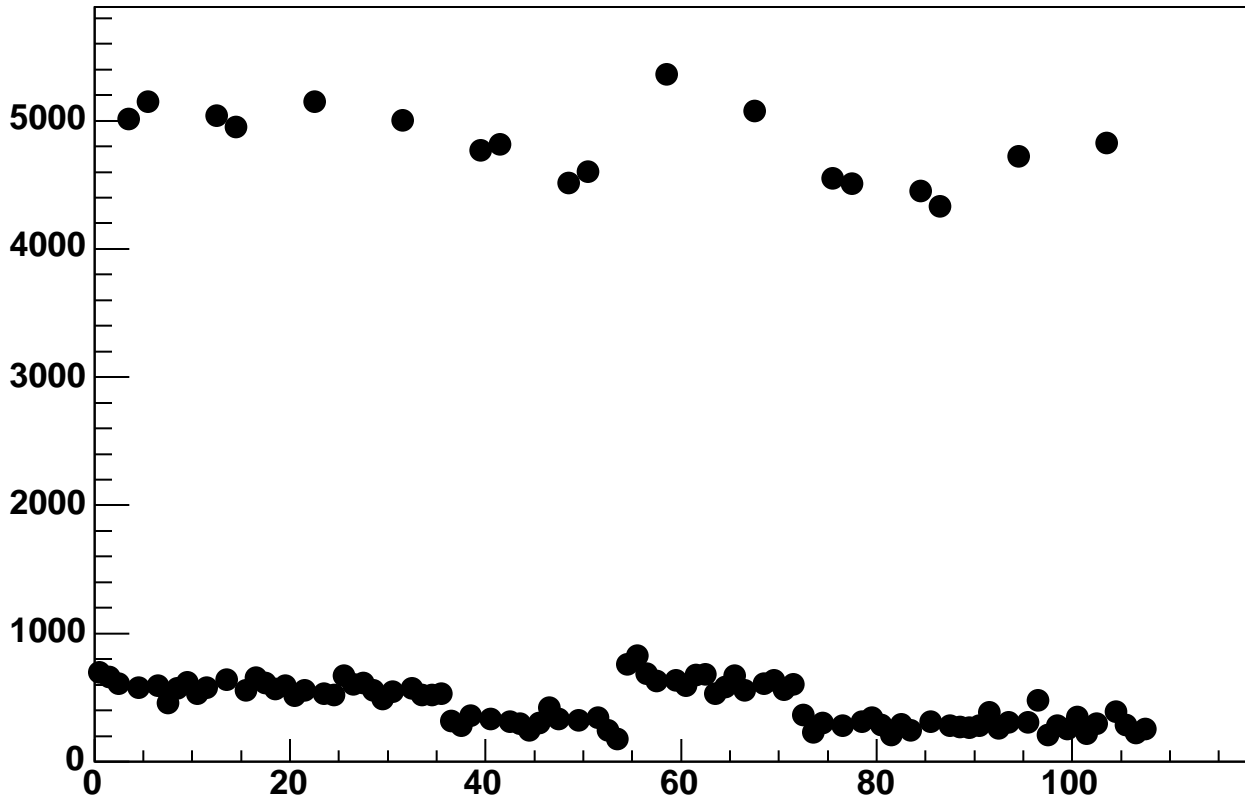
Enable 1, DAC=4000, Hold=85, ADC Mean vs 18\*Chip+Chan



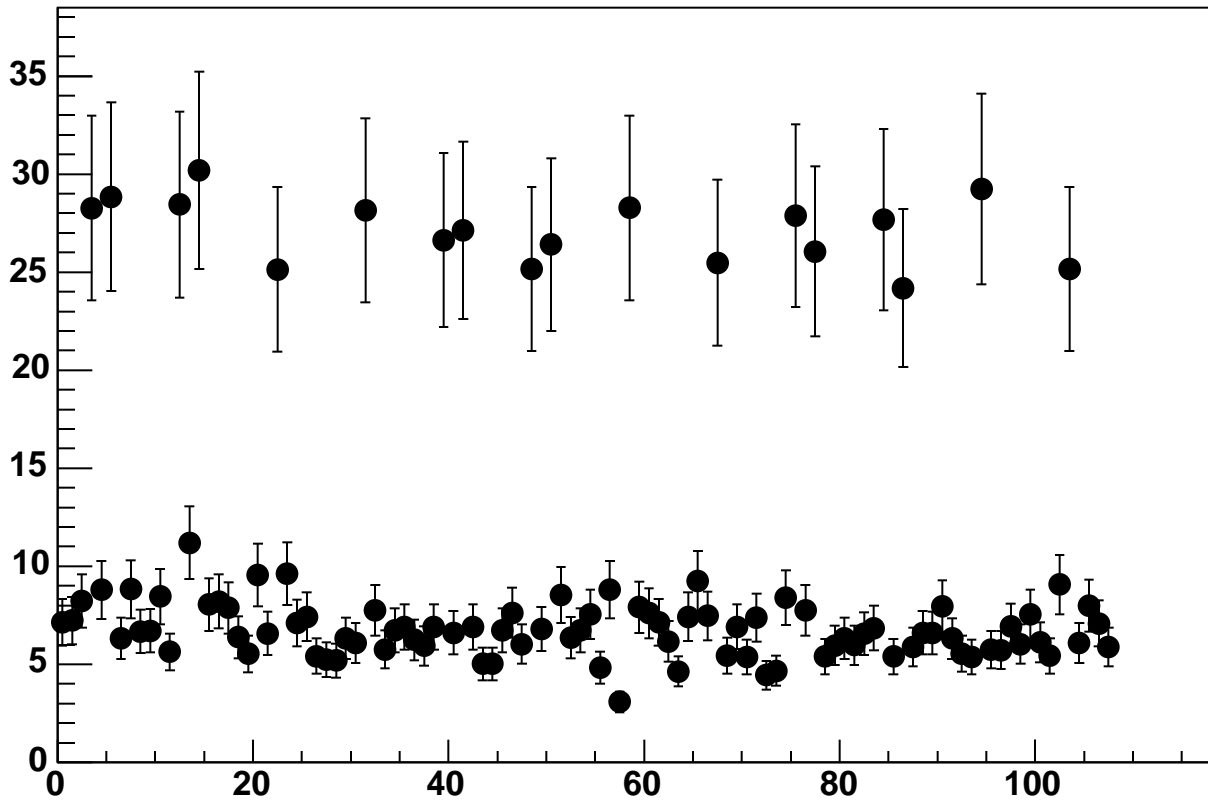
Enable 1, DAC=4000, Hold=85, ADC Noise vs 18\*Chip+Chan



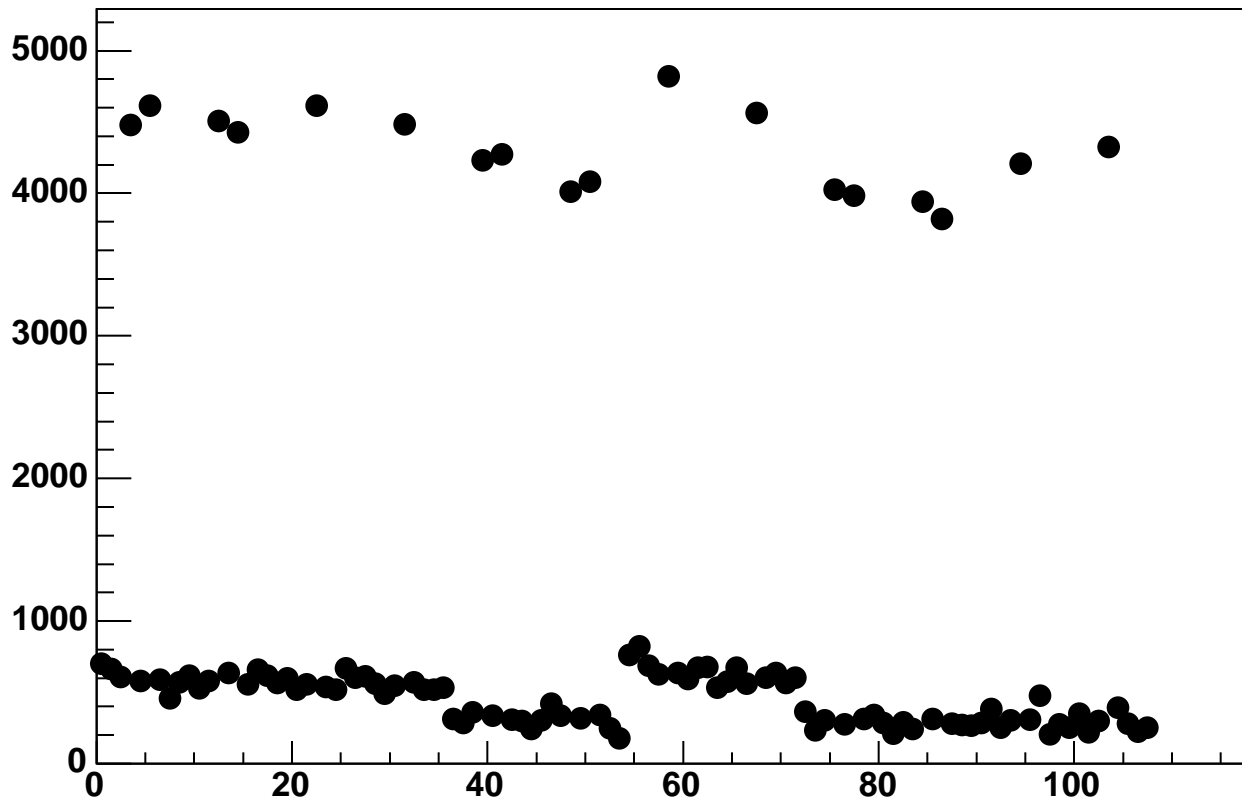
Enable 1, DAC=4000, Hold=90, ADC Mean vs 18\*Chip+Chan



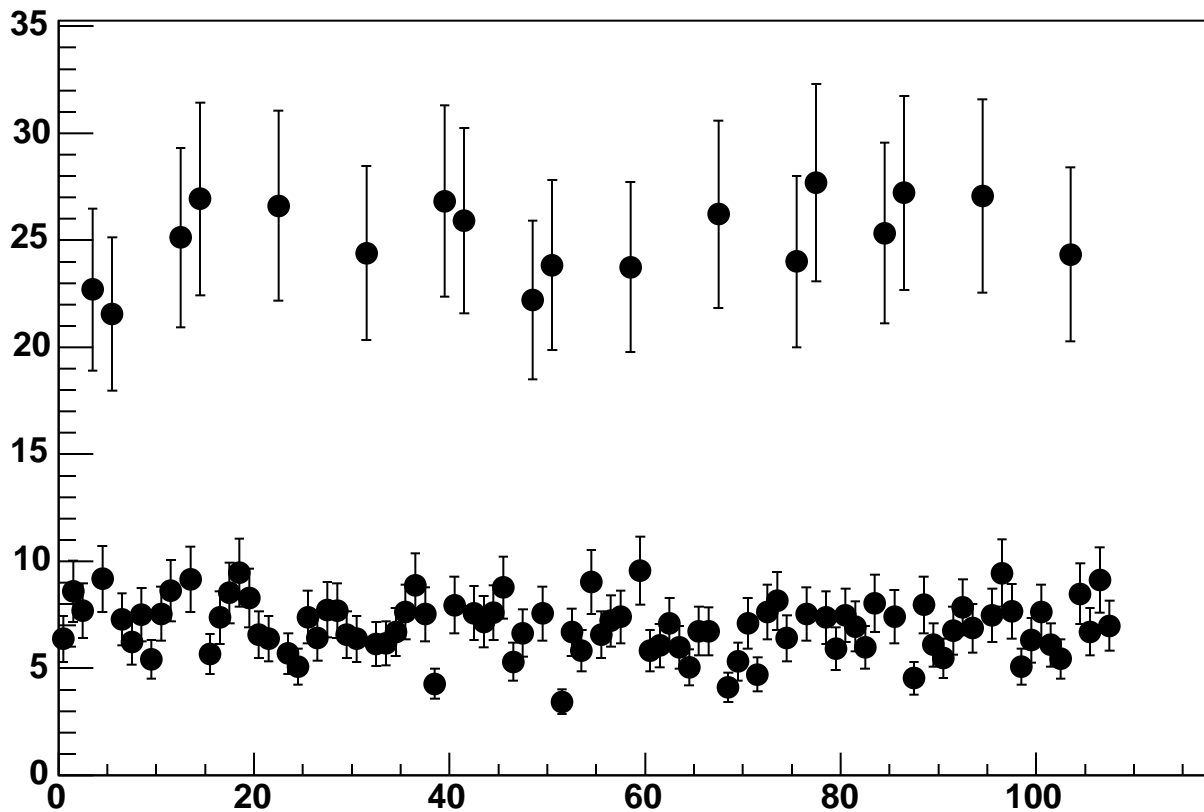
Enable 1, DAC=4000, Hold=90, ADC Noise vs 18\*Chip+Chan



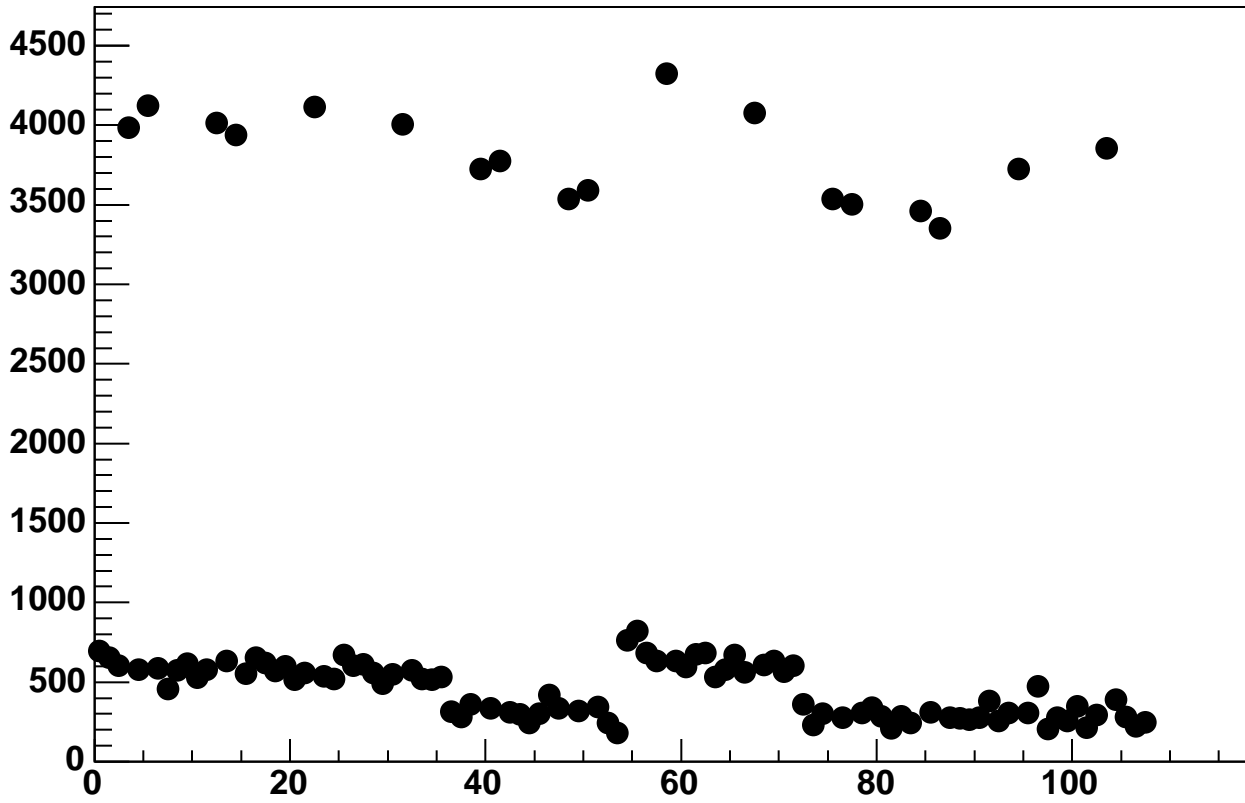
Enable 1, DAC=4000, Hold=95, ADC Mean vs 18\*Chip+Chan



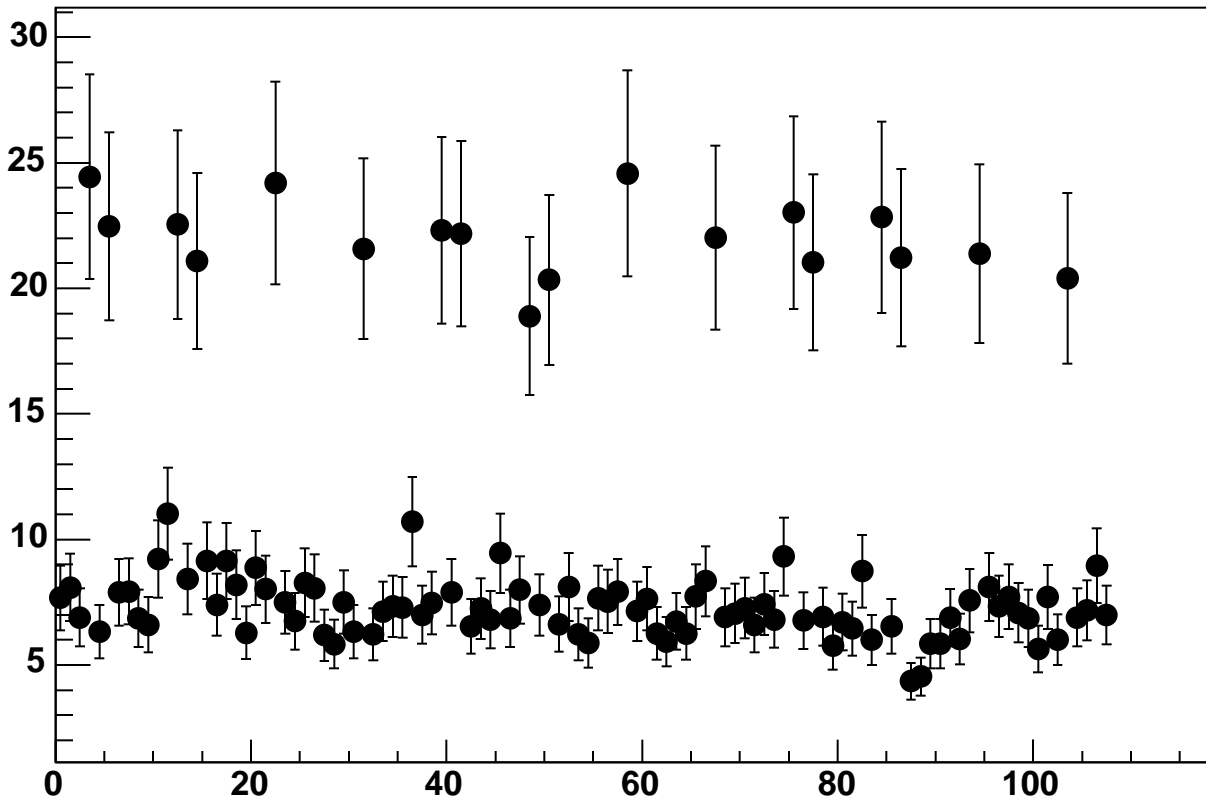
Enable 1, DAC=4000, Hold=95, ADC Noise vs 18\*Chip+Chan



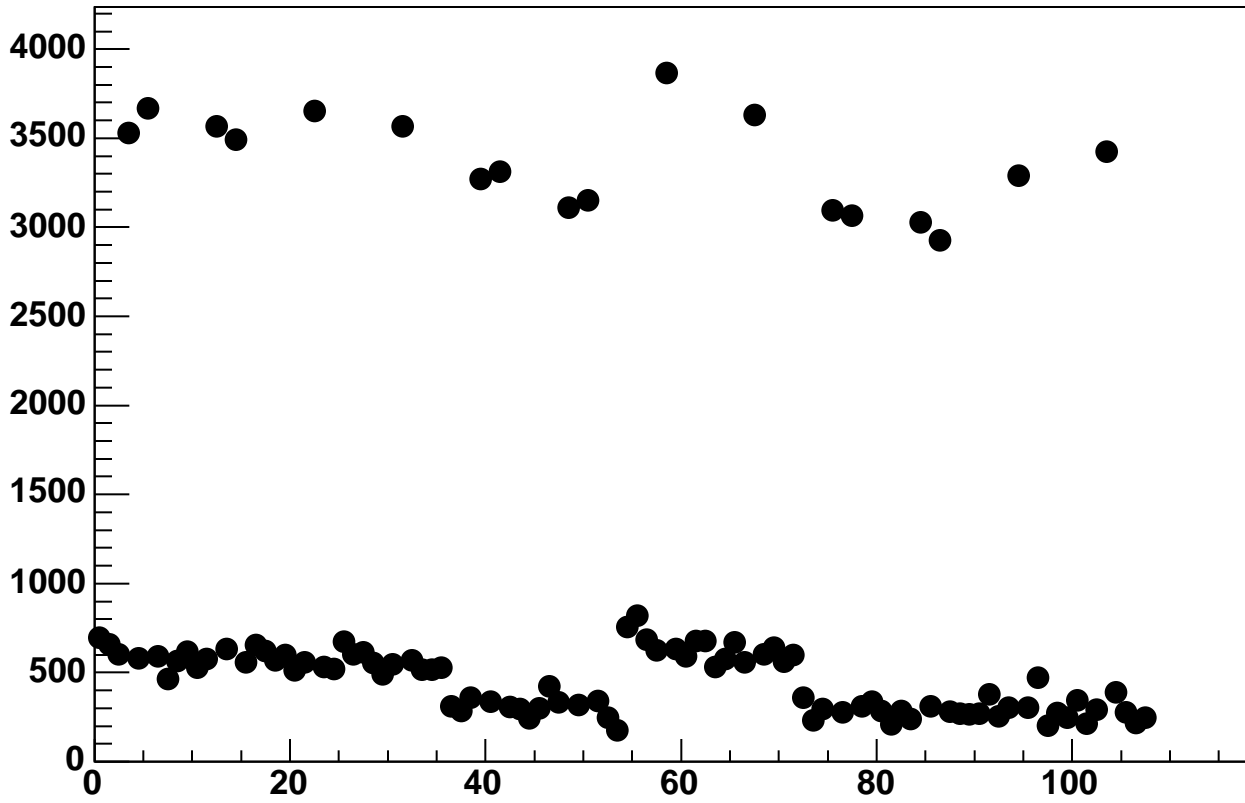
Enable 1, DAC=4000, Hold=100, ADC Mean vs 18\*Chip+Chan



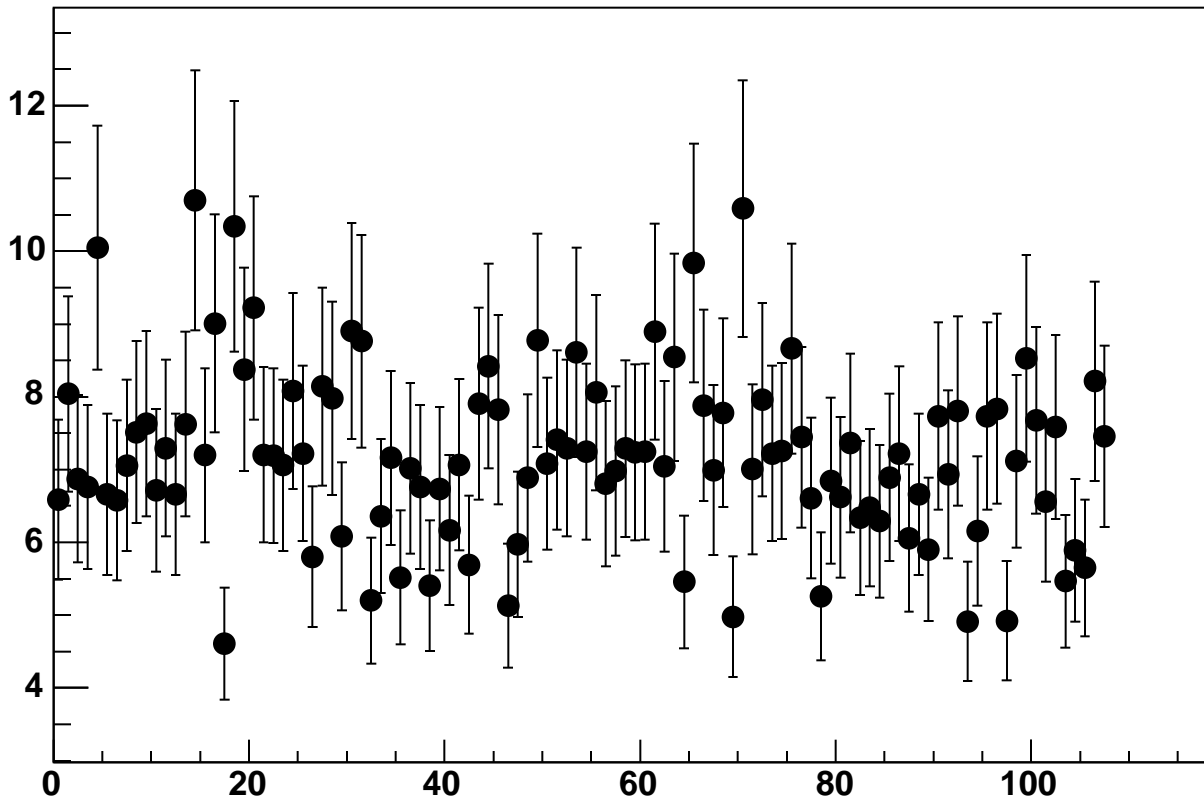
Enable 1, DAC=4000, Hold=100, ADC Noise vs 18\*Chip+Chan



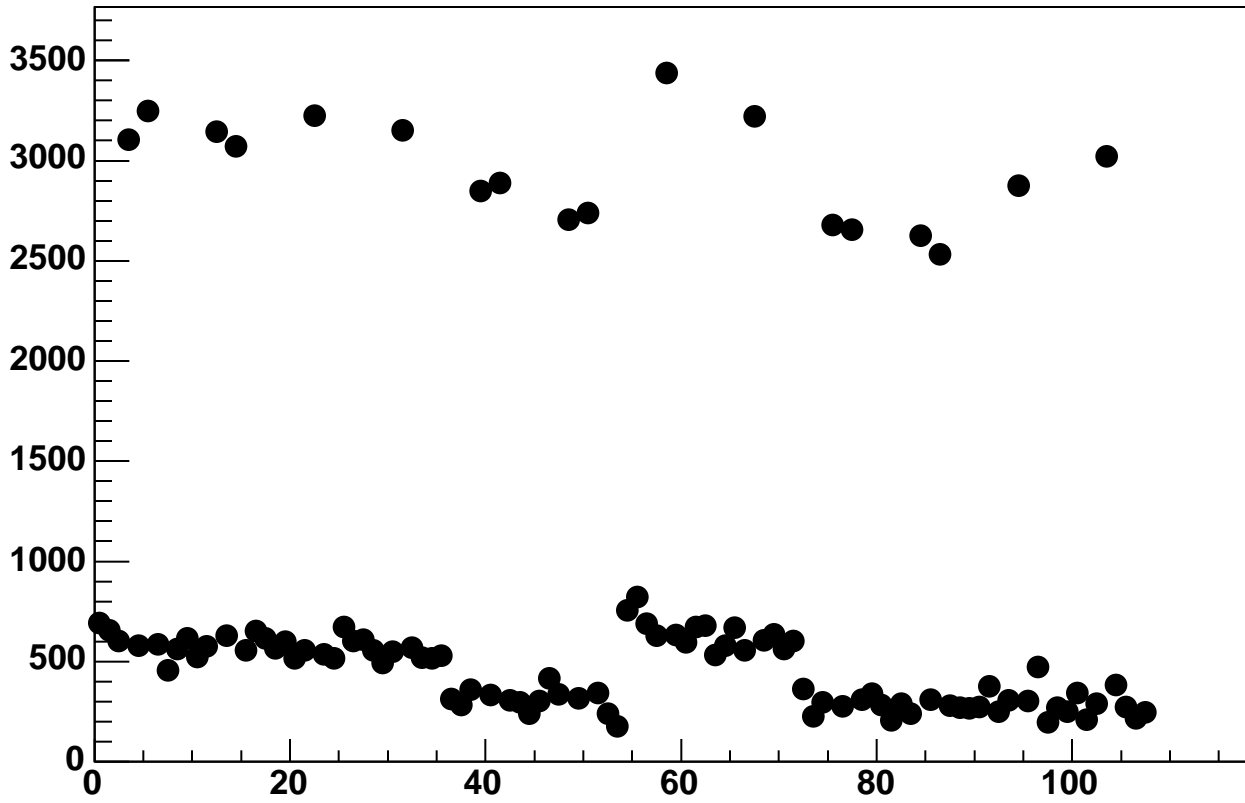
Enable 1, DAC=4000, Hold=105, ADC Mean vs 18\*Chip+Chan



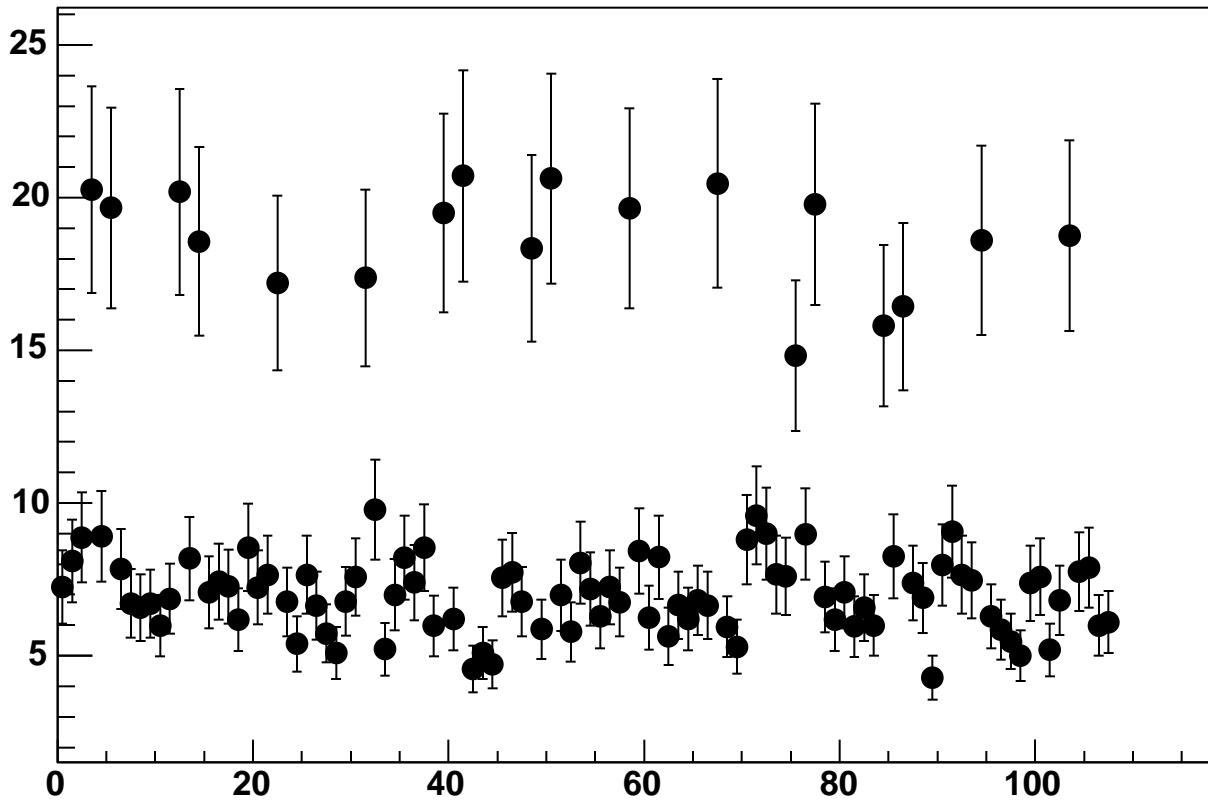
Enable 1, DAC=4000, Hold=105, ADC Noise vs 18\*Chip+Chan



Enable 1, DAC=4000, Hold=110, ADC Mean vs 18\*Chip+Chan

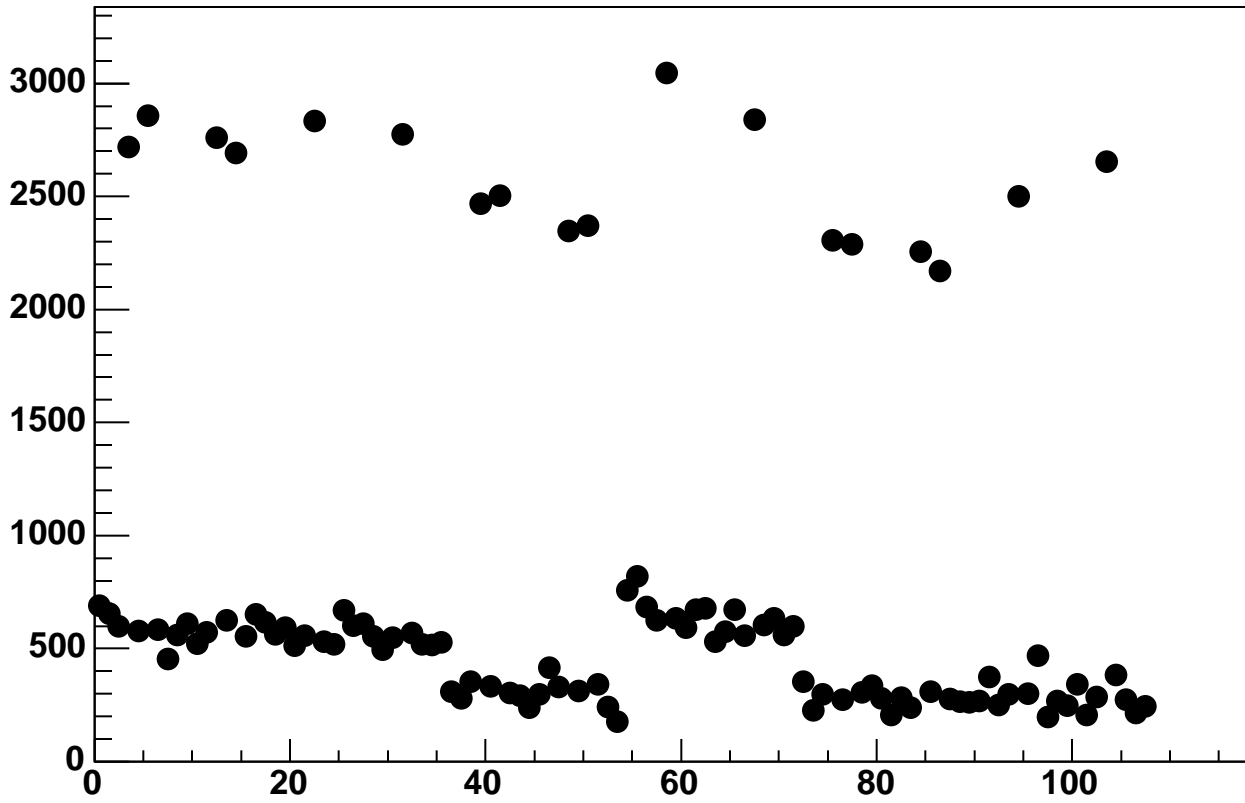


Enable 1, DAC=4000, Hold=110, ADC Noise vs 18\*Chip+Chan

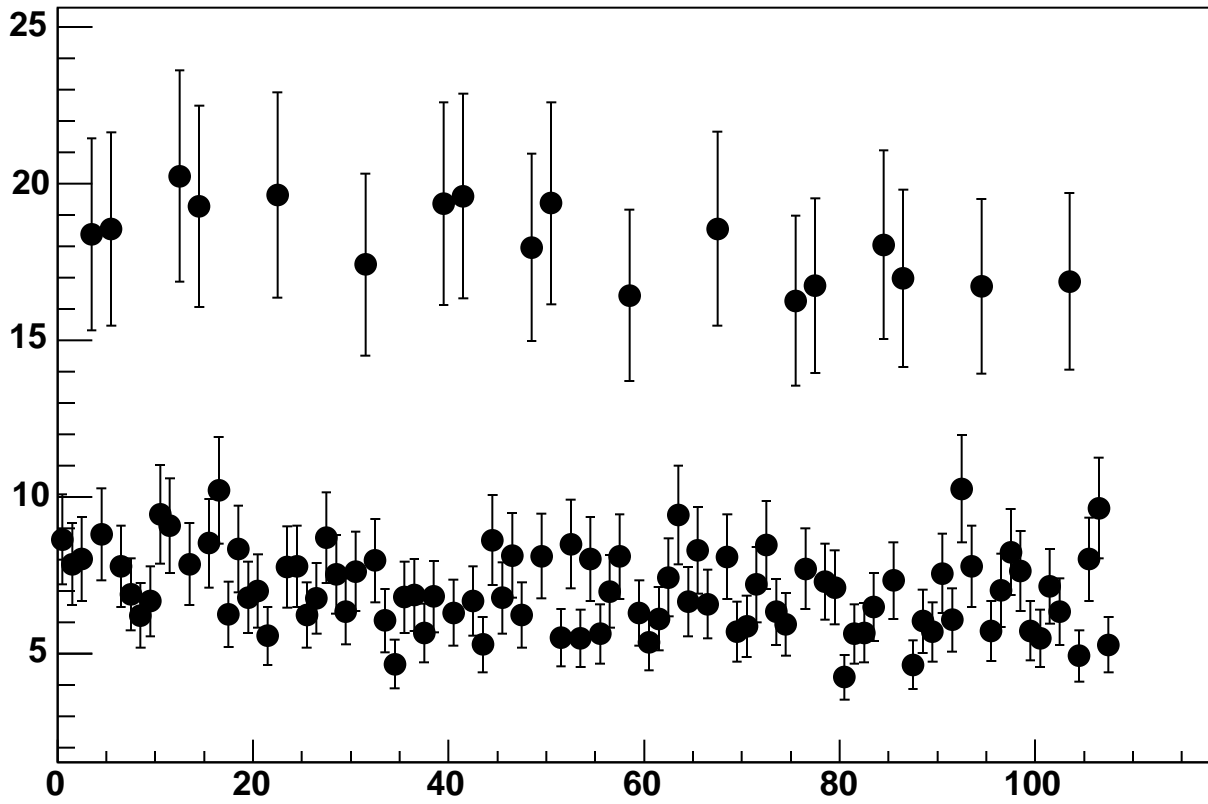




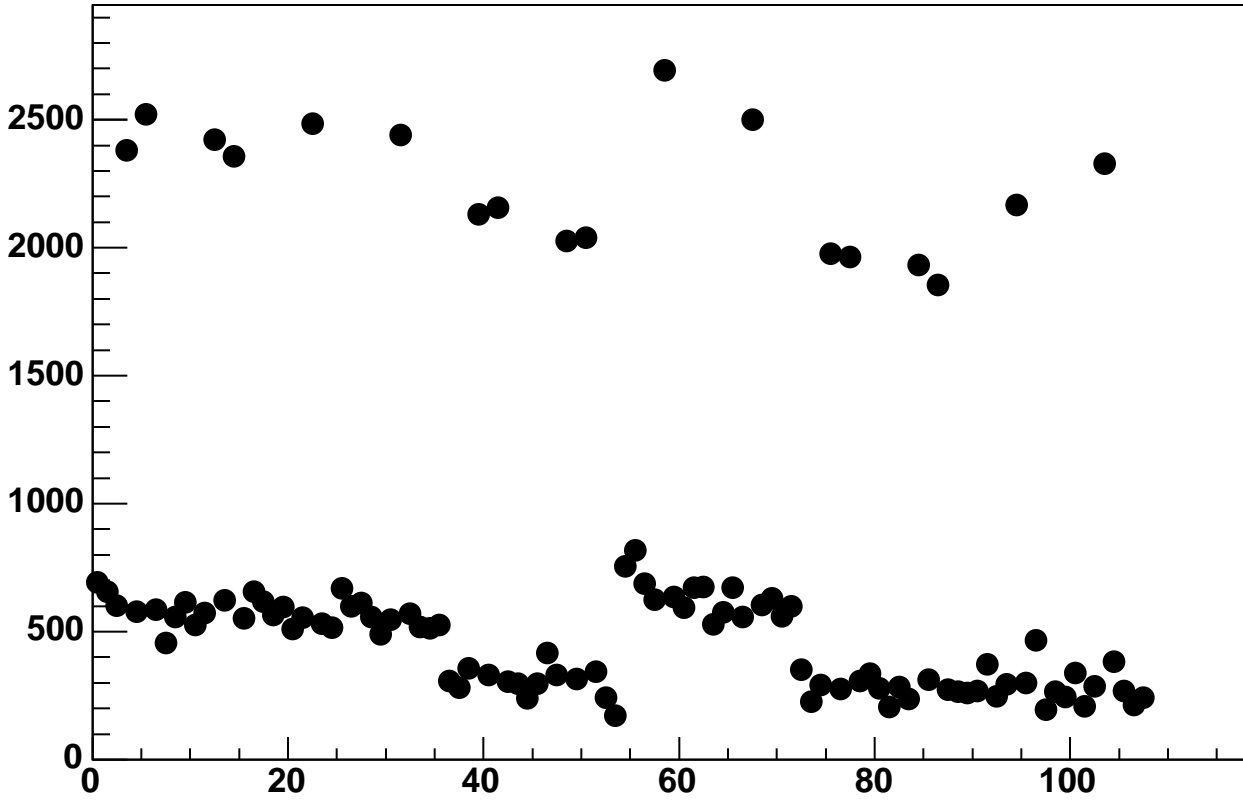
Enable 1, DAC=4000, Hold=115, ADC Mean vs 18\*Chip+Chan



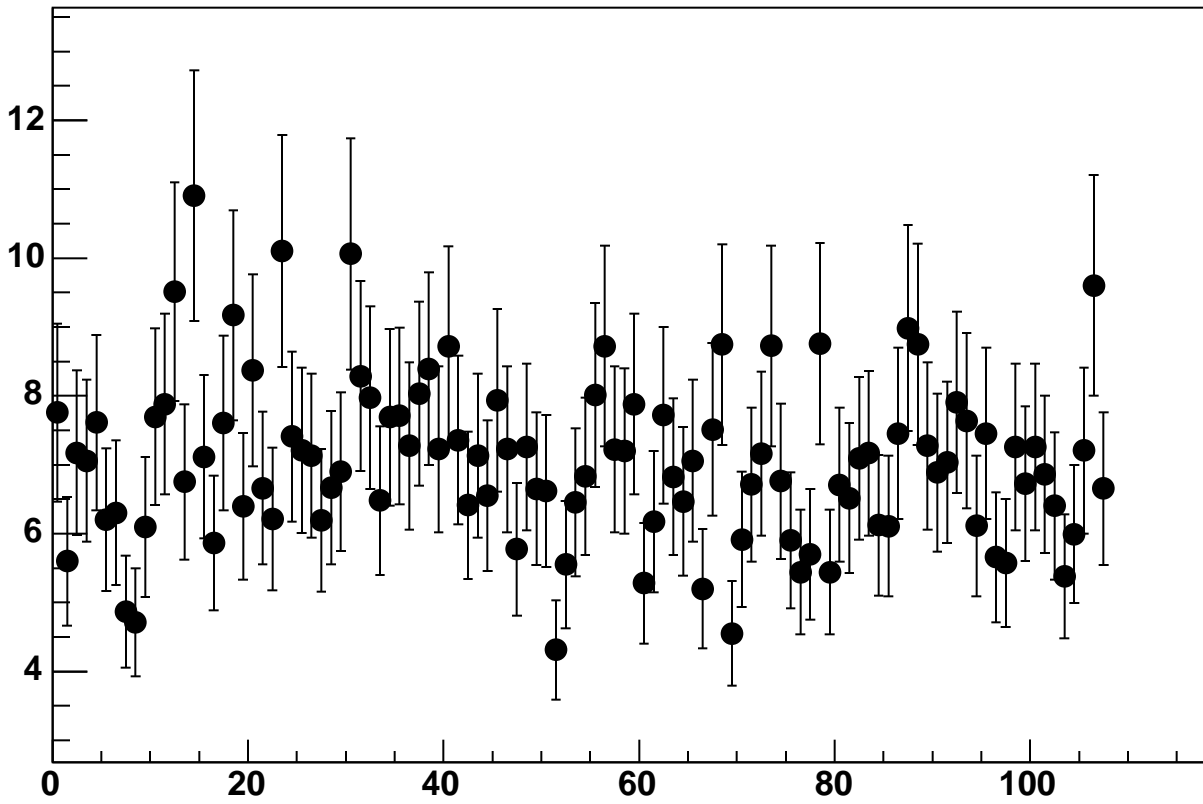
Enable 1, DAC=4000, Hold=115, ADC Noise vs 18\*Chip+Chan



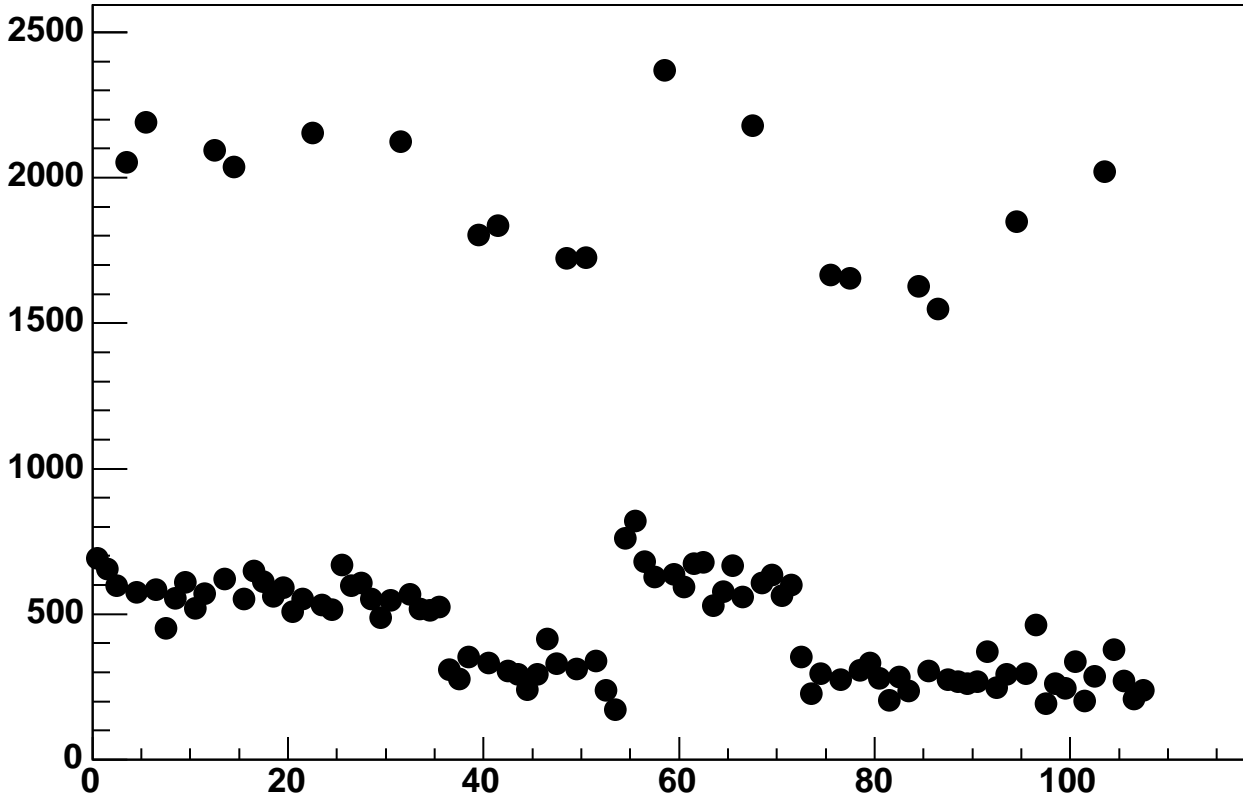
Enable 1, DAC=4000, Hold=120, ADC Mean vs 18\*Chip+Chan



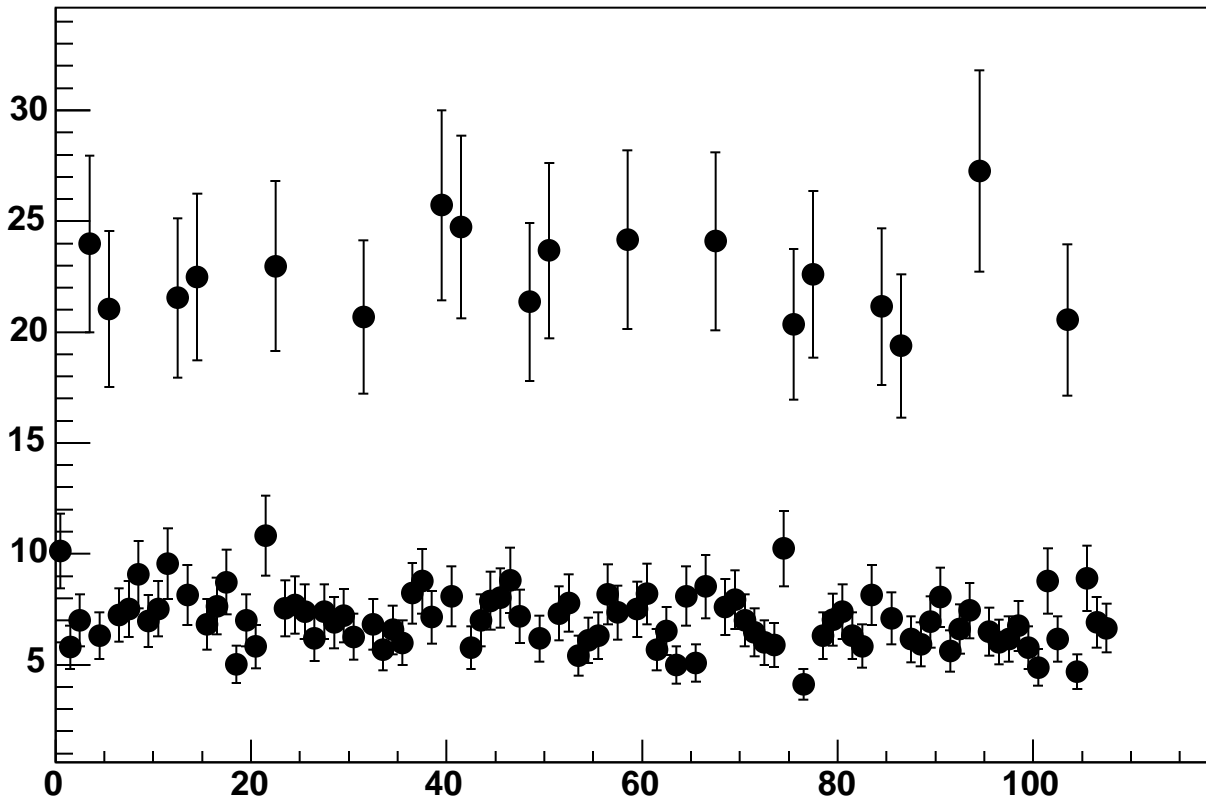
Enable 1, DAC=4000, Hold=120, ADC Noise vs 18\*Chip+Chan



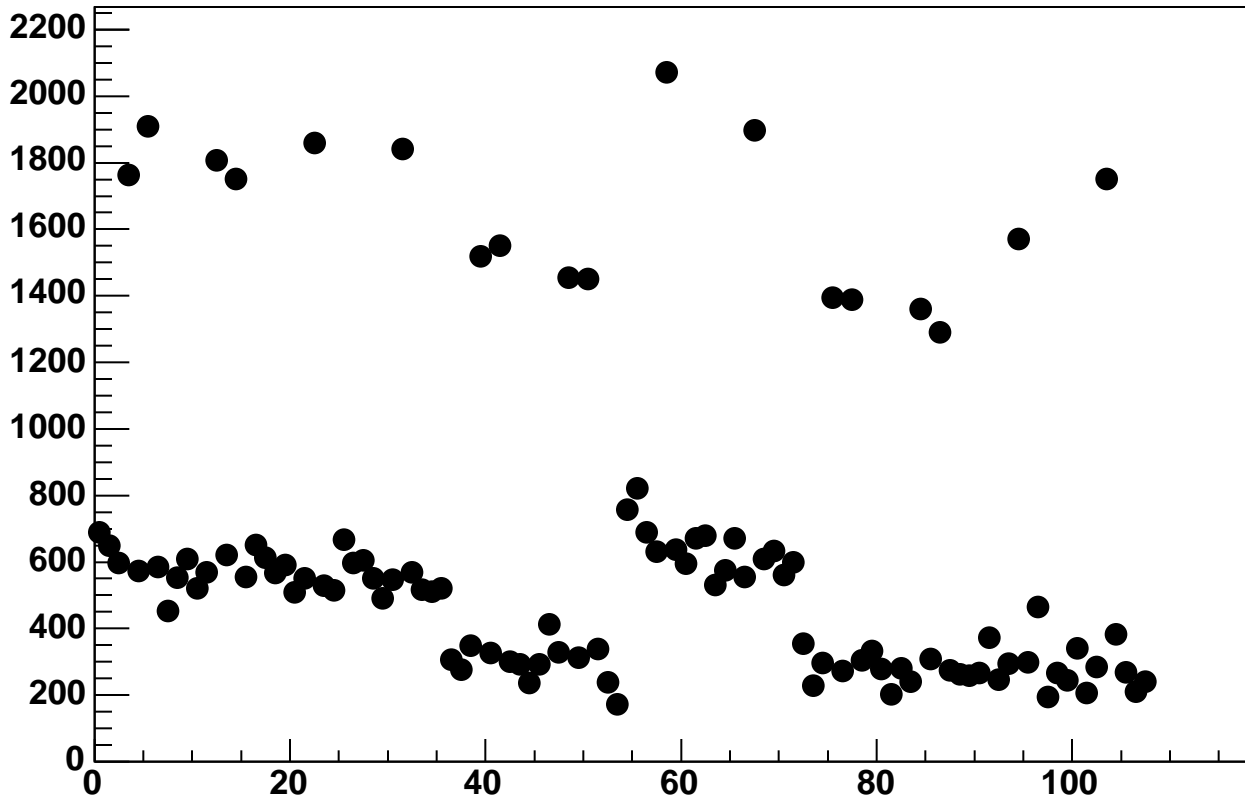
Enable 1, DAC=4000, Hold=125, ADC Mean vs 18\*Chip+Chan



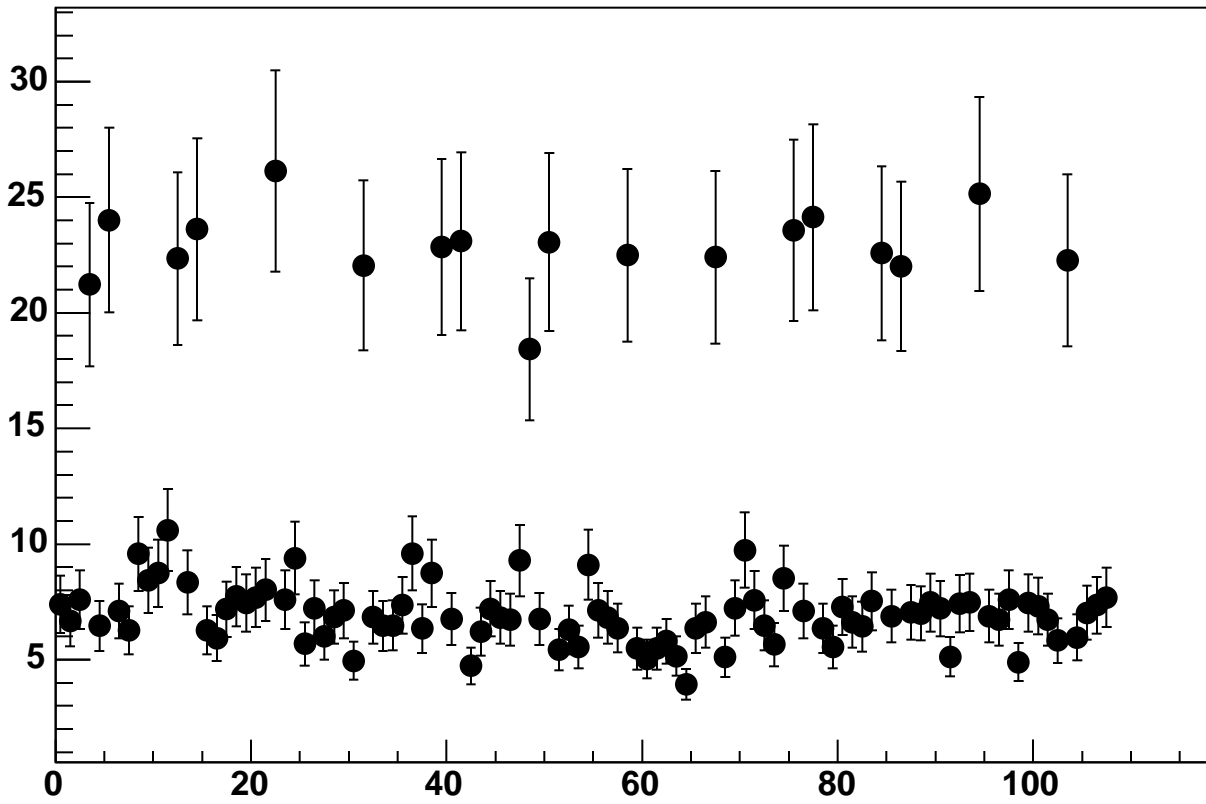
Enable 1, DAC=4000, Hold=125, ADC Noise vs 18\*Chip+Chan



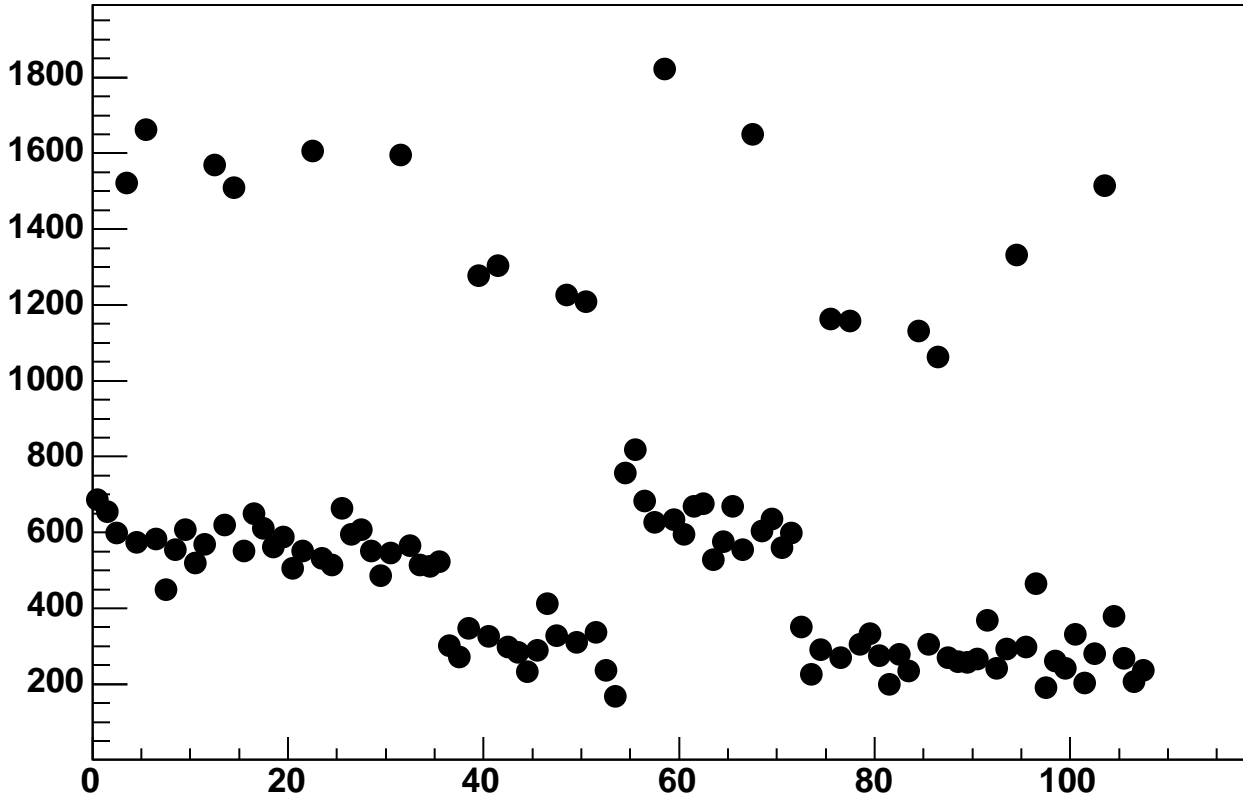
Enable 1, DAC=4000, Hold=130, ADC Mean vs 18\*Chip+Chan



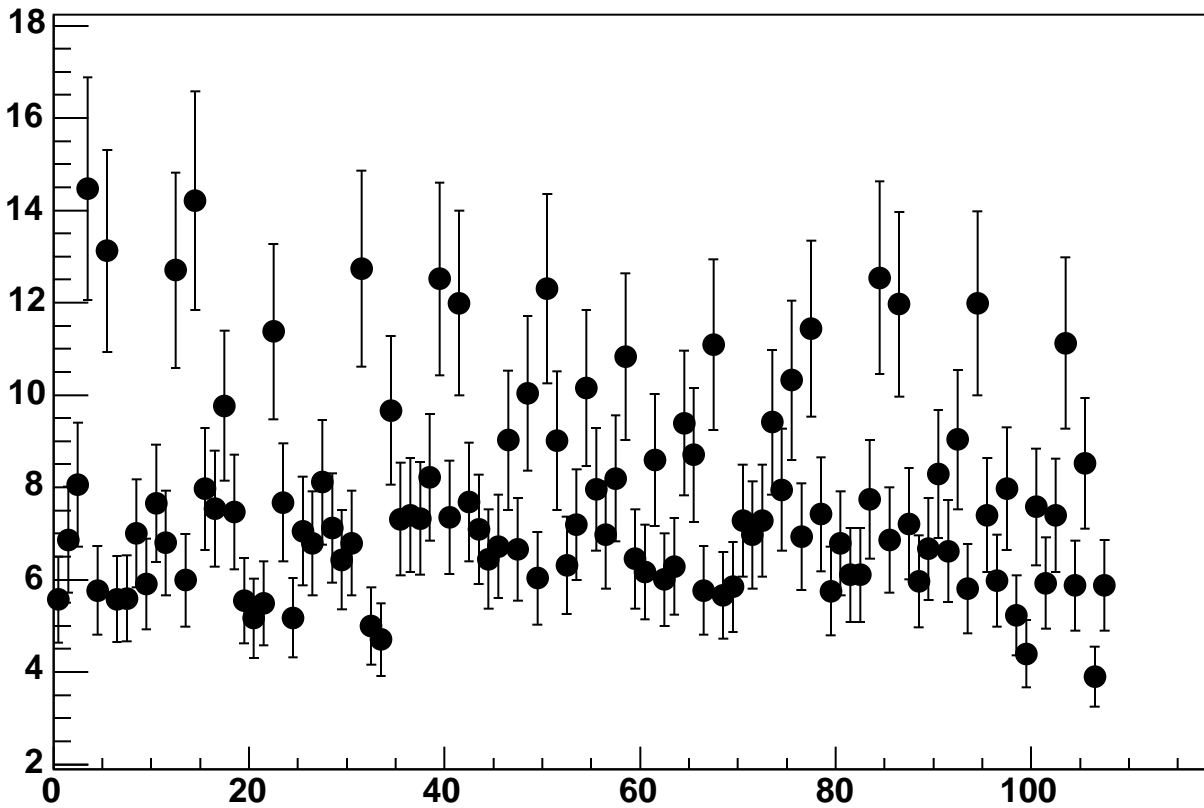
Enable 1, DAC=4000, Hold=130, ADC Noise vs 18\*Chip+Chan



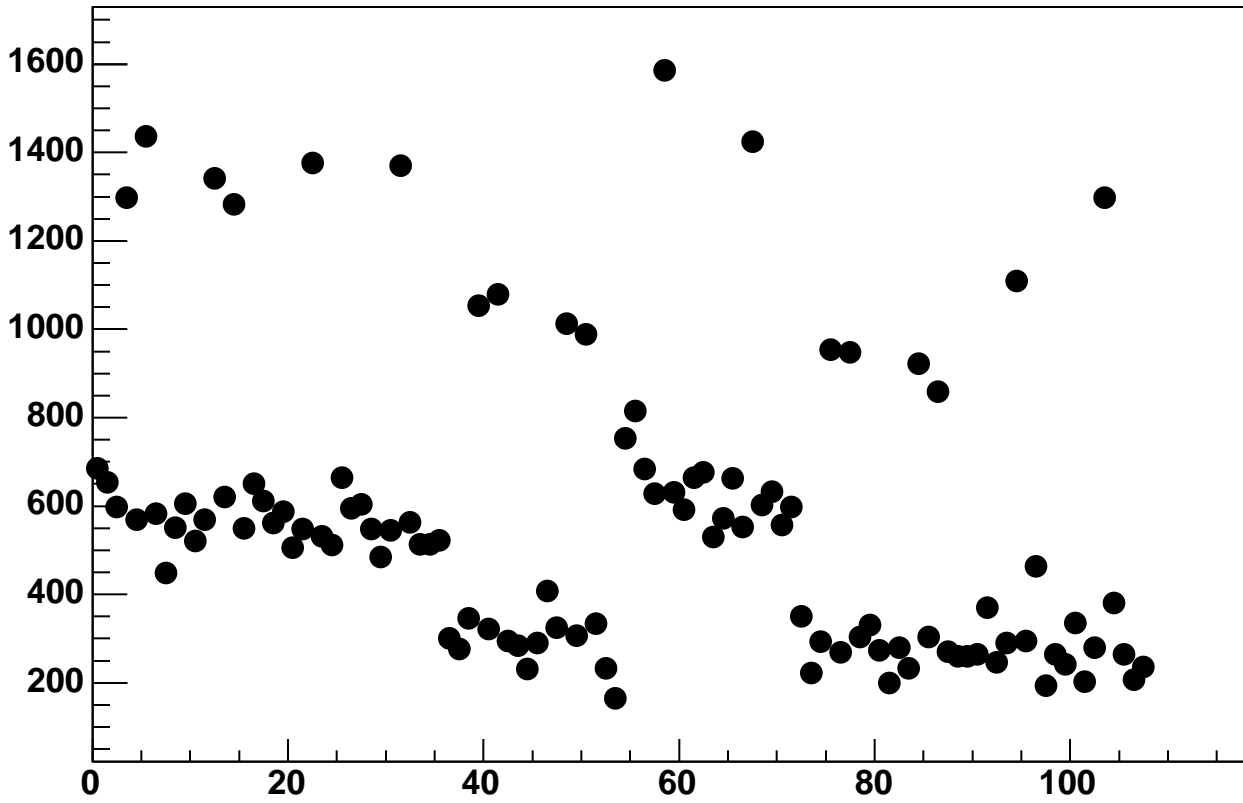
Enable 1, DAC=4000, Hold=135, ADC Mean vs 18\*Chip+Chan



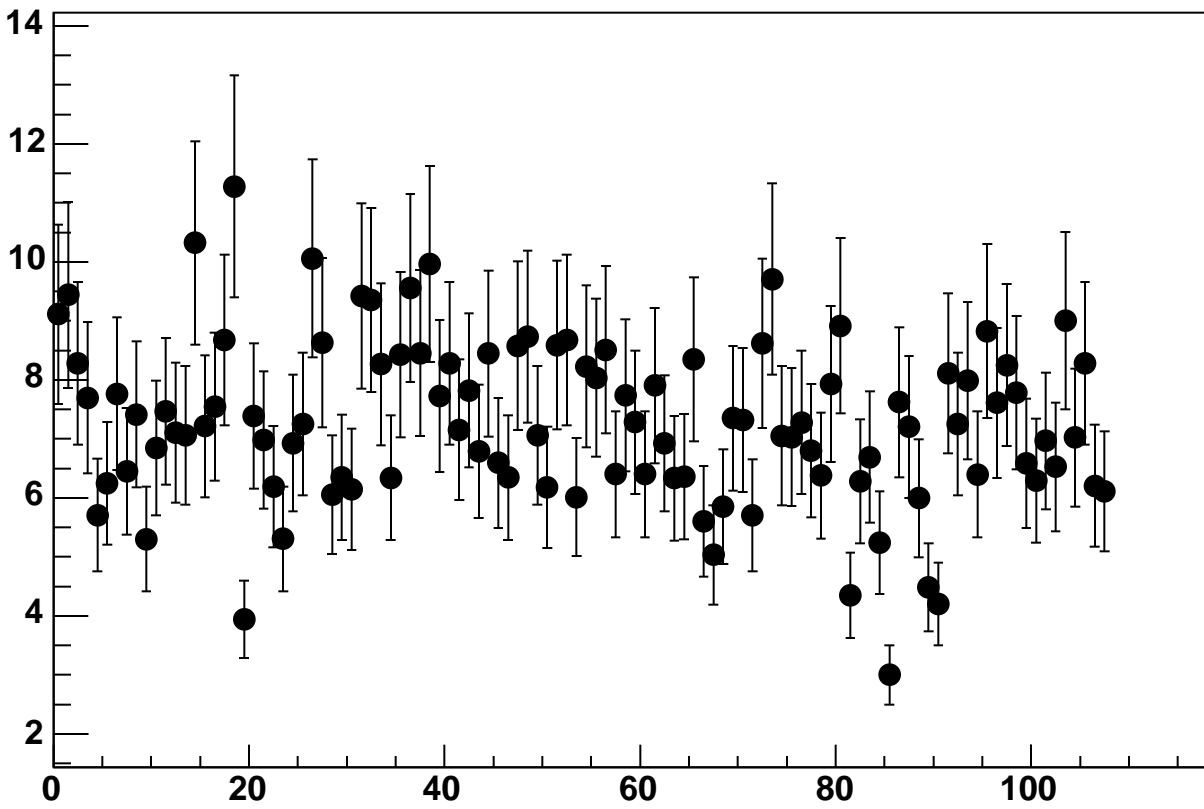
Enable 1, DAC=4000, Hold=135, ADC Noise vs 18\*Chip+Chan



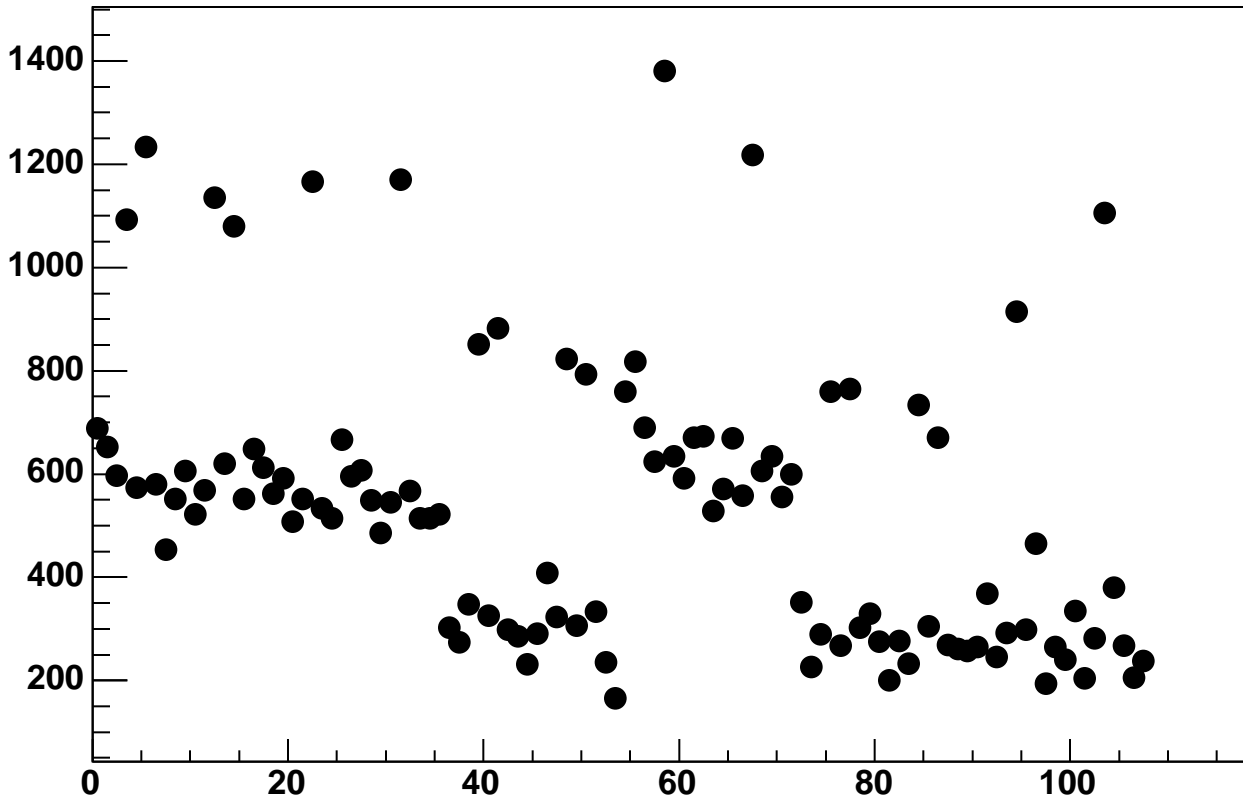
Enable 1, DAC=4000, Hold=140, ADC Mean vs 18\*Chip+Chan



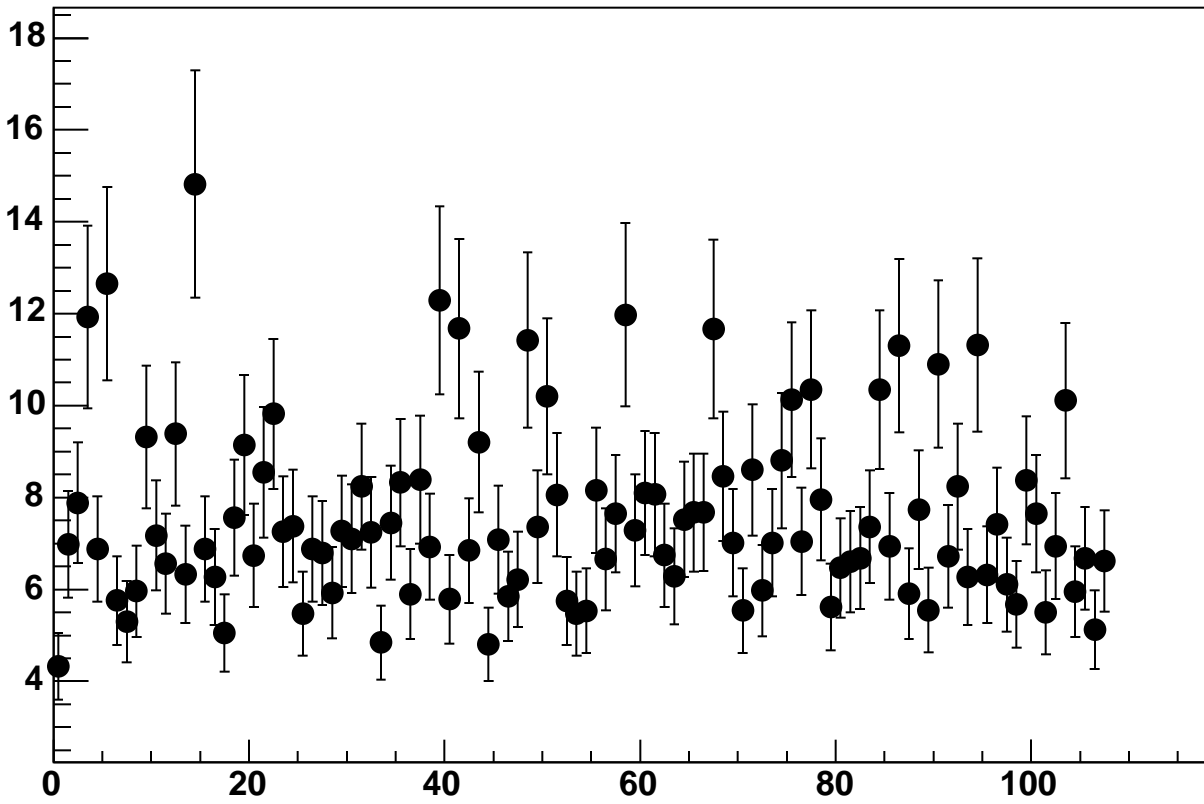
Enable 1, DAC=4000, Hold=140, ADC Noise vs 18\*Chip+Chan



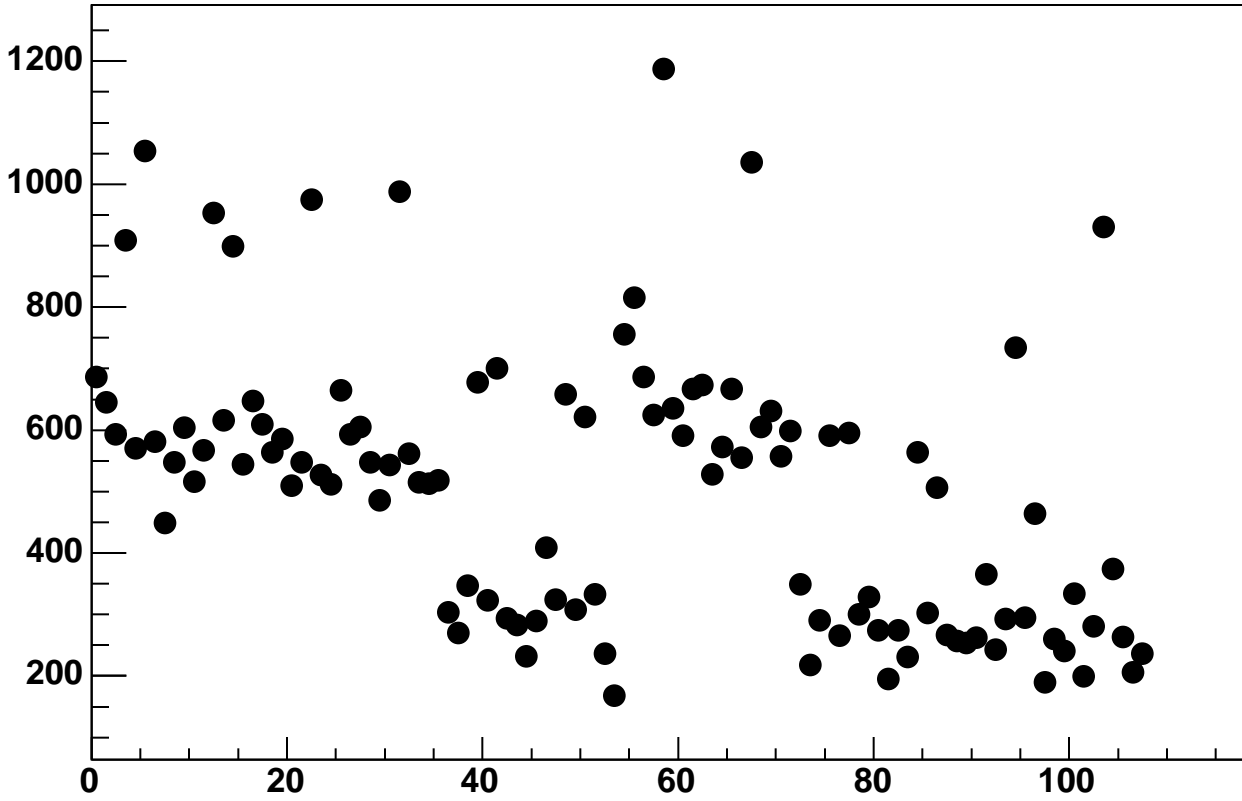
Enable 1, DAC=4000, Hold=145, ADC Mean vs 18\*Chip+Chan



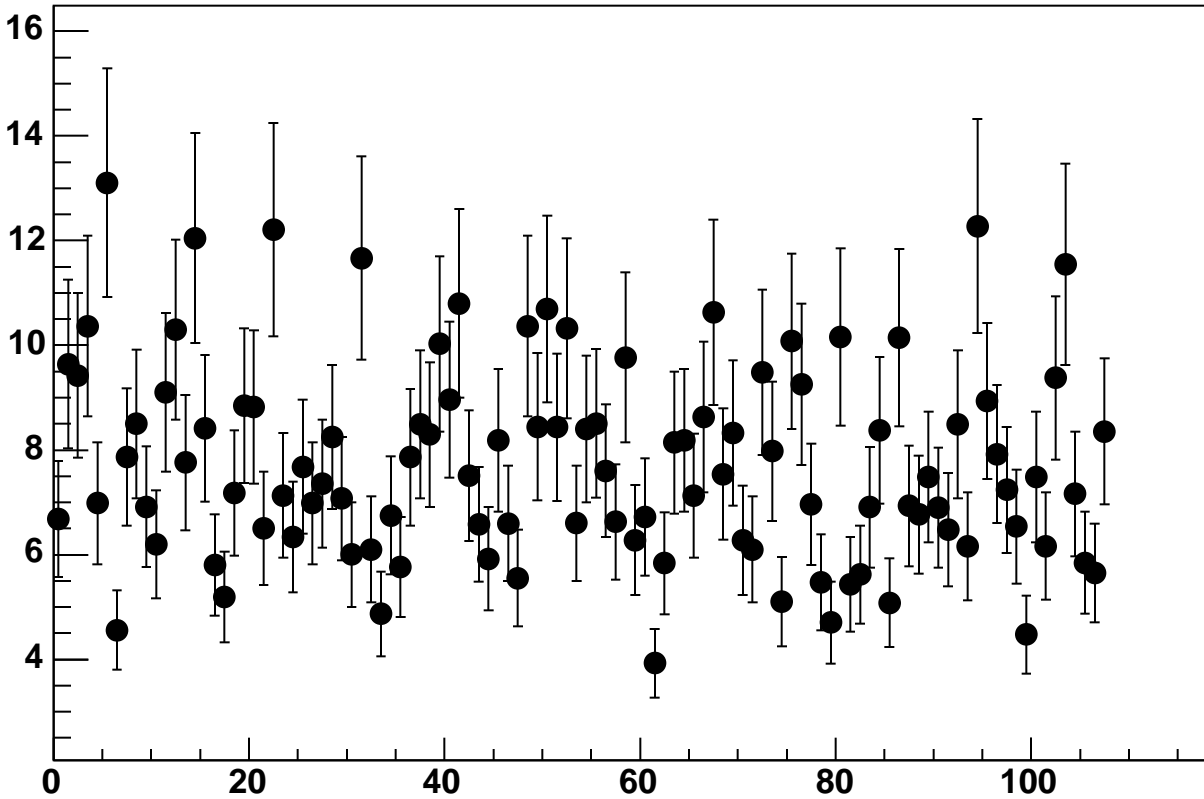
Enable 1, DAC=4000, Hold=145, ADC Noise vs 18\*Chip+Chan



Enable 1, DAC=4000, Hold=150, ADC Mean vs 18\*Chip+Chan

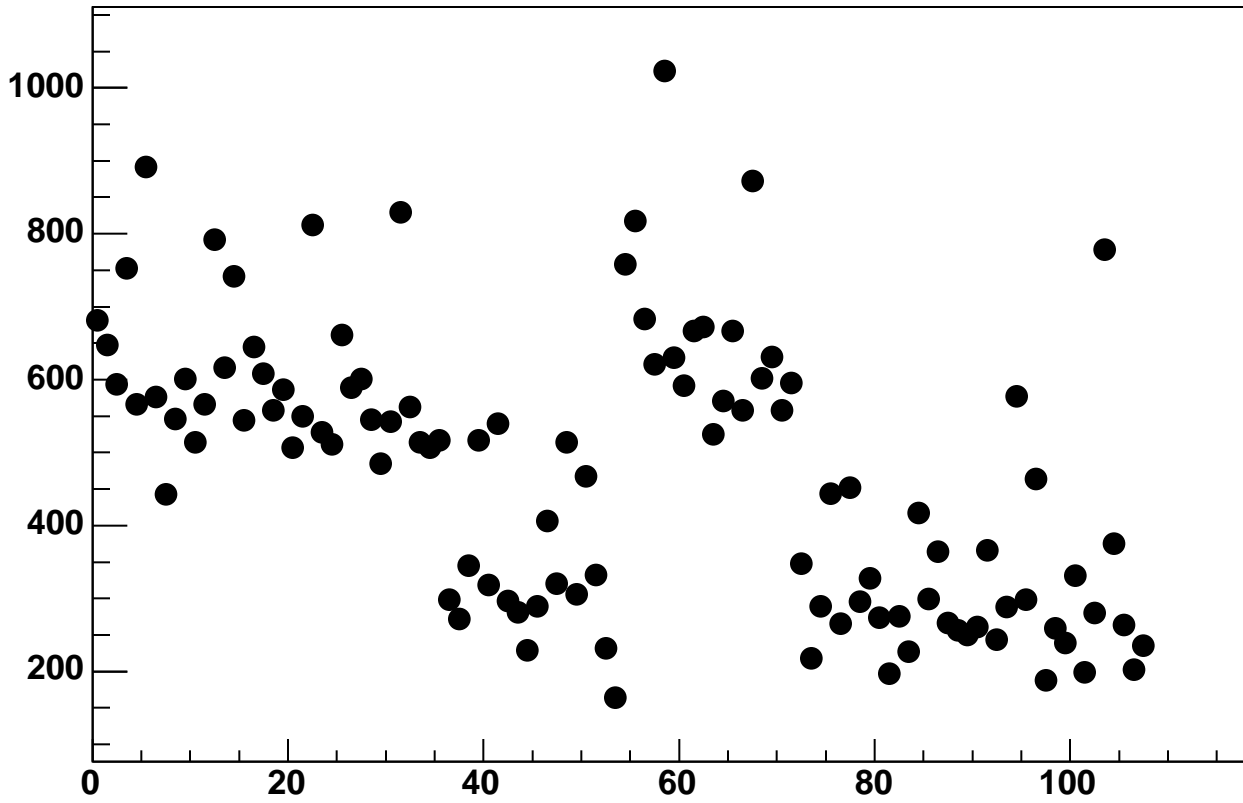


Enable 1, DAC=4000, Hold=150, ADC Noise vs 18\*Chip+Chan

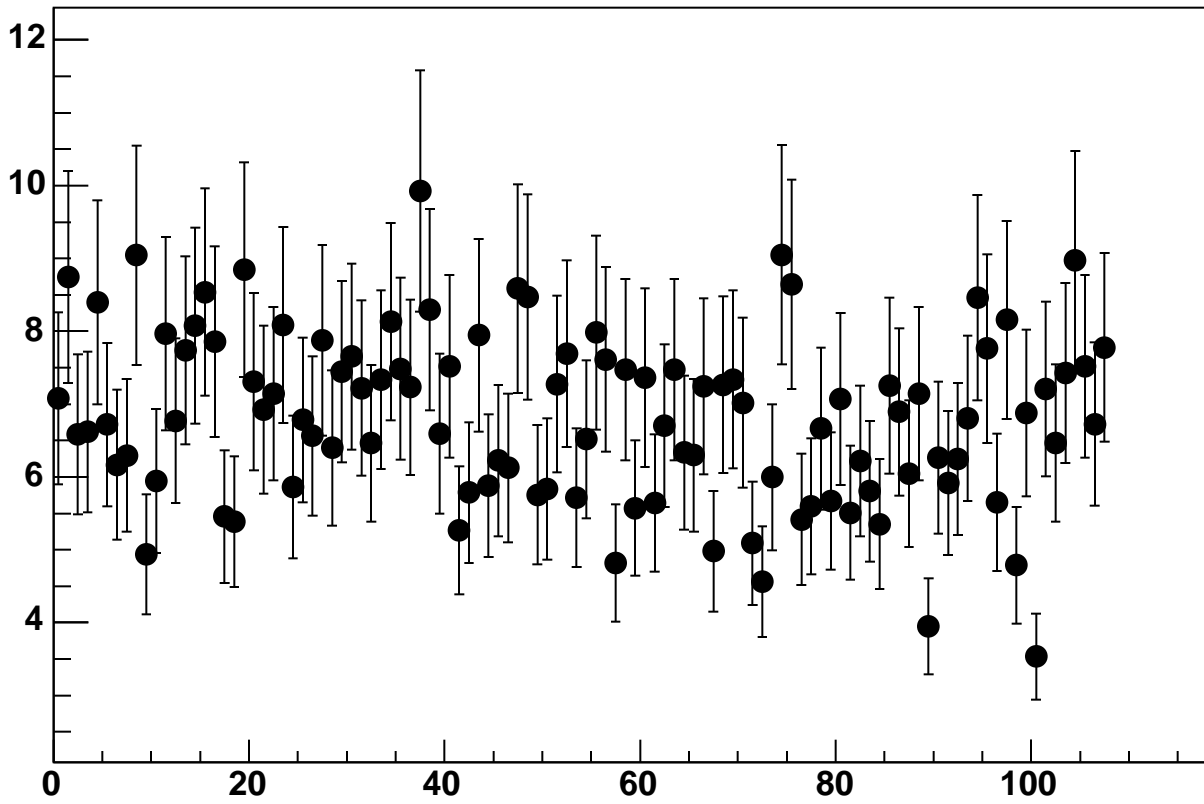




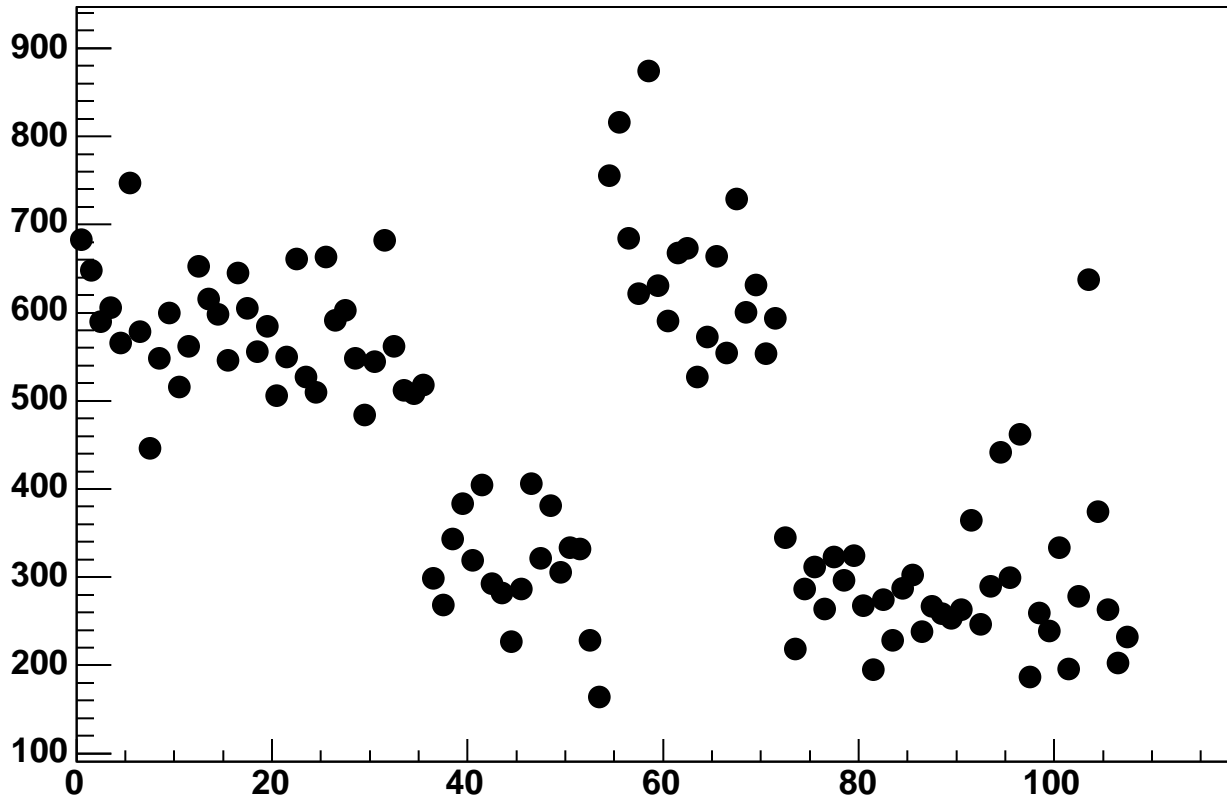
Enable 1, DAC=4000, Hold=155, ADC Mean vs 18\*Chip+Chan



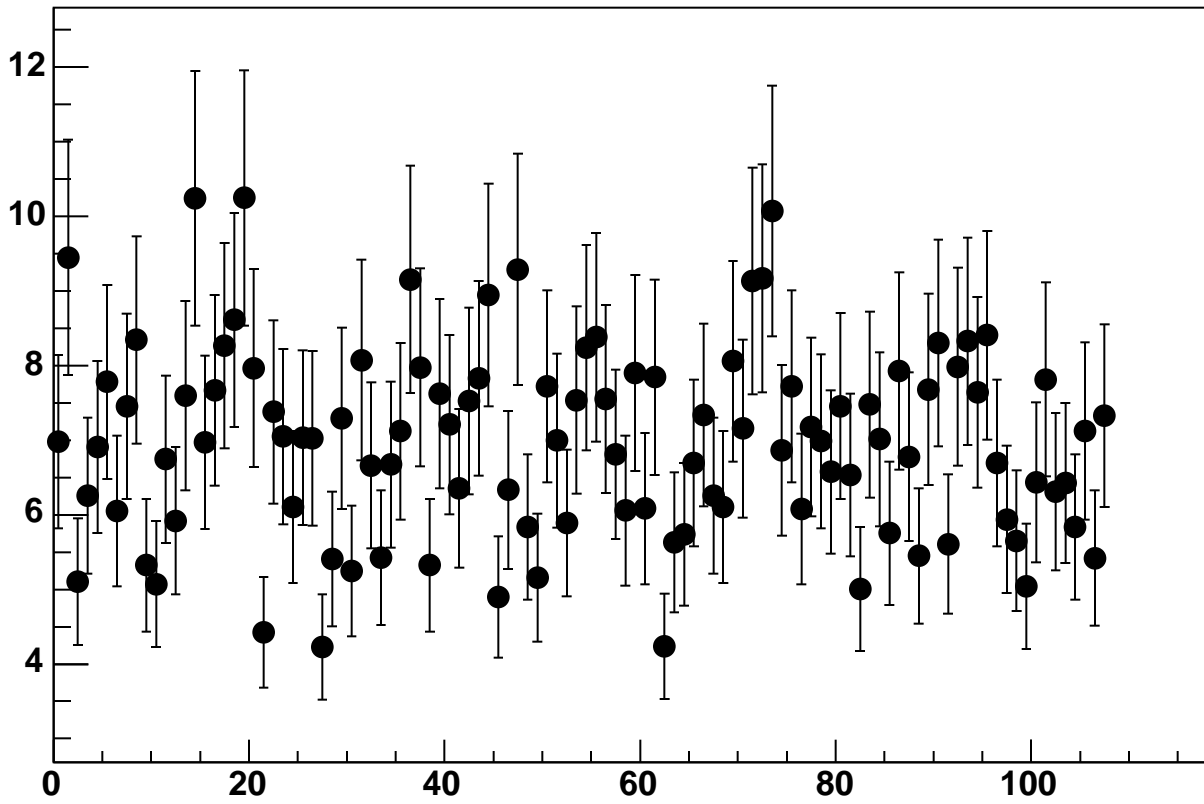
Enable 1, DAC=4000, Hold=155, ADC Noise vs 18\*Chip+Chan



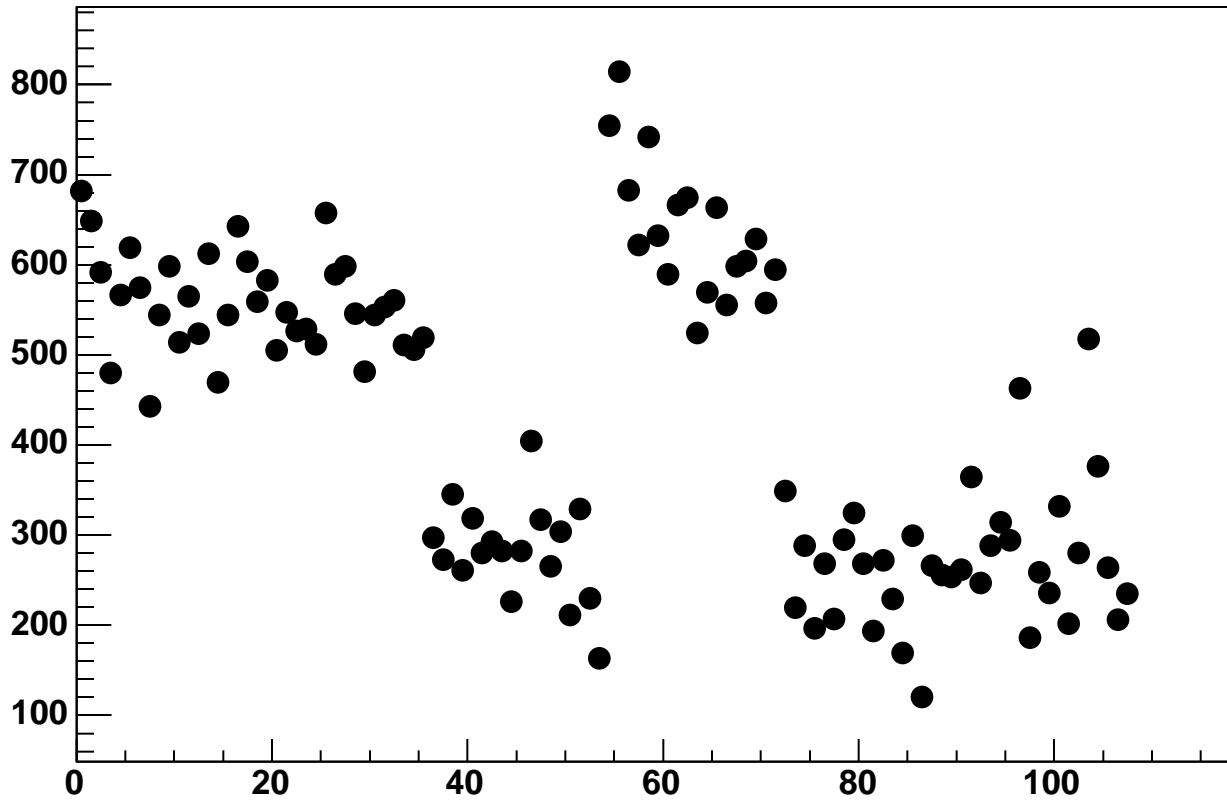
Enable 1, DAC=4000, Hold=160, ADC Mean vs 18\*Chip+Chan



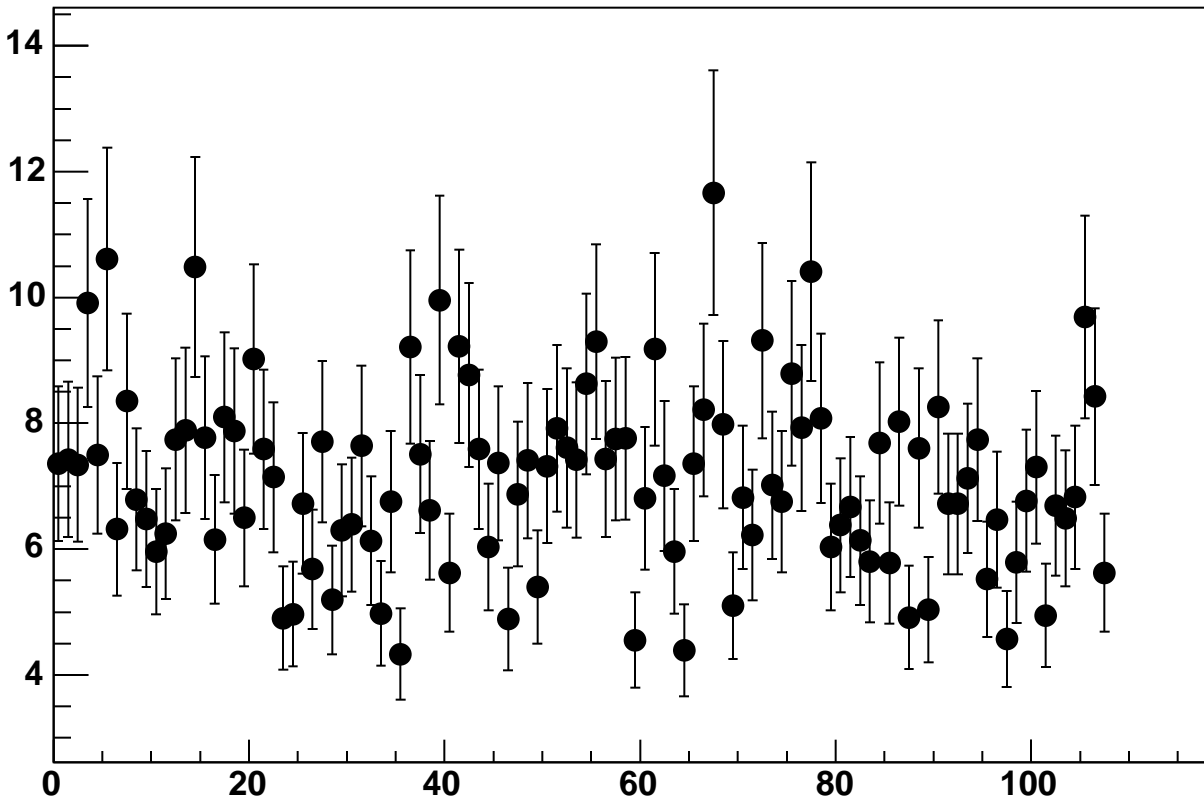
Enable 1, DAC=4000, Hold=160, ADC Noise vs 18\*Chip+Chan



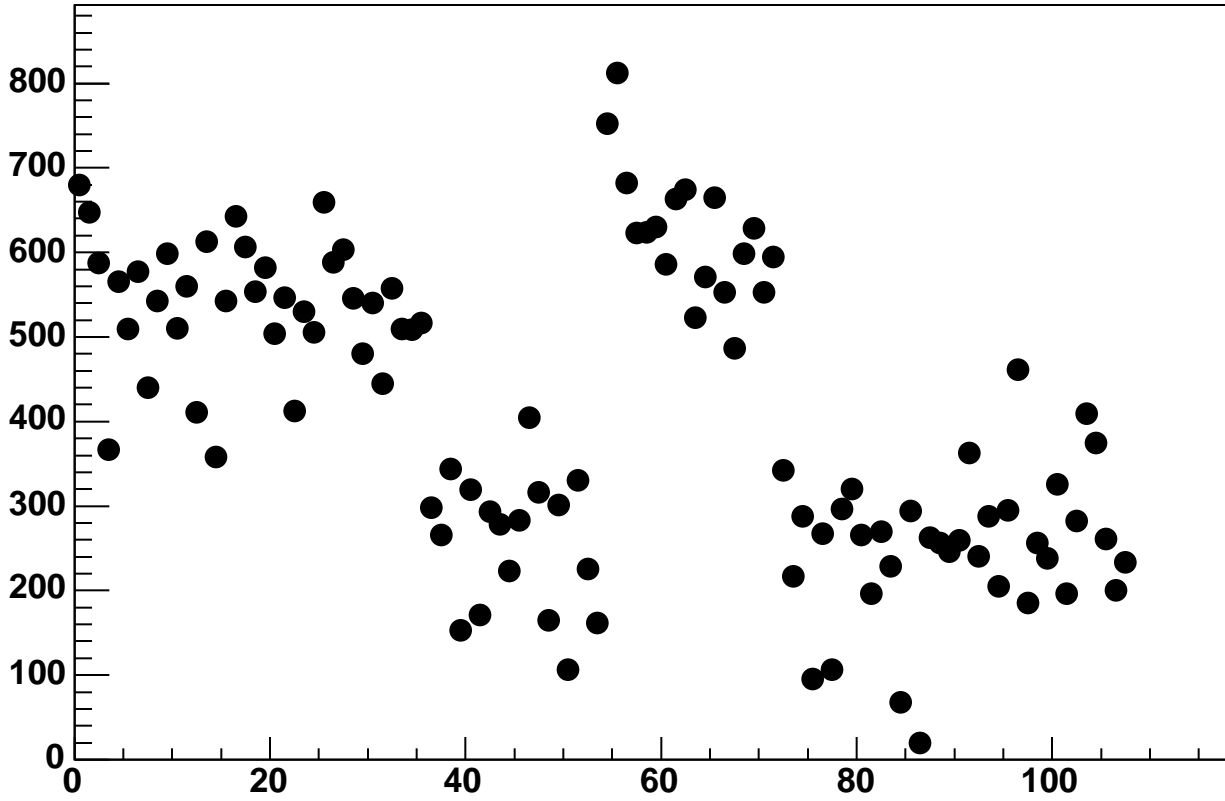
Enable 1, DAC=4000, Hold=165, ADC Mean vs 18\*Chip+Chan



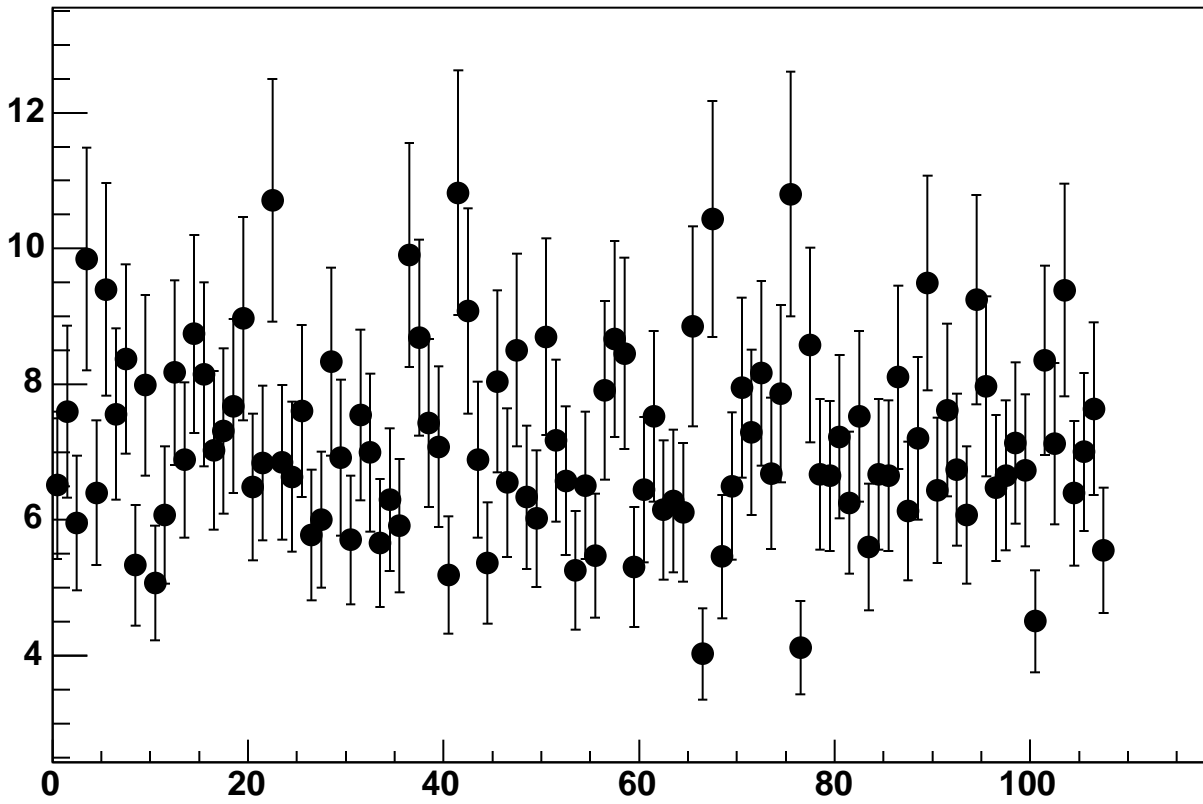
Enable 1, DAC=4000, Hold=165, ADC Noise vs 18\*Chip+Chan



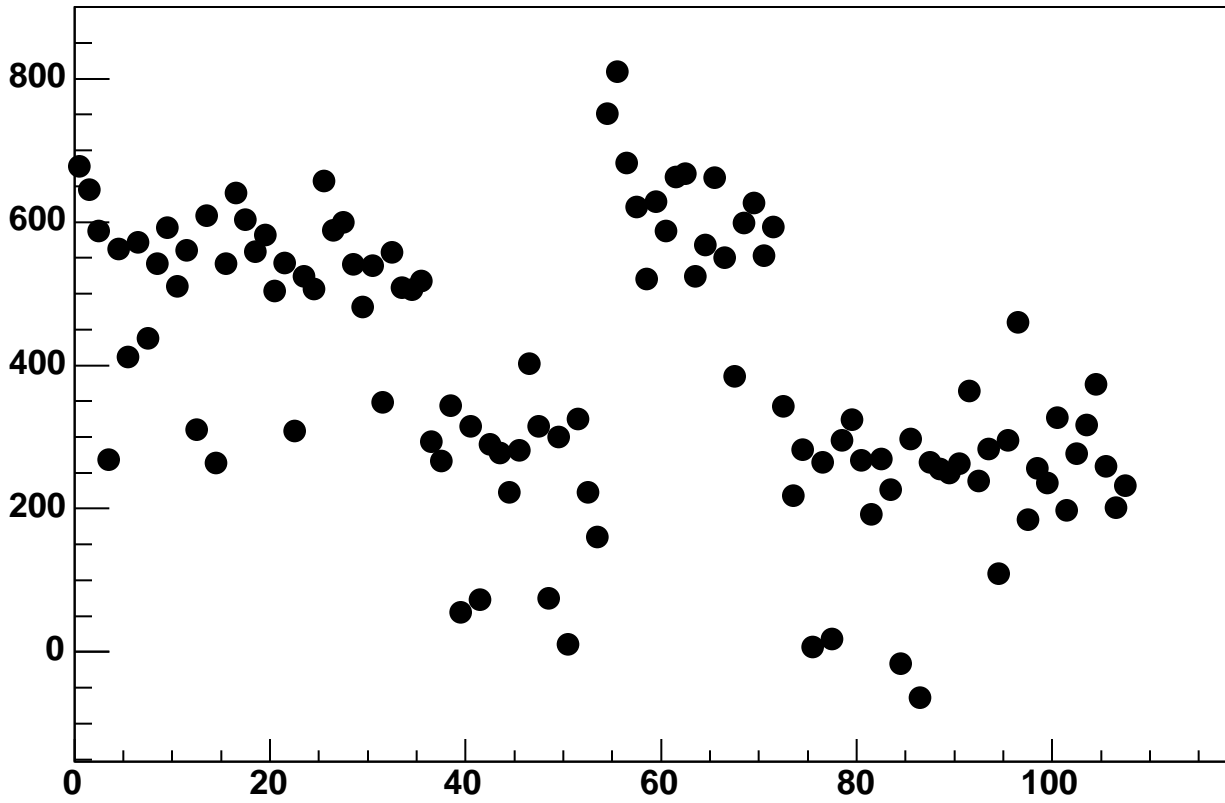
Enable 1, DAC=4000, Hold=170, ADC Mean vs 18\*Chip+Chan



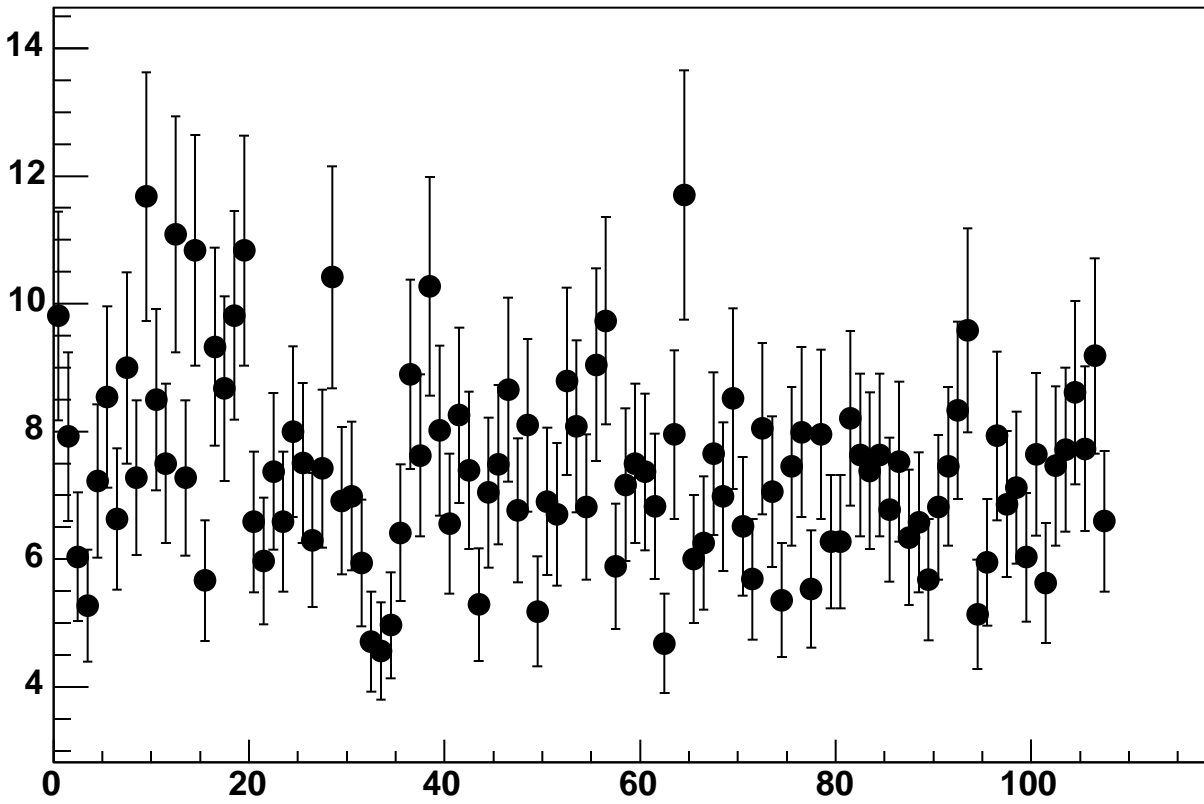
Enable 1, DAC=4000, Hold=170, ADC Noise vs 18\*Chip+Chan



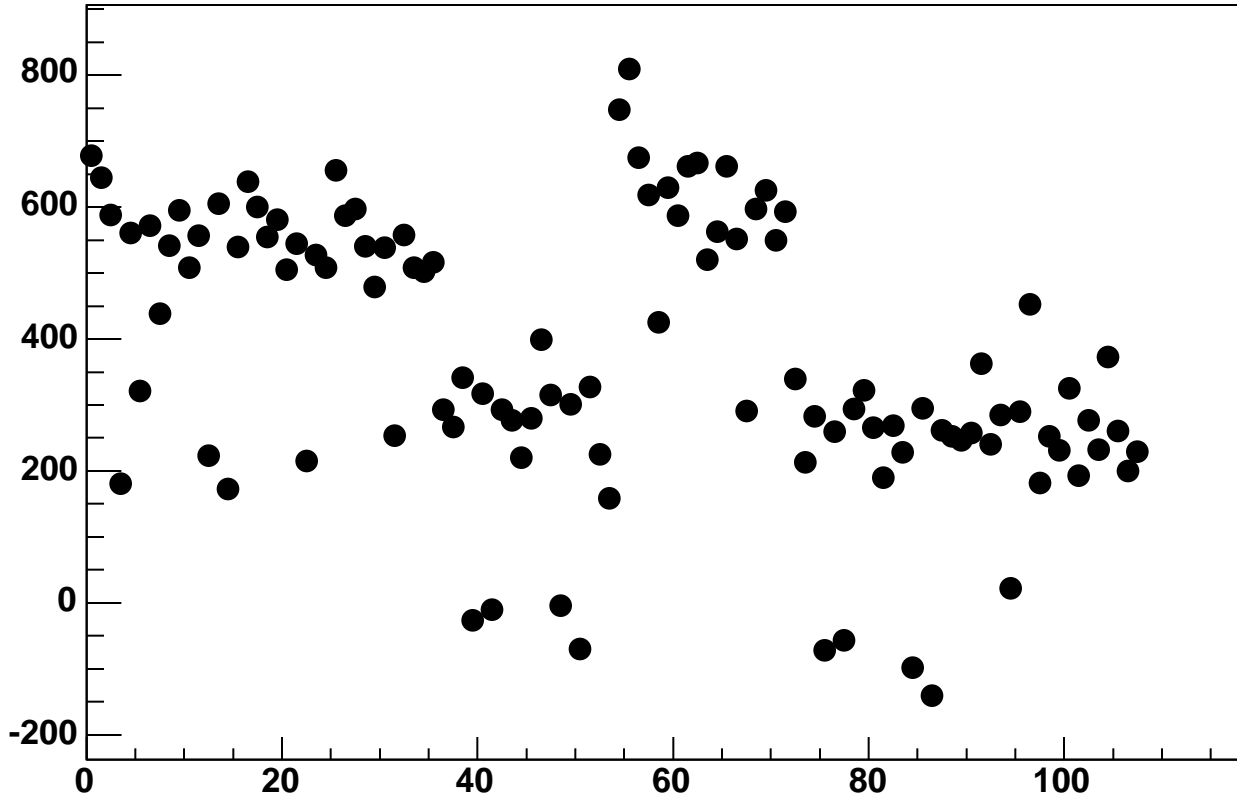
Enable 1, DAC=4000, Hold=175, ADC Mean vs 18\*Chip+Chan



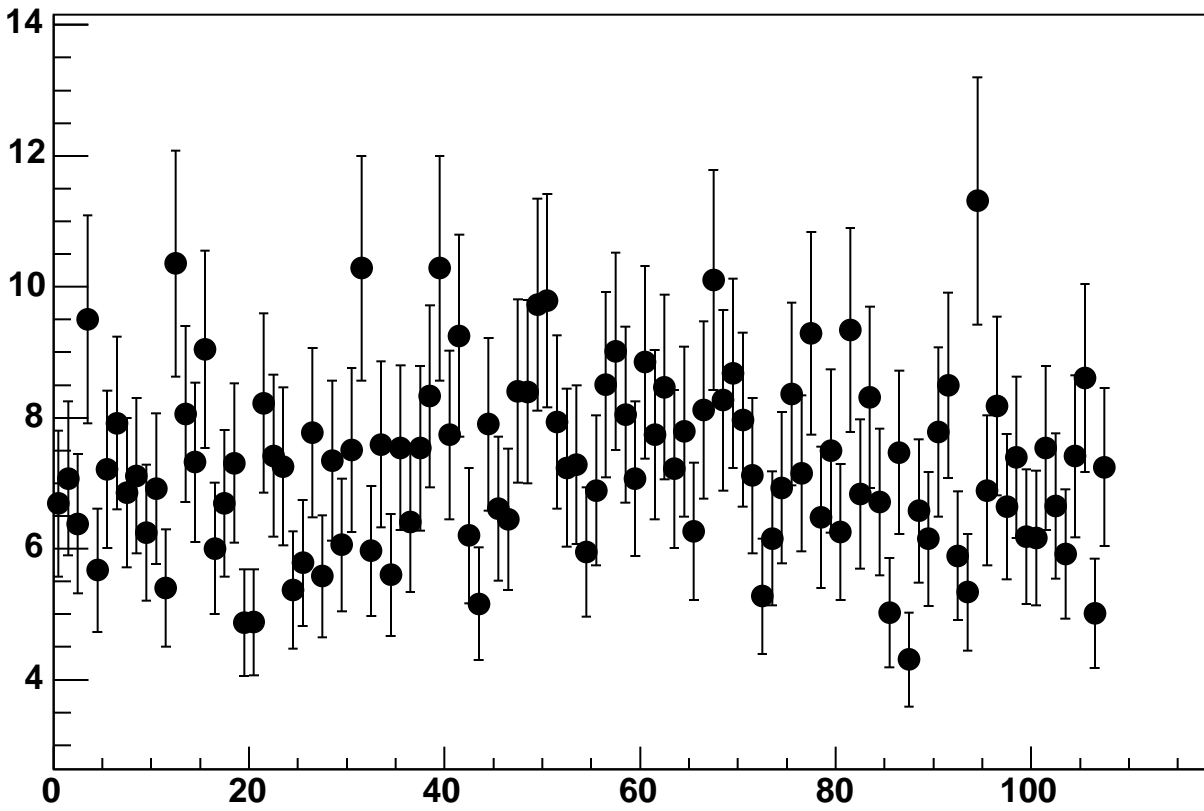
Enable 1, DAC=4000, Hold=175, ADC Noise vs 18\*Chip+Chan



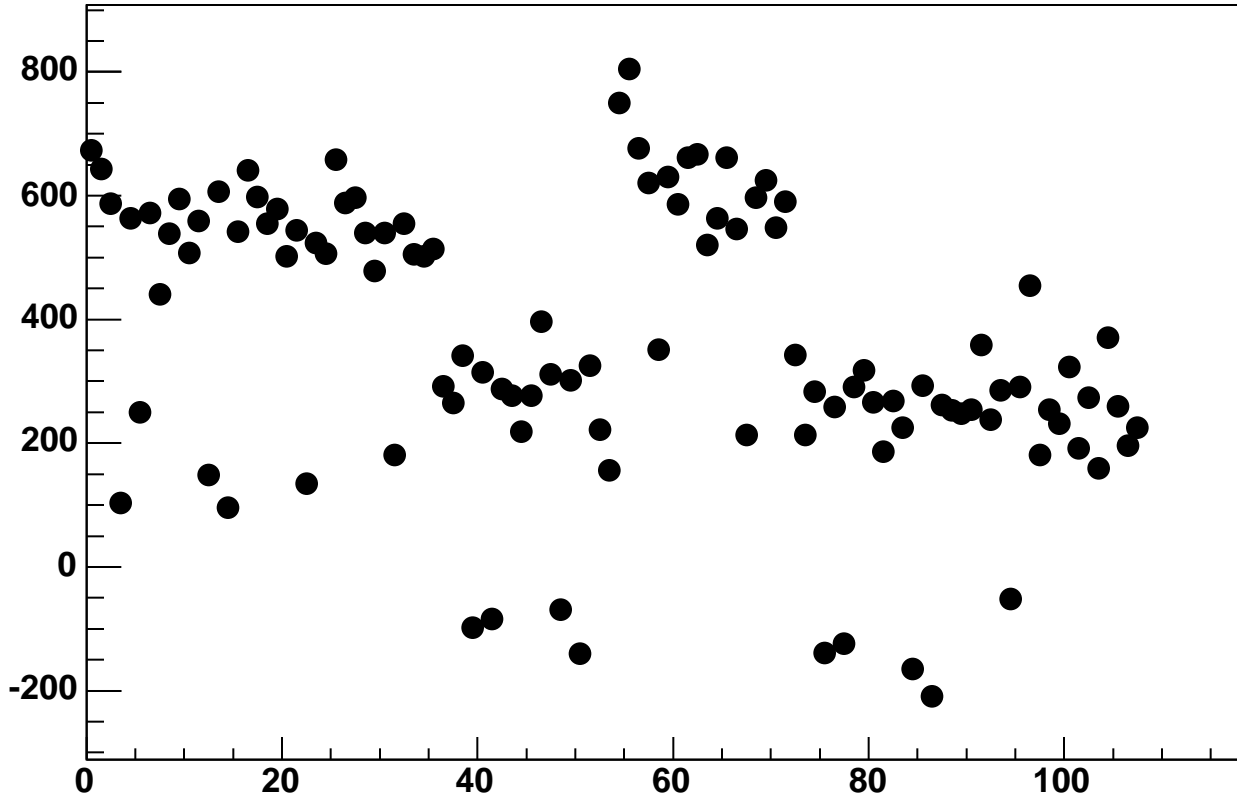
Enable 1, DAC=4000, Hold=180, ADC Mean vs 18\*Chip+Chan



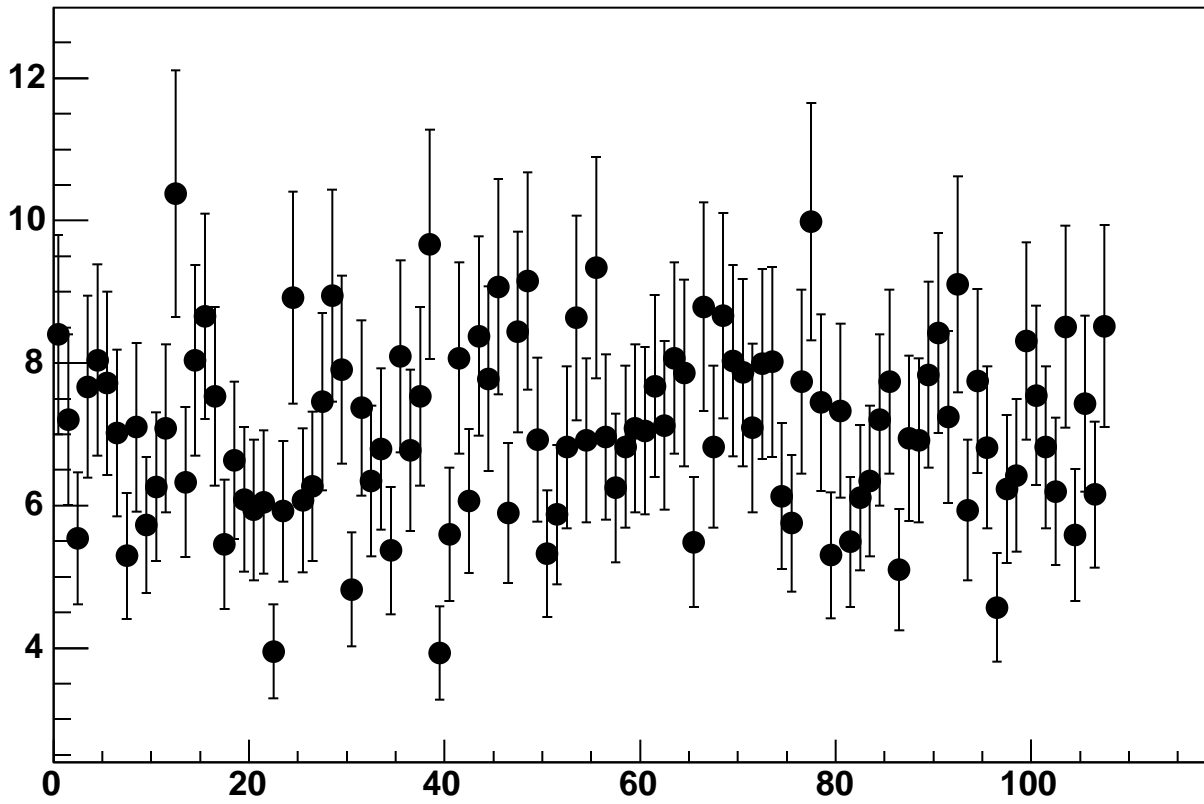
Enable 1, DAC=4000, Hold=180, ADC Noise vs 18\*Chip+Chan



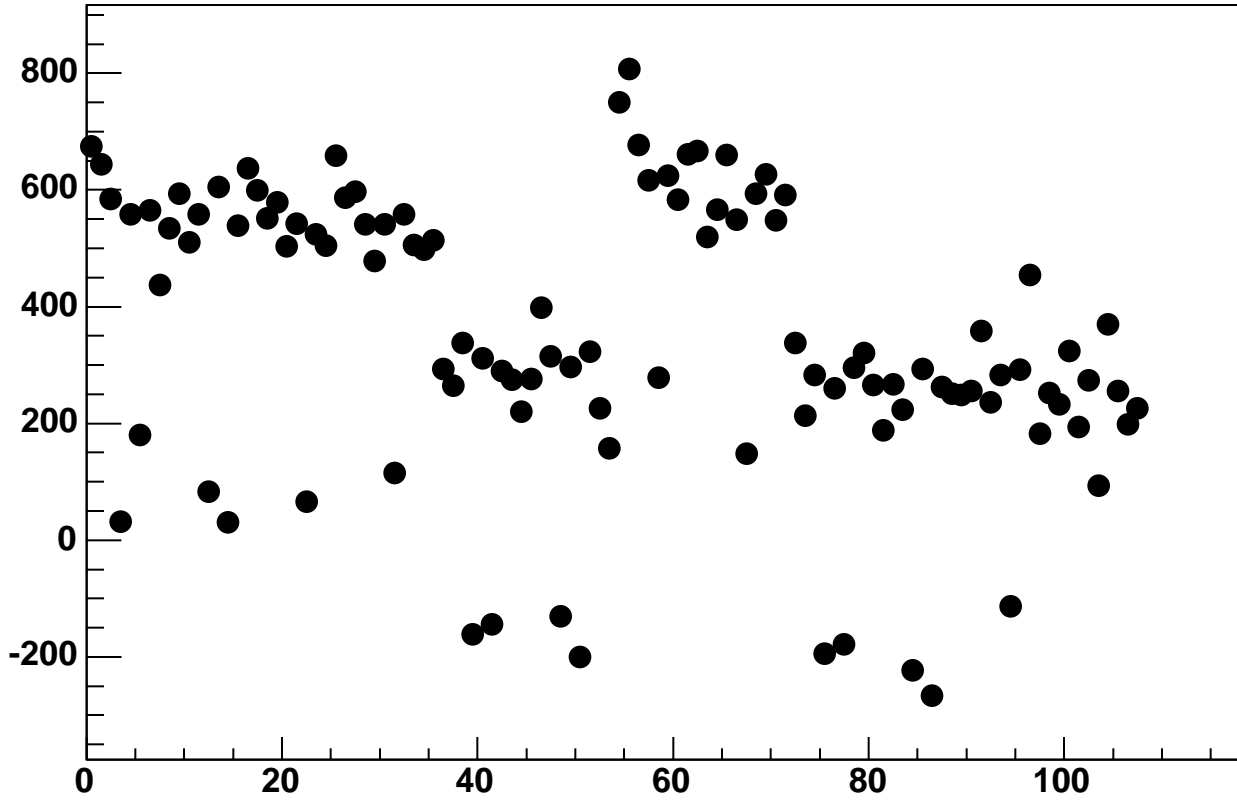
Enable 1, DAC=4000, Hold=185, ADC Mean vs 18\*Chip+Chan



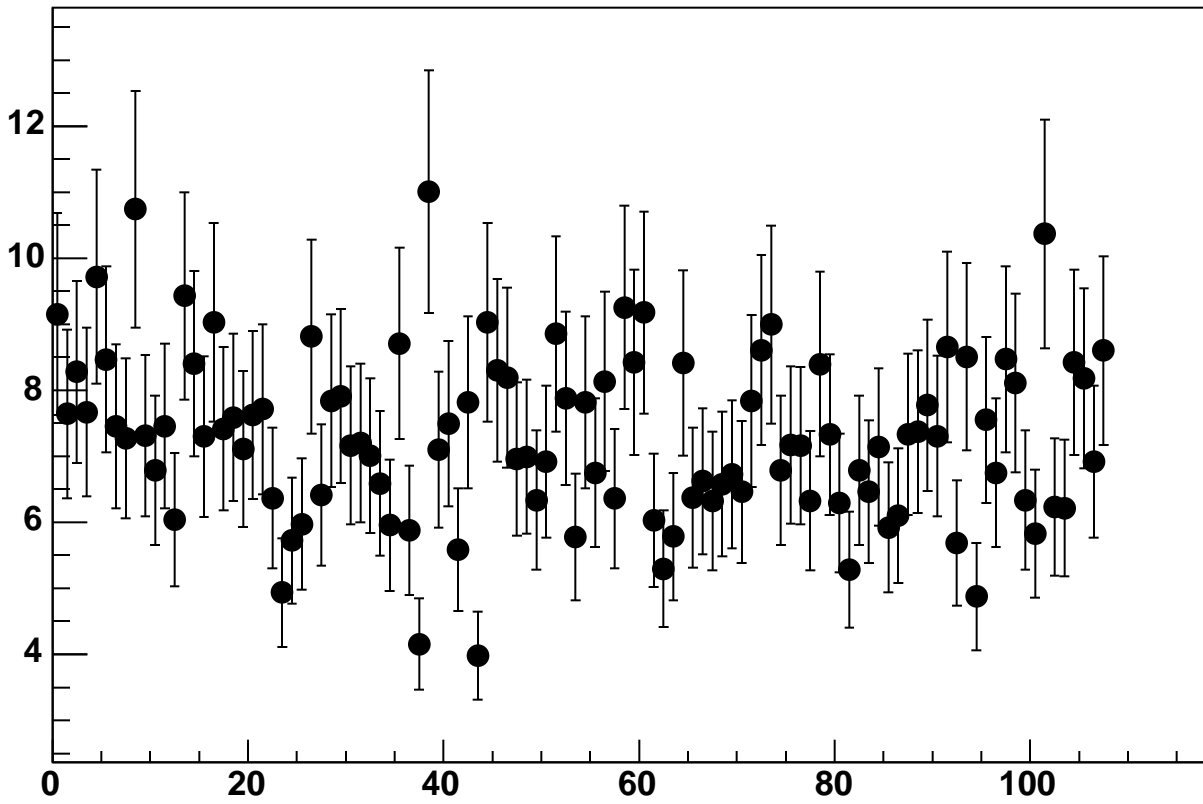
Enable 1, DAC=4000, Hold=185, ADC Noise vs 18\*Chip+Chan



Enable 1, DAC=4000, Hold=190, ADC Mean vs 18\*Chip+Chan

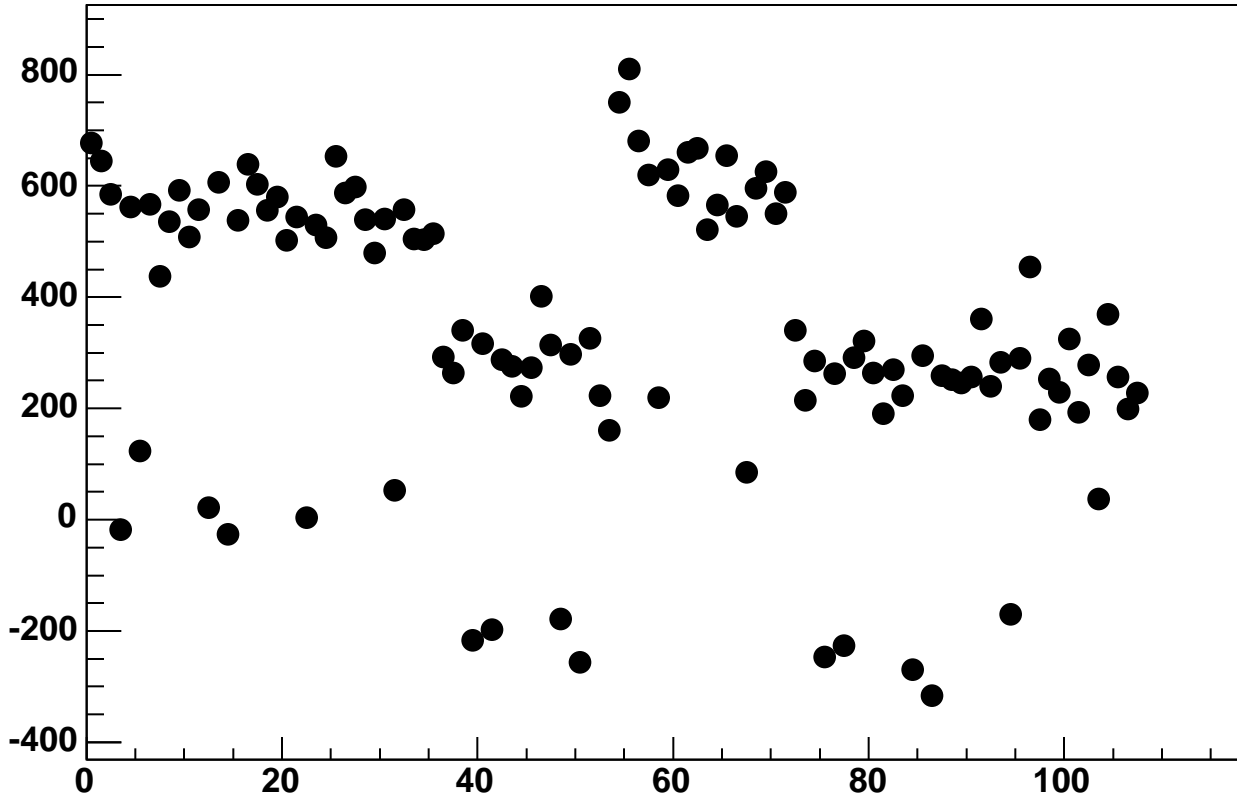


Enable 1, DAC=4000, Hold=190, ADC Noise vs 18\*Chip+Chan

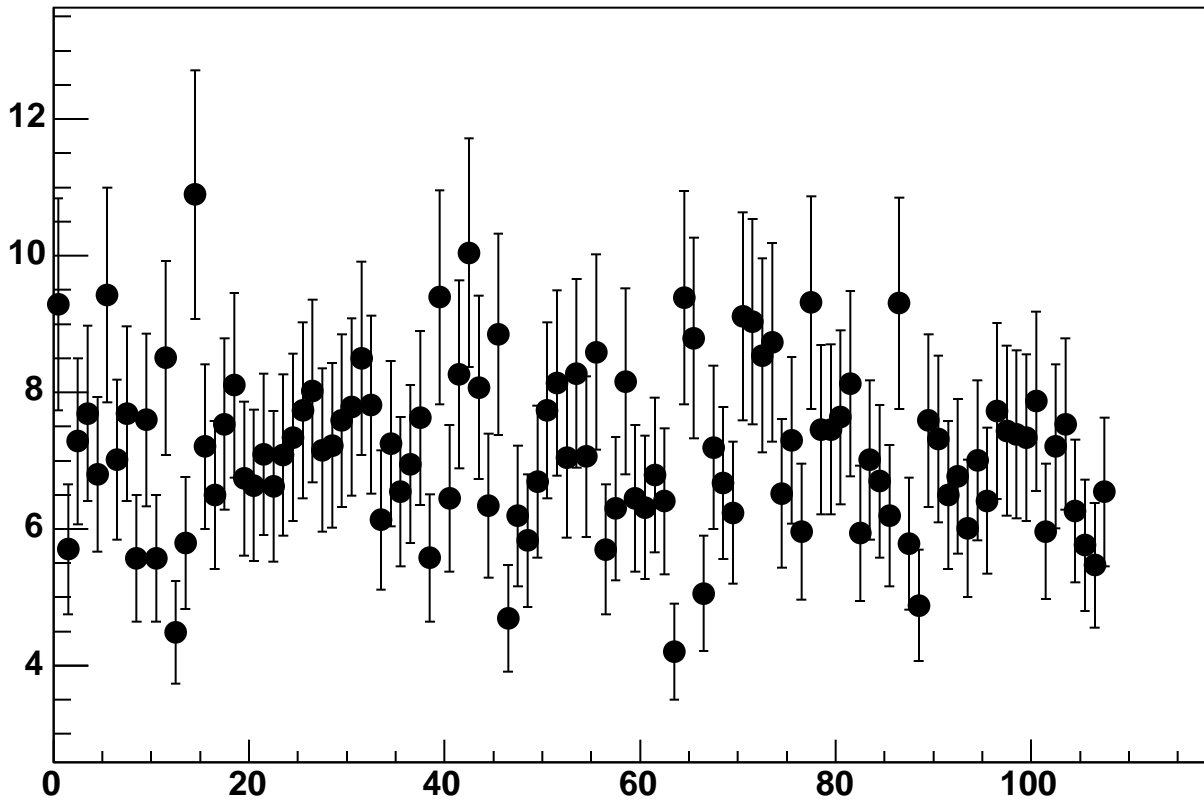




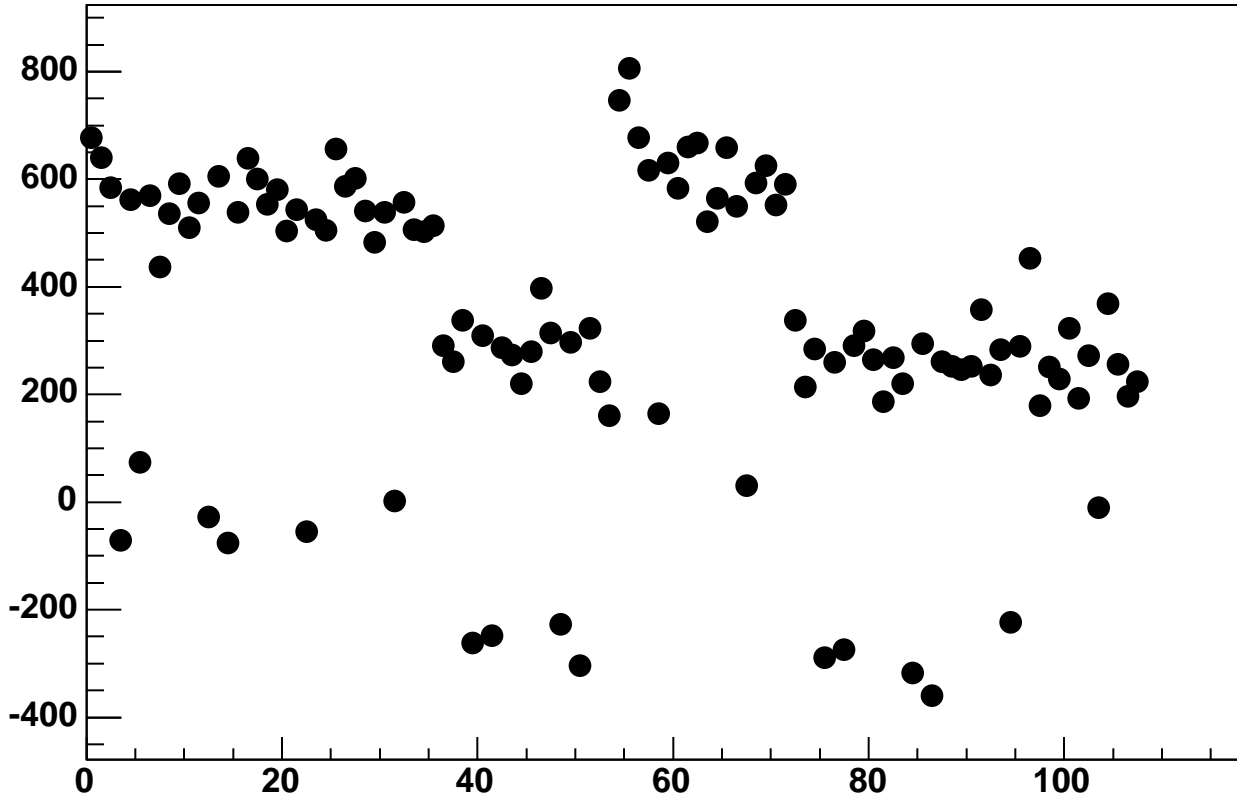
Enable 1, DAC=4000, Hold=195, ADC Mean vs 18\*Chip+Chan



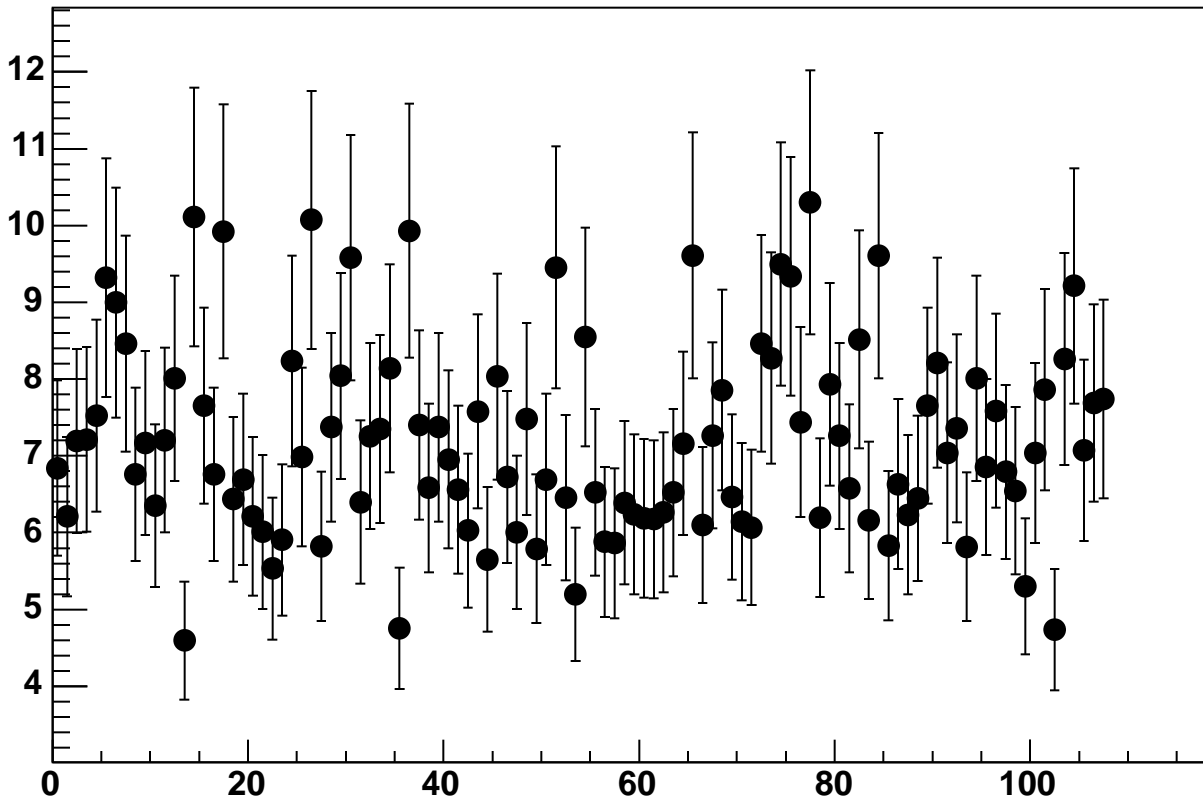
Enable 1, DAC=4000, Hold=195, ADC Noise vs 18\*Chip+Chan



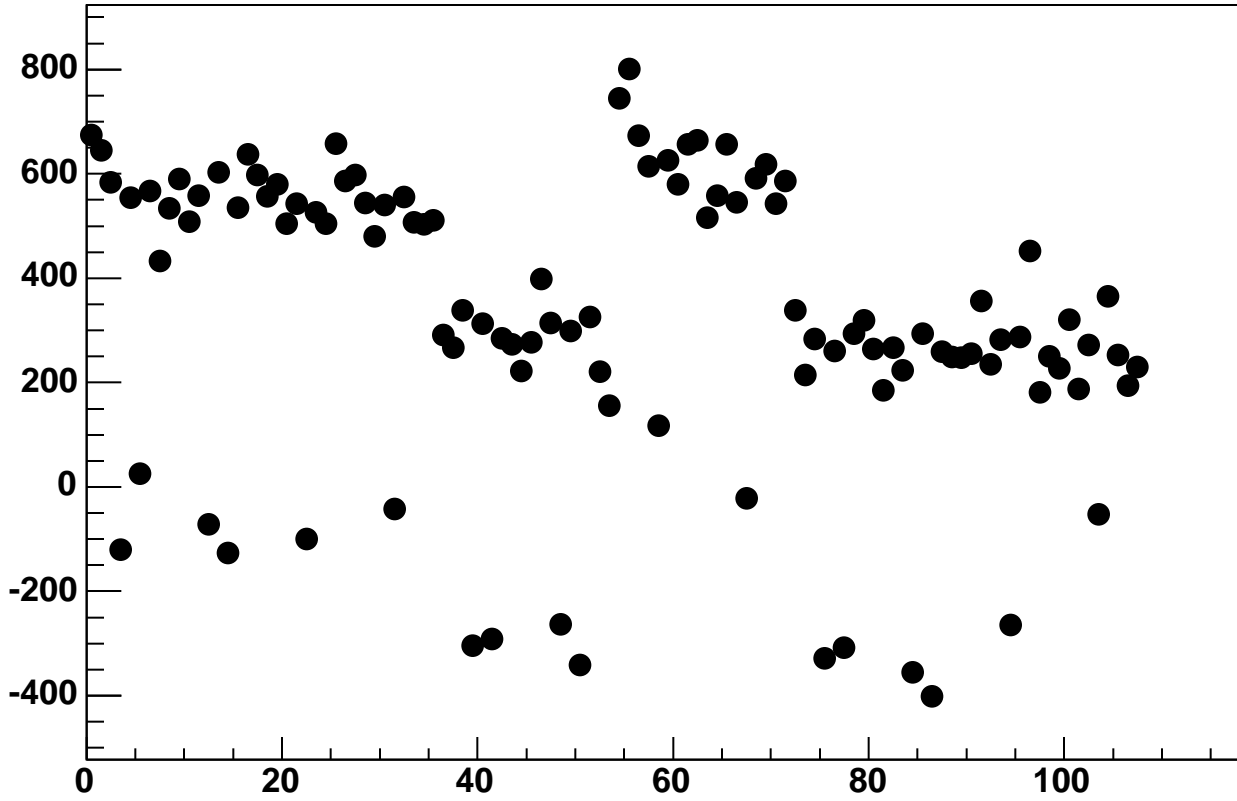
Enable 1, DAC=4000, Hold=200, ADC Mean vs 18\*Chip+Chan



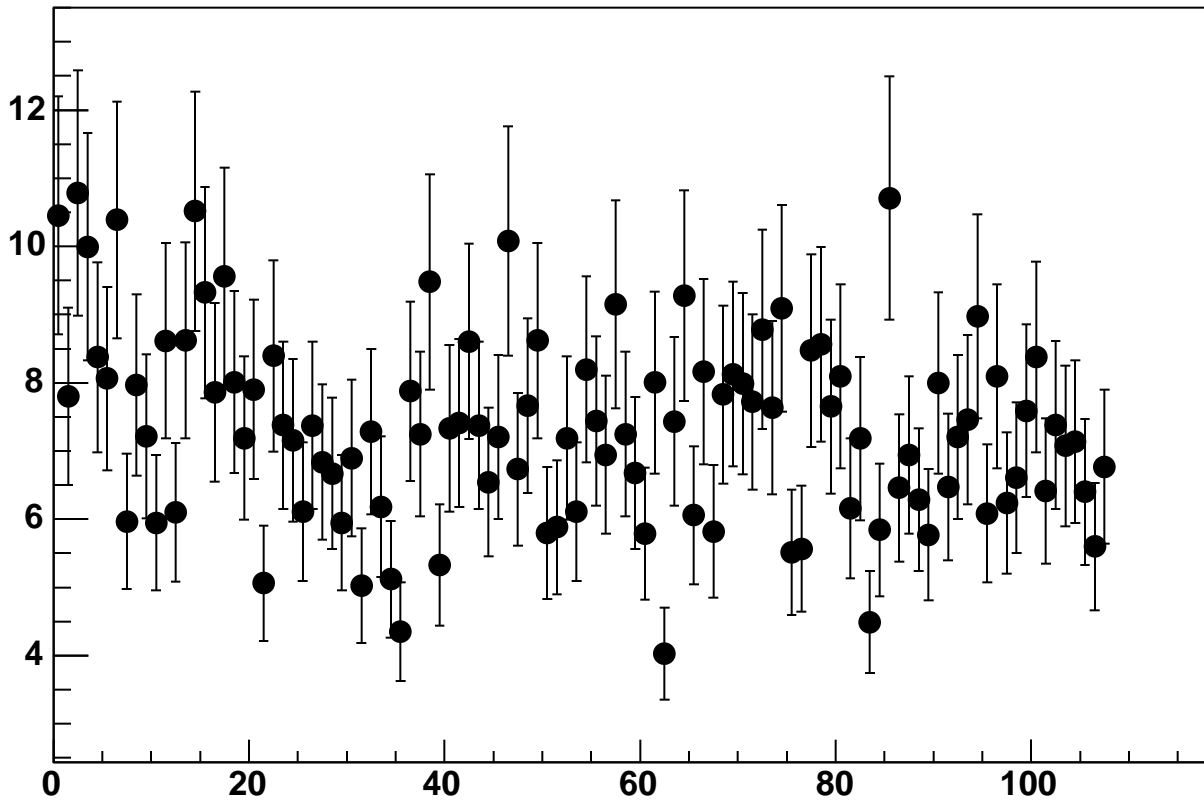
Enable 1, DAC=4000, Hold=200, ADC Noise vs 18\*Chip+Chan



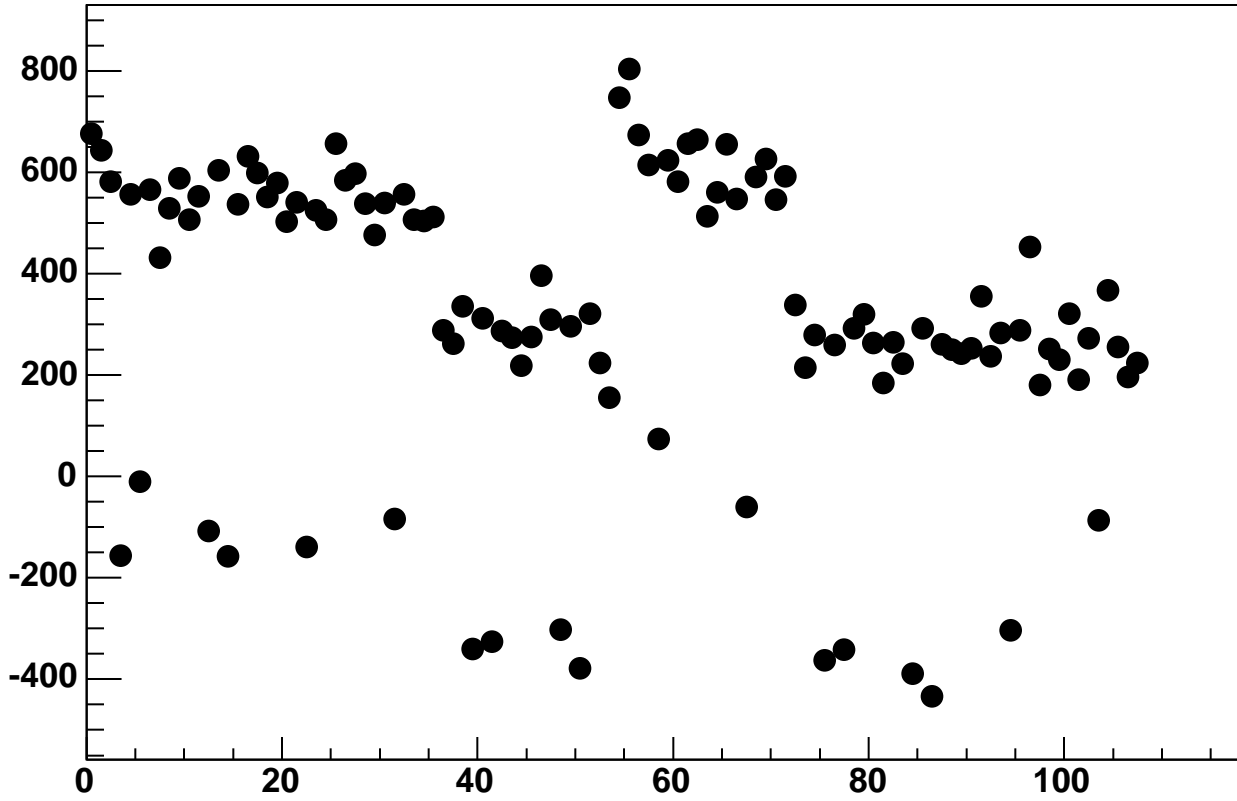
Enable 1, DAC=4000, Hold=205, ADC Mean vs 18\*Chip+Chan



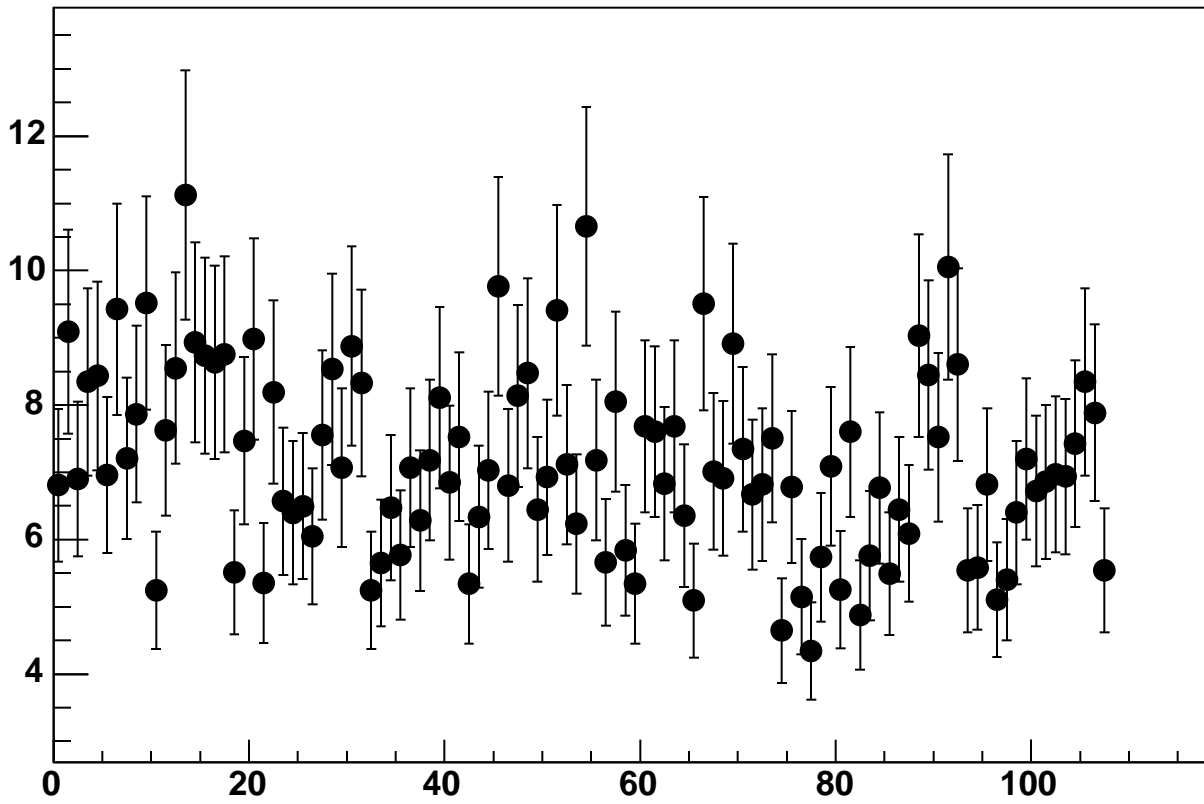
Enable 1, DAC=4000, Hold=205, ADC Noise vs 18\*Chip+Chan



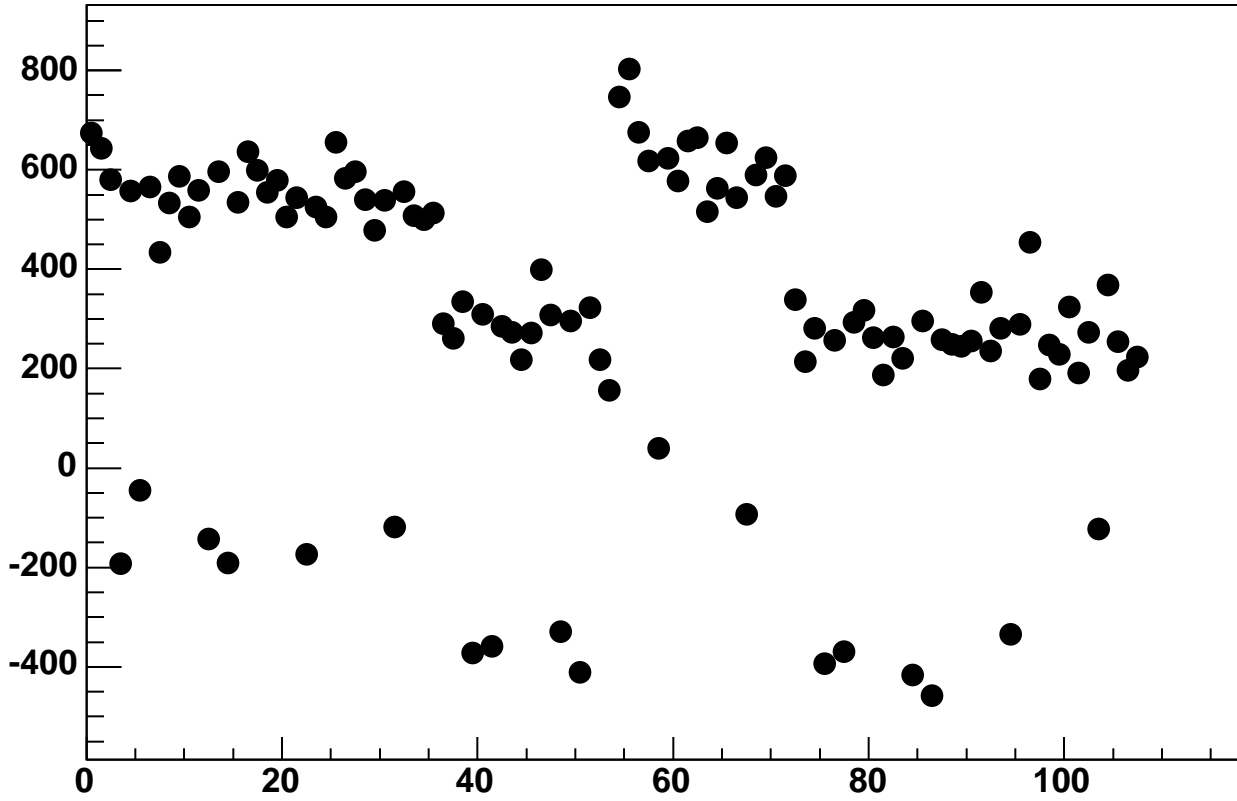
Enable 1, DAC=4000, Hold=210, ADC Mean vs 18\*Chip+Chan



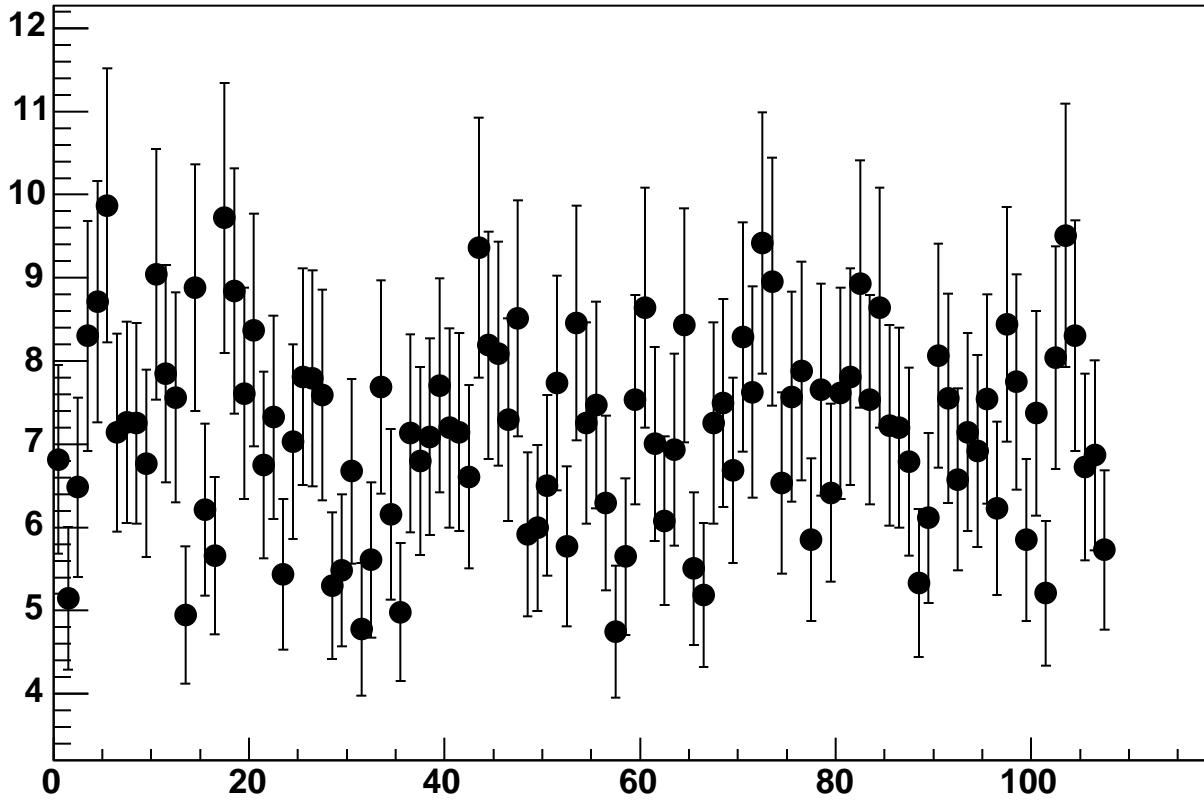
Enable 1, DAC=4000, Hold=210, ADC Noise vs 18\*Chip+Chan



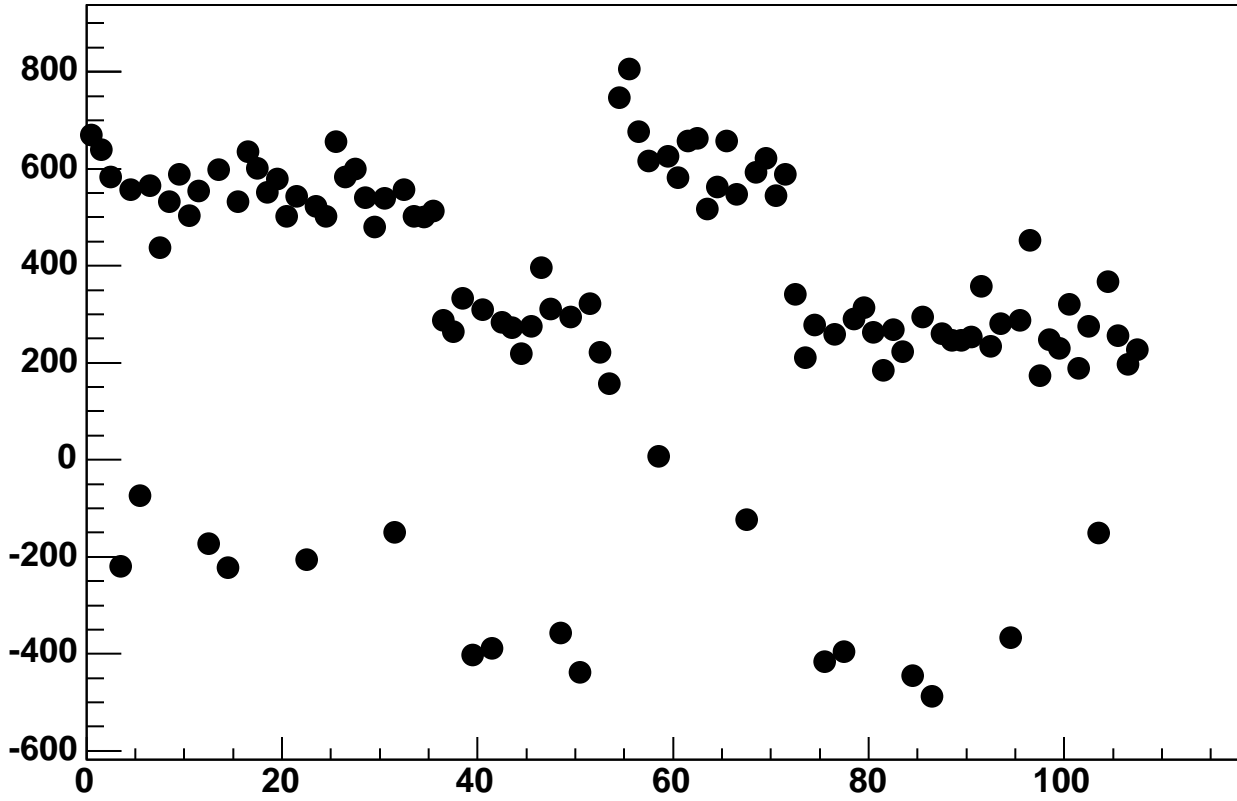
Enable 1, DAC=4000, Hold=215, ADC Mean vs 18\*Chip+Chan



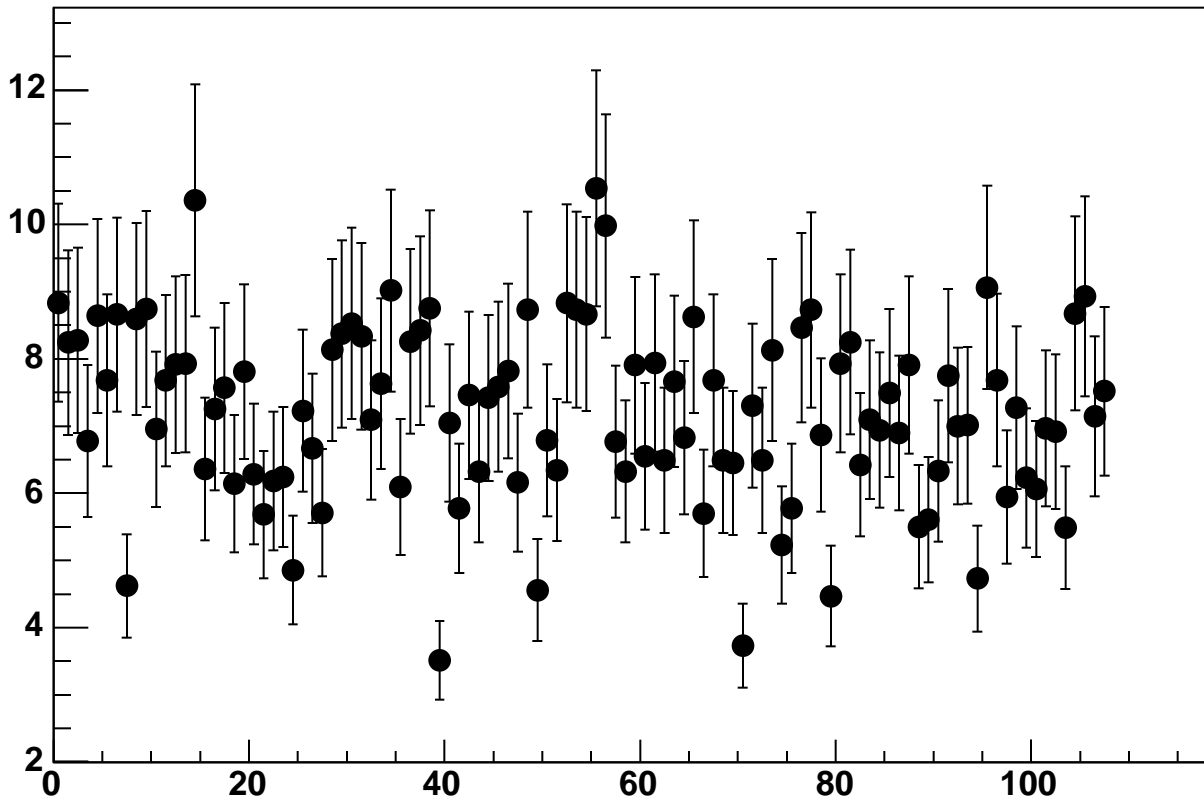
Enable 1, DAC=4000, Hold=215, ADC Noise vs 18\*Chip+Chan



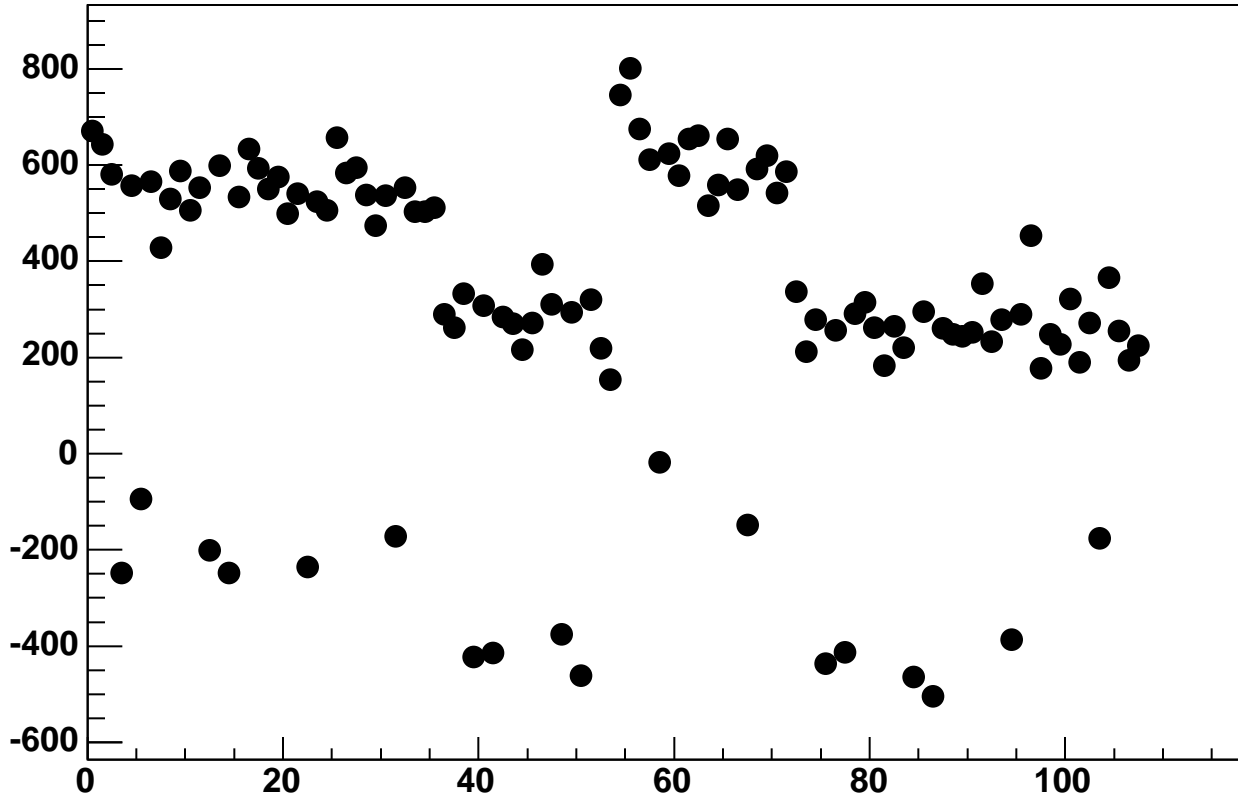
Enable 1, DAC=4000, Hold=220, ADC Mean vs 18\*Chip+Chan



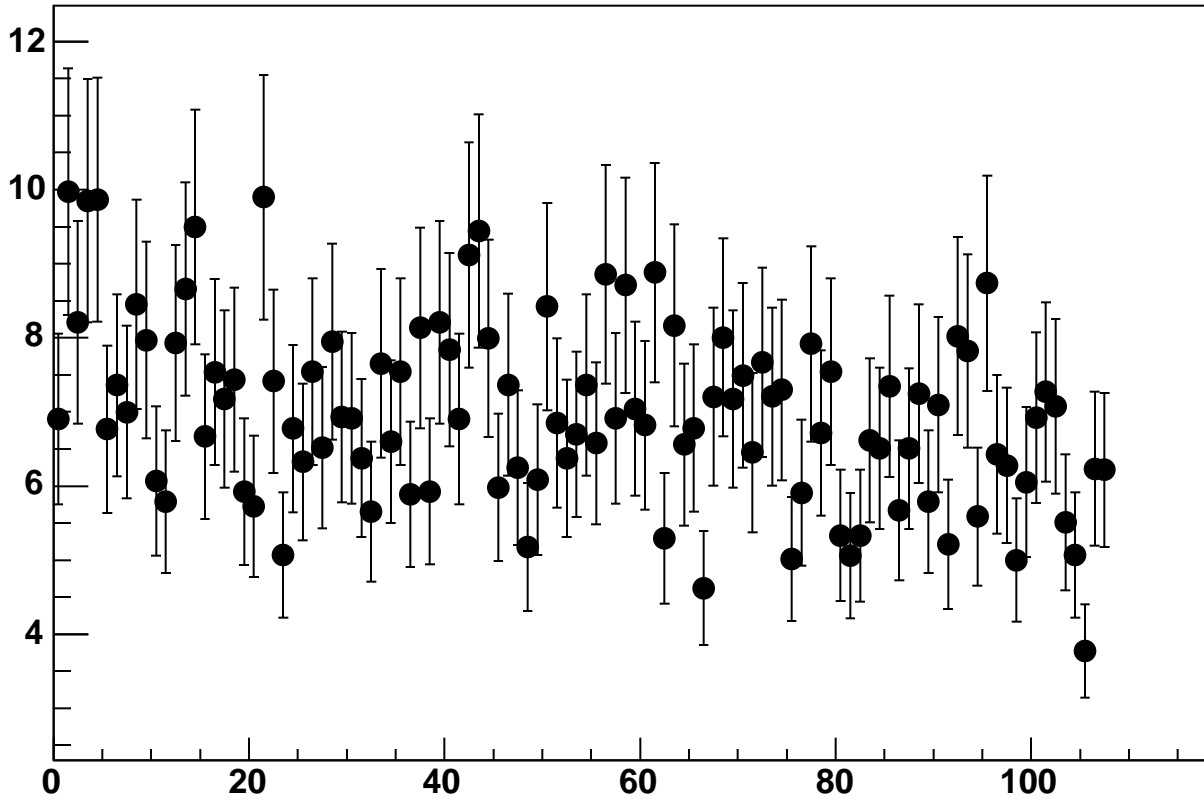
Enable 1, DAC=4000, Hold=220, ADC Noise vs 18\*Chip+Chan



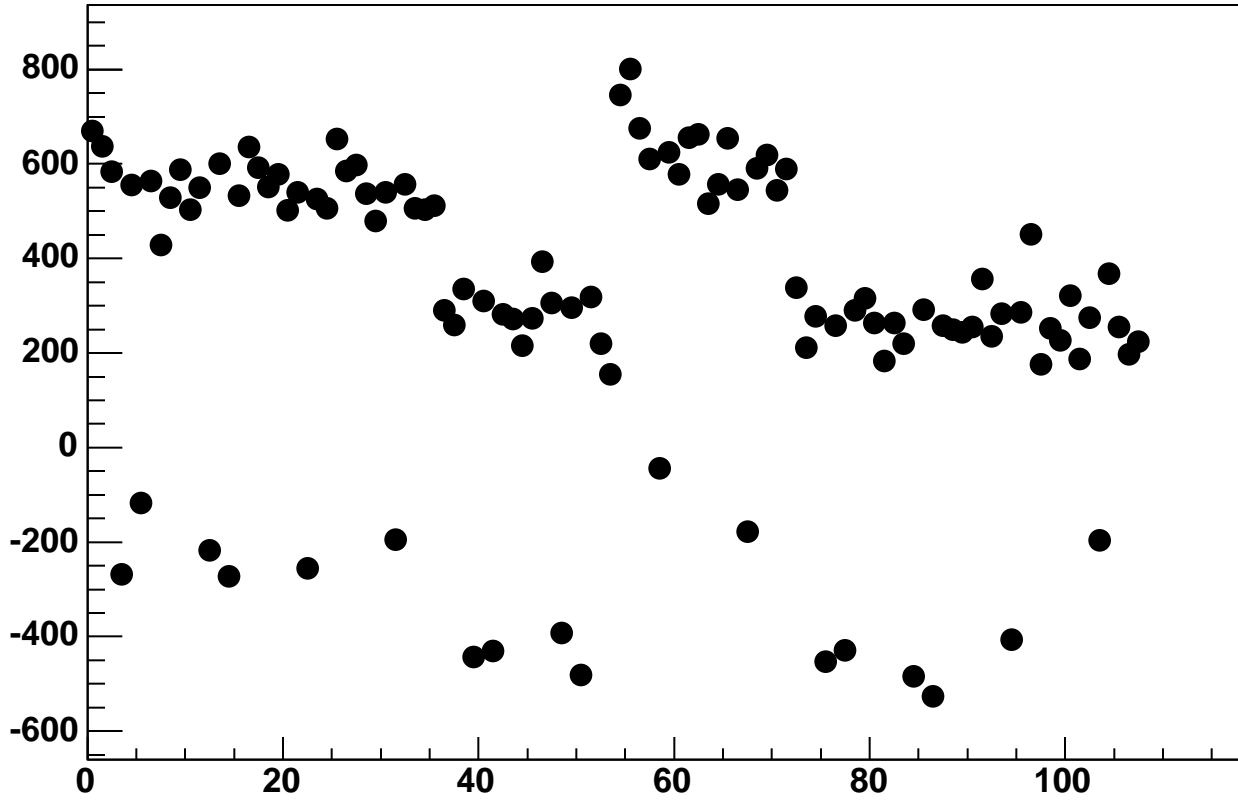
Enable 1, DAC=4000, Hold=225, ADC Mean vs 18\*Chip+Chan



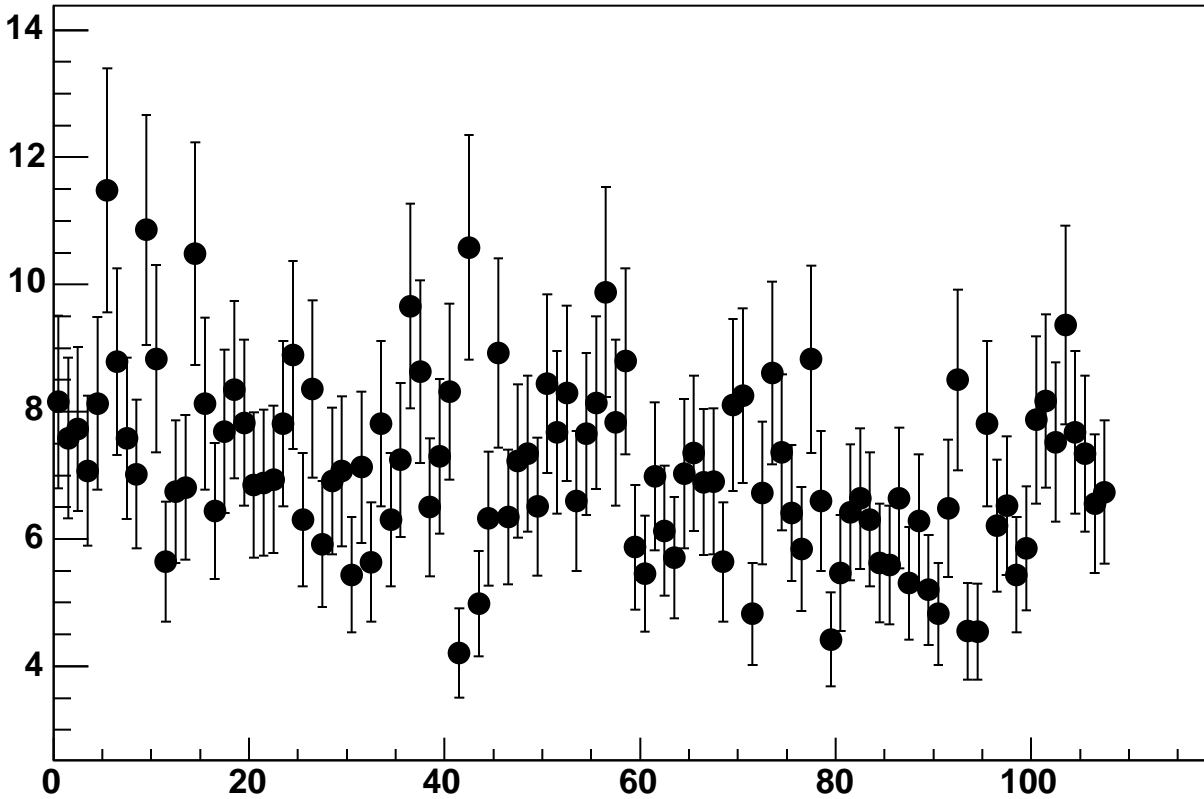
Enable 1, DAC=4000, Hold=225, ADC Noise vs 18\*Chip+Chan



Enable 1, DAC=4000, Hold=230, ADC Mean vs 18\*Chip+Chan

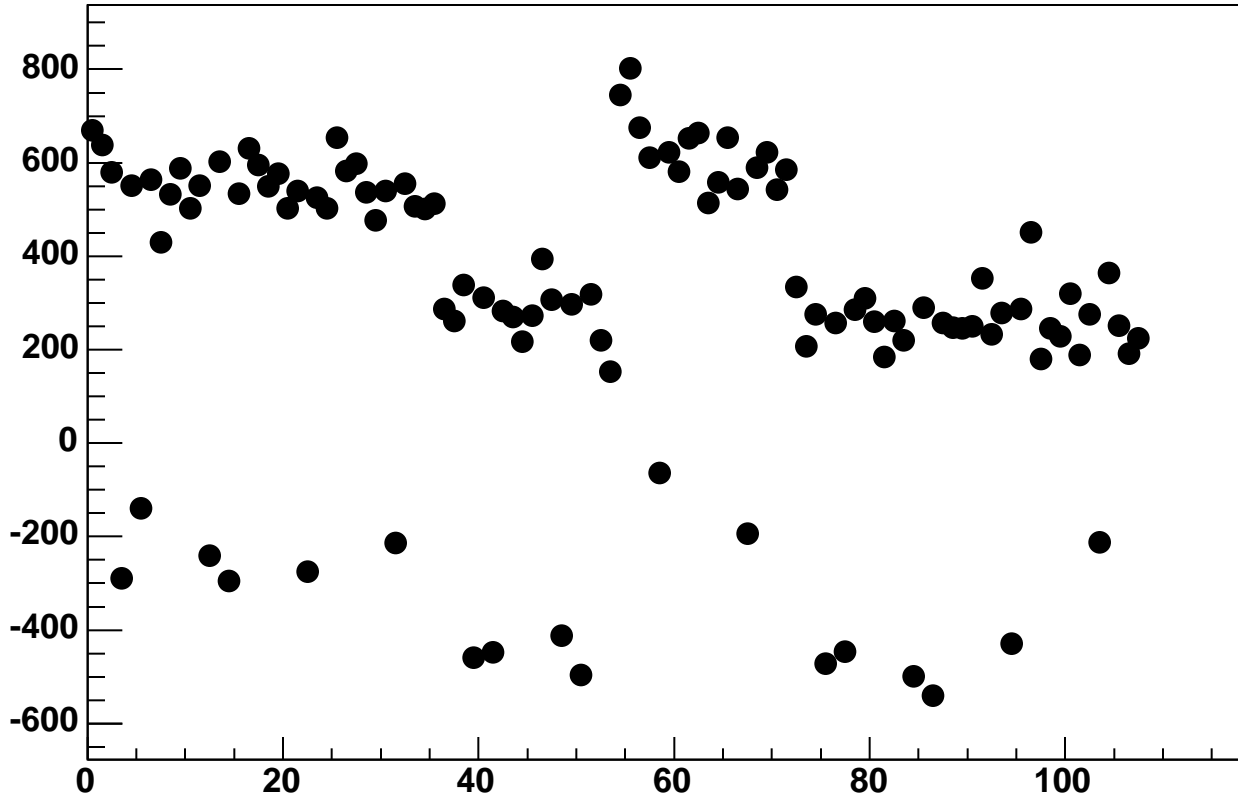


Enable 1, DAC=4000, Hold=230, ADC Noise vs 18\*Chip+Chan

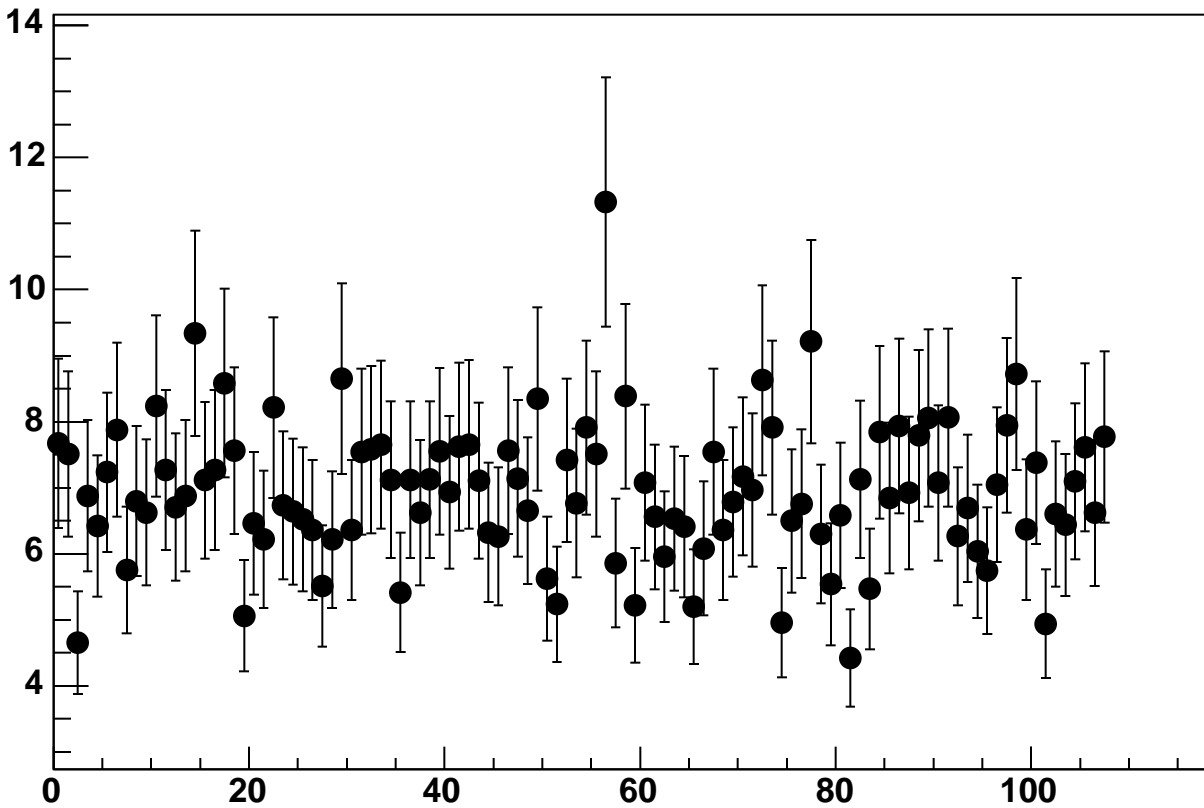




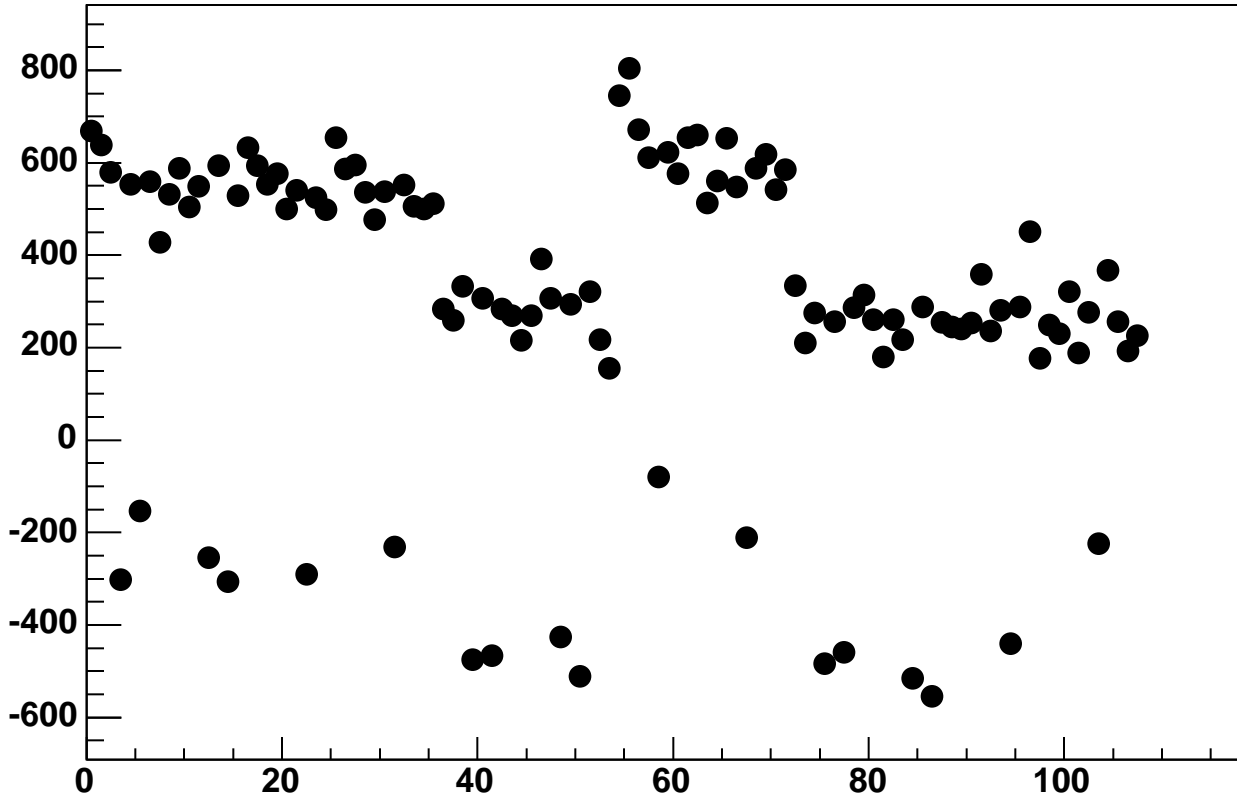
Enable 1, DAC=4000, Hold=235, ADC Mean vs 18\*Chip+Chan



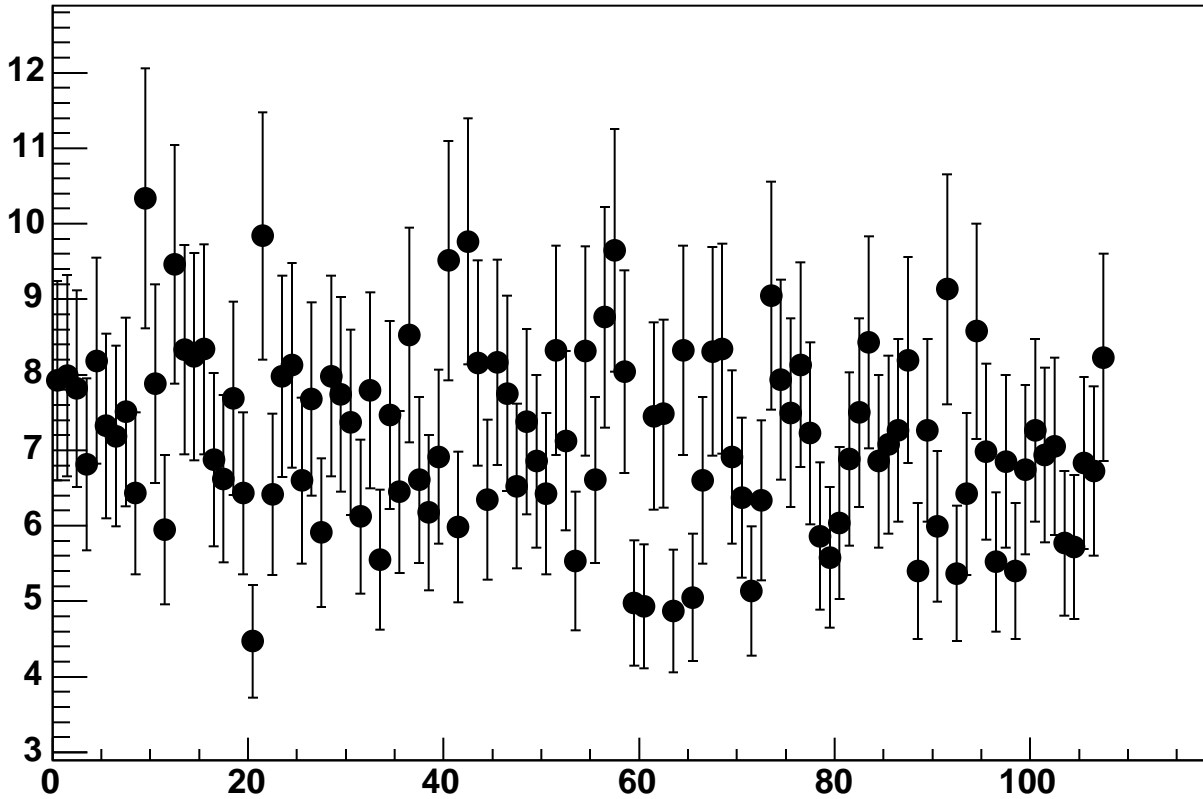
Enable 1, DAC=4000, Hold=235, ADC Noise vs 18\*Chip+Chan



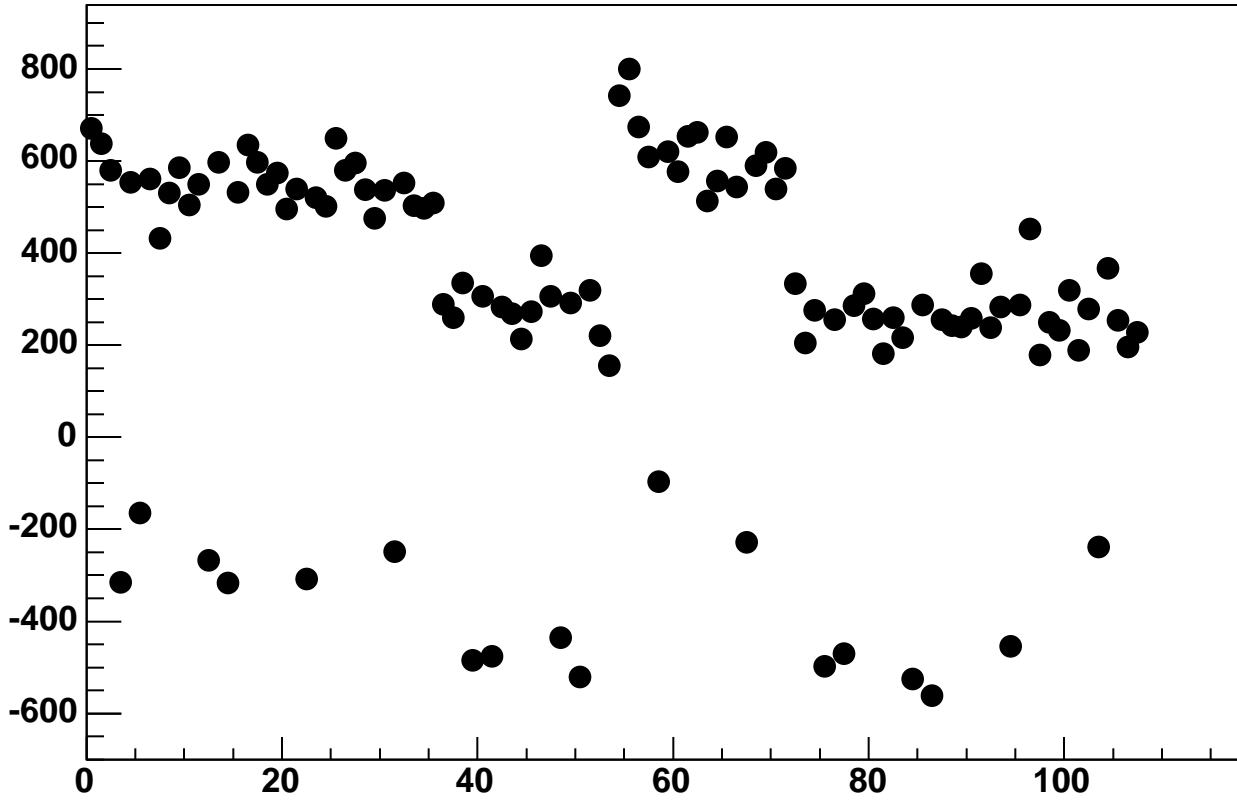
Enable 1, DAC=4000, Hold=240, ADC Mean vs 18\*Chip+Chan



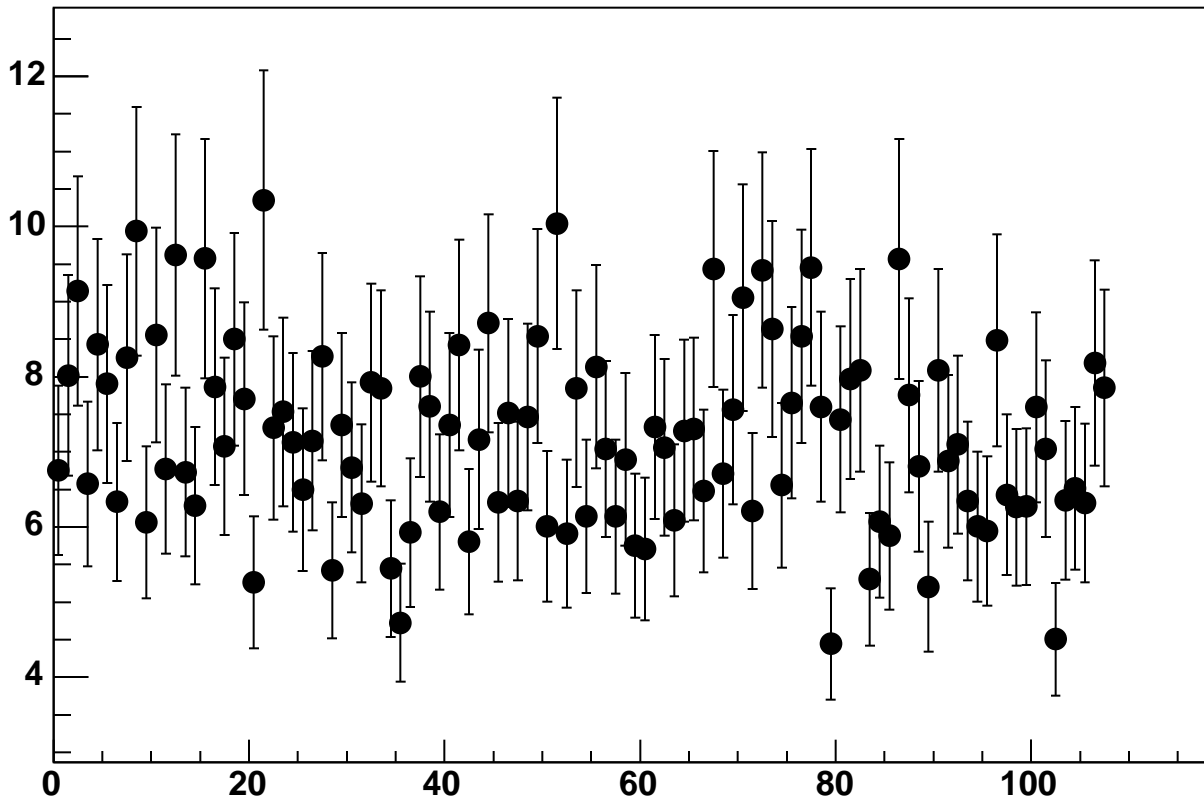
Enable 1, DAC=4000, Hold=240, ADC Noise vs 18\*Chip+Chan



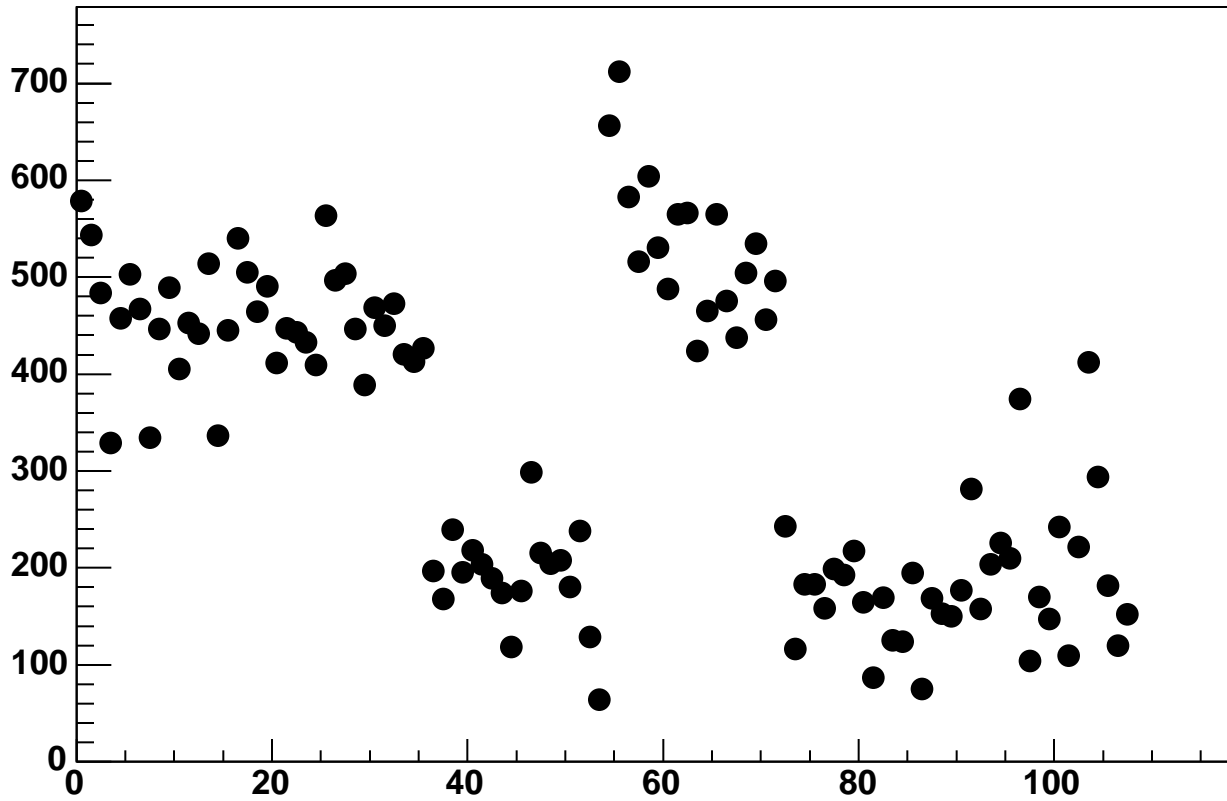
Enable 1, DAC=4000, Hold=245, ADC Mean vs 18\*Chip+Chan



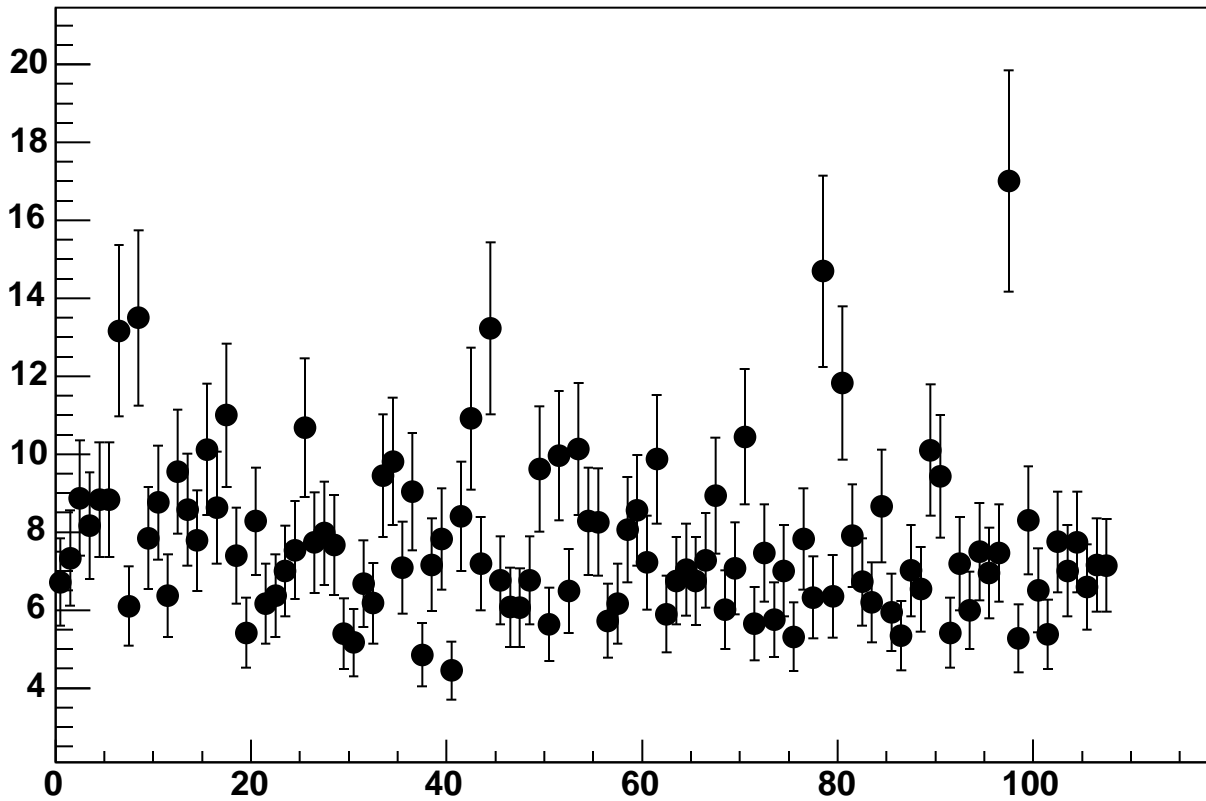
Enable 1, DAC=4000, Hold=245, ADC Noise vs 18\*Chip+Chan



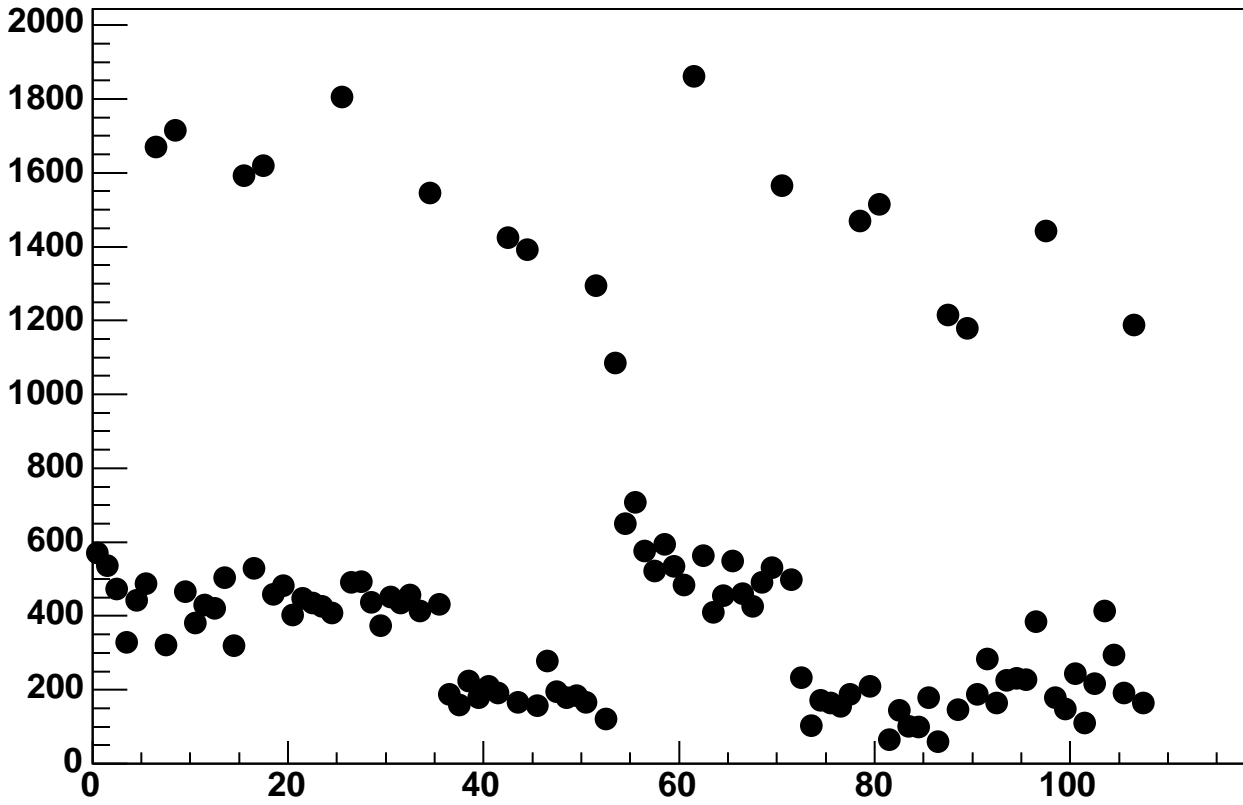
Enable 2, DAC=4000, Hold=0, ADC Mean vs 18\*Chip+Chan



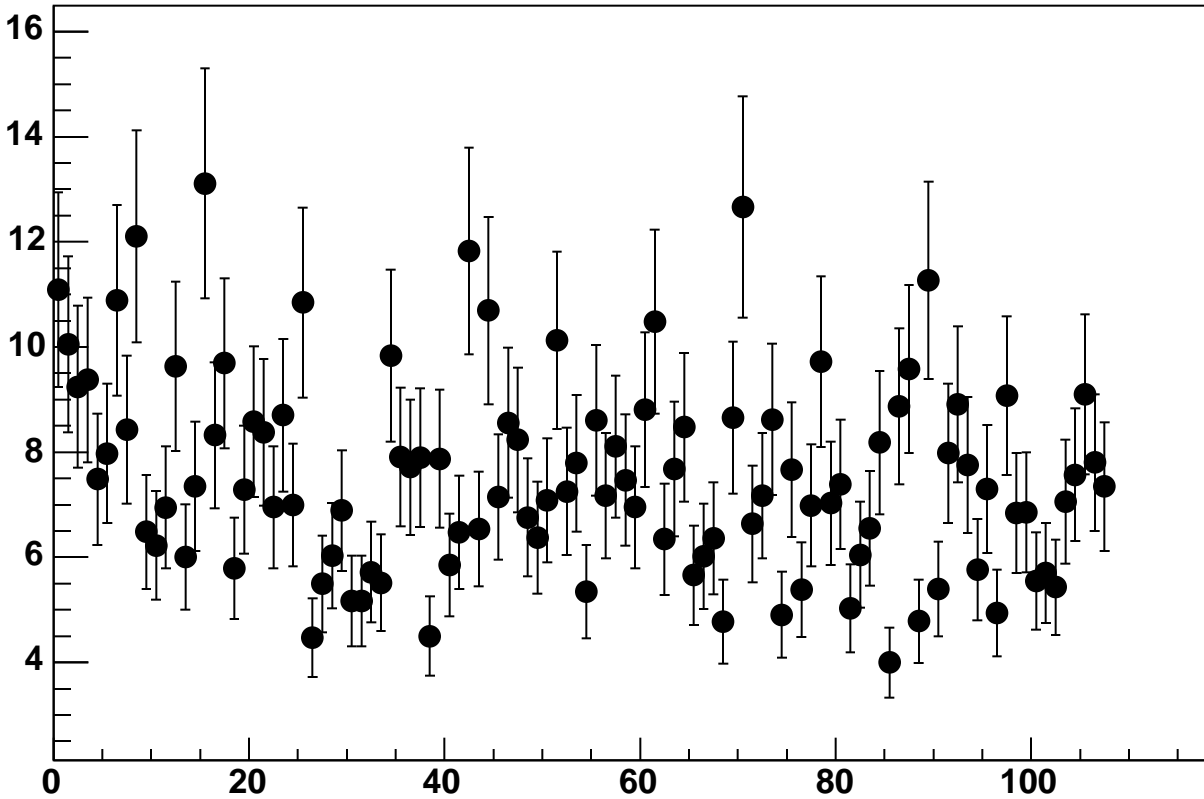
Enable 2, DAC=4000, Hold=0, ADC Noise vs 18\*Chip+Chan



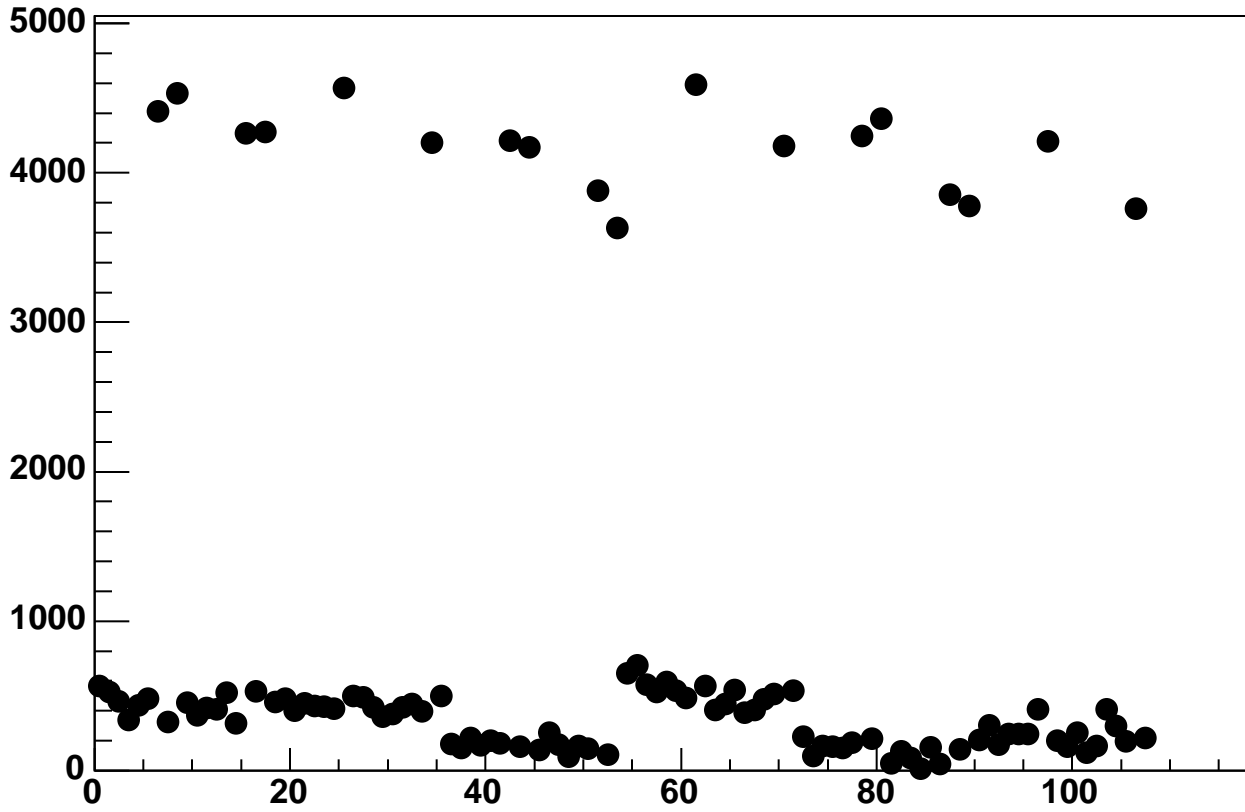
Enable 2, DAC=4000, Hold=5, ADC Mean vs 18\*Chip+Chan



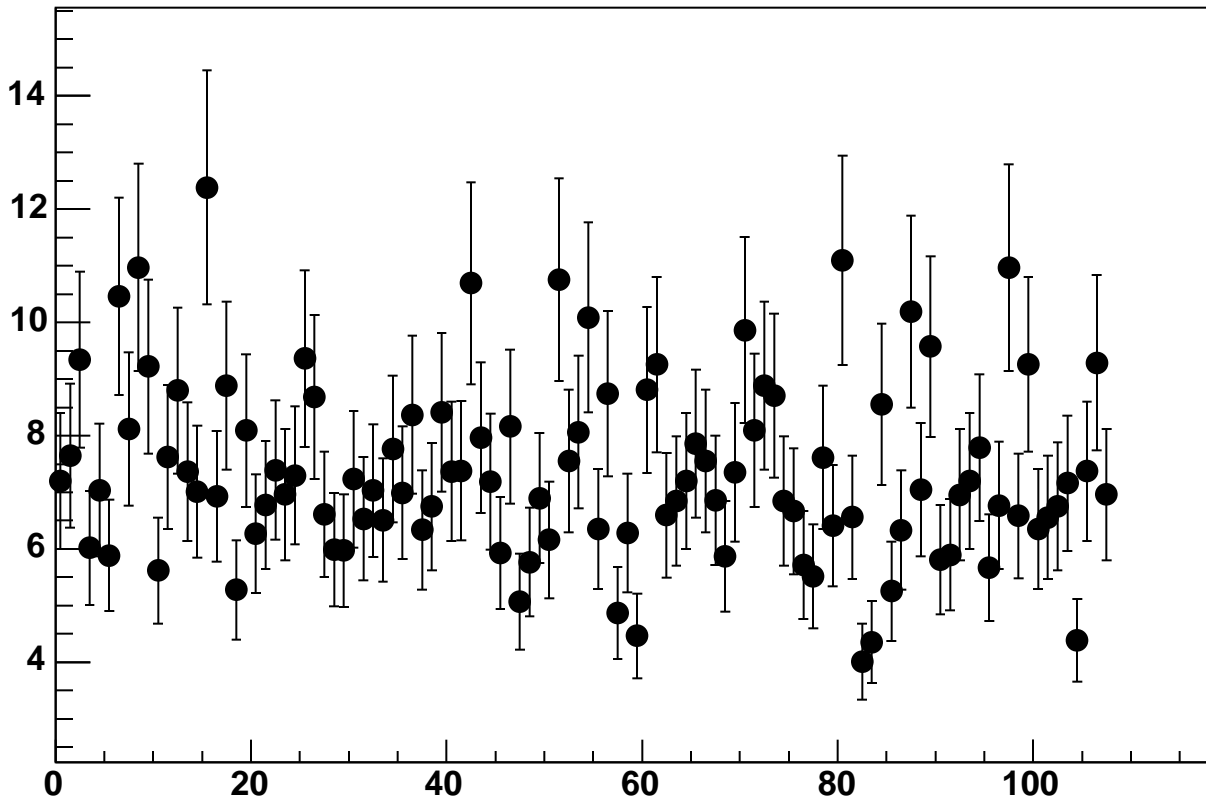
Enable 2, DAC=4000, Hold=5, ADC Noise vs 18\*Chip+Chan



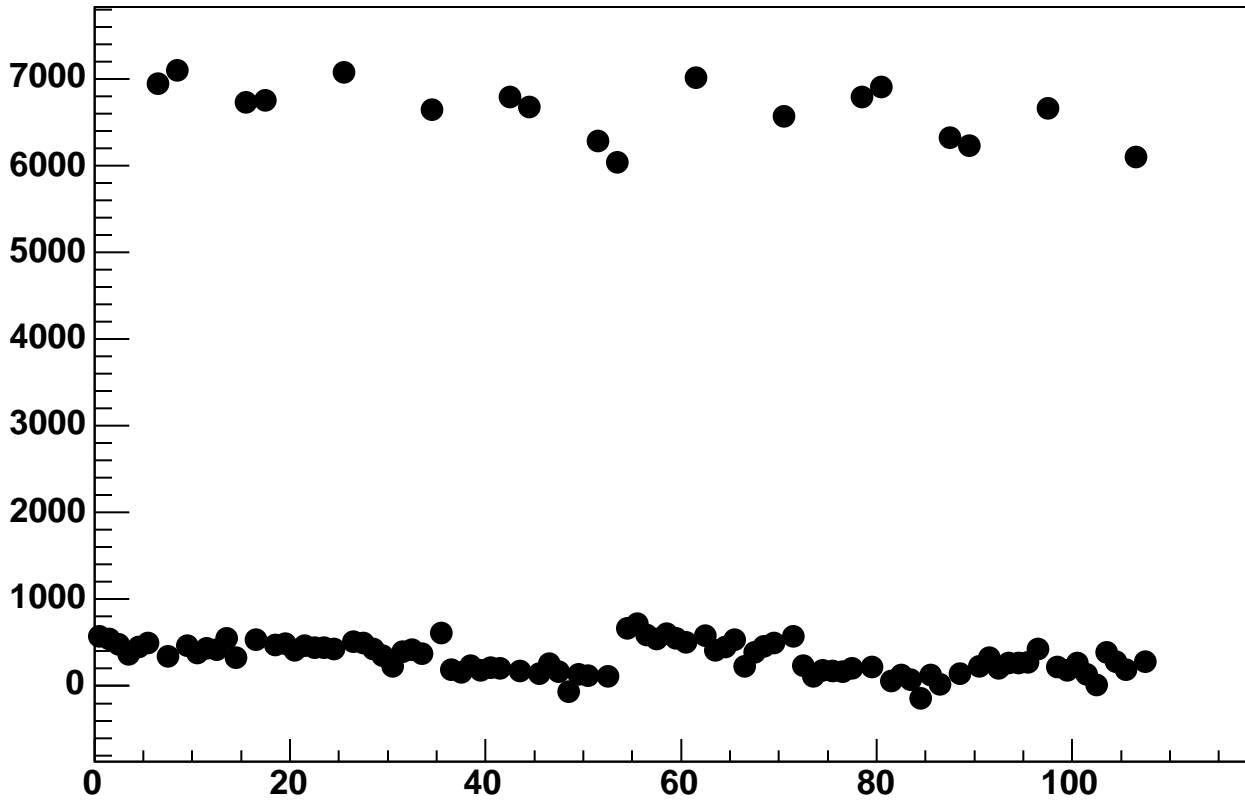
Enable 2, DAC=4000, Hold=10, ADC Mean vs 18\*Chip+Chan



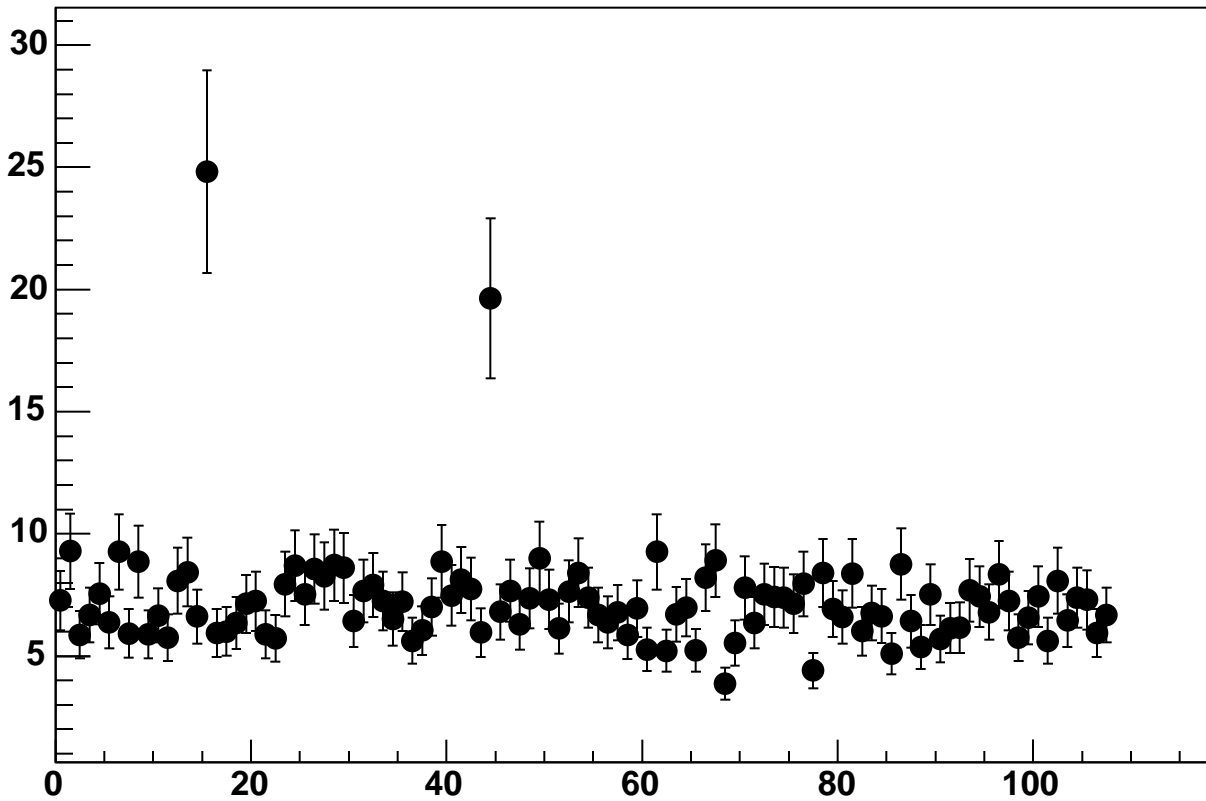
Enable 2, DAC=4000, Hold=10, ADC Noise vs 18\*Chip+Chan



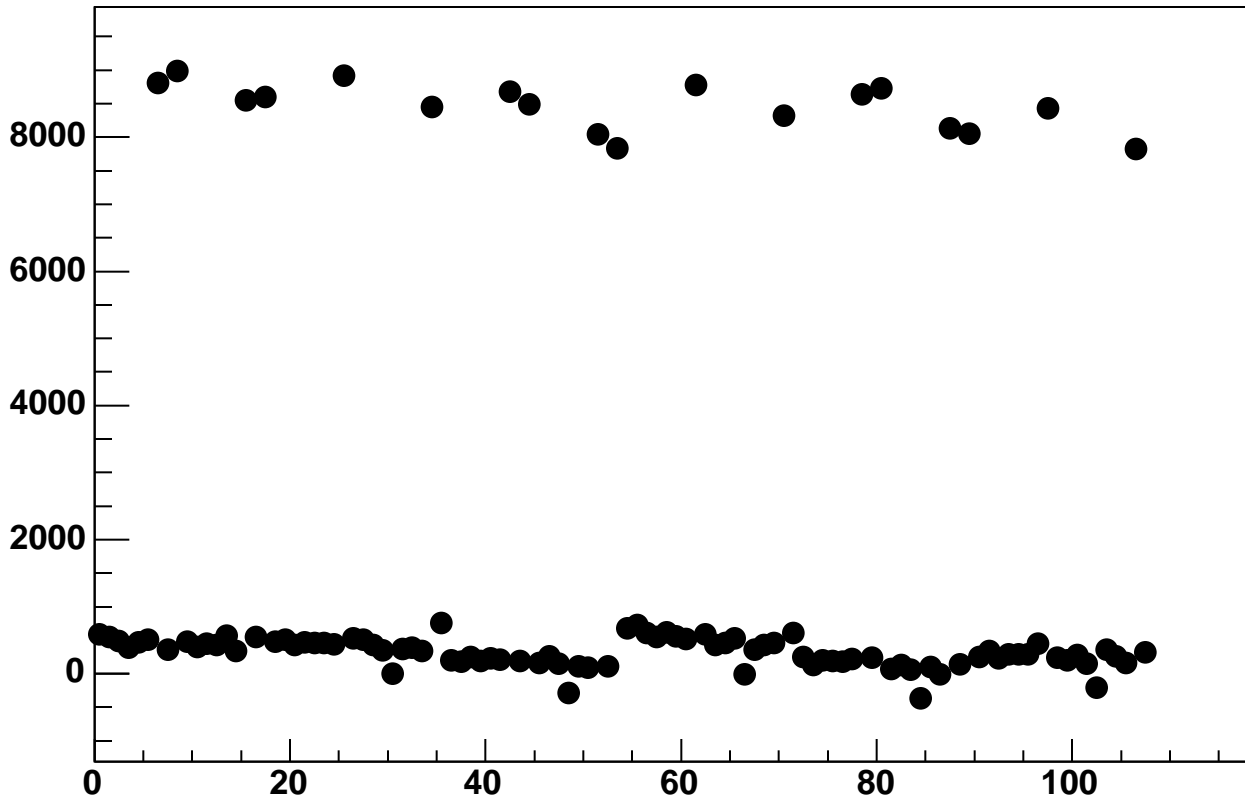
Enable 2, DAC=4000, Hold=15, ADC Mean vs 18\*Chip+Chan



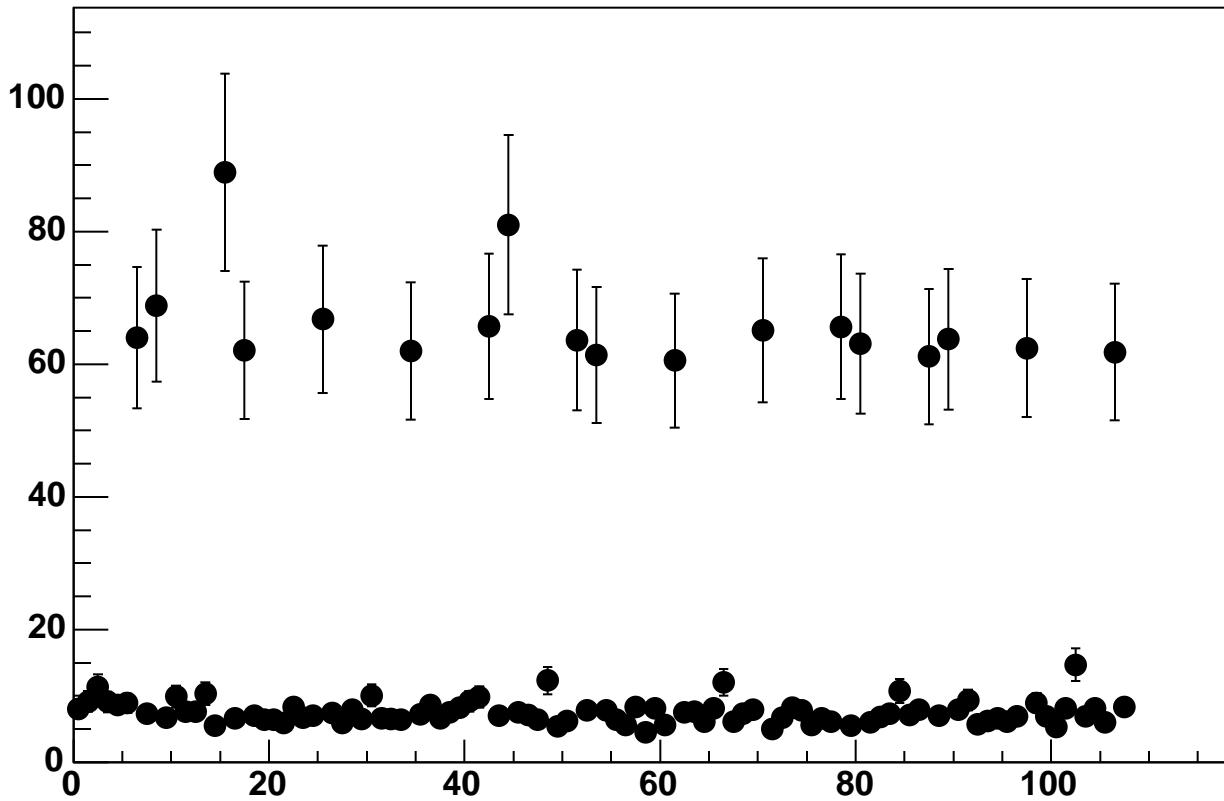
Enable 2, DAC=4000, Hold=15, ADC Noise vs 18\*Chip+Chan



Enable 2, DAC=4000, Hold=20, ADC Mean vs 18\*Chip+Chan

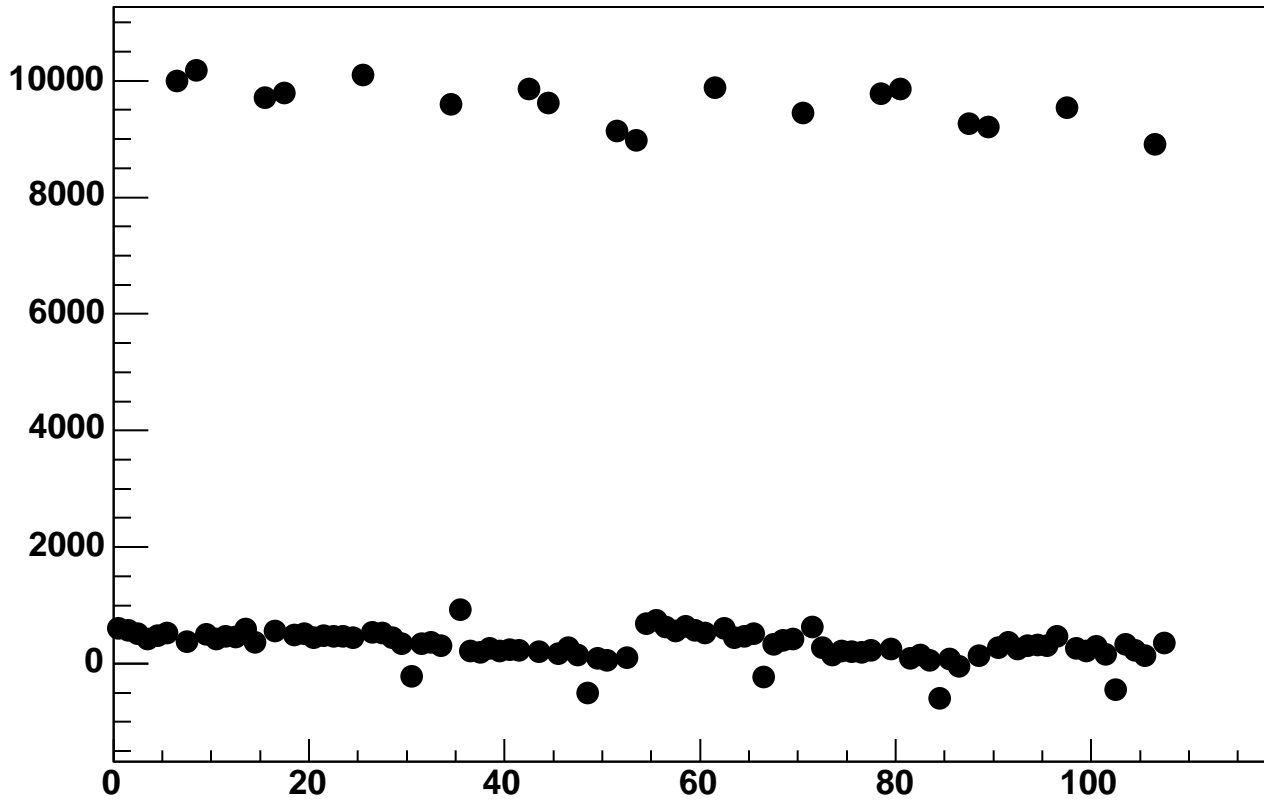


Enable 2, DAC=4000, Hold=20, ADC Noise vs 18\*Chip+Chan

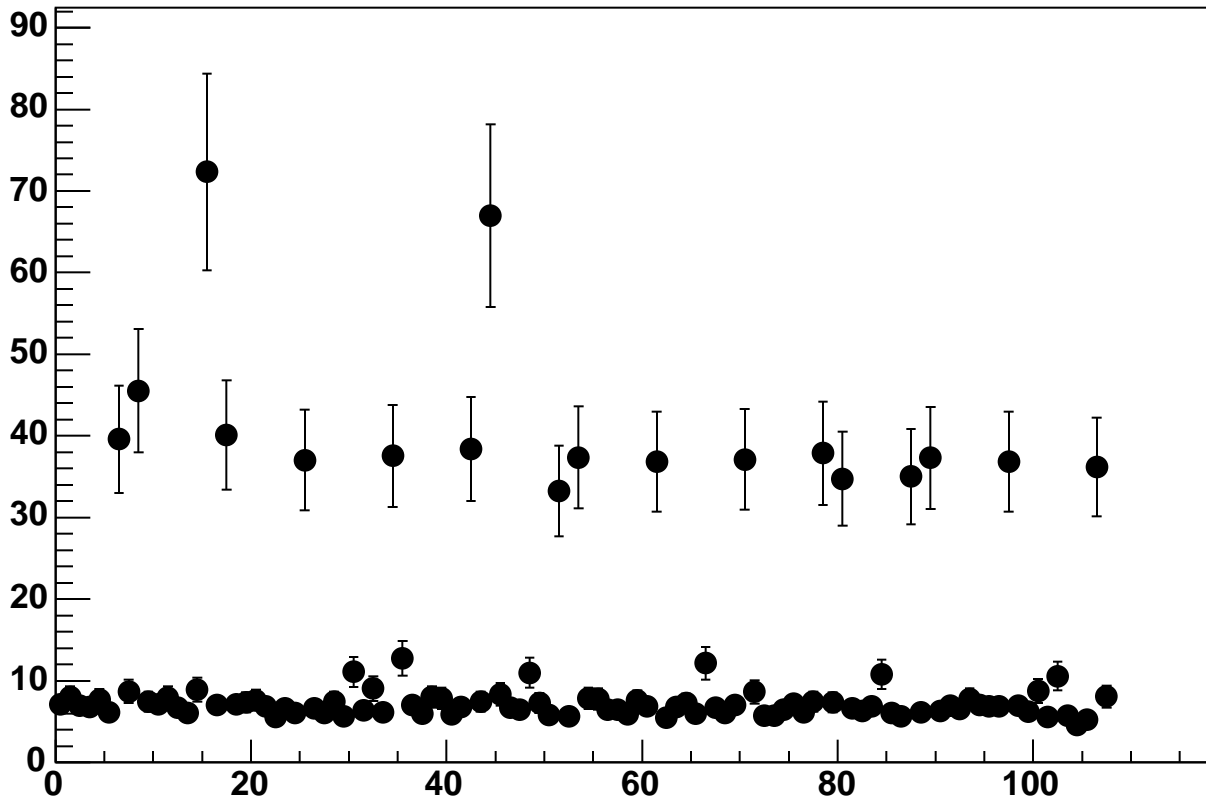




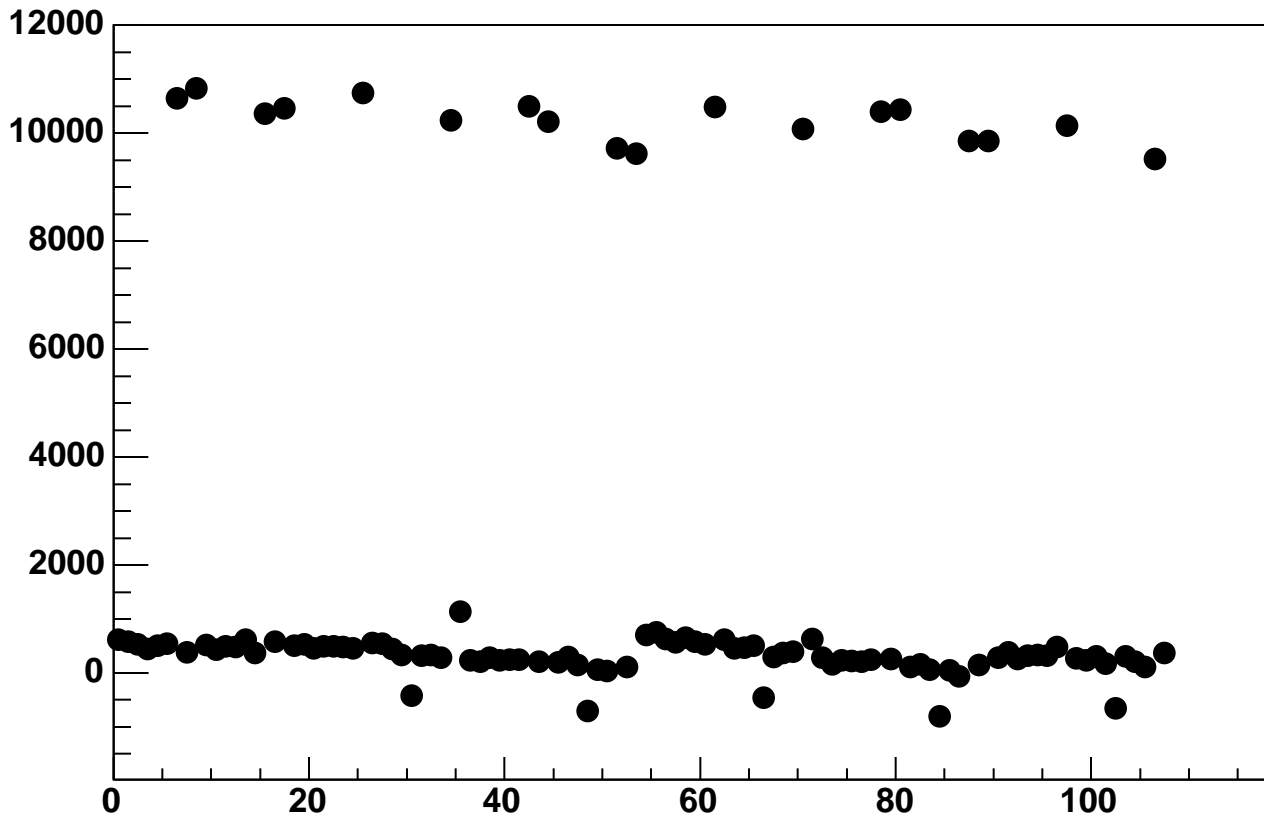
Enable 2, DAC=4000, Hold=25, ADC Mean vs 18\*Chip+Chan



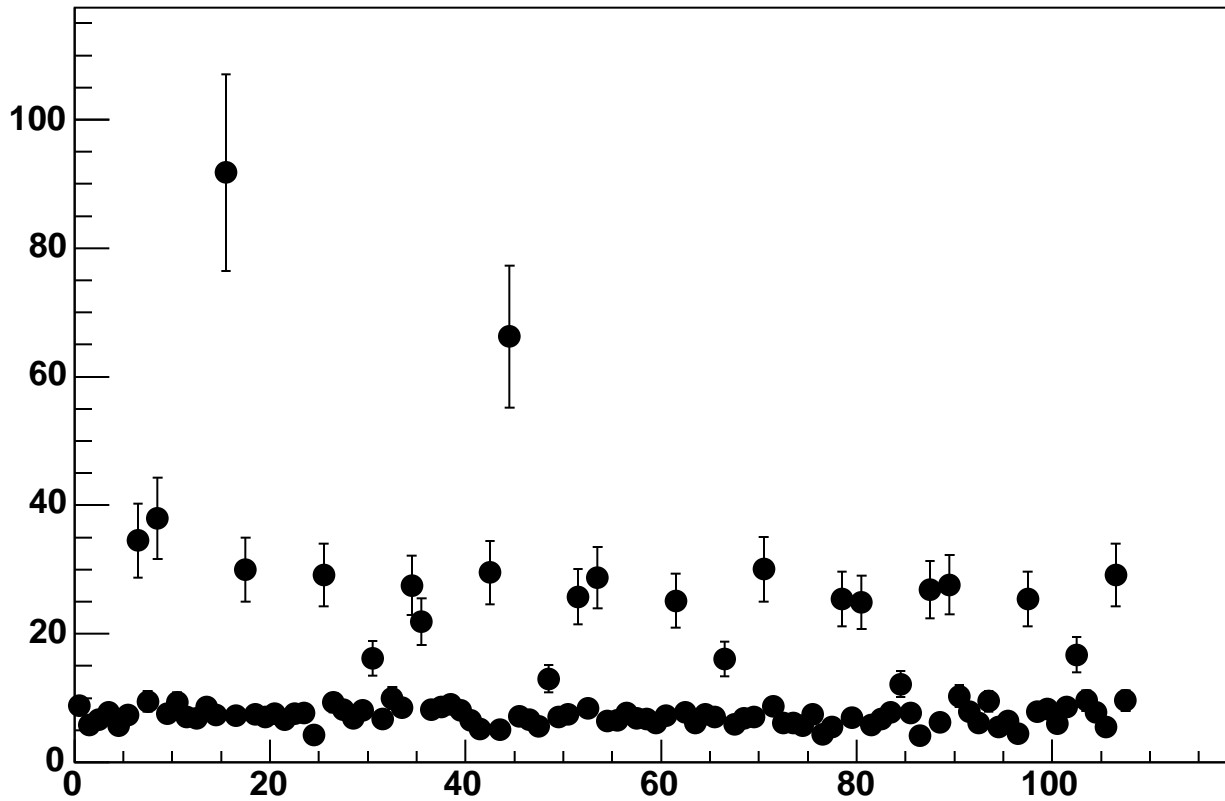
Enable 2, DAC=4000, Hold=25, ADC Noise vs 18\*Chip+Chan



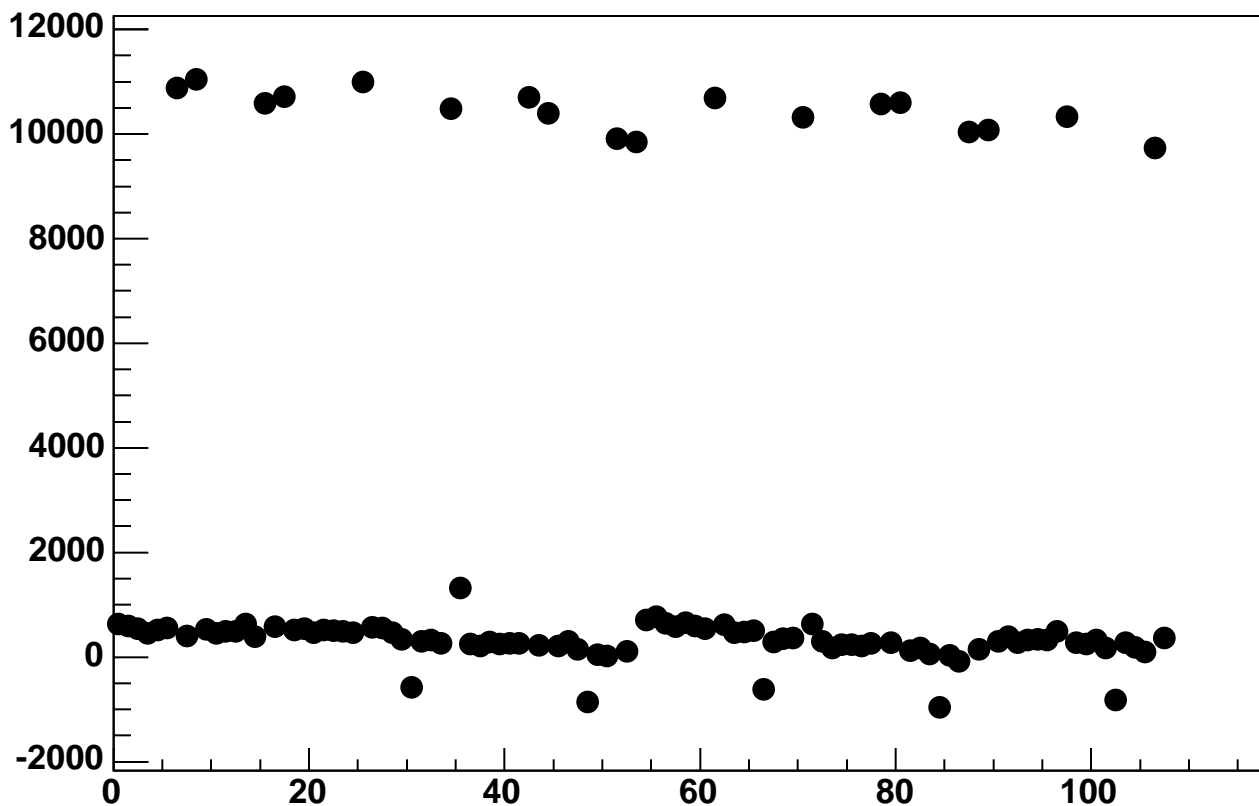
Enable 2, DAC=4000, Hold=30, ADC Mean vs 18\*Chip+Chan



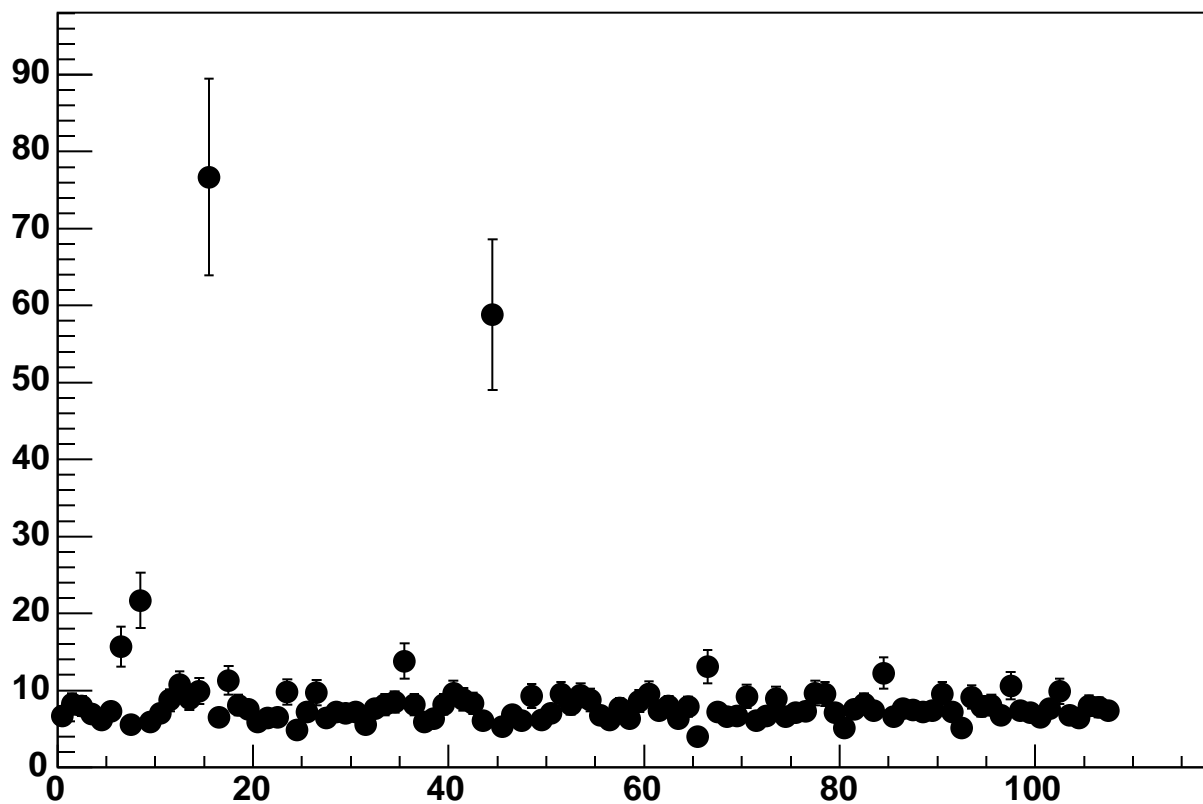
Enable 2, DAC=4000, Hold=30, ADC Noise vs 18\*Chip+Chan



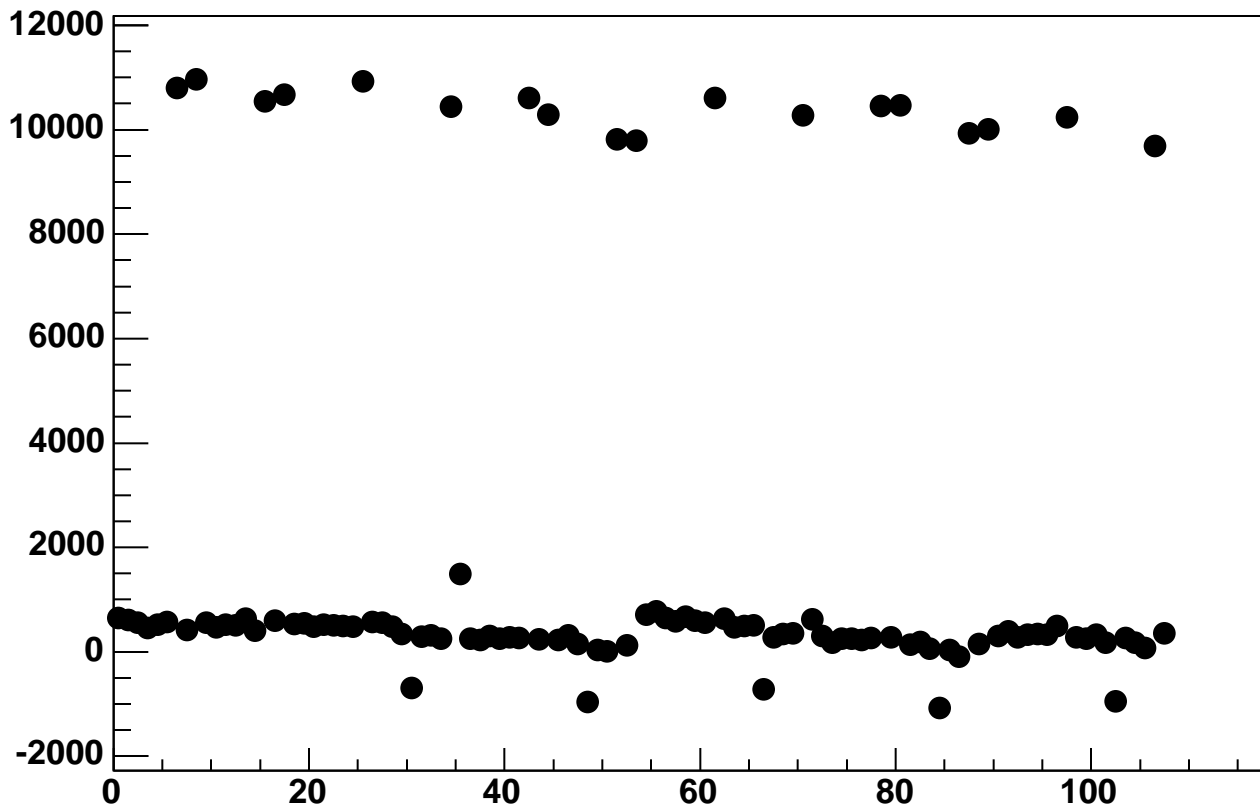
Enable 2, DAC=4000, Hold=35, ADC Mean vs 18\*Chip+Chan



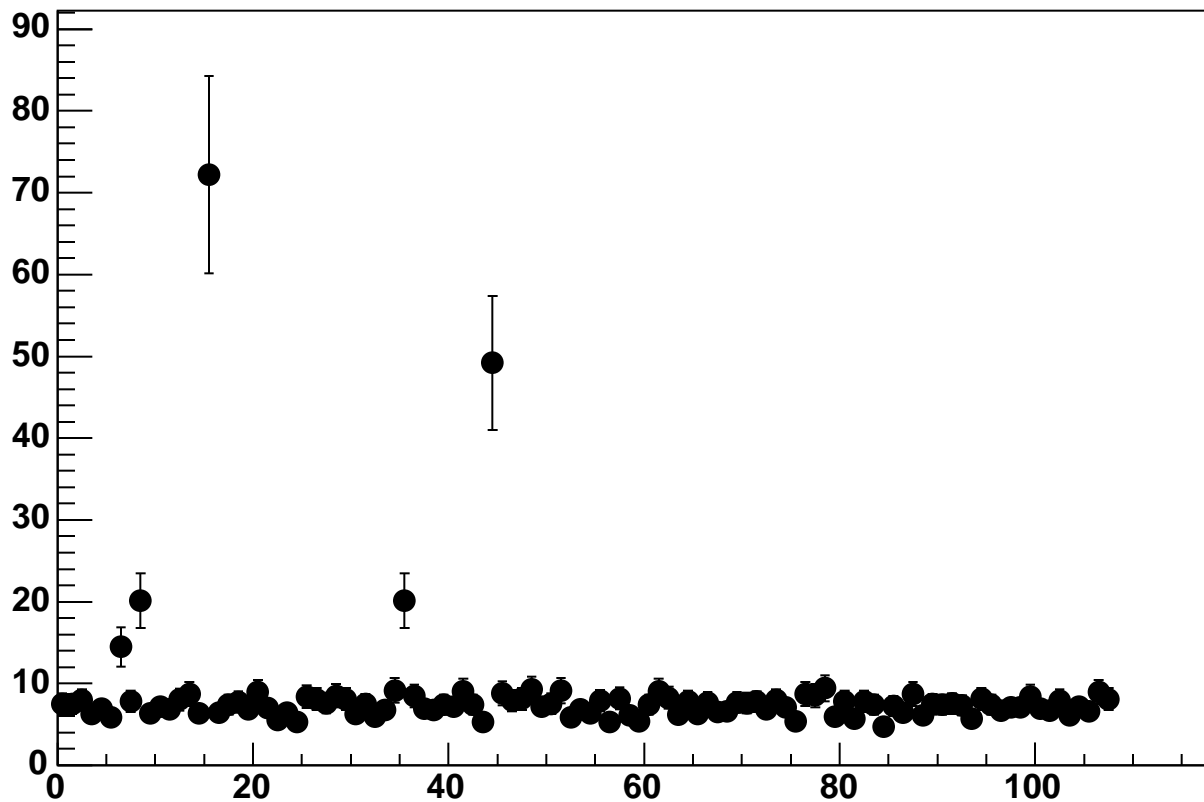
Enable 2, DAC=4000, Hold=35, ADC Noise vs 18\*Chip+Chan



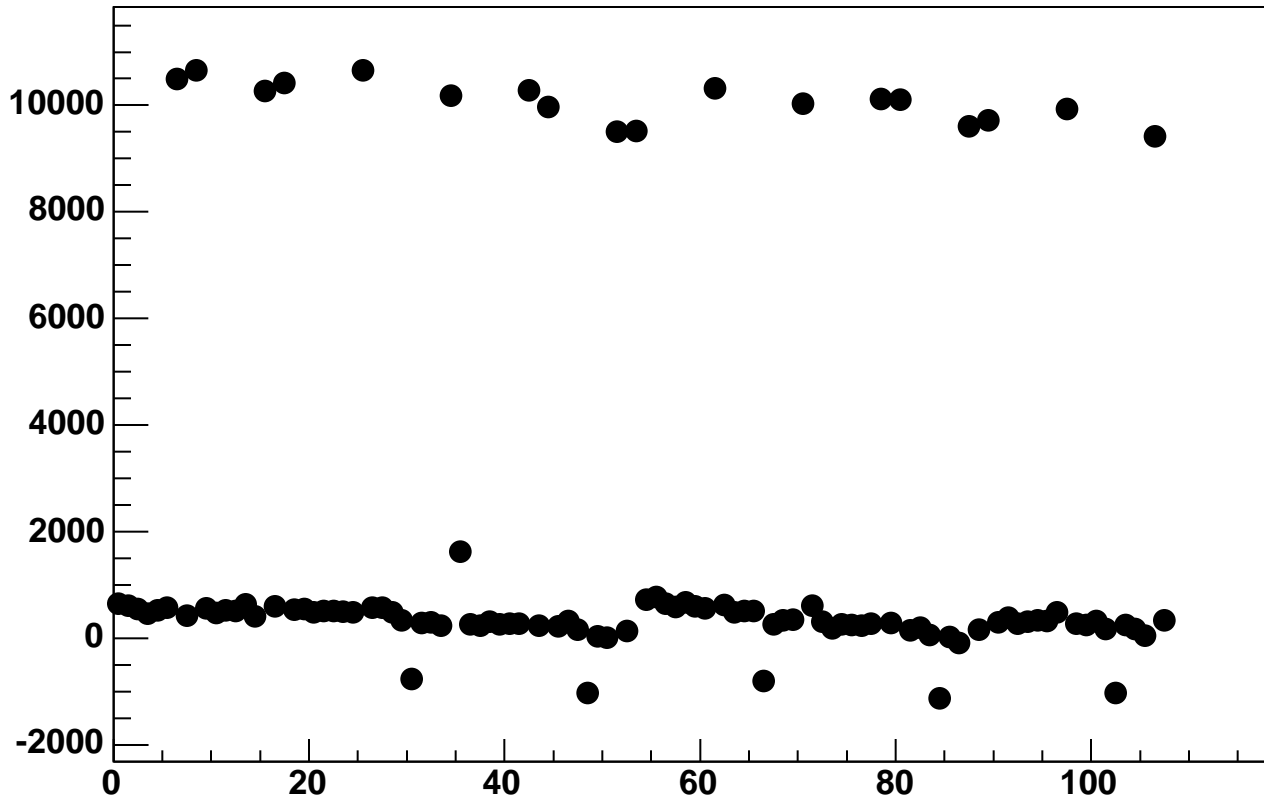
Enable 2, DAC=4000, Hold=40, ADC Mean vs 18\*Chip+Chan



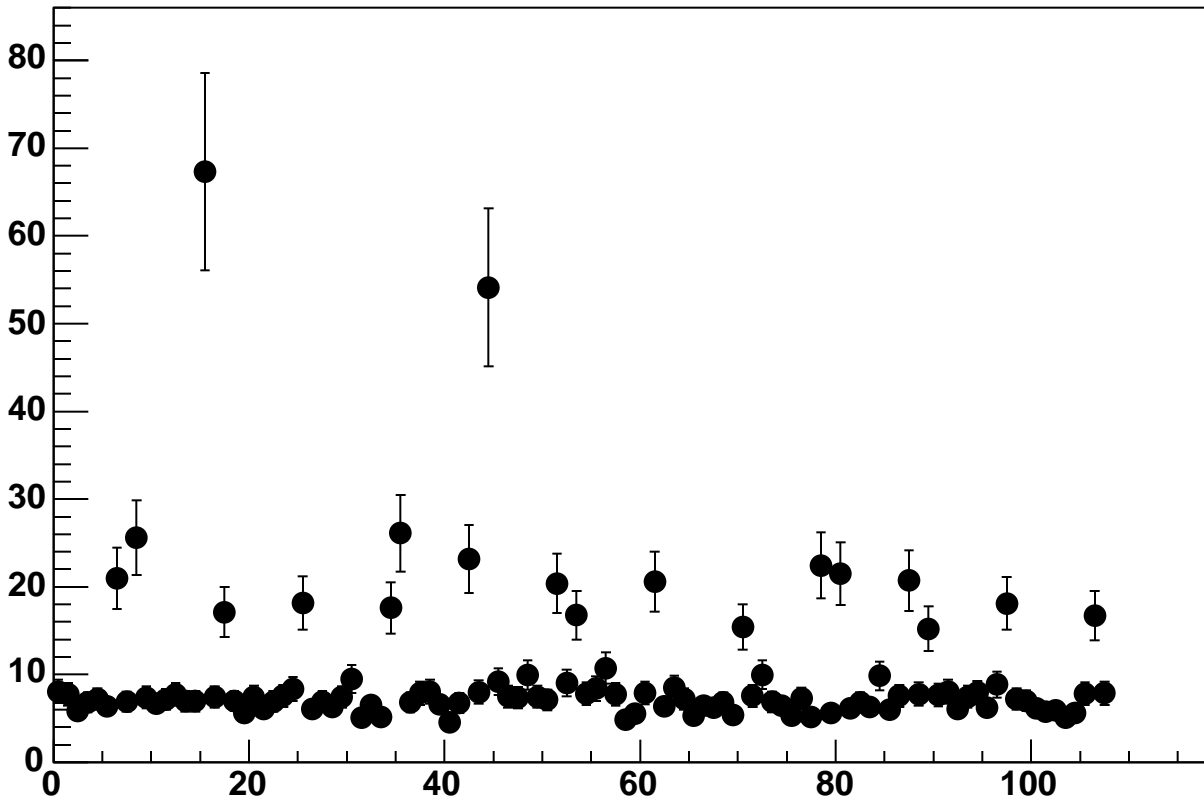
Enable 2, DAC=4000, Hold=40, ADC Noise vs 18\*Chip+Chan



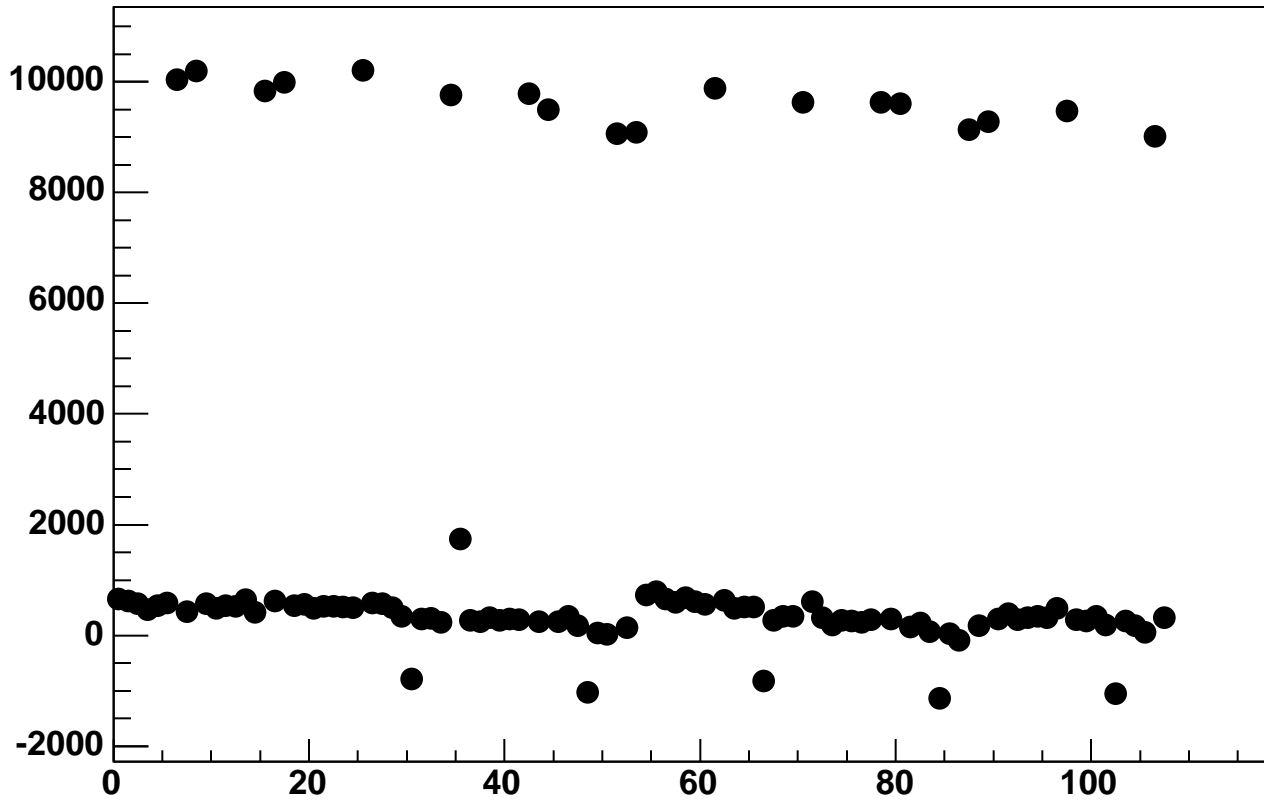
Enable 2, DAC=4000, Hold=45, ADC Mean vs 18\*Chip+Chan



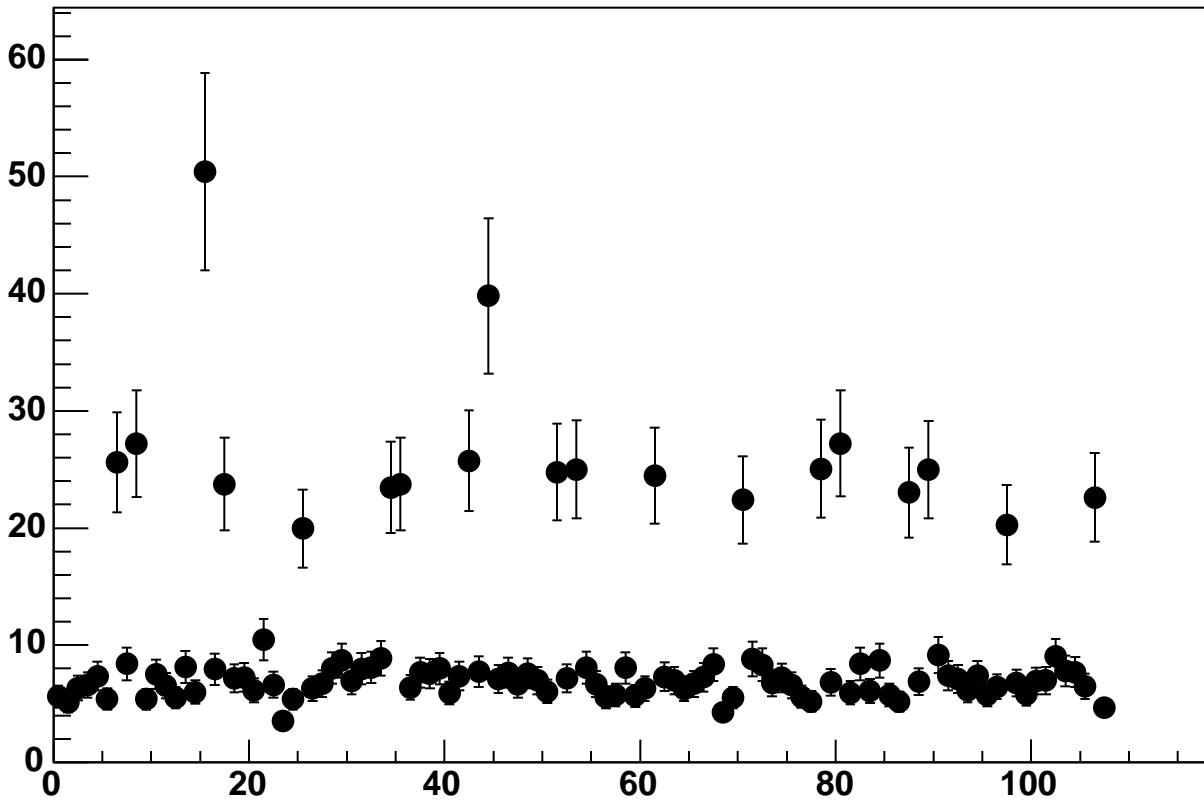
Enable 2, DAC=4000, Hold=45, ADC Noise vs 18\*Chip+Chan



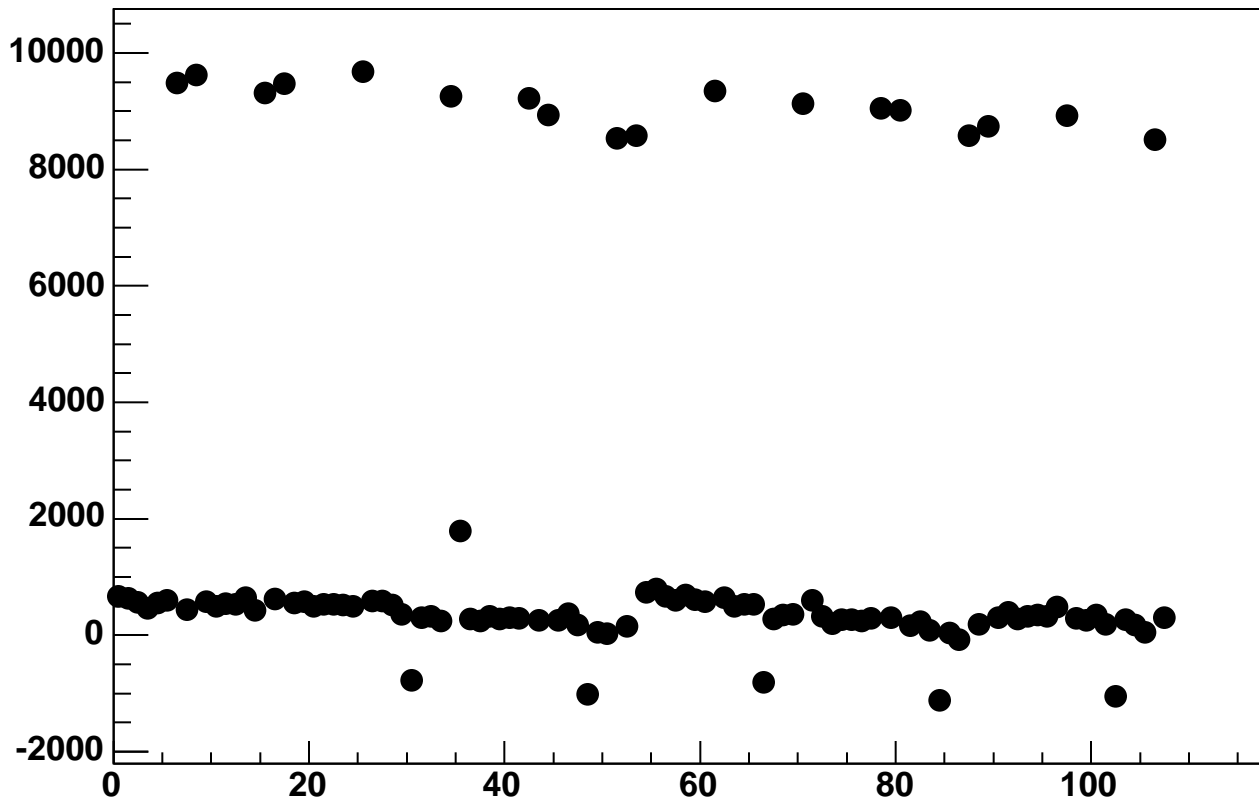
Enable 2, DAC=4000, Hold=50, ADC Mean vs 18\*Chip+Chan



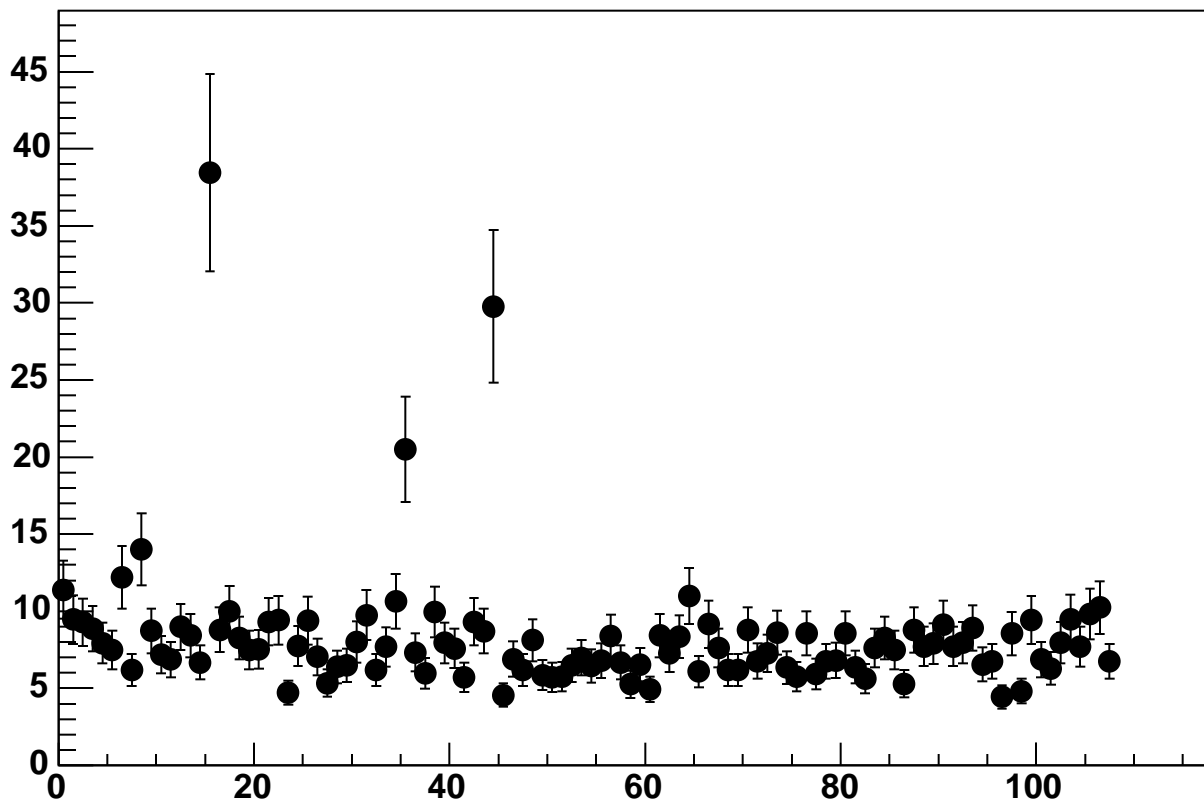
Enable 2, DAC=4000, Hold=50, ADC Noise vs 18\*Chip+Chan



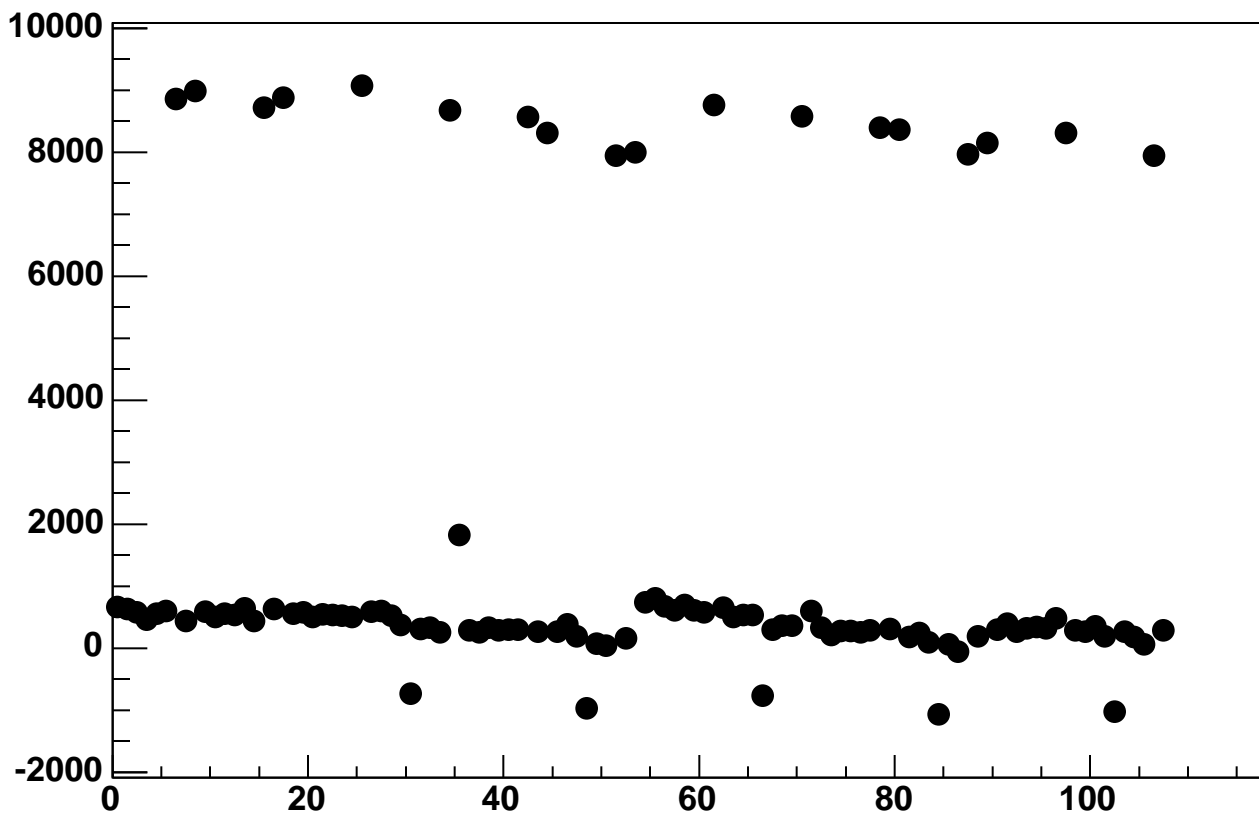
Enable 2, DAC=4000, Hold=55, ADC Mean vs 18\*Chip+Chan



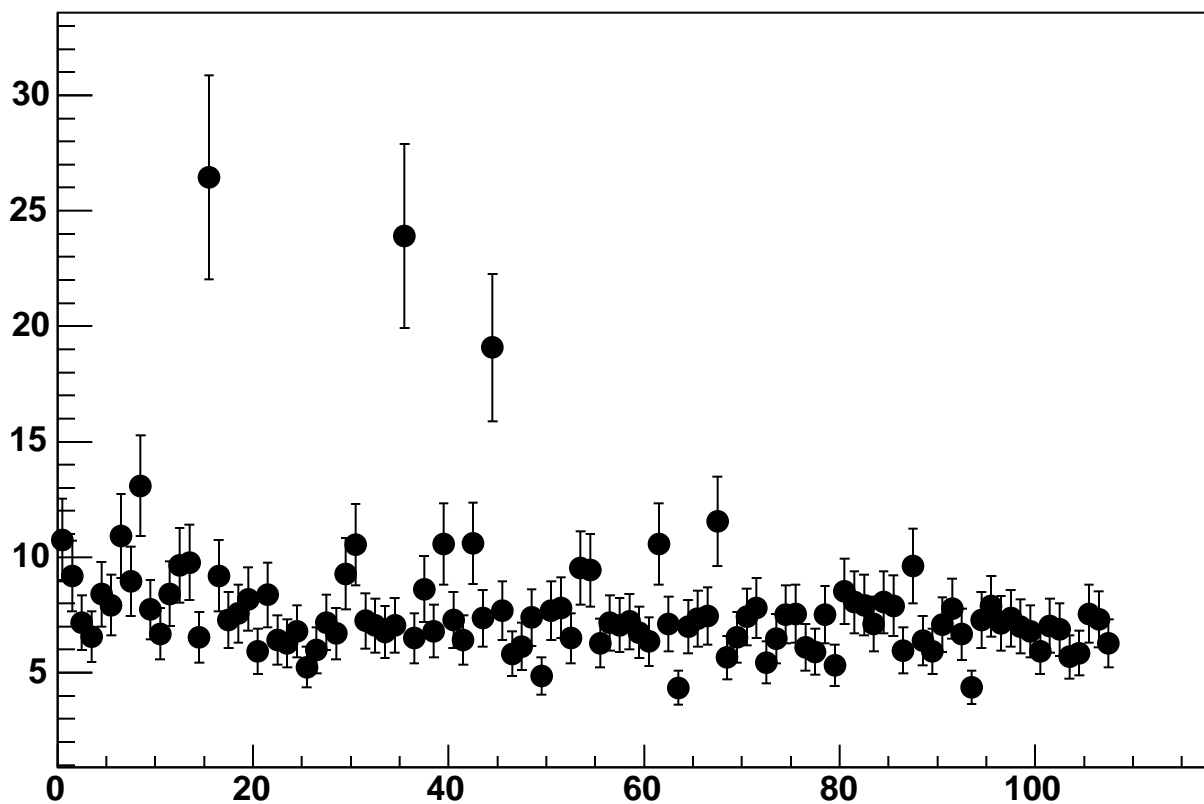
Enable 2, DAC=4000, Hold=55, ADC Noise vs 18\*Chip+Chan



Enable 2, DAC=4000, Hold=60, ADC Mean vs 18\*Chip+Chan

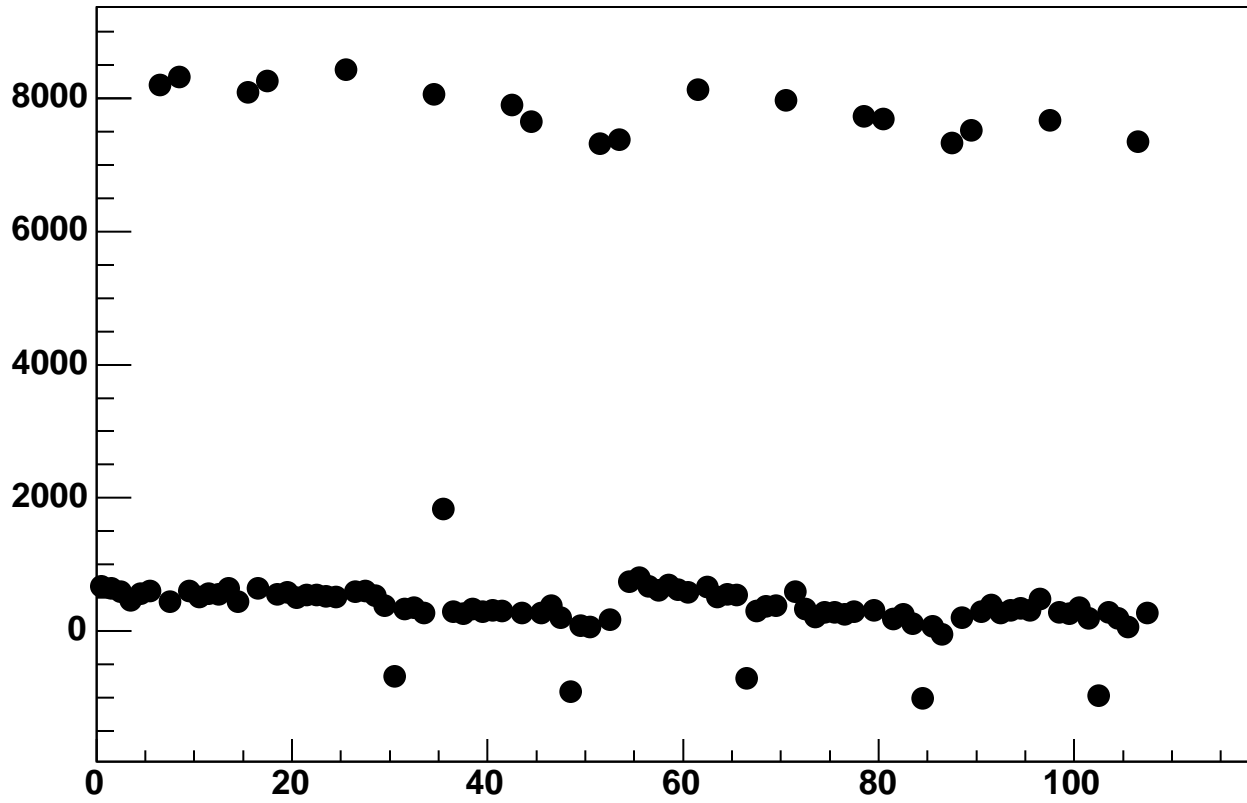


Enable 2, DAC=4000, Hold=60, ADC Noise vs 18\*Chip+Chan

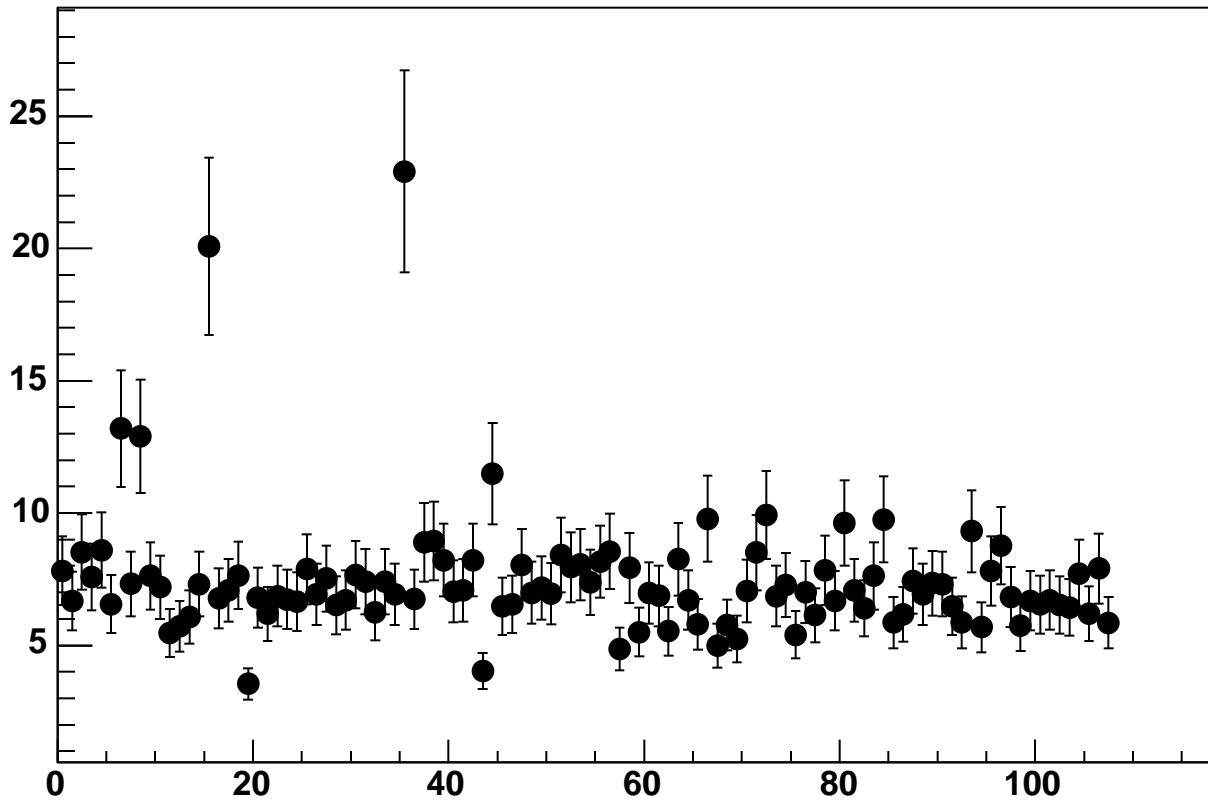




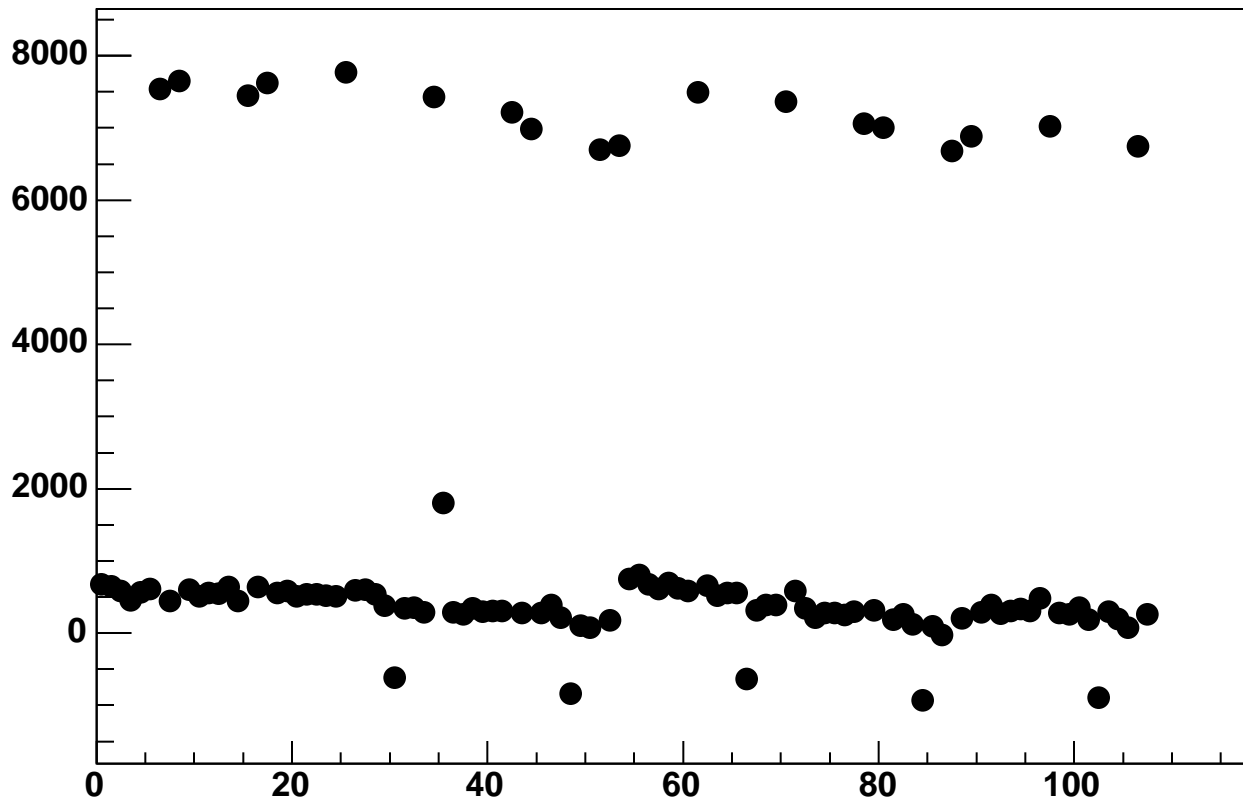
Enable 2, DAC=4000, Hold=65, ADC Mean vs 18\*Chip+Chan



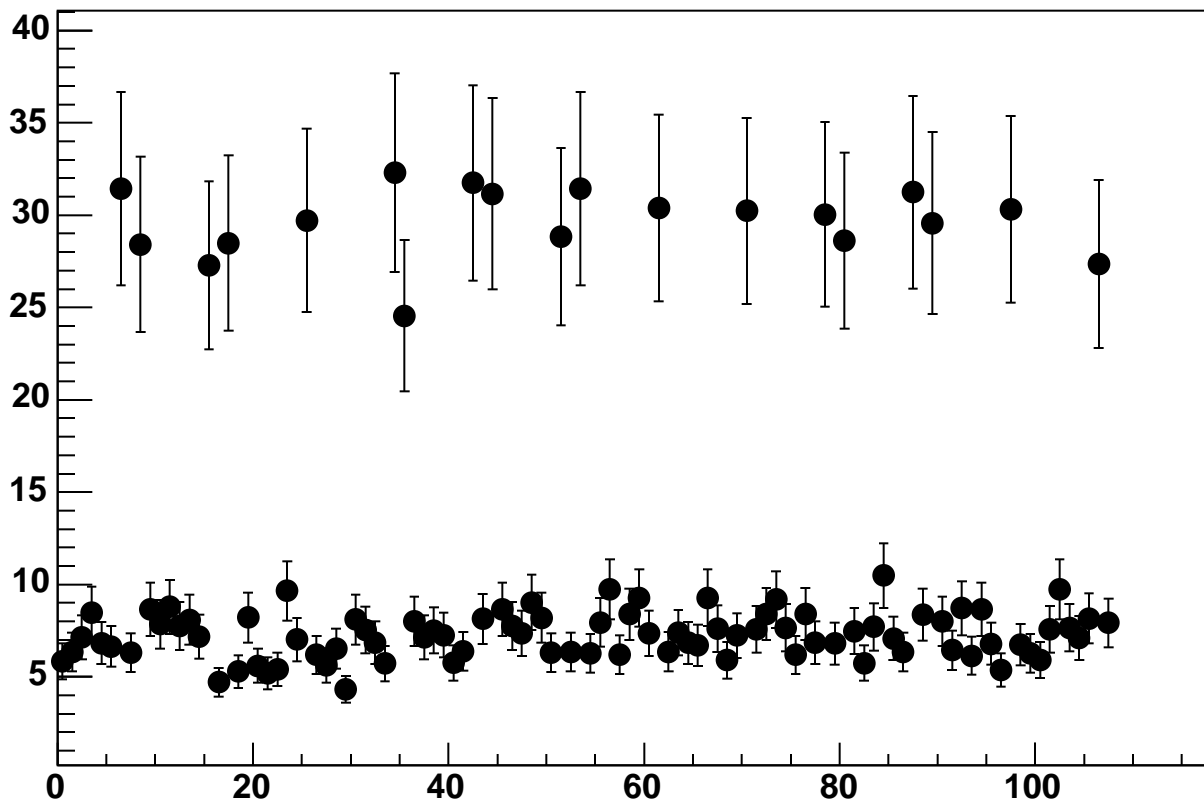
Enable 2, DAC=4000, Hold=65, ADC Noise vs 18\*Chip+Chan



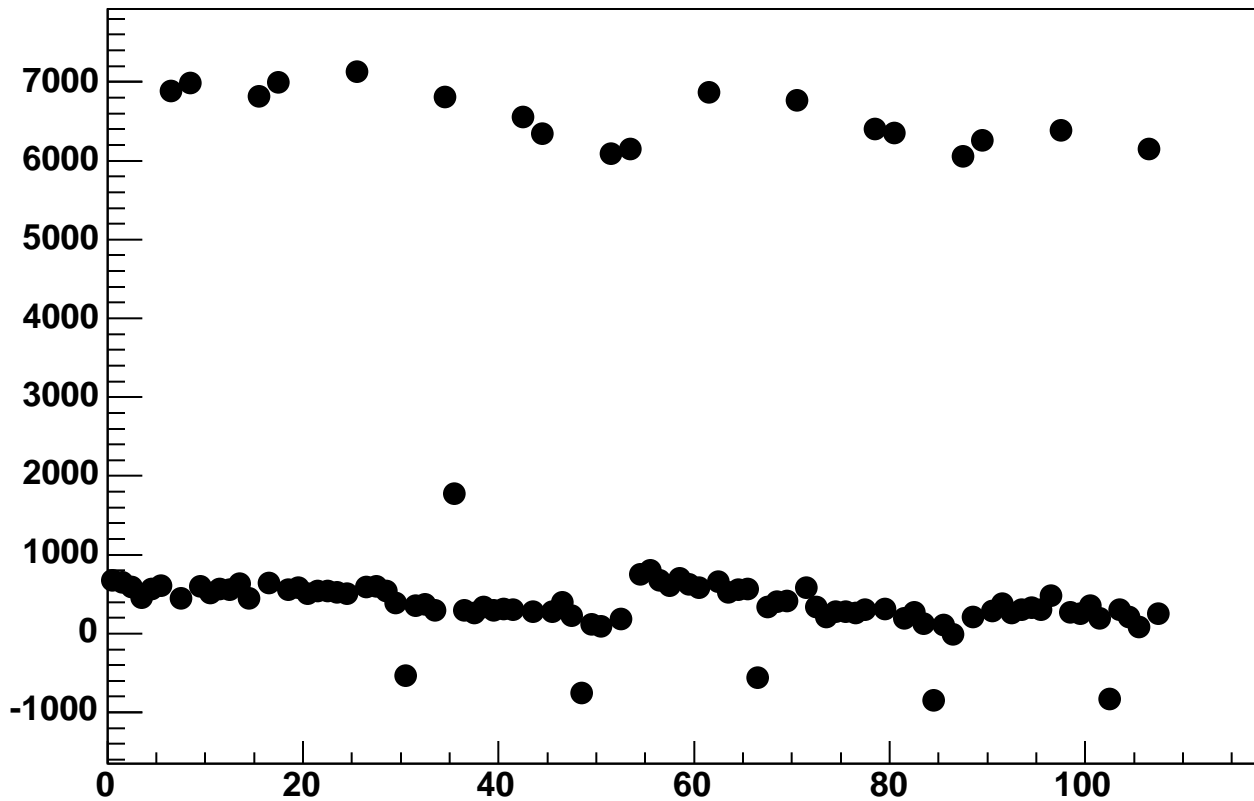
Enable 2, DAC=4000, Hold=70, ADC Mean vs 18\*Chip+Chan



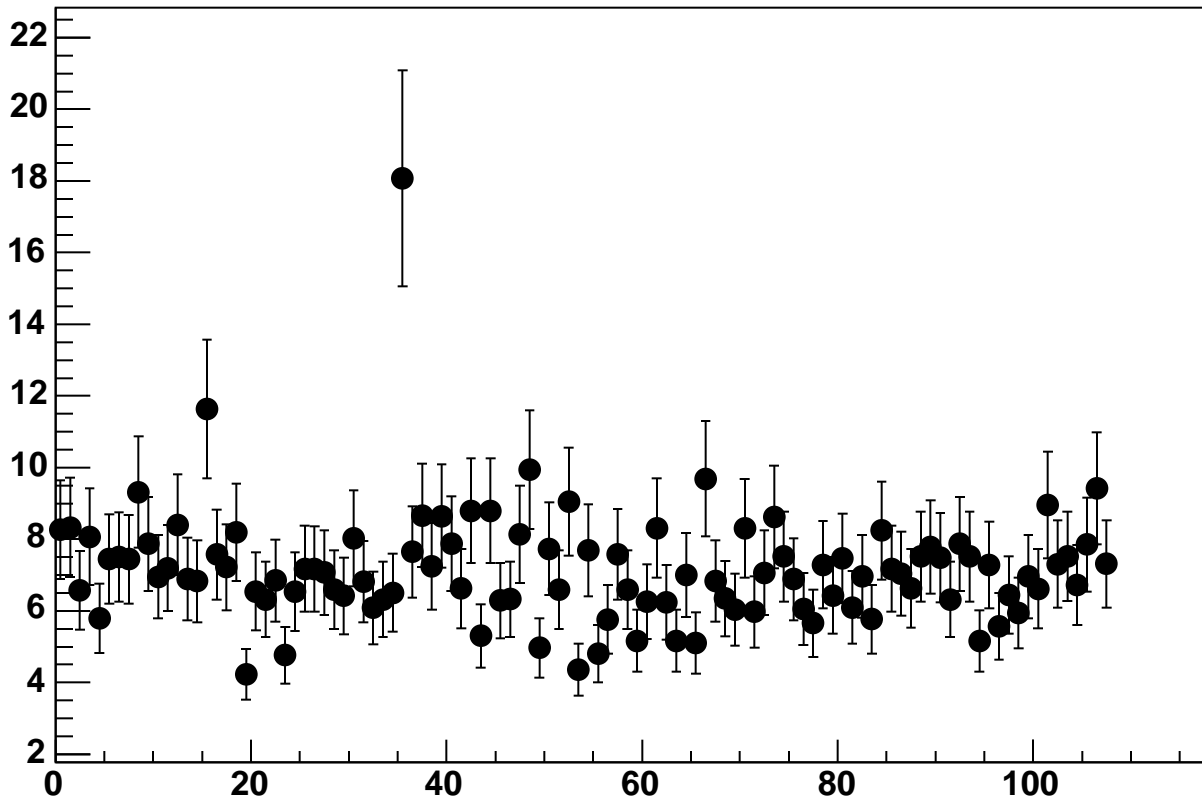
Enable 2, DAC=4000, Hold=70, ADC Noise vs 18\*Chip+Chan



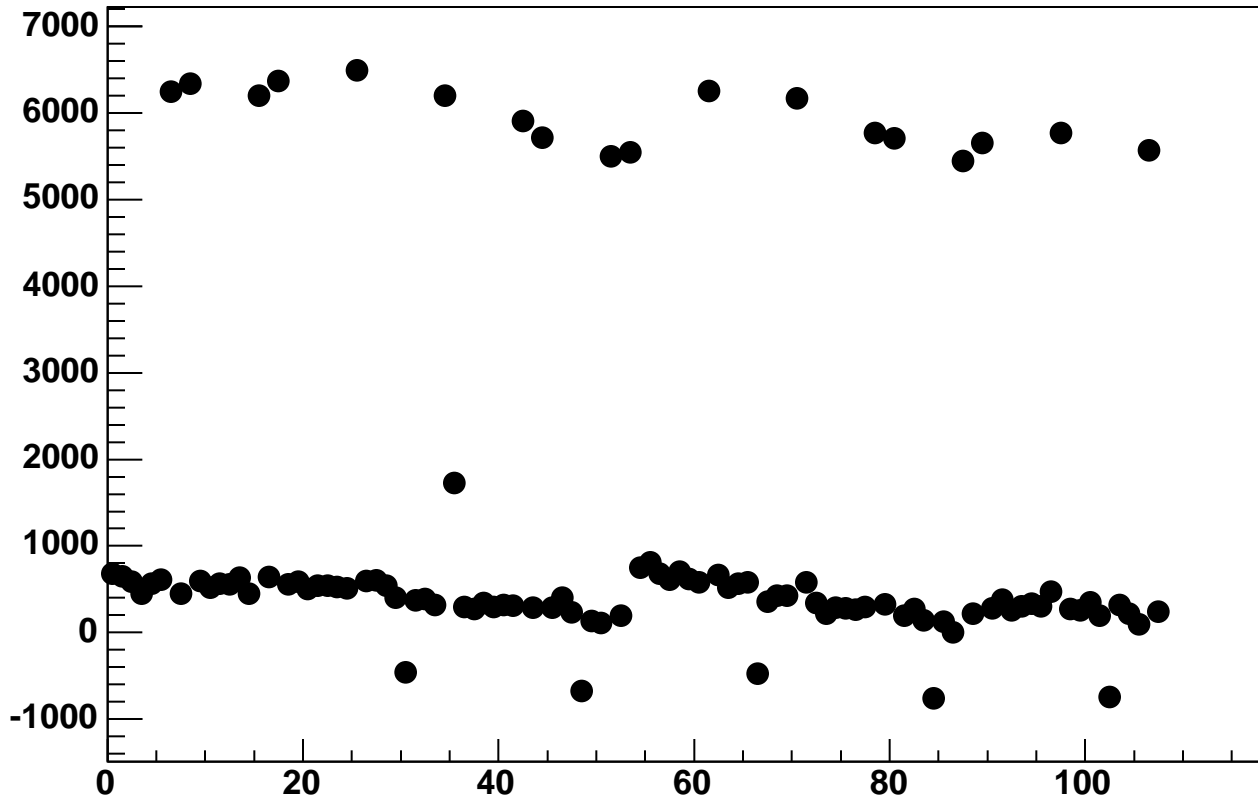
Enable 2, DAC=4000, Hold=75, ADC Mean vs 18\*Chip+Chan



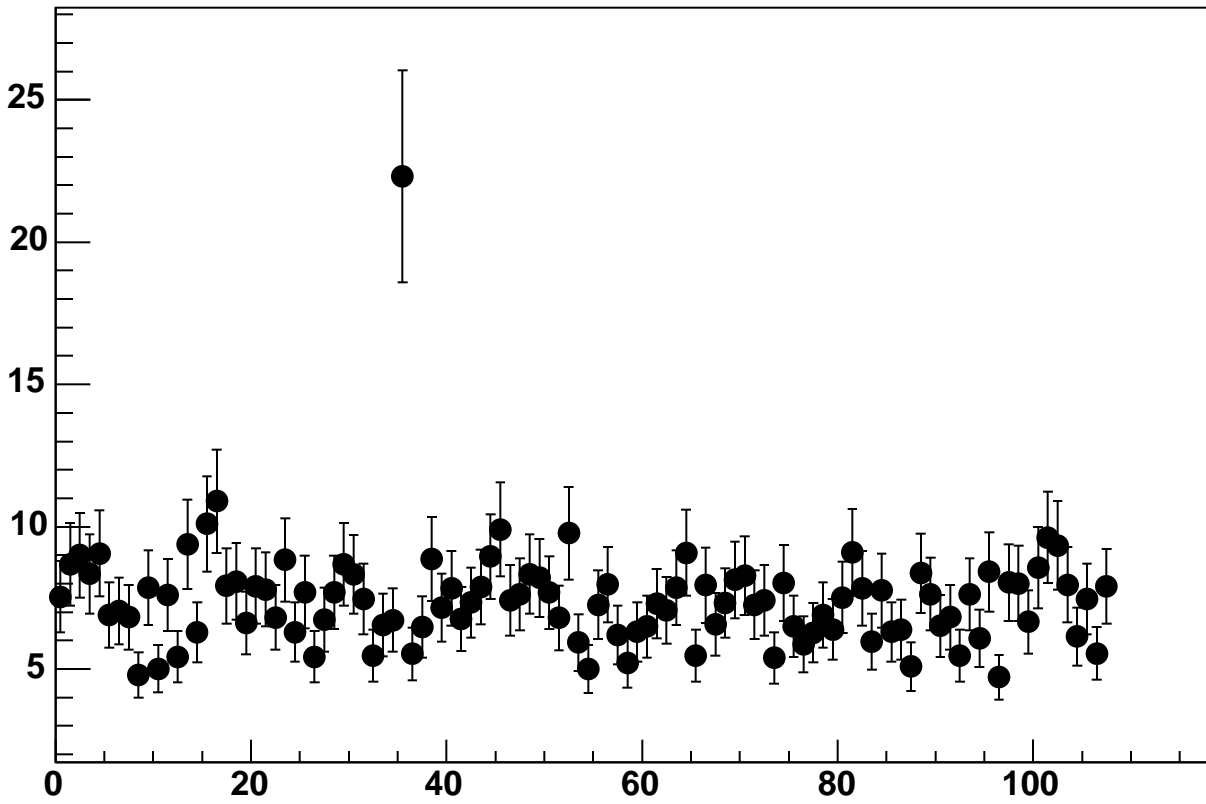
Enable 2, DAC=4000, Hold=75, ADC Noise vs 18\*Chip+Chan



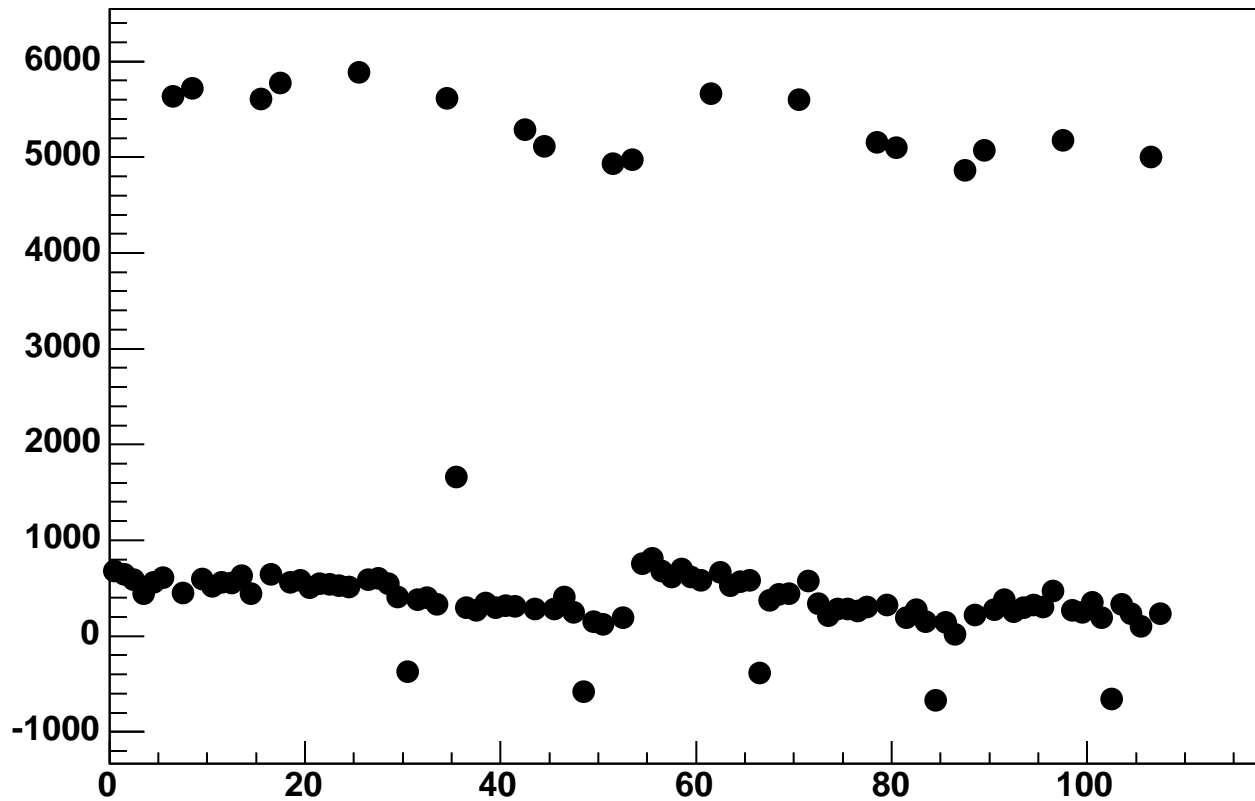
Enable 2, DAC=4000, Hold=80, ADC Mean vs 18\*Chip+Chan



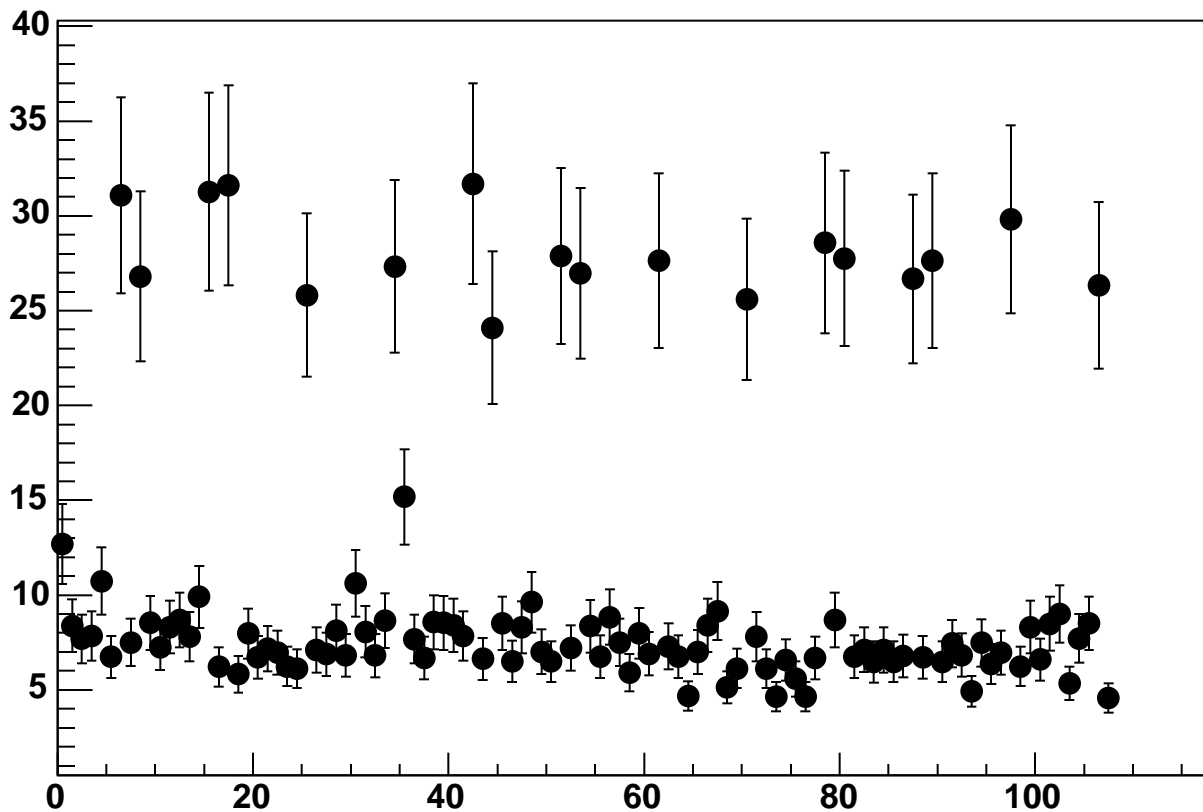
Enable 2, DAC=4000, Hold=80, ADC Noise vs 18\*Chip+Chan



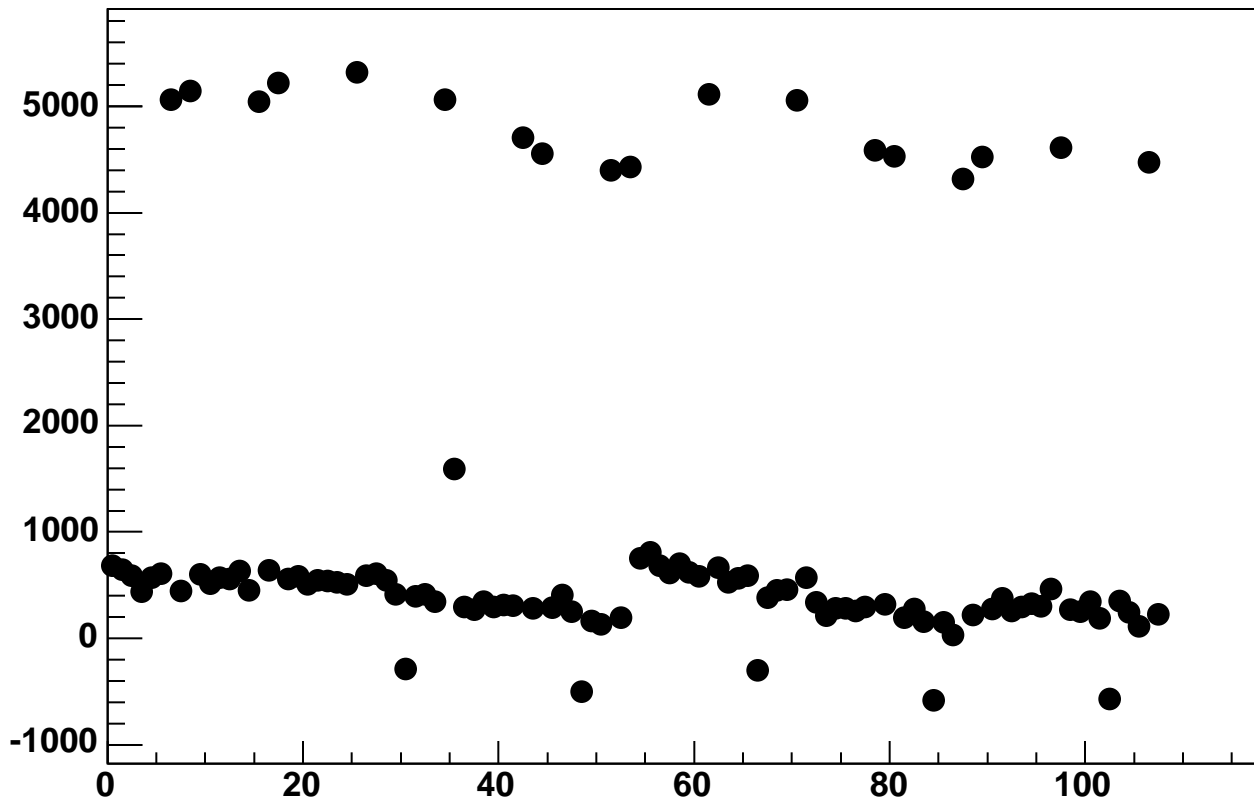
Enable 2, DAC=4000, Hold=85, ADC Mean vs 18\*Chip+Chan



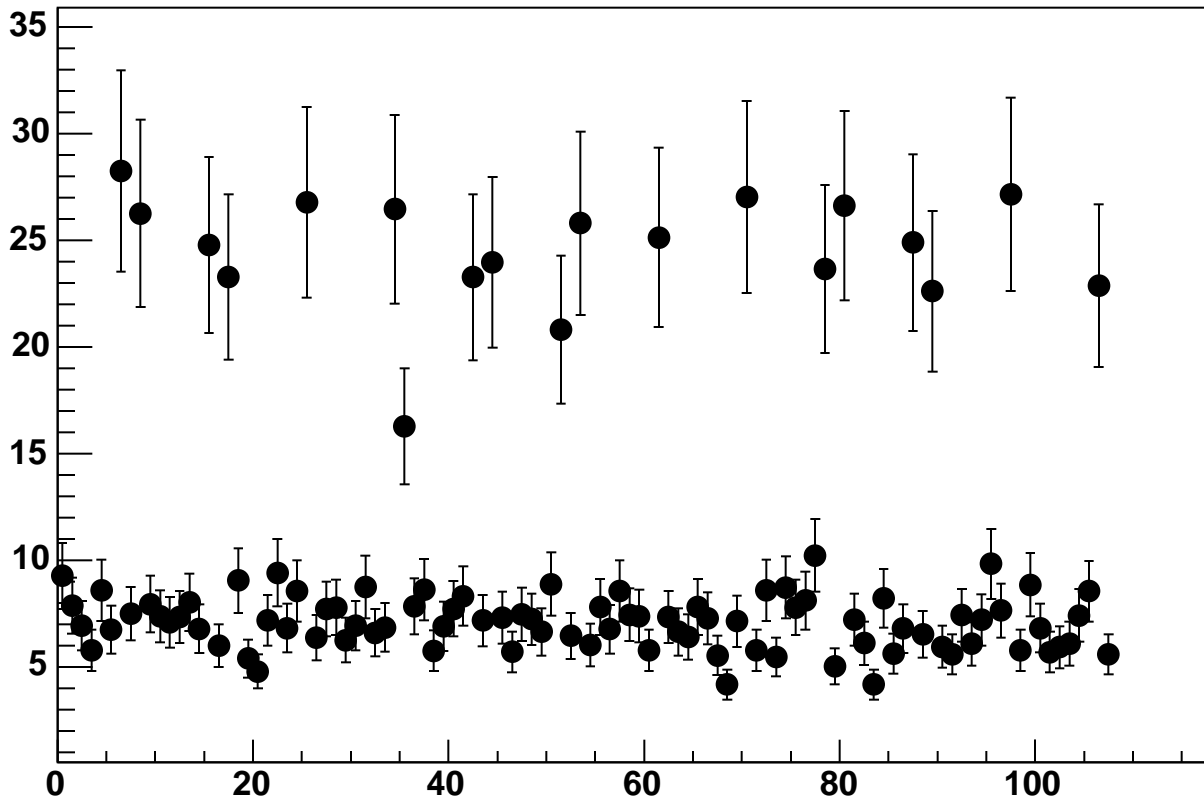
Enable 2, DAC=4000, Hold=85, ADC Noise vs 18\*Chip+Chan



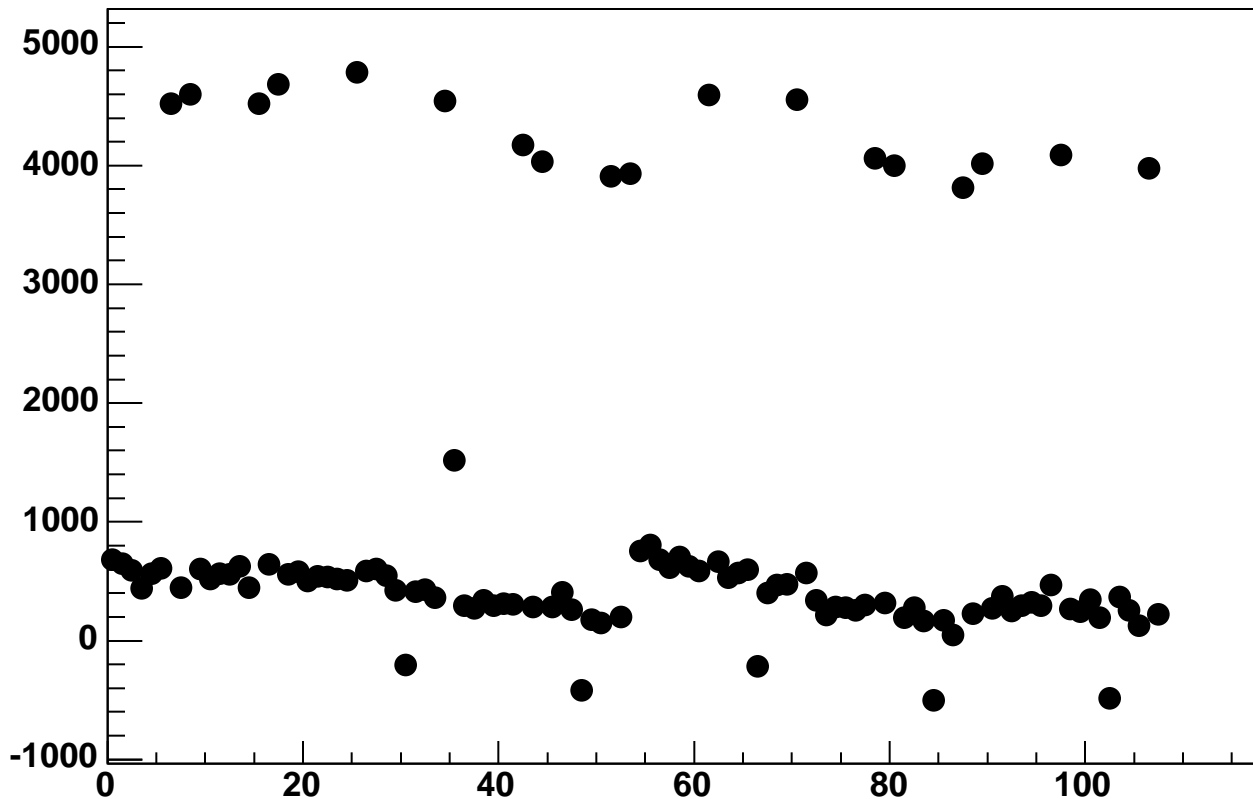
Enable 2, DAC=4000, Hold=90, ADC Mean vs 18\*Chip+Chan



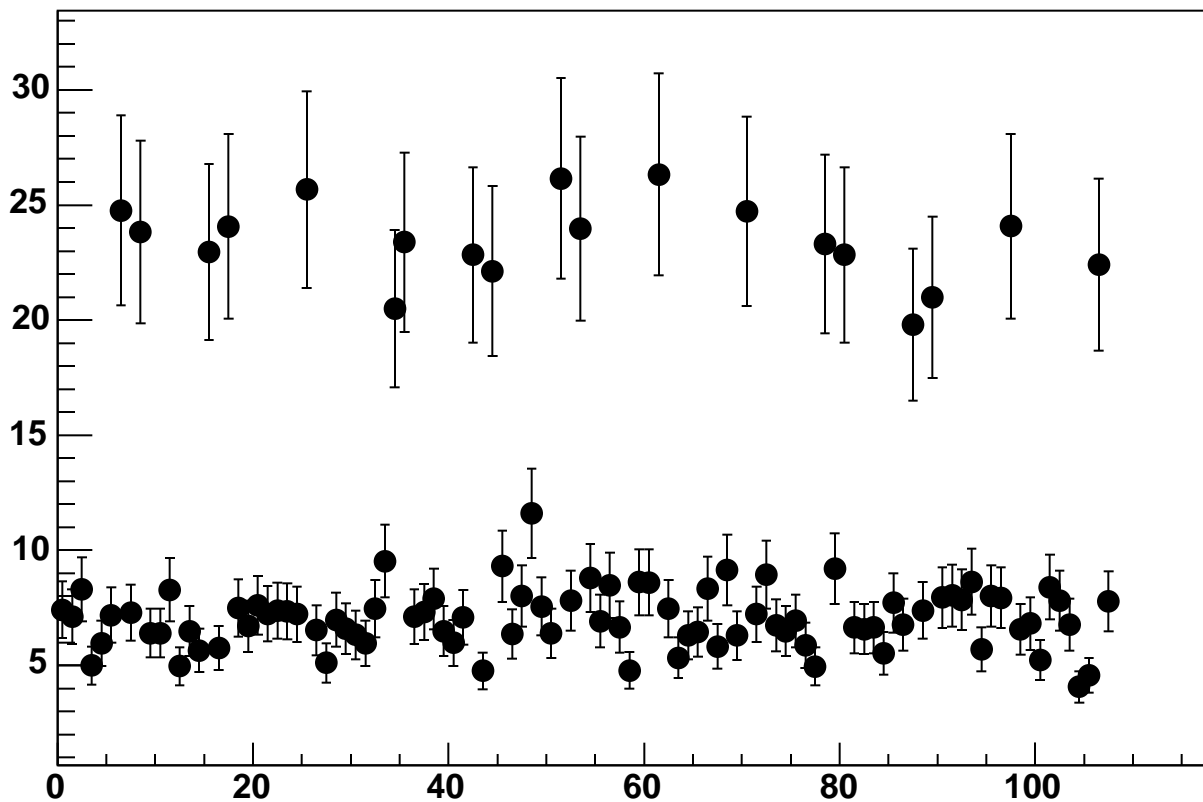
Enable 2, DAC=4000, Hold=90, ADC Noise vs 18\*Chip+Chan



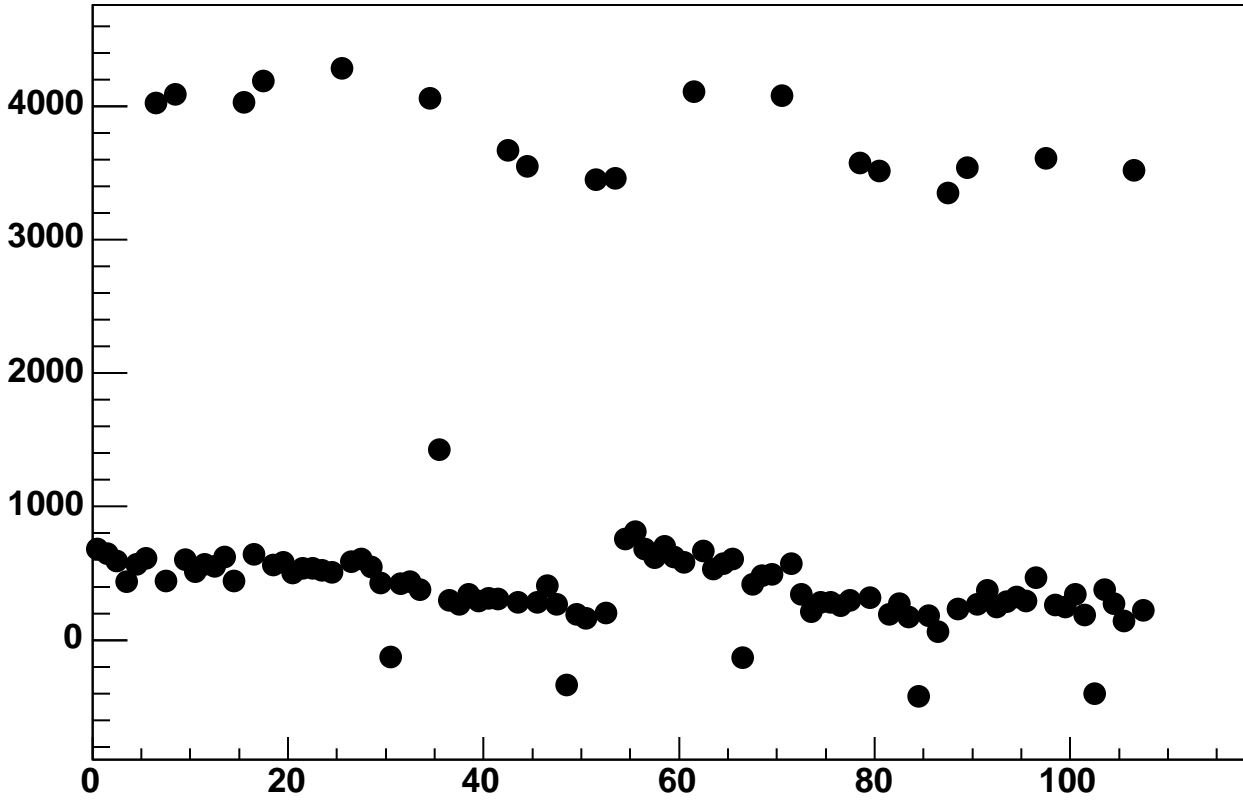
Enable 2, DAC=4000, Hold=95, ADC Mean vs 18\*Chip+Chan



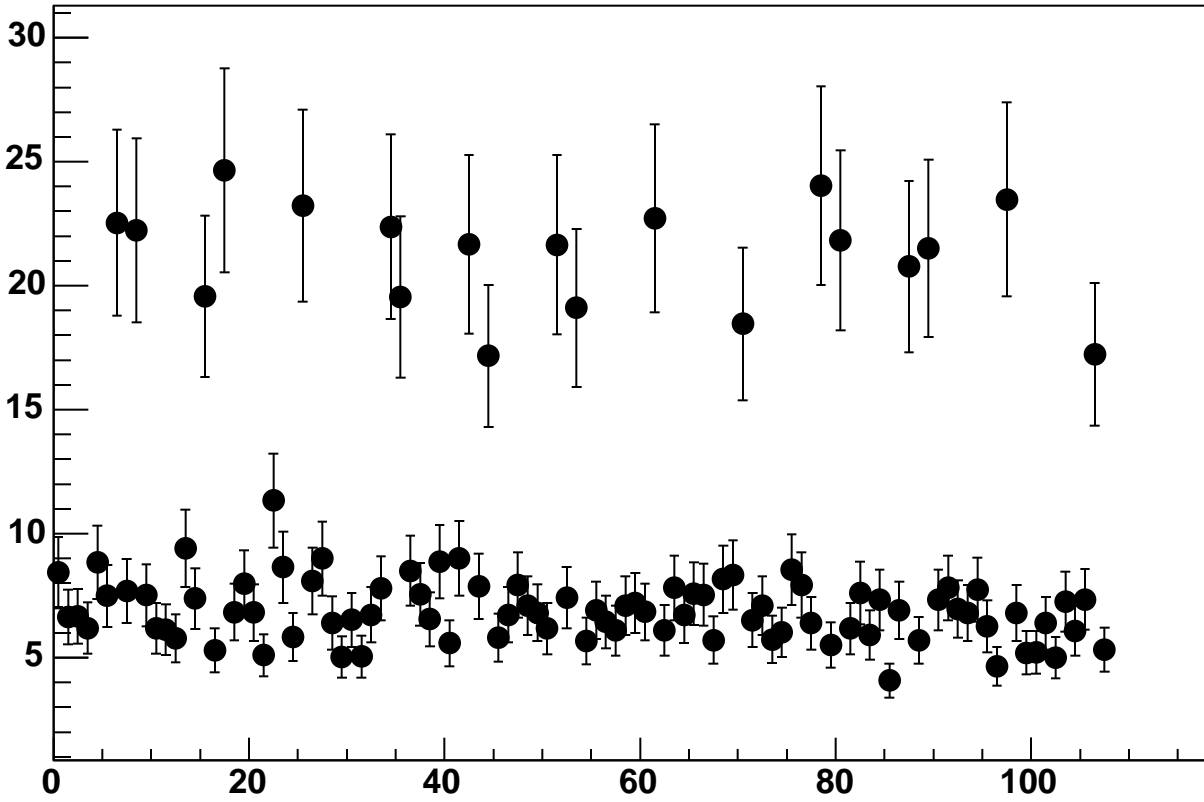
Enable 2, DAC=4000, Hold=95, ADC Noise vs 18\*Chip+Chan



Enable 2, DAC=4000, Hold=100, ADC Mean vs 18\*Chip+Chan

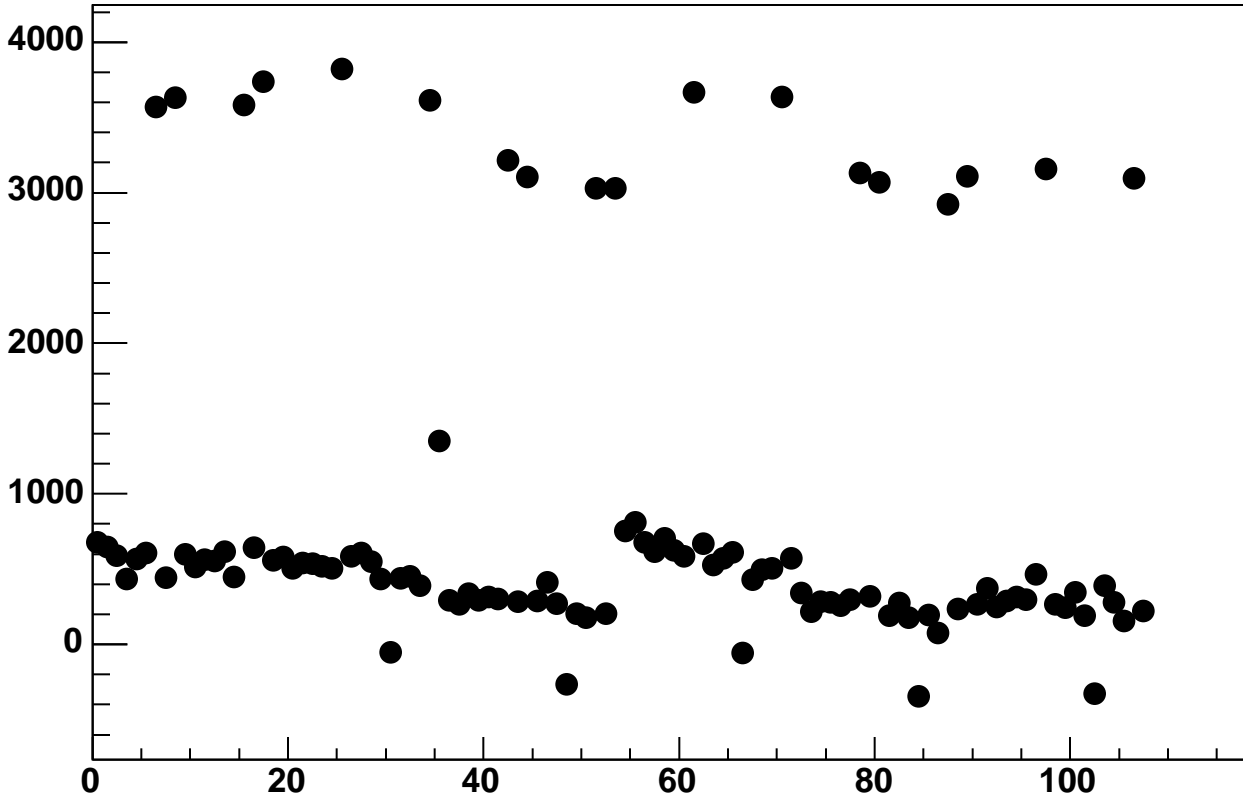


Enable 2, DAC=4000, Hold=100, ADC Noise vs 18\*Chip+Chan

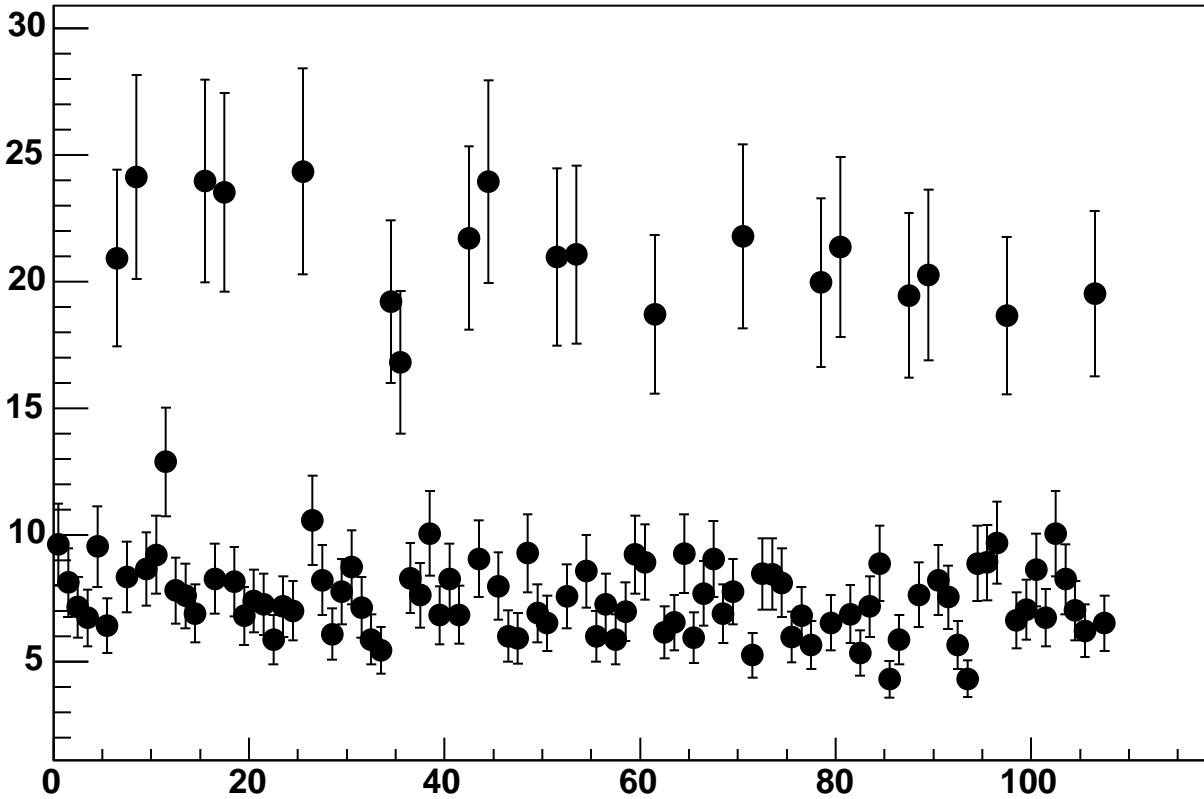




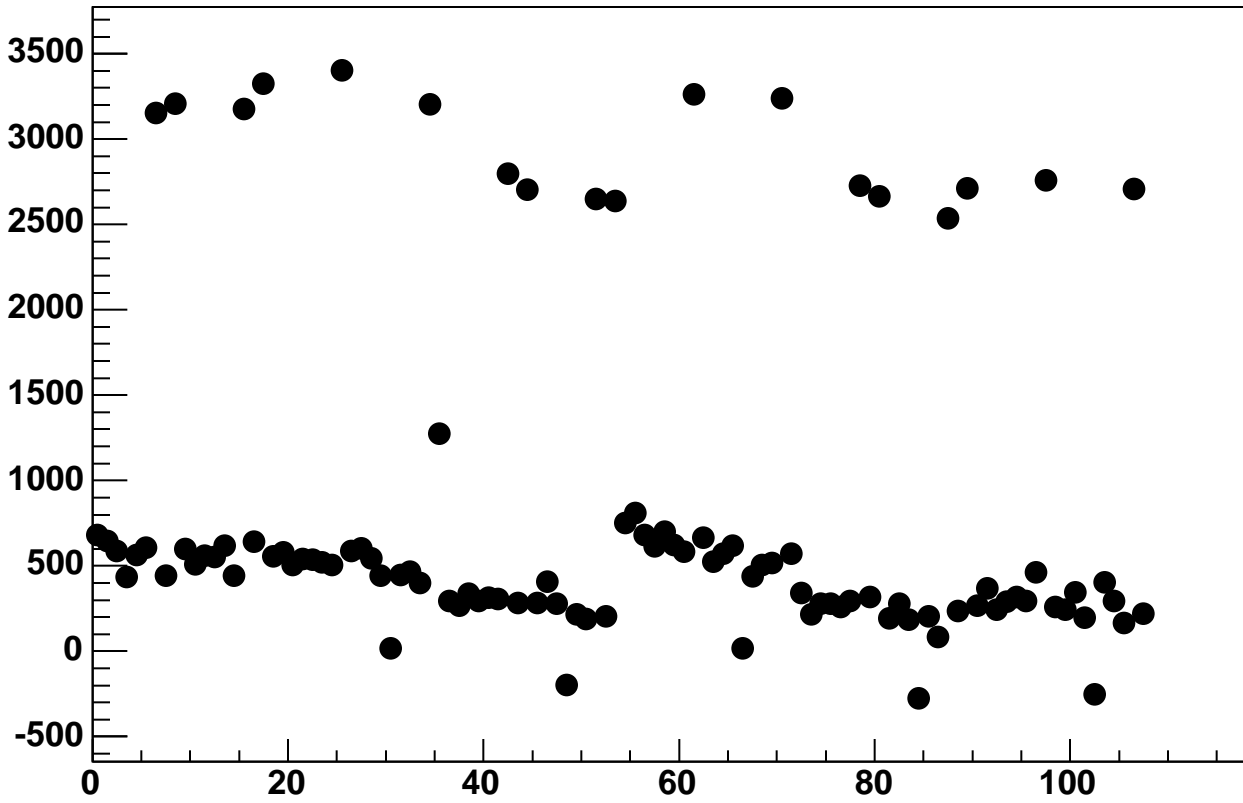
Enable 2, DAC=4000, Hold=105, ADC Mean vs 18\*Chip+Chan



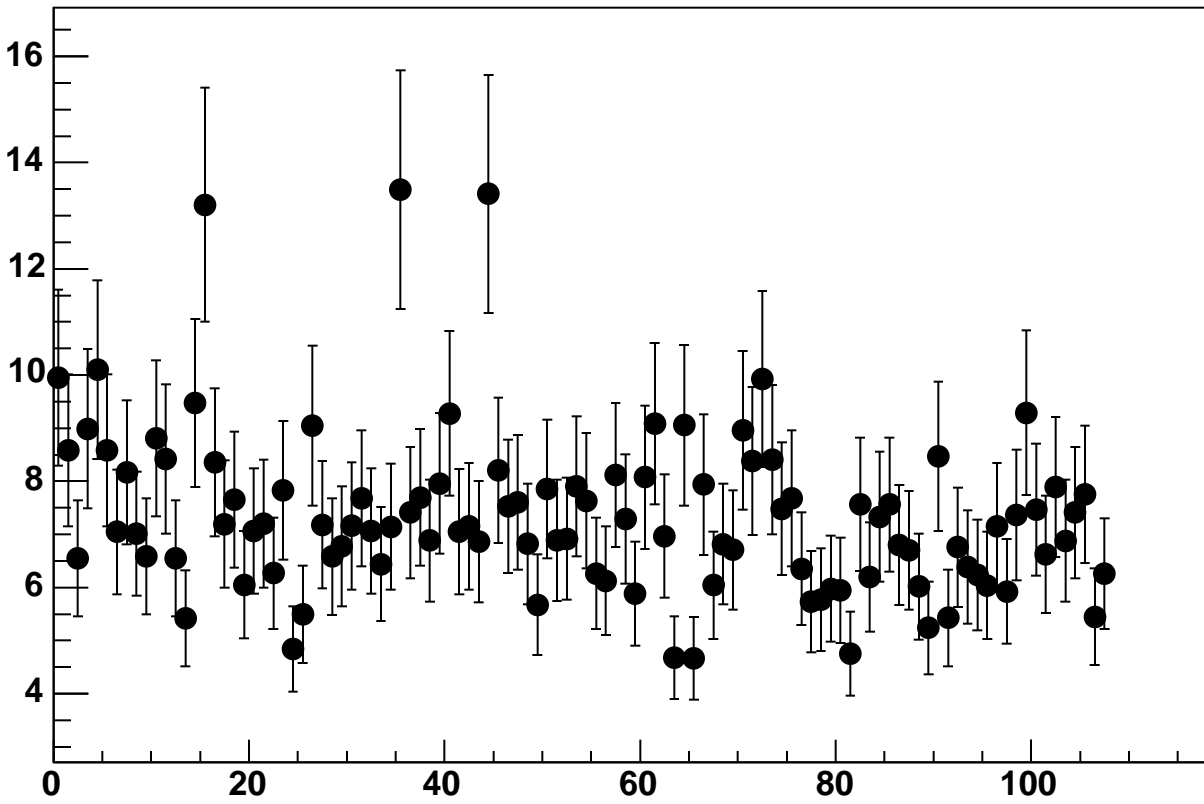
Enable 2, DAC=4000, Hold=105, ADC Noise vs 18\*Chip+Chan



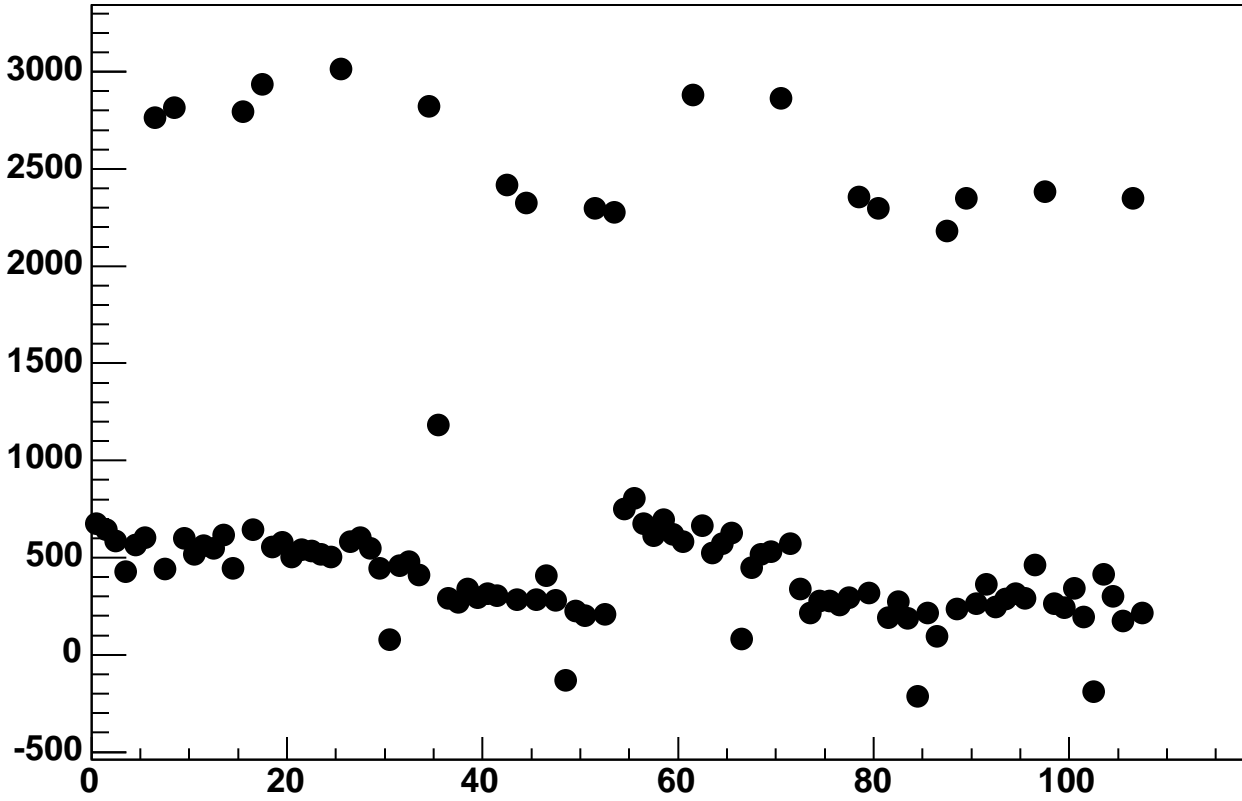
Enable 2, DAC=4000, Hold=110, ADC Mean vs 18\*Chip+Chan



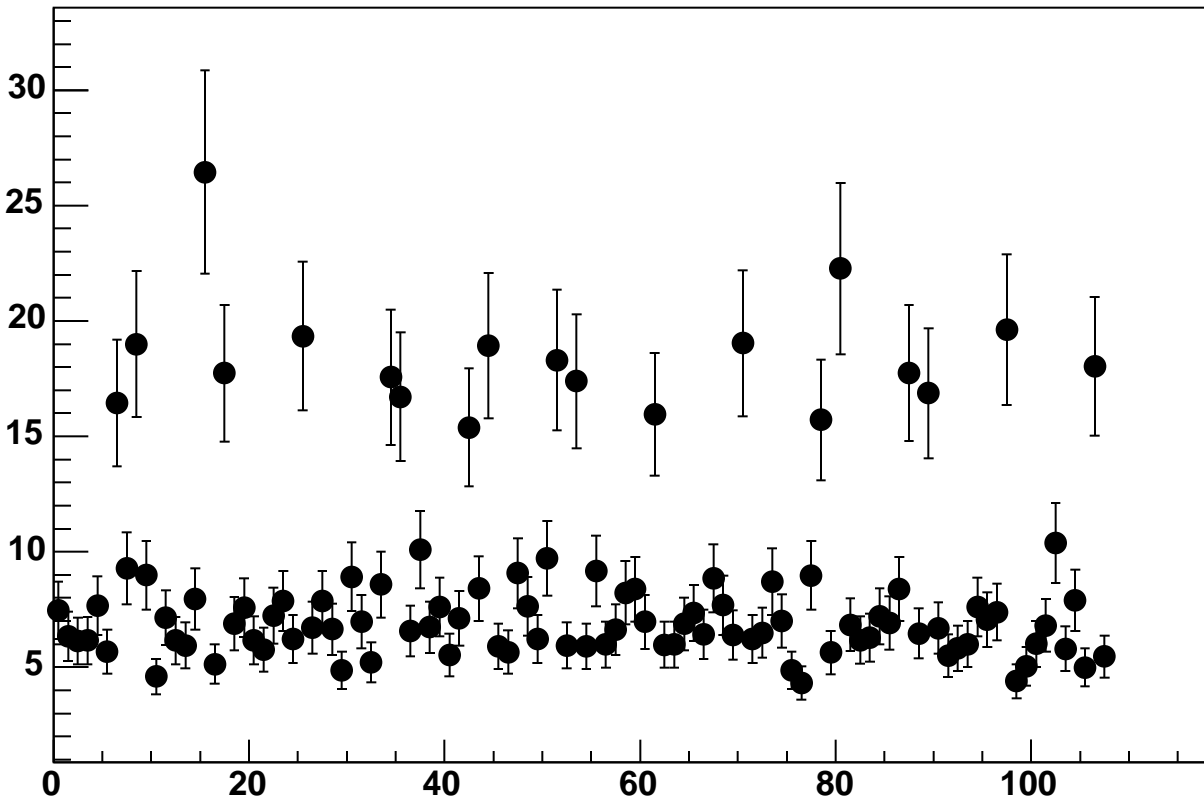
Enable 2, DAC=4000, Hold=110, ADC Noise vs 18\*Chip+Chan



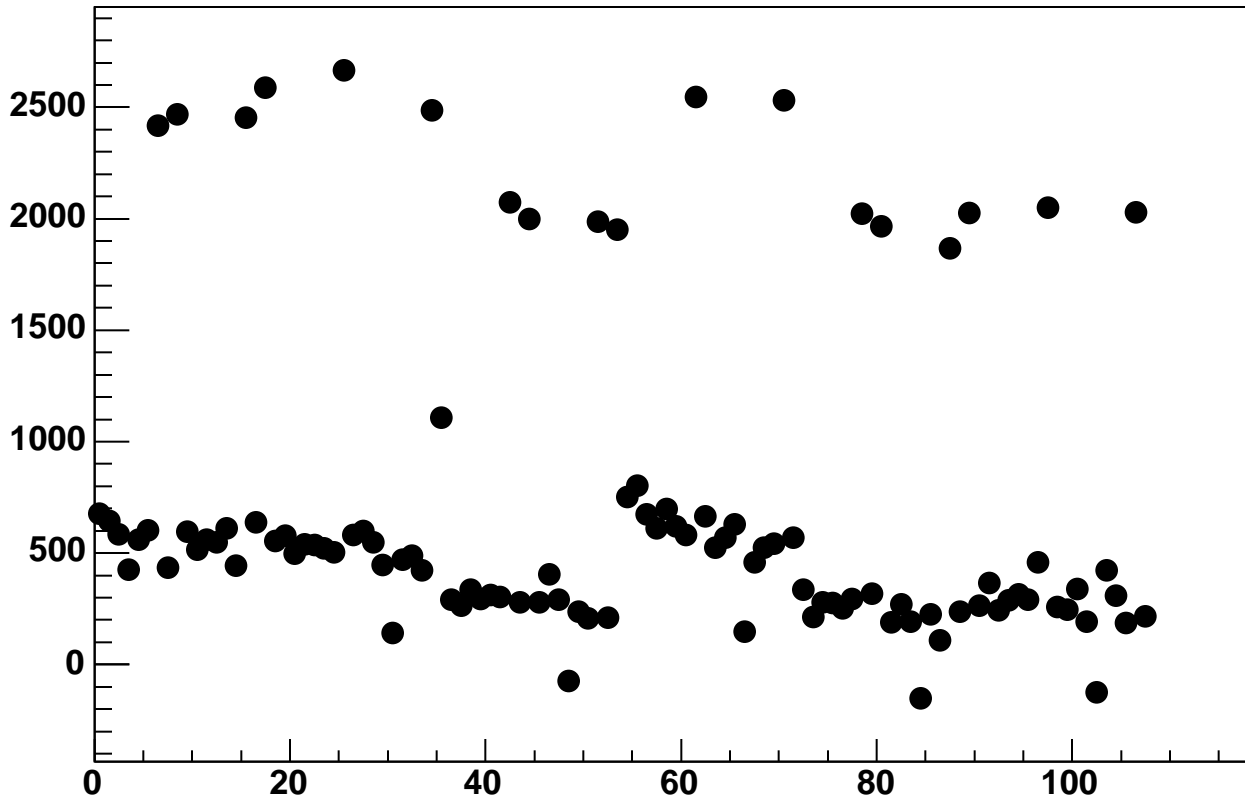
Enable 2, DAC=4000, Hold=115, ADC Mean vs 18\*Chip+Chan



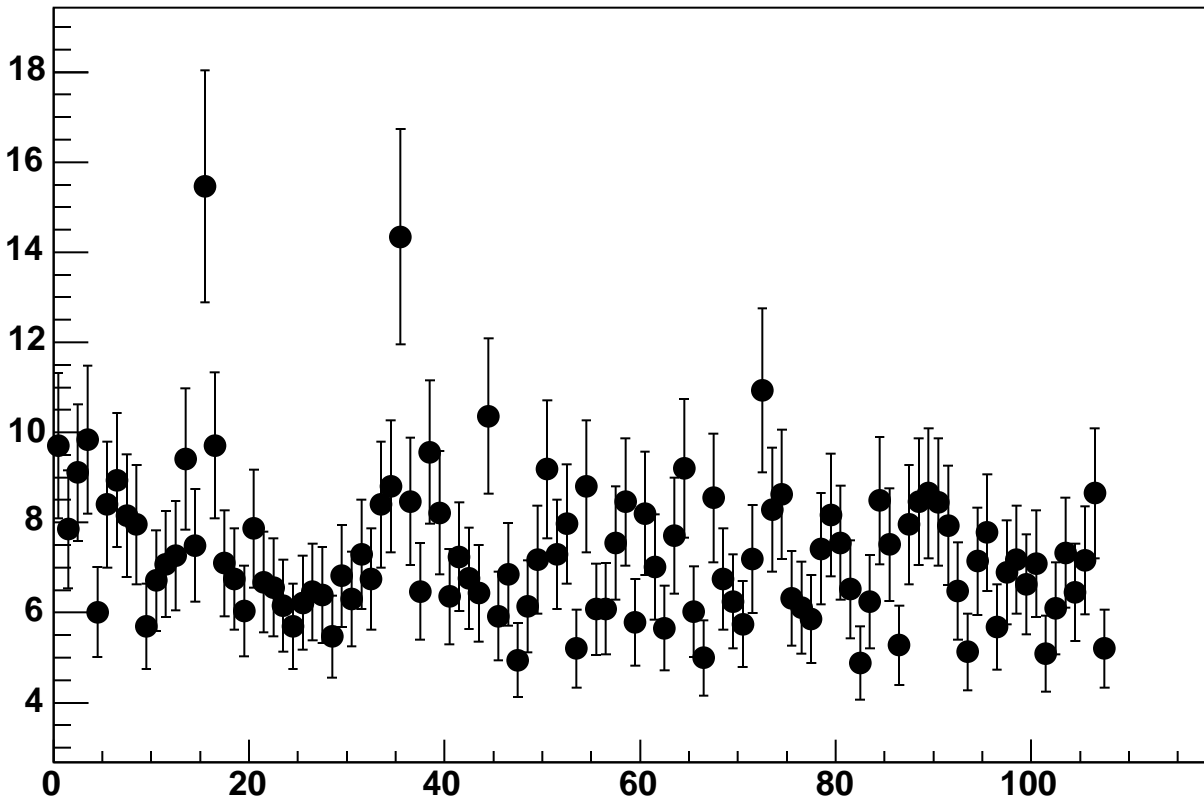
Enable 2, DAC=4000, Hold=115, ADC Noise vs 18\*Chip+Chan



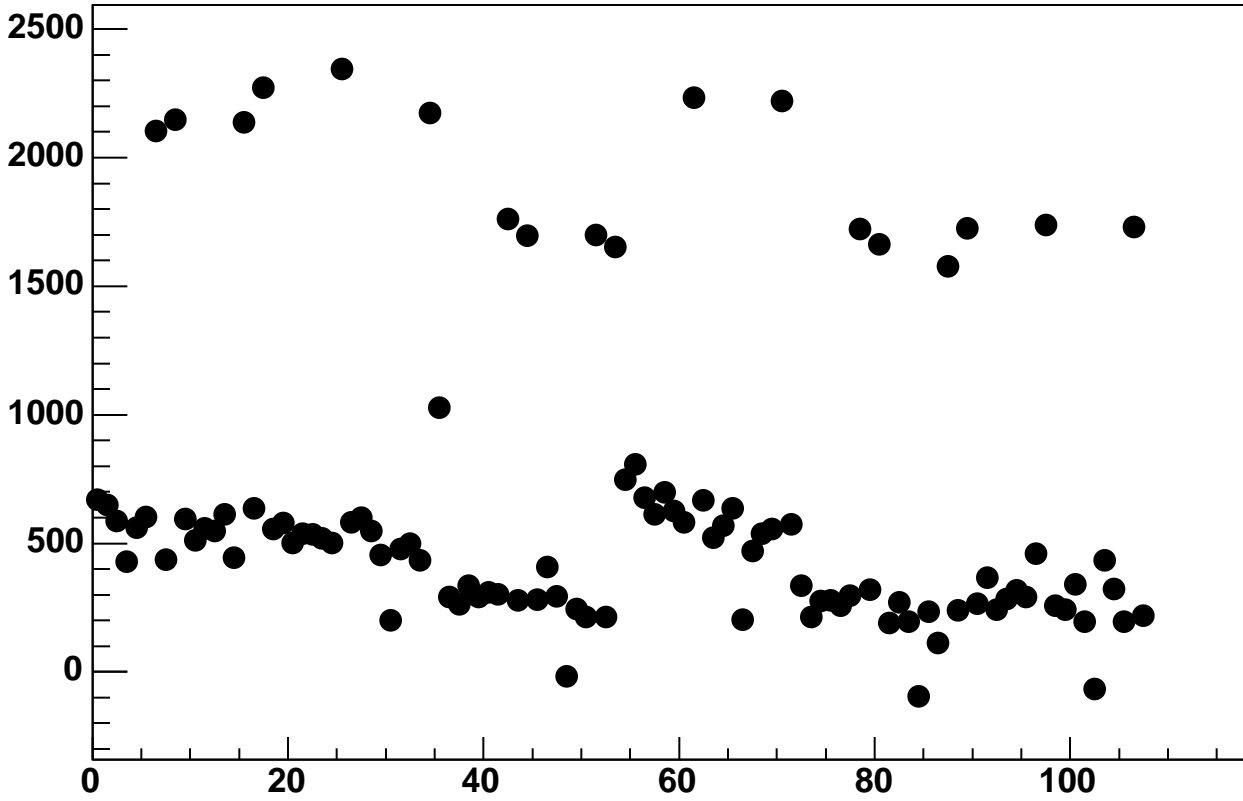
Enable 2, DAC=4000, Hold=120, ADC Mean vs 18\*Chip+Chan



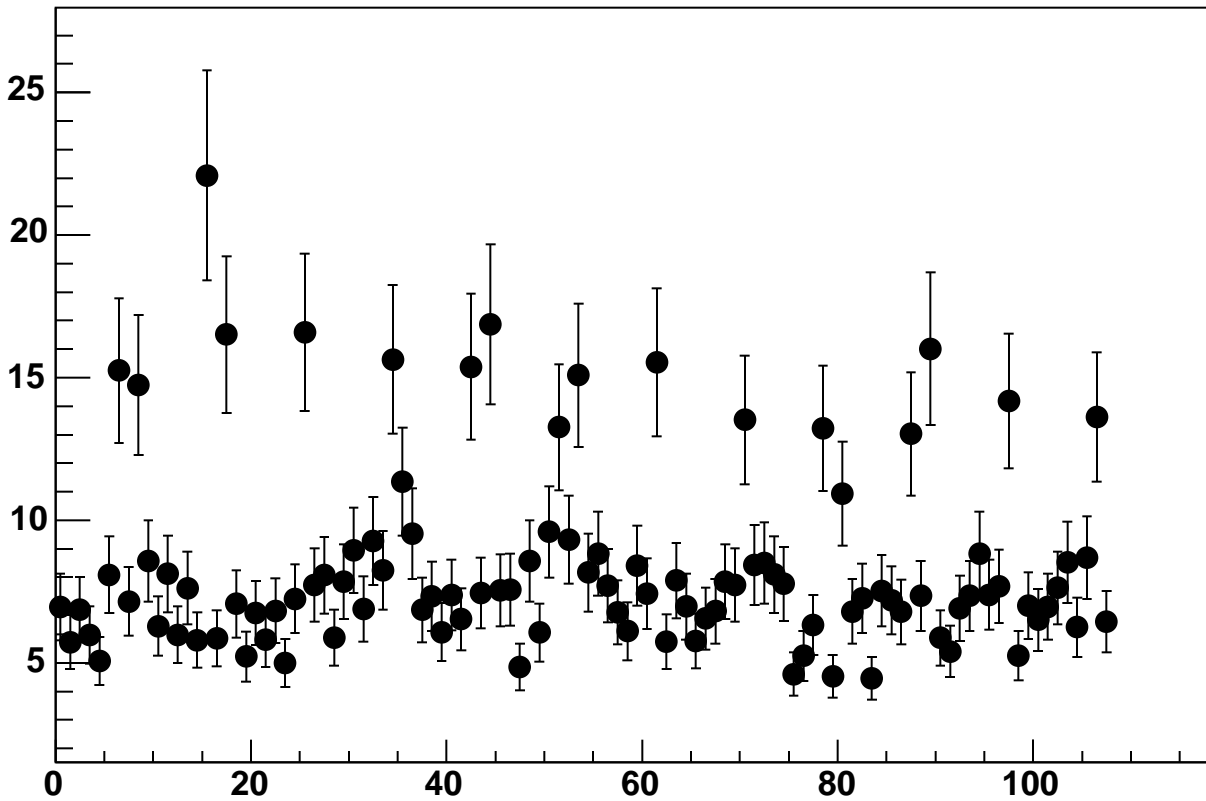
Enable 2, DAC=4000, Hold=120, ADC Noise vs 18\*Chip+Chan



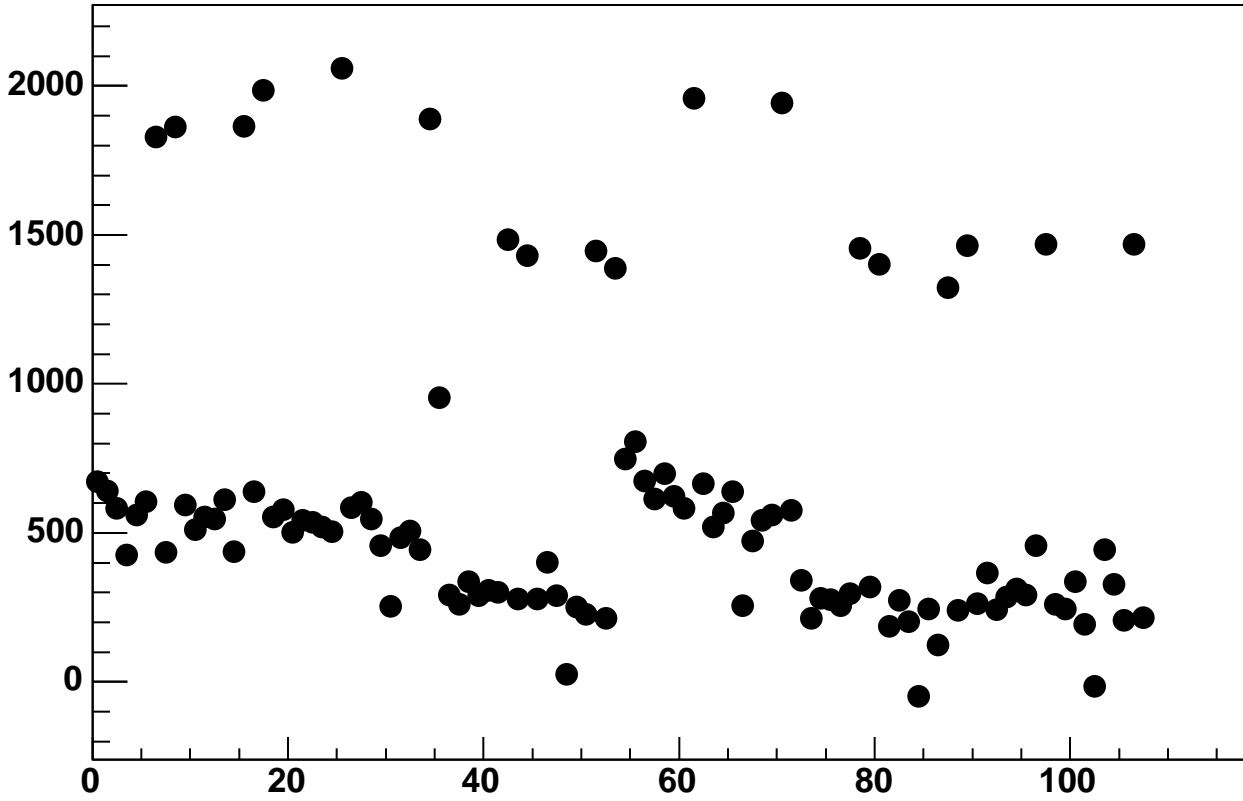
Enable 2, DAC=4000, Hold=125, ADC Mean vs 18\*Chip+Chan



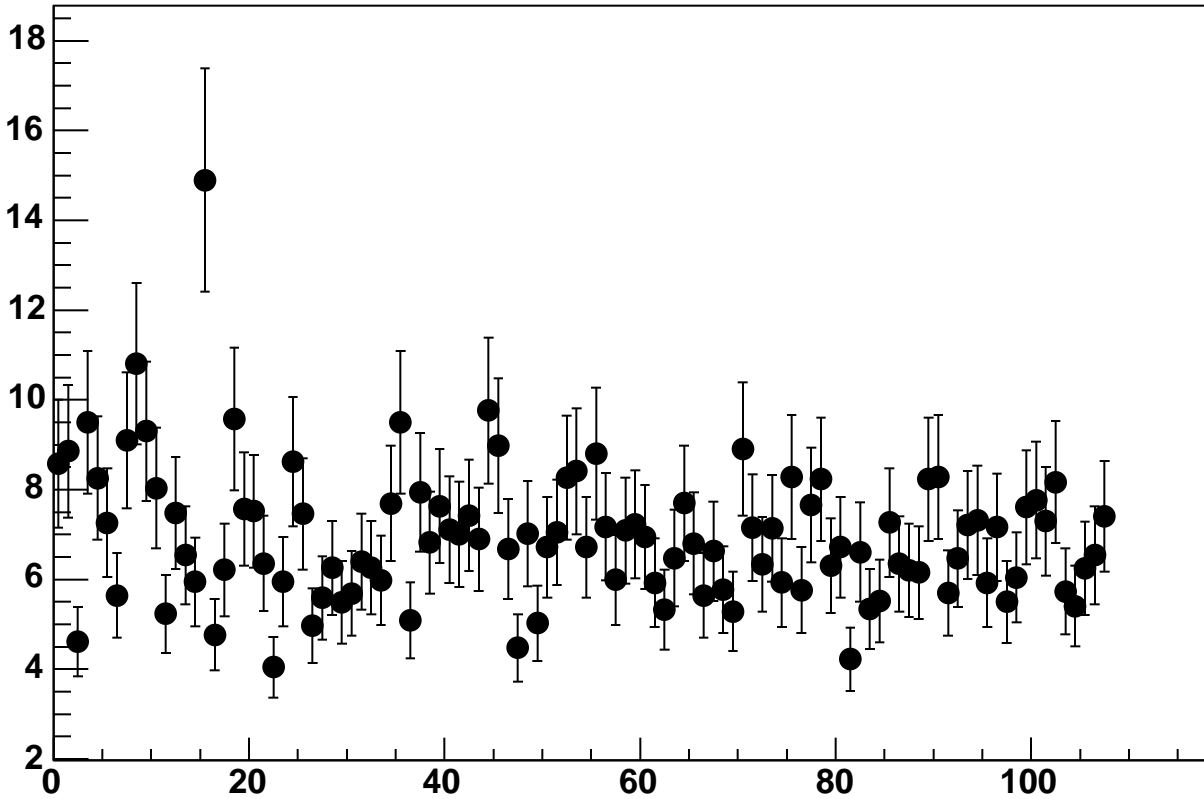
Enable 2, DAC=4000, Hold=125, ADC Noise vs 18\*Chip+Chan



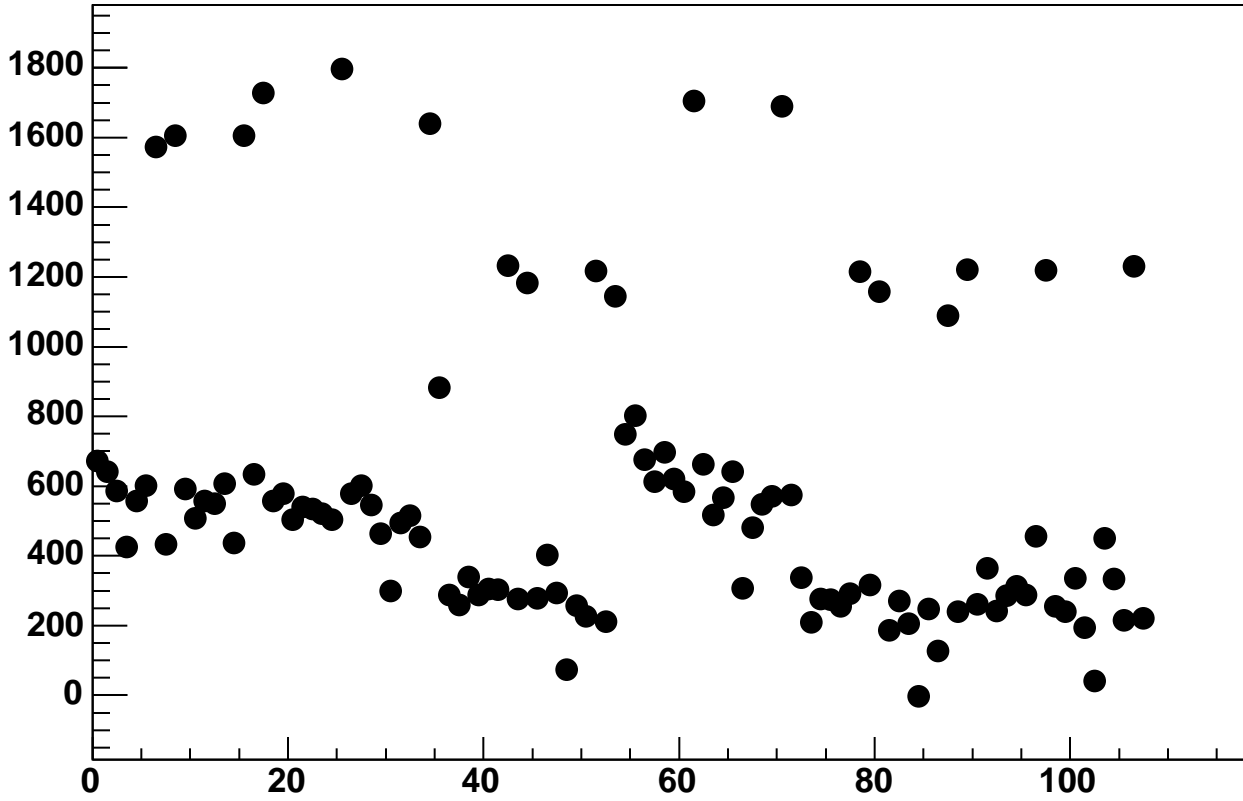
Enable 2, DAC=4000, Hold=130, ADC Mean vs 18\*Chip+Chan



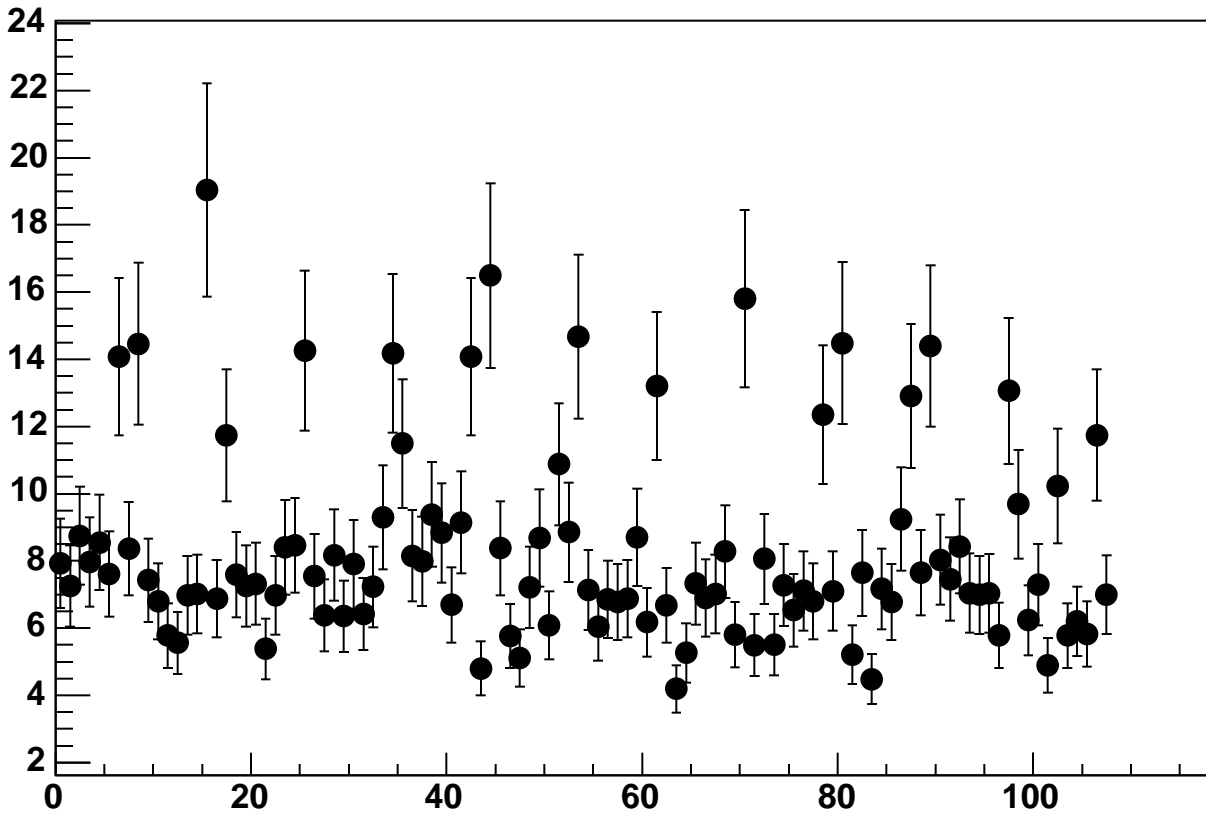
Enable 2, DAC=4000, Hold=130, ADC Noise vs 18\*Chip+Chan



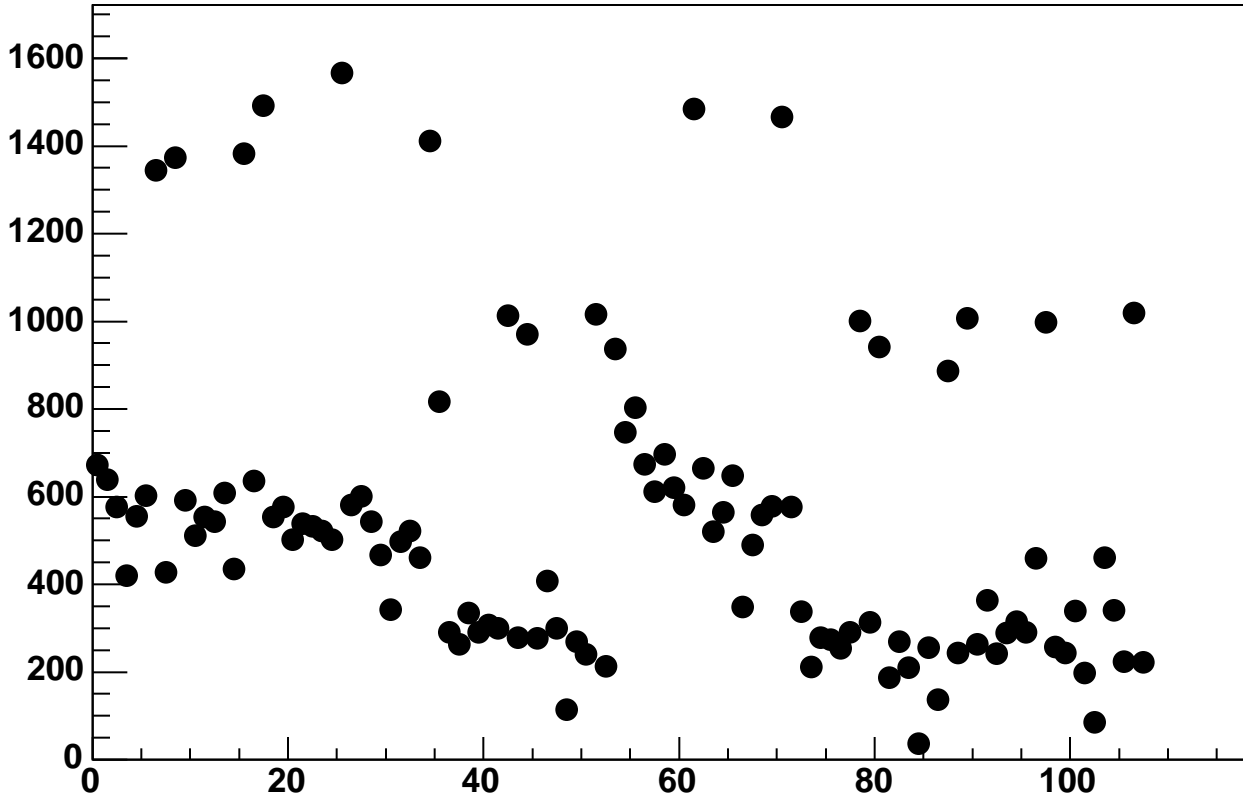
Enable 2, DAC=4000, Hold=135, ADC Mean vs 18\*Chip+Chan



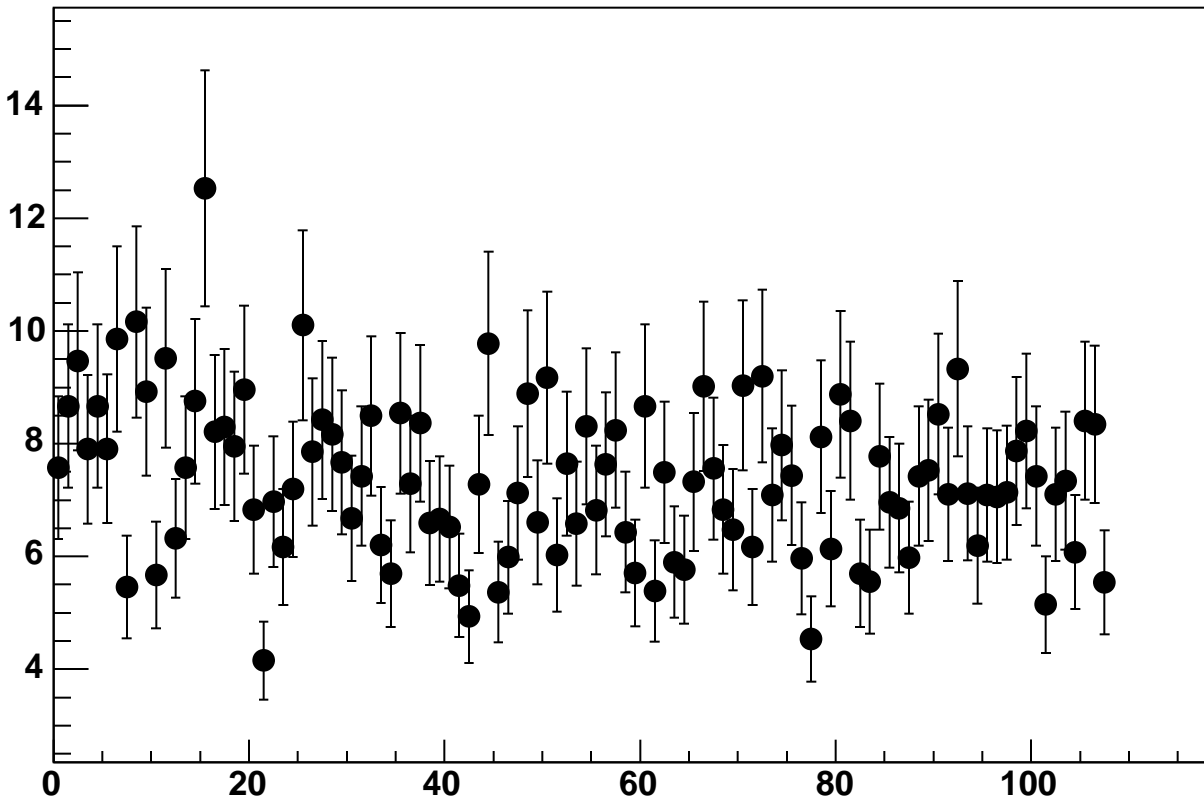
Enable 2, DAC=4000, Hold=135, ADC Noise vs 18\*Chip+Chan



Enable 2, DAC=4000, Hold=140, ADC Mean vs 18\*Chip+Chan

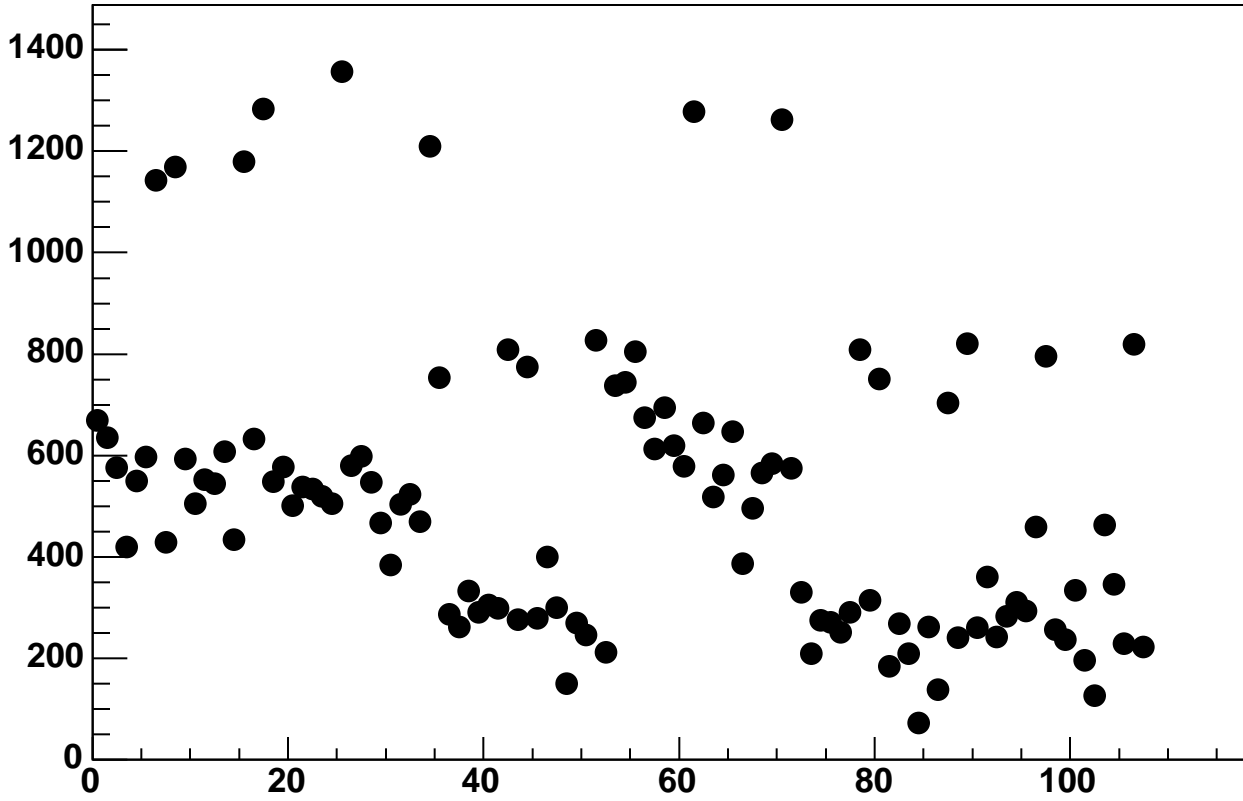


Enable 2, DAC=4000, Hold=140, ADC Noise vs 18\*Chip+Chan

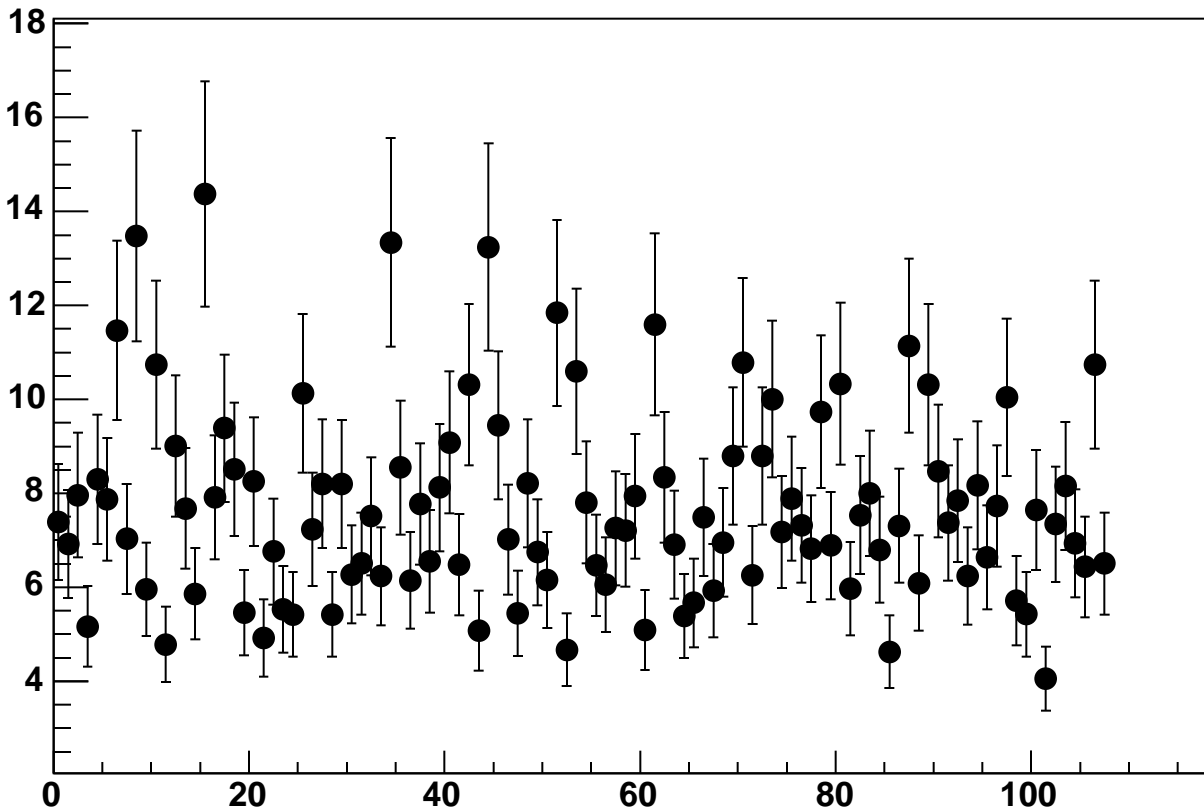




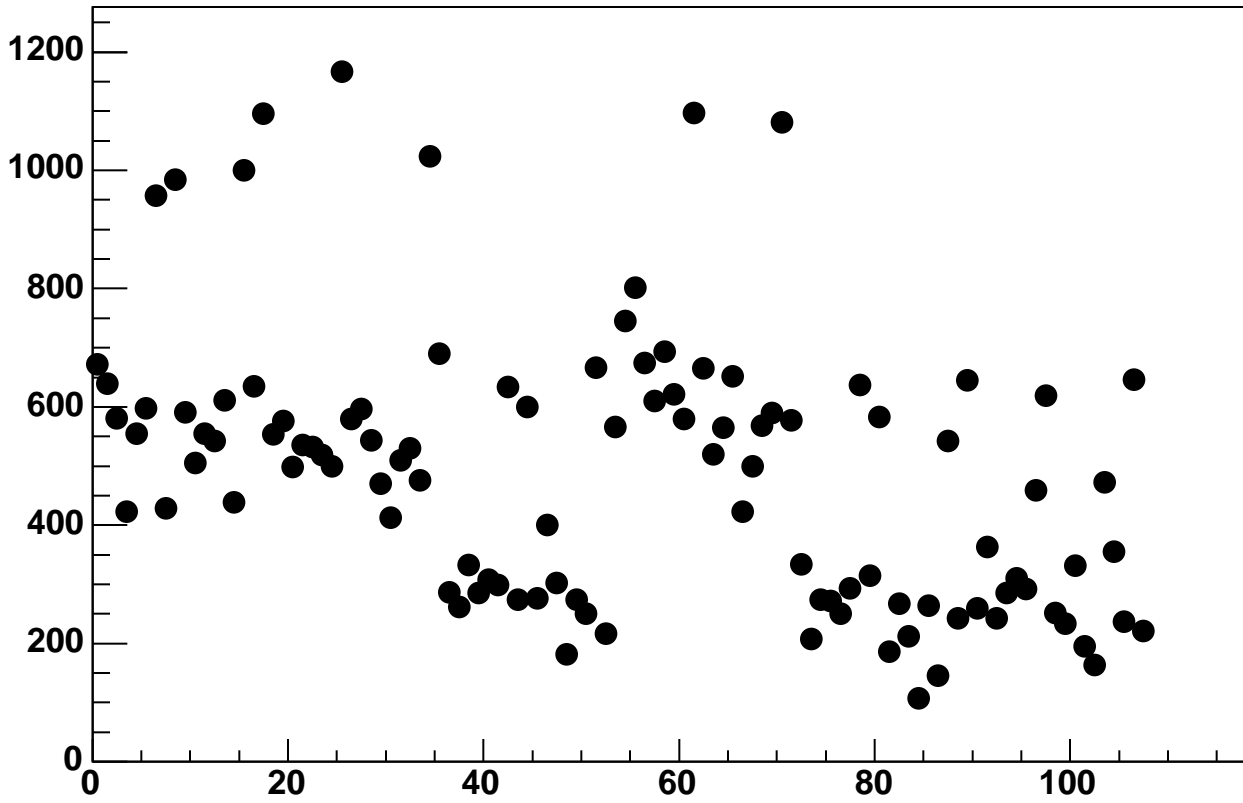
Enable 2, DAC=4000, Hold=145, ADC Mean vs 18\*Chip+Chan



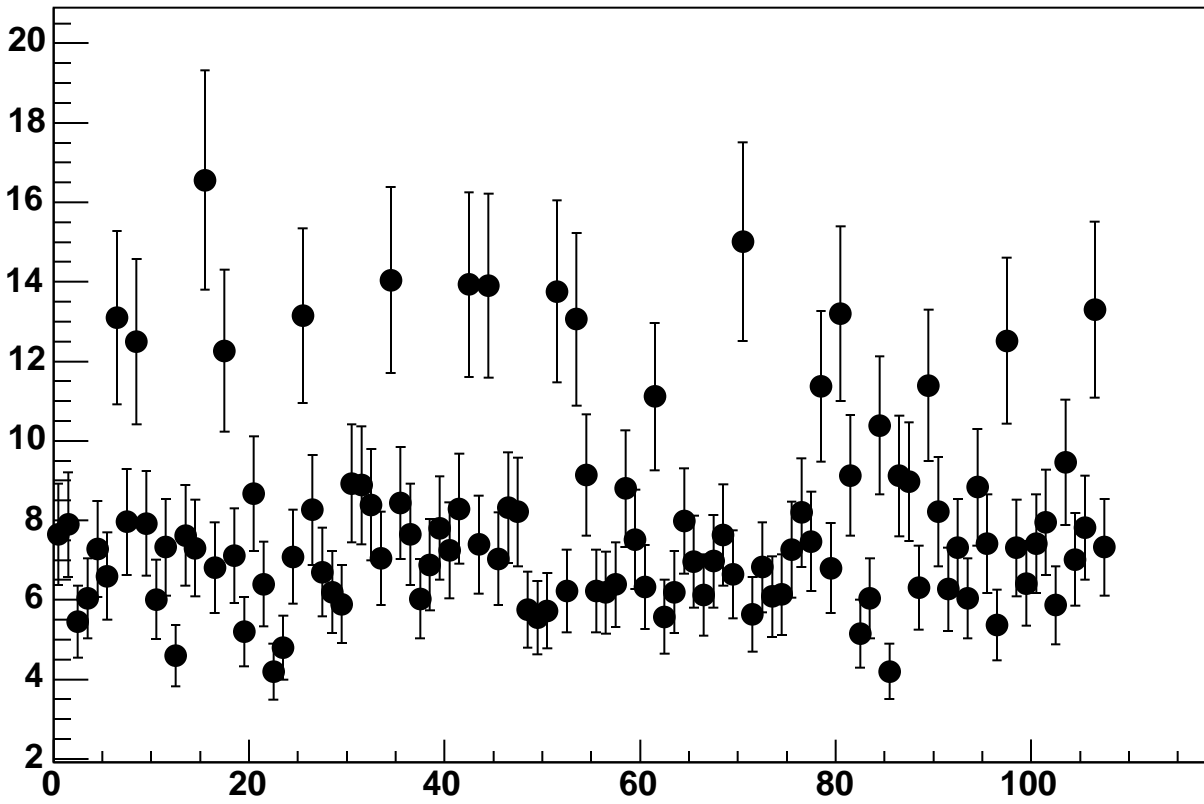
Enable 2, DAC=4000, Hold=145, ADC Noise vs 18\*Chip+Chan



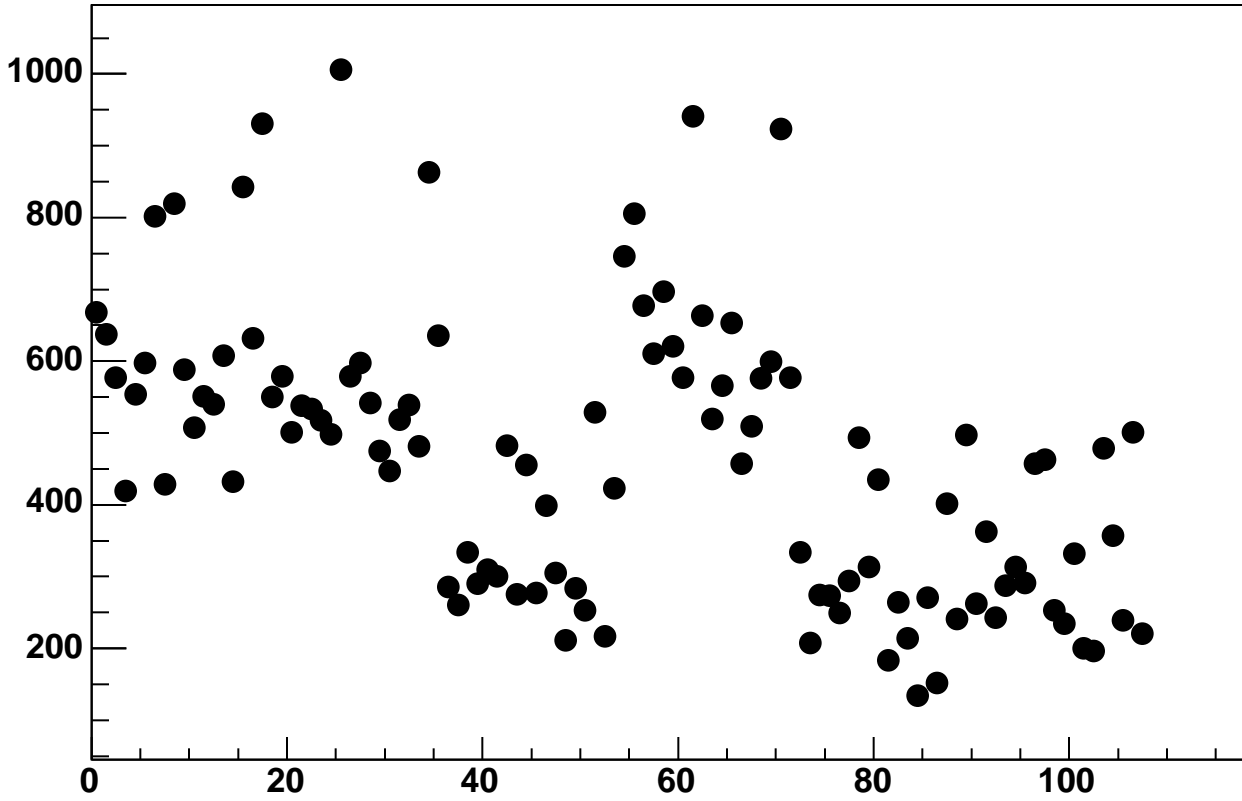
Enable 2, DAC=4000, Hold=150, ADC Mean vs 18\*Chip+Chan



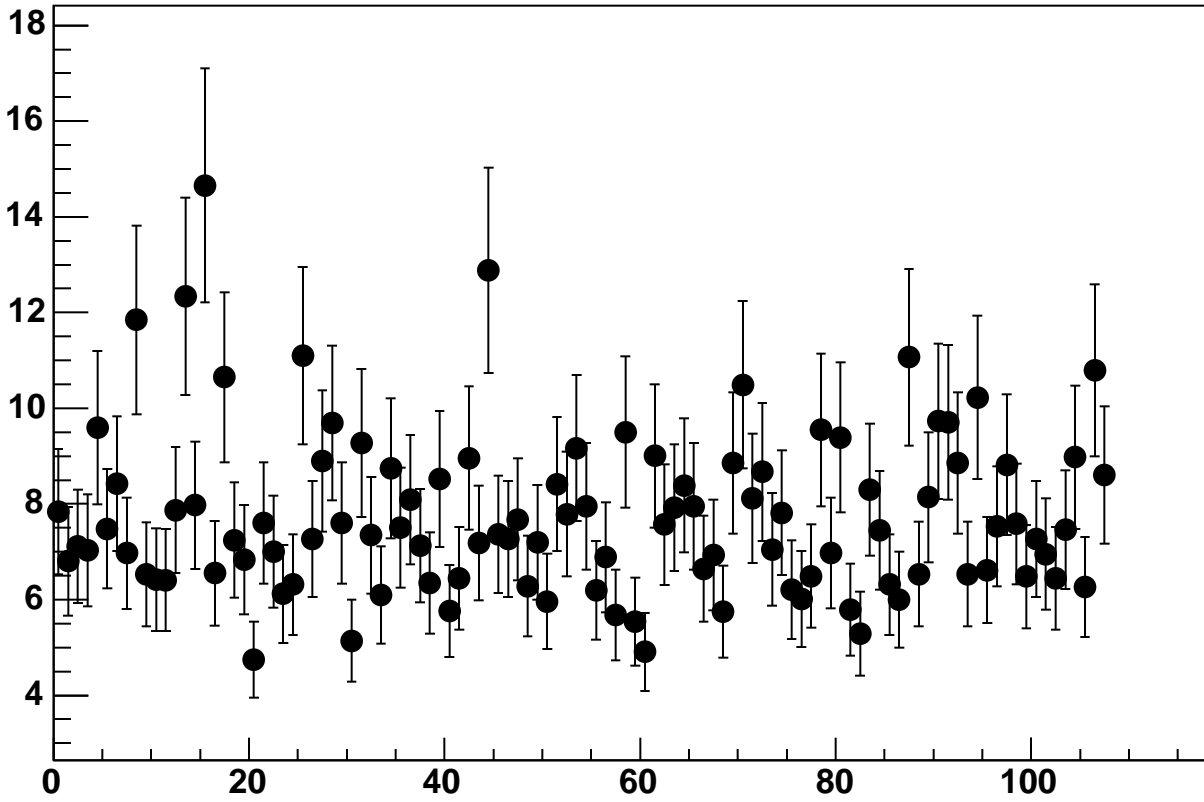
Enable 2, DAC=4000, Hold=150, ADC Noise vs 18\*Chip+Chan



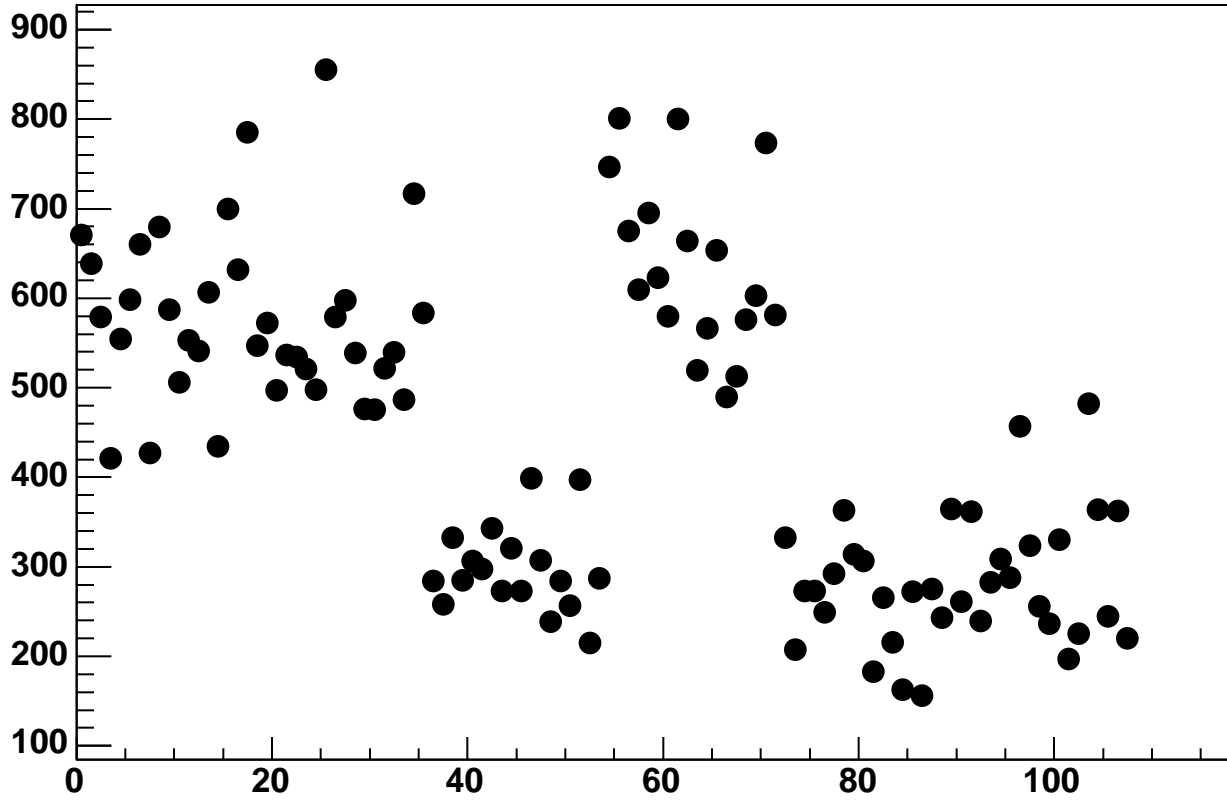
Enable 2, DAC=4000, Hold=155, ADC Mean vs 18\*Chip+Chan



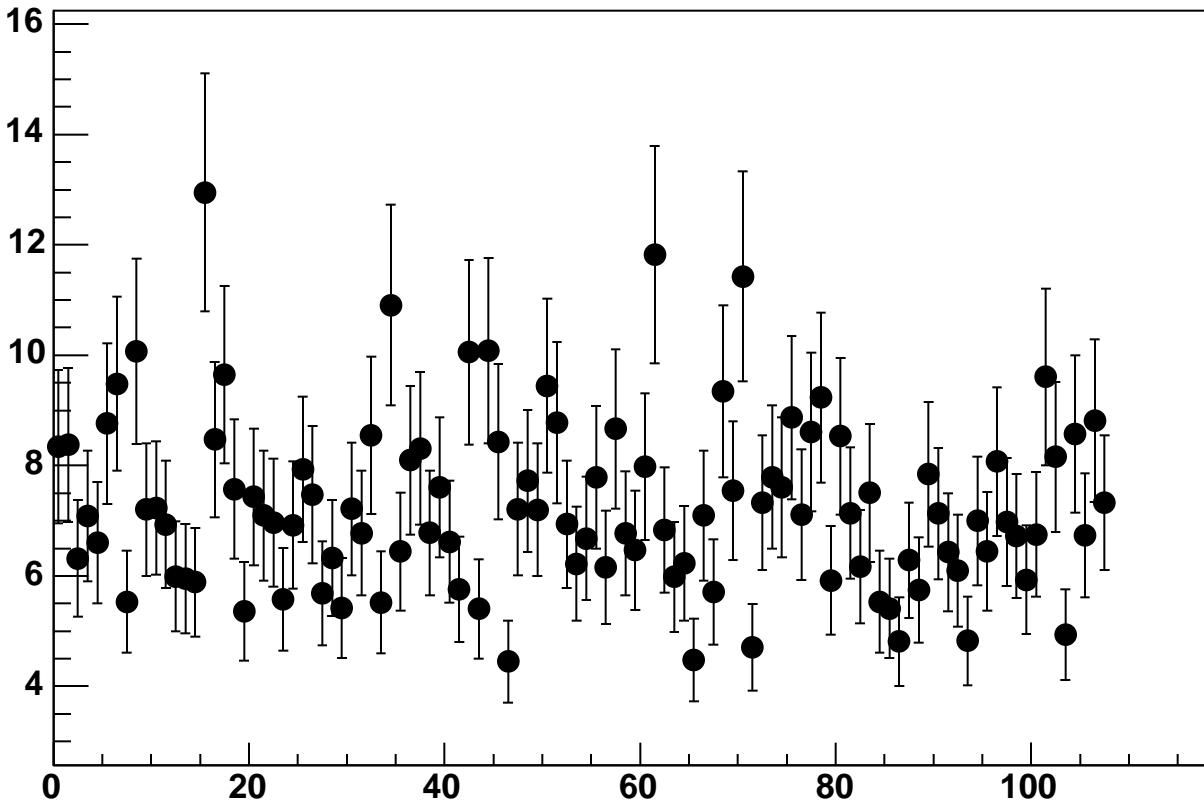
Enable 2, DAC=4000, Hold=155, ADC Noise vs 18\*Chip+Chan



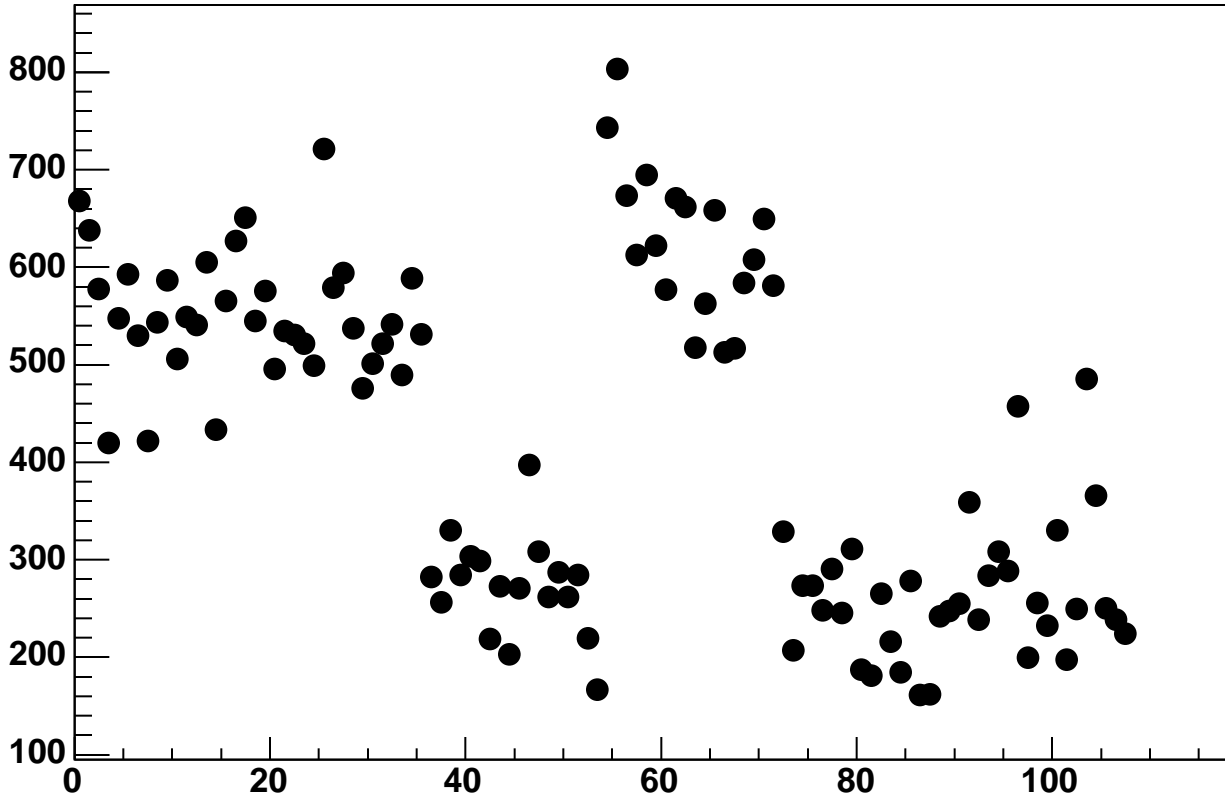
Enable 2, DAC=4000, Hold=160, ADC Mean vs 18\*Chip+Chan



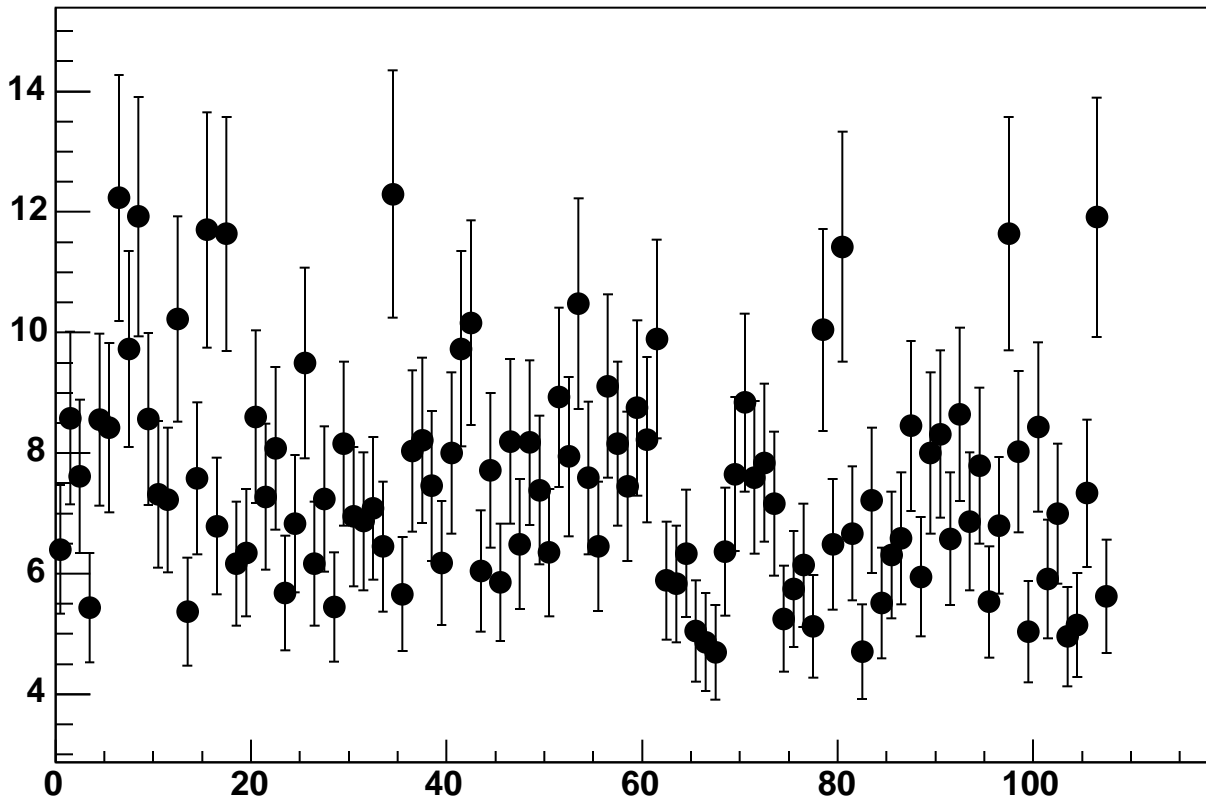
Enable 2, DAC=4000, Hold=160, ADC Noise vs 18\*Chip+Chan



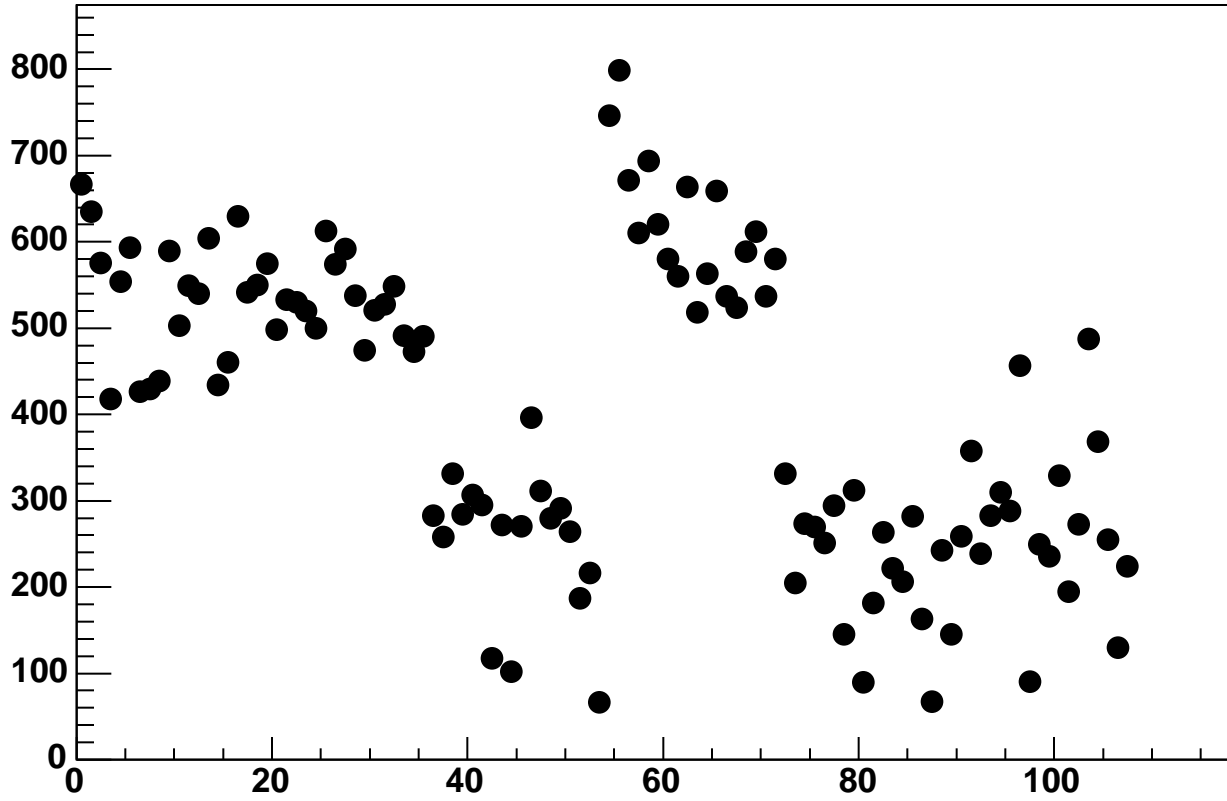
Enable 2, DAC=4000, Hold=165, ADC Mean vs 18\*Chip+Chan



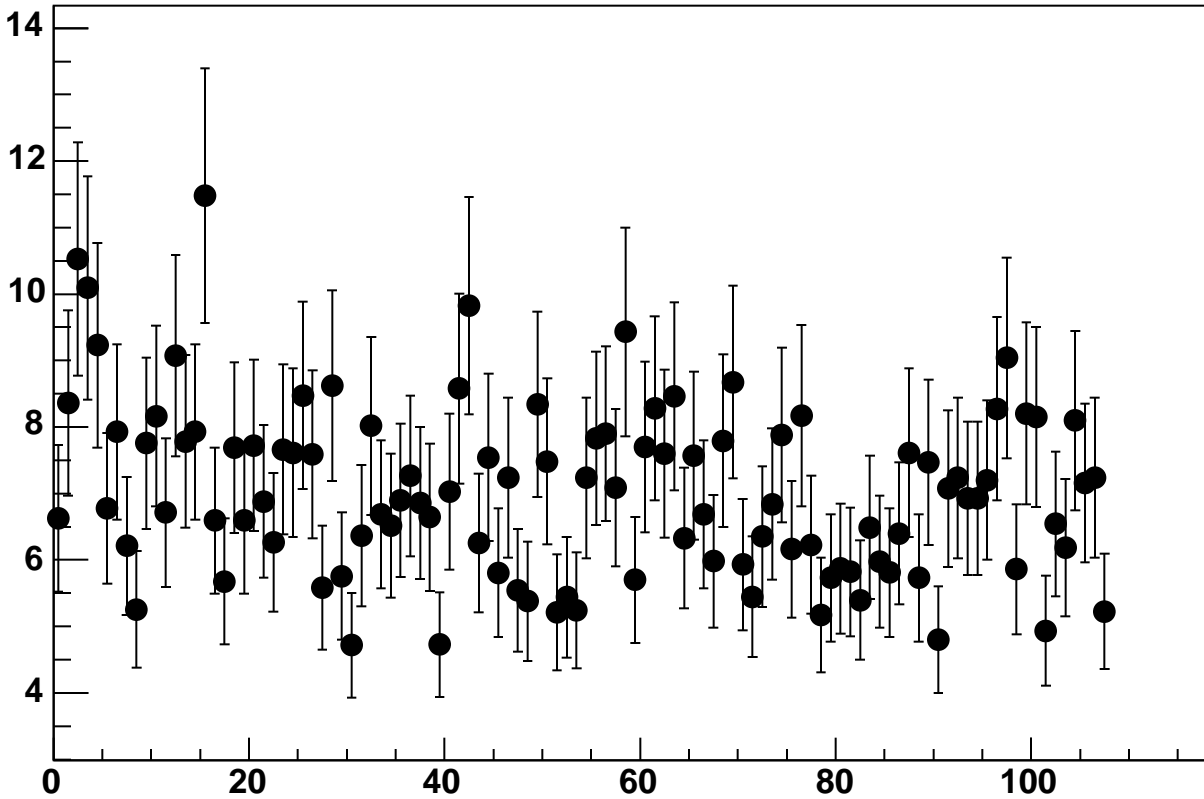
Enable 2, DAC=4000, Hold=165, ADC Noise vs 18\*Chip+Chan



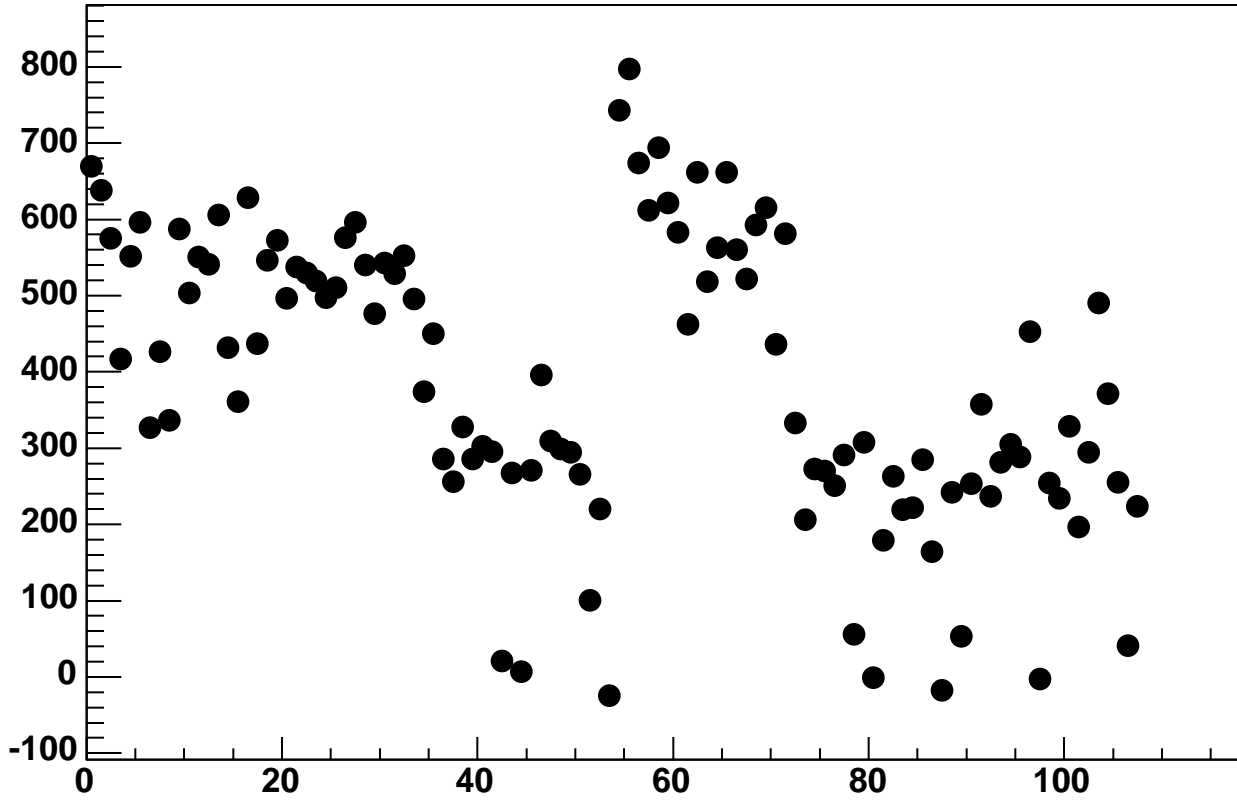
Enable 2, DAC=4000, Hold=170, ADC Mean vs 18\*Chip+Chan



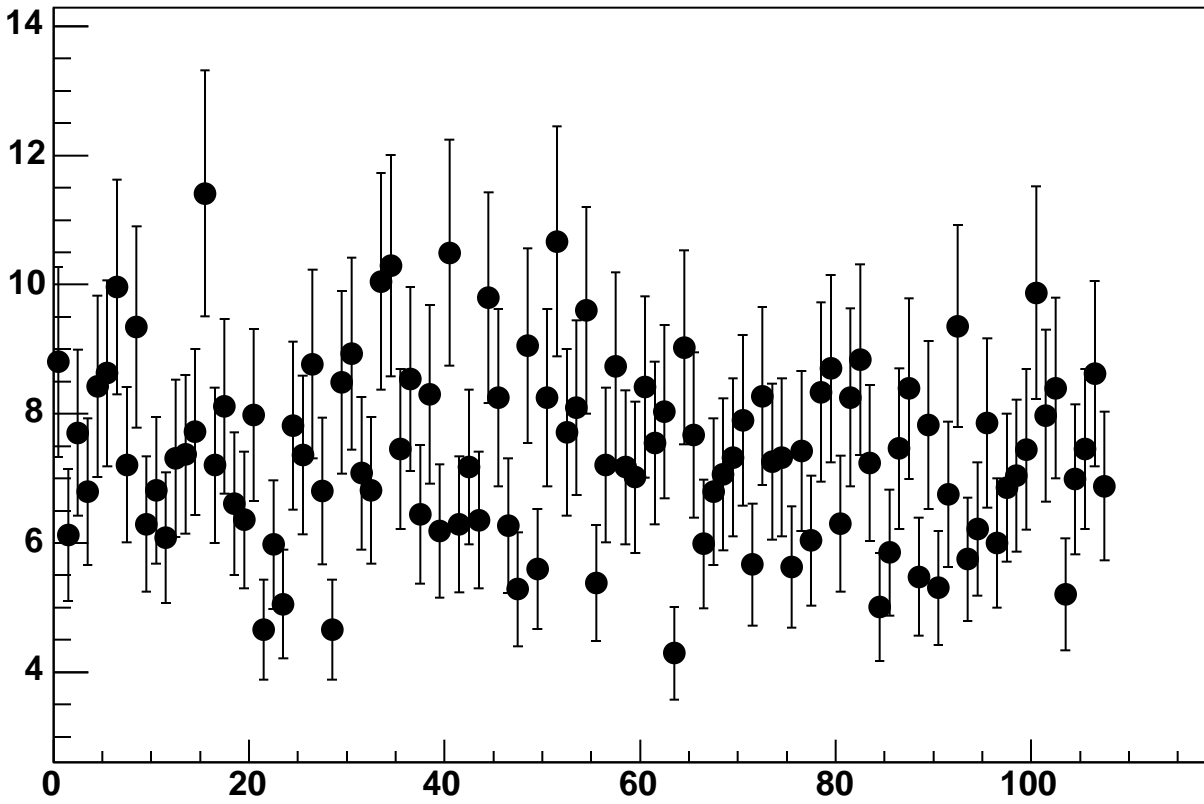
Enable 2, DAC=4000, Hold=170, ADC Noise vs 18\*Chip+Chan



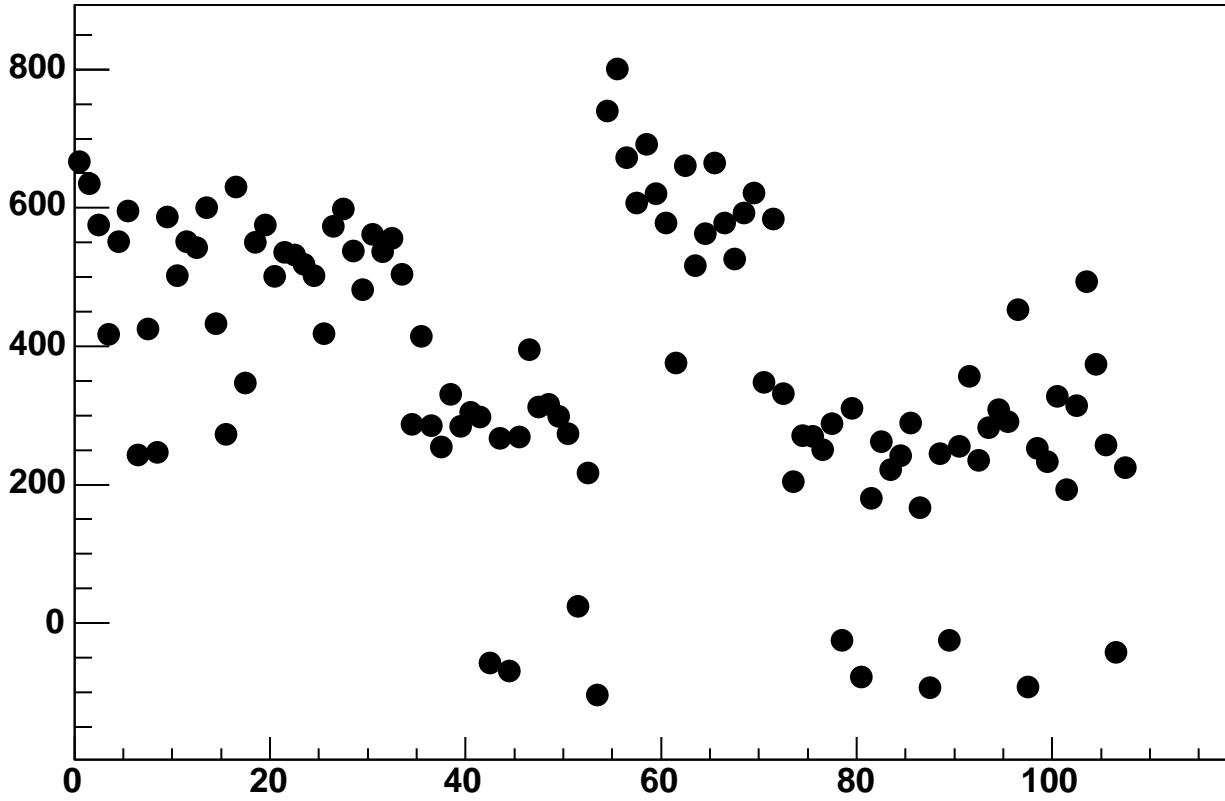
Enable 2, DAC=4000, Hold=175, ADC Mean vs 18\*Chip+Chan



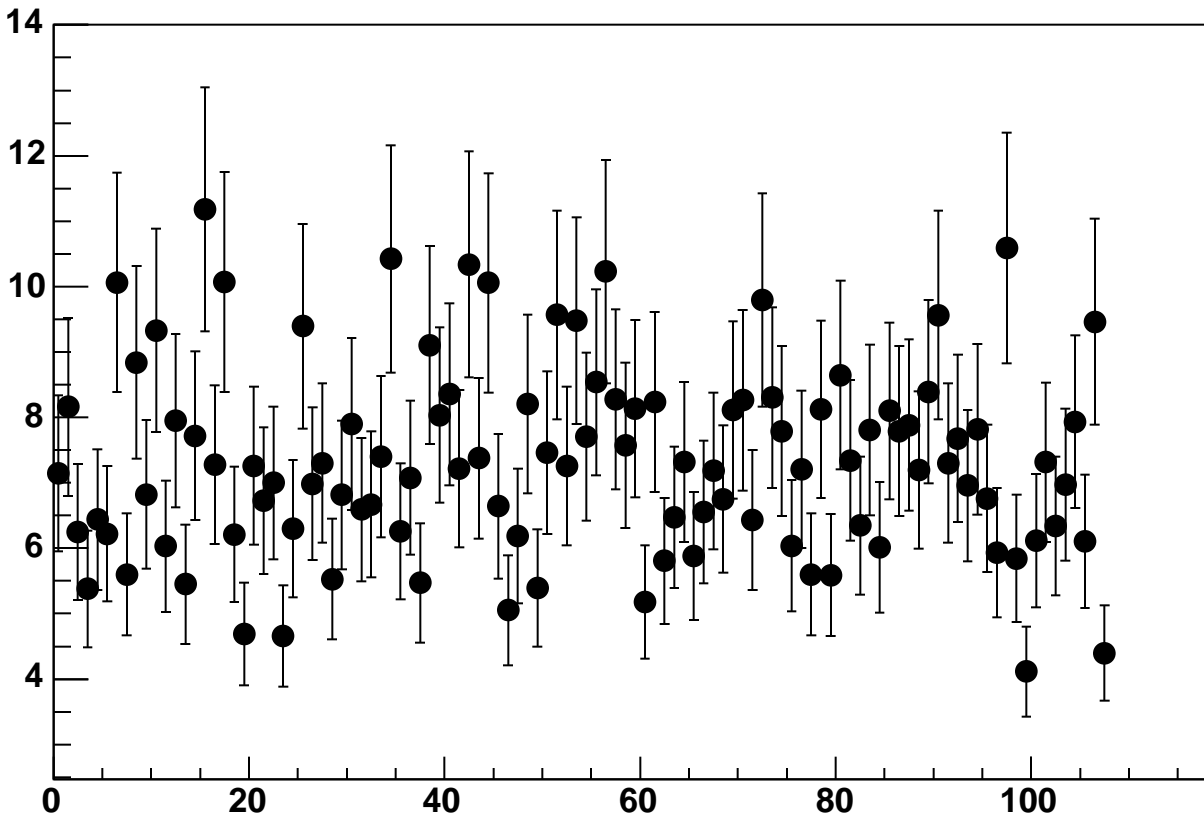
Enable 2, DAC=4000, Hold=175, ADC Noise vs 18\*Chip+Chan



Enable 2, DAC=4000, Hold=180, ADC Mean vs 18\*Chip+Chan

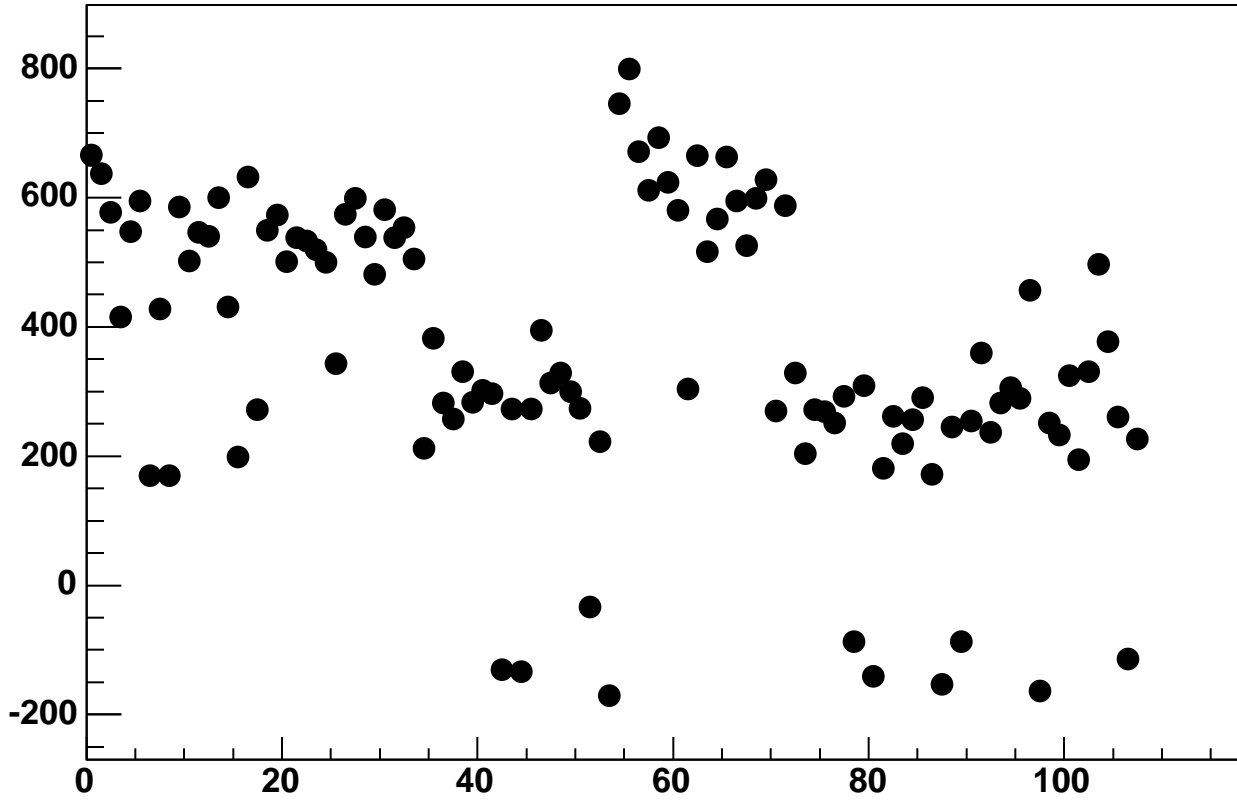


Enable 2, DAC=4000, Hold=180, ADC Noise vs 18\*Chip+Chan

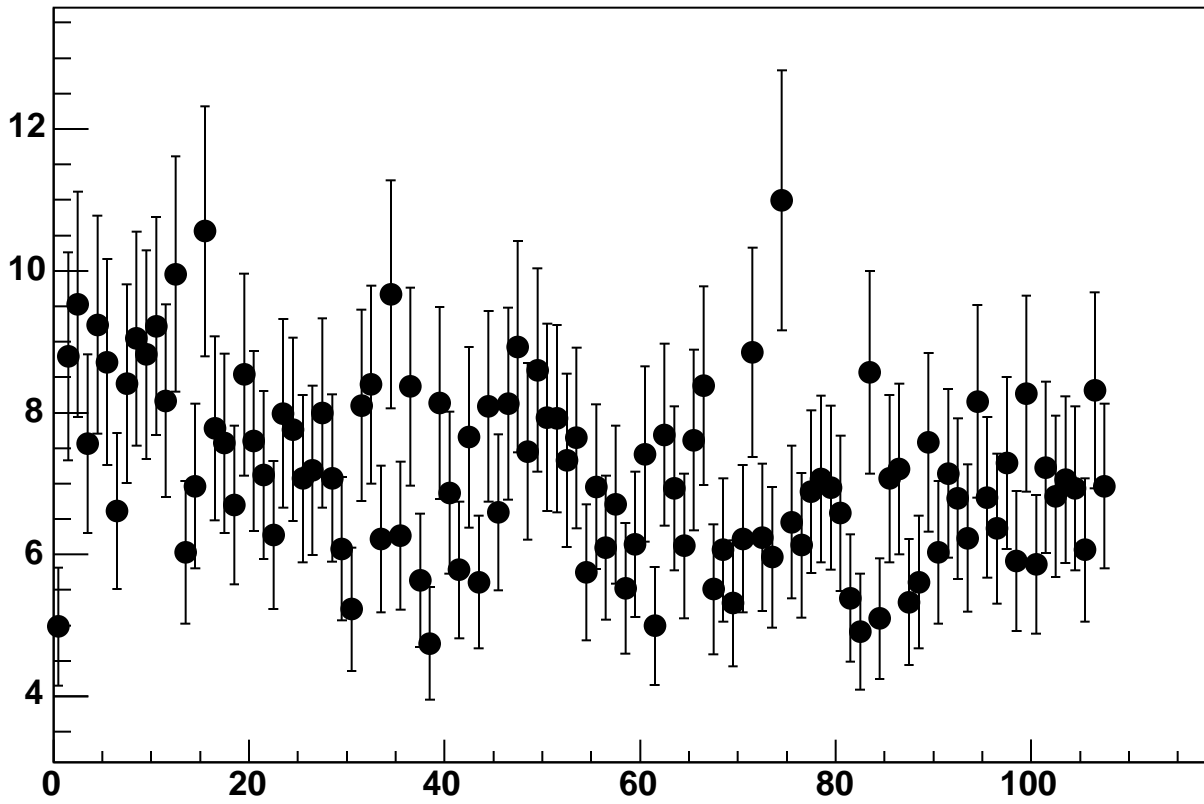




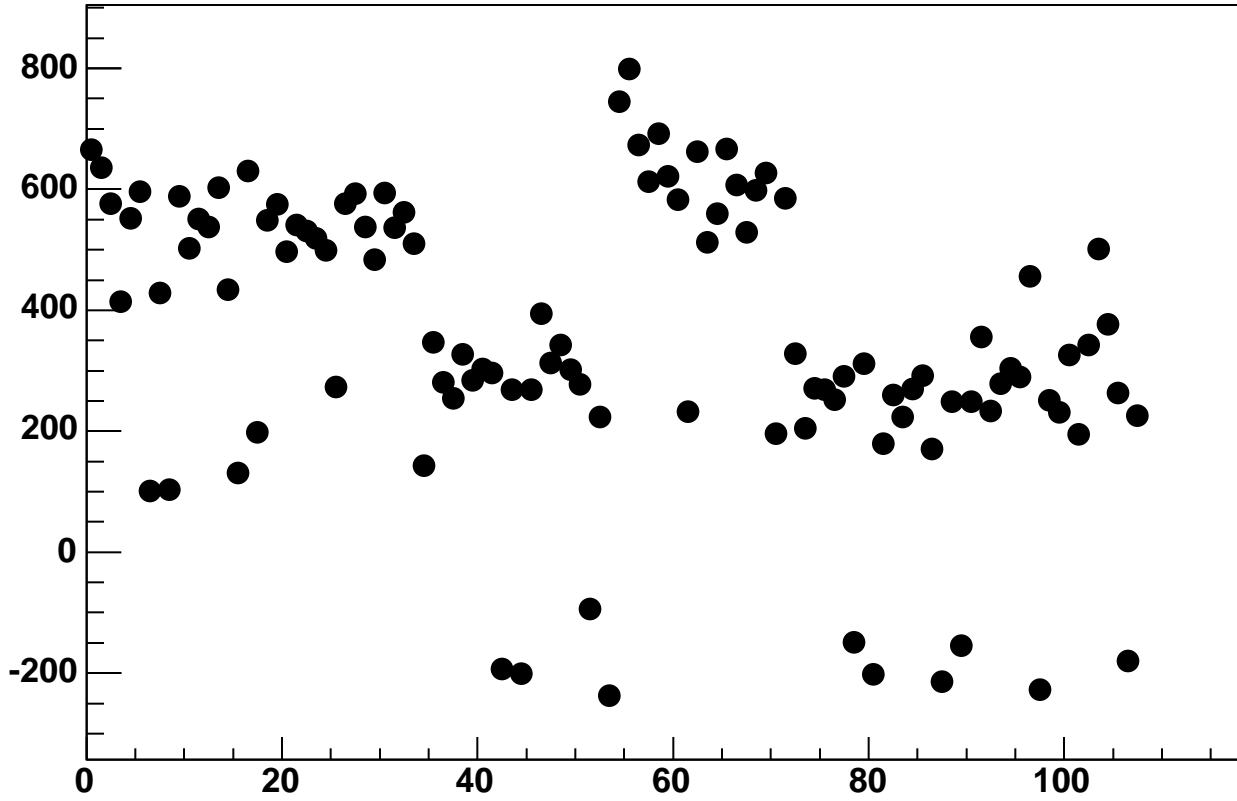
Enable 2, DAC=4000, Hold=185, ADC Mean vs 18\*Chip+Chan



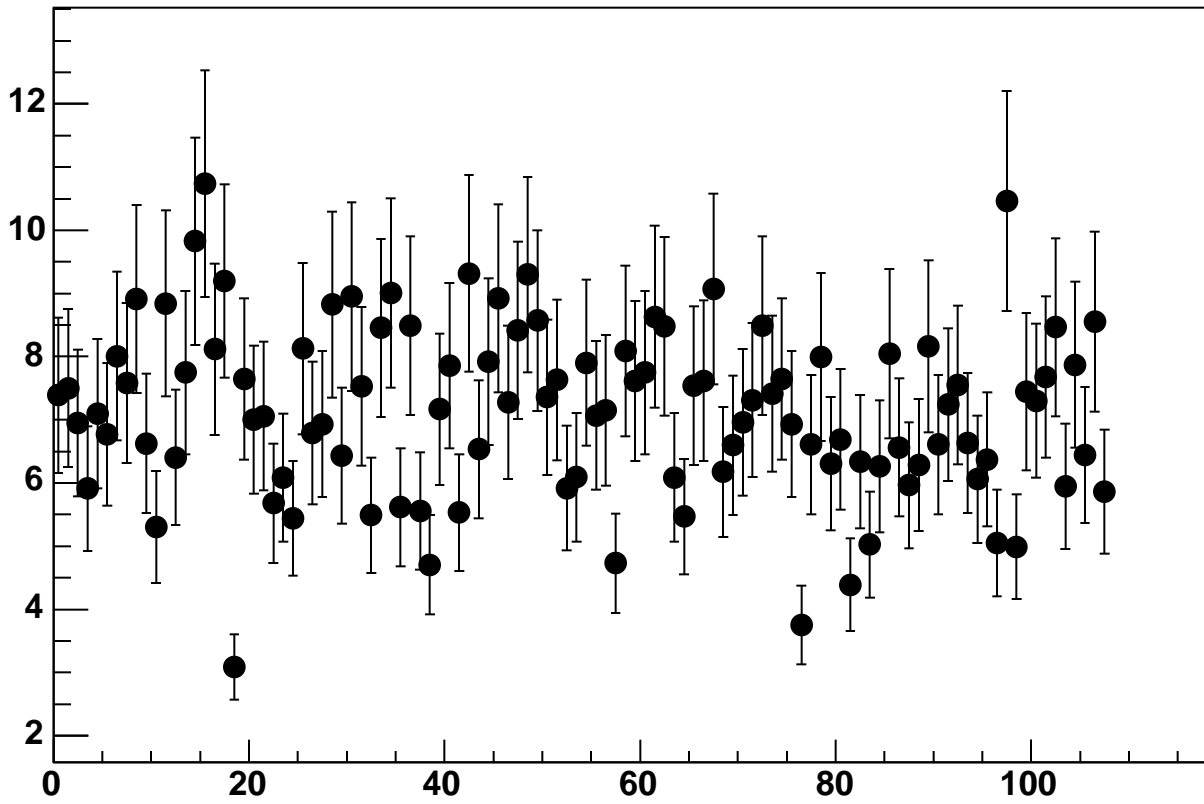
Enable 2, DAC=4000, Hold=185, ADC Noise vs 18\*Chip+Chan



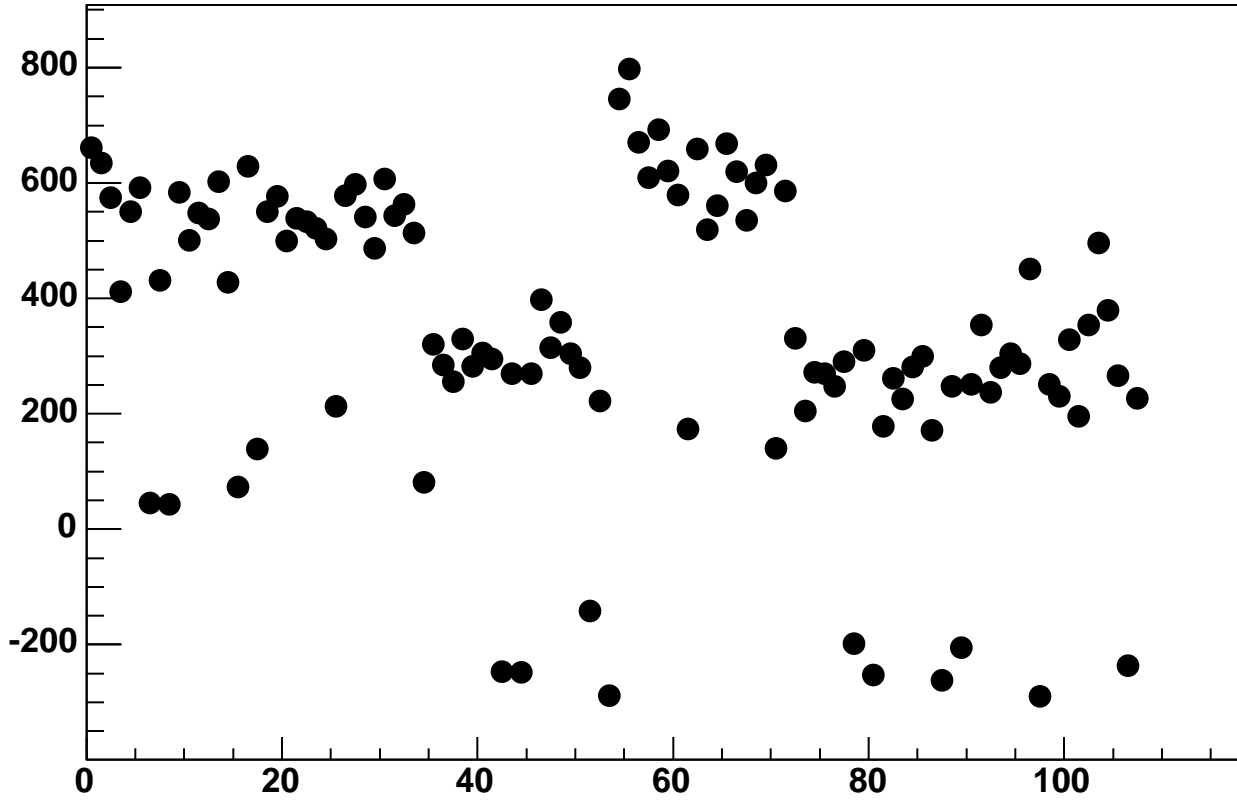
Enable 2, DAC=4000, Hold=190, ADC Mean vs 18\*Chip+Chan



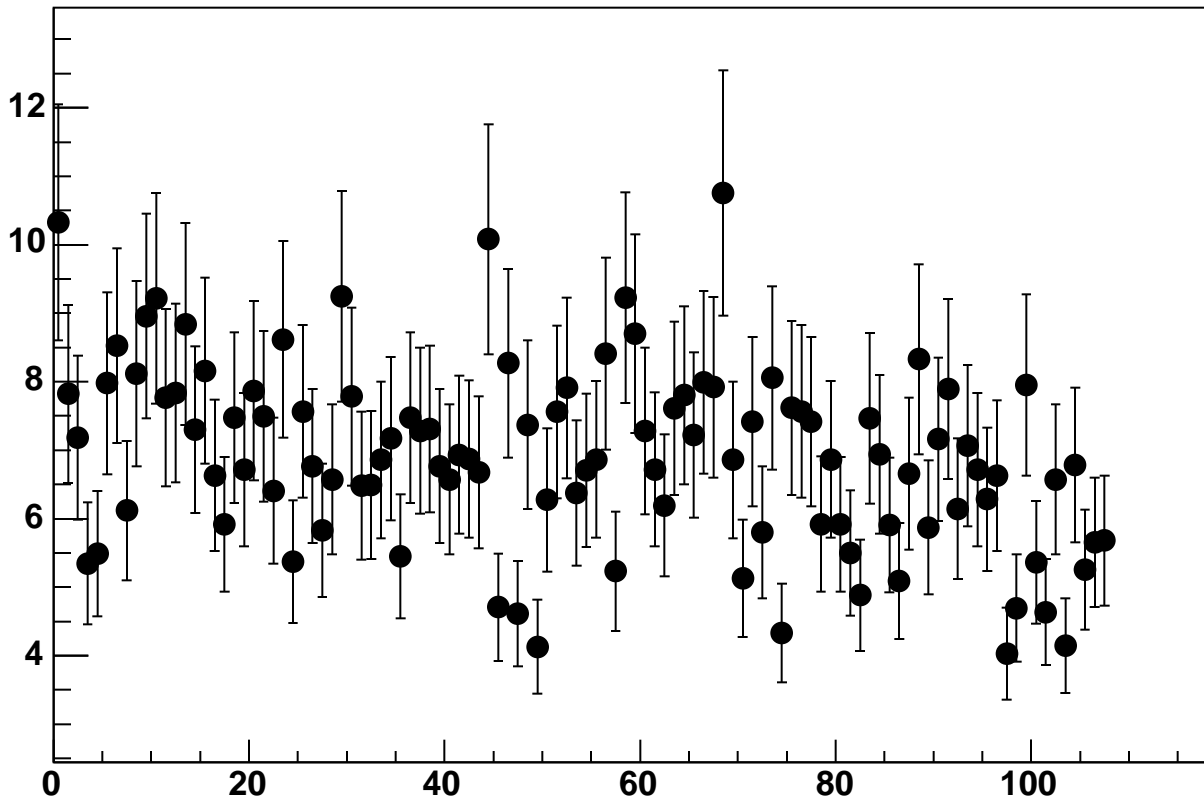
Enable 2, DAC=4000, Hold=190, ADC Noise vs 18\*Chip+Chan



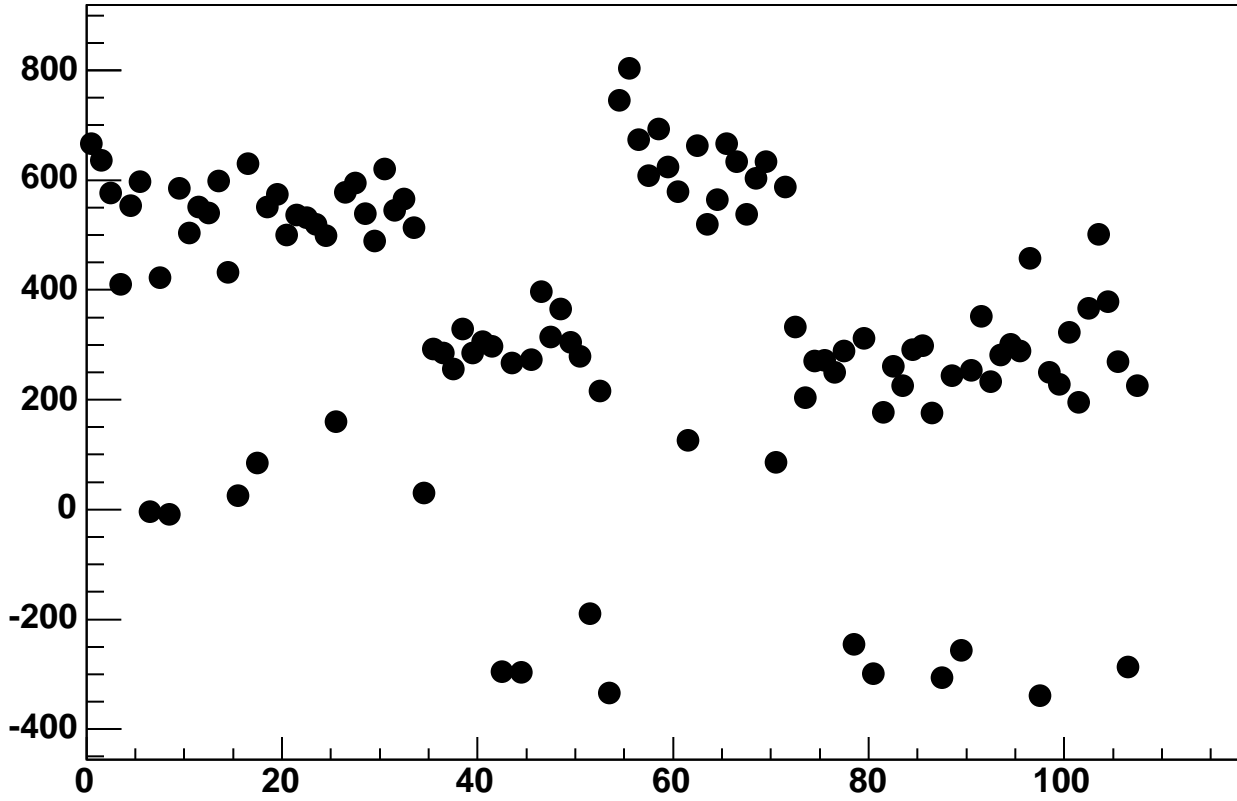
Enable 2, DAC=4000, Hold=195, ADC Mean vs 18\*Chip+Chan



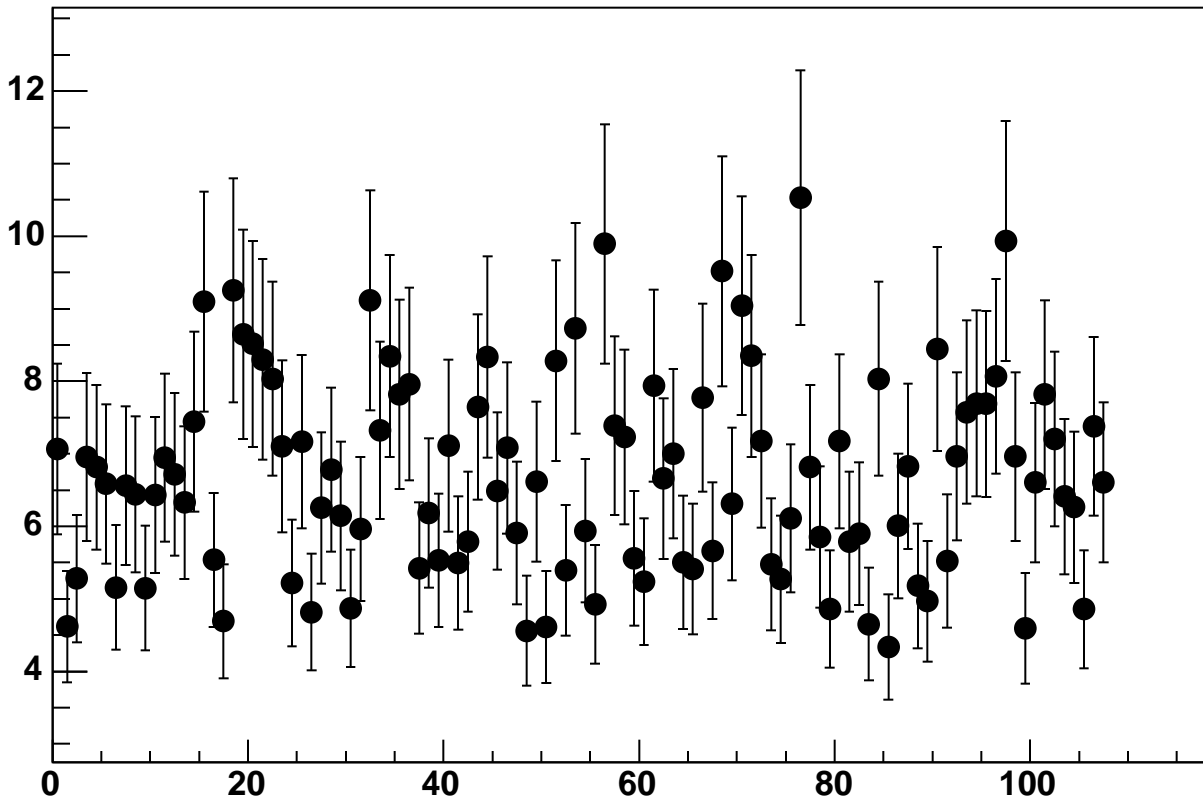
Enable 2, DAC=4000, Hold=195, ADC Noise vs 18\*Chip+Chan



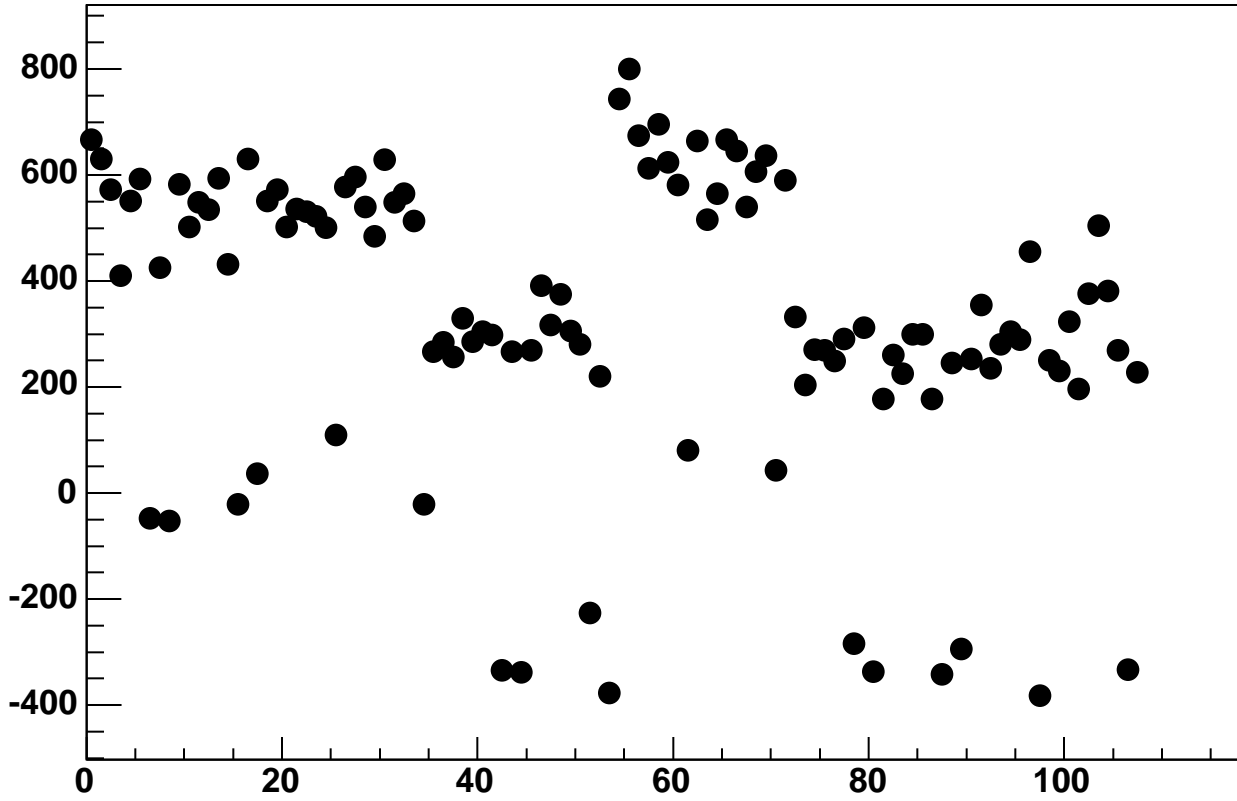
Enable 2, DAC=4000, Hold=200, ADC Mean vs 18\*Chip+Chan



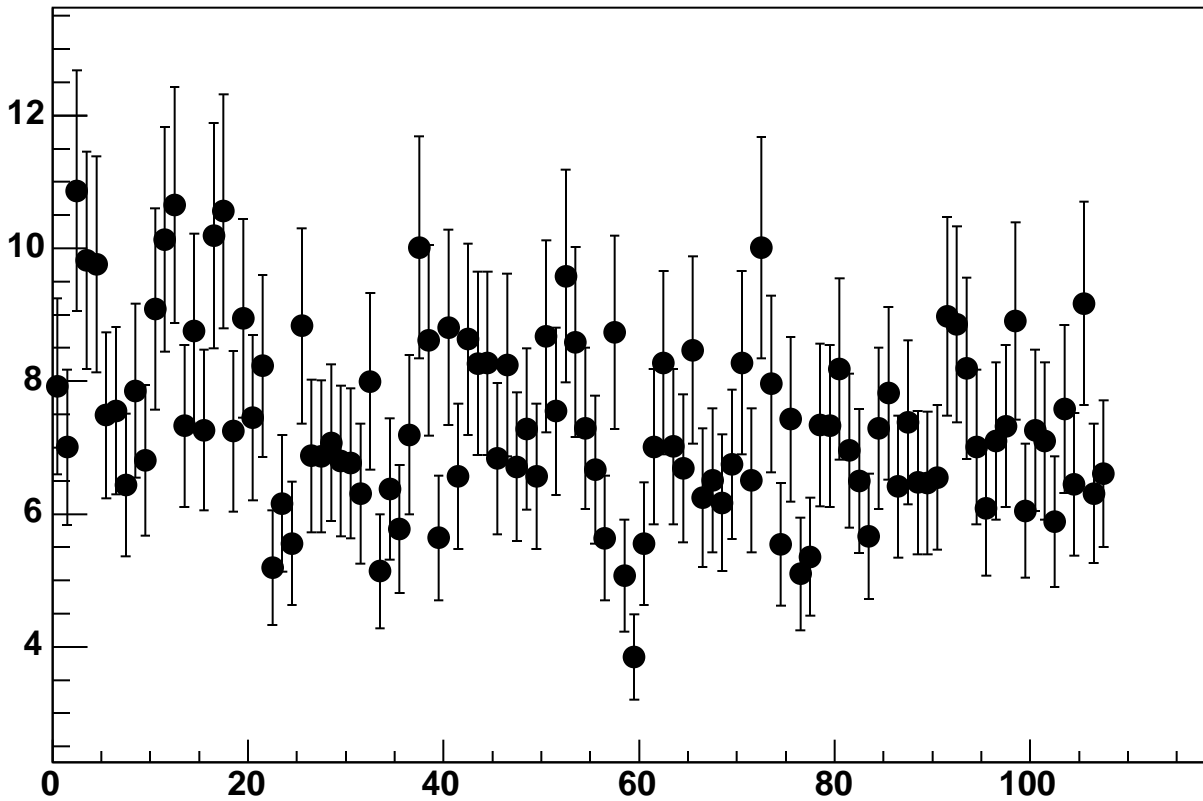
Enable 2, DAC=4000, Hold=200, ADC Noise vs 18\*Chip+Chan



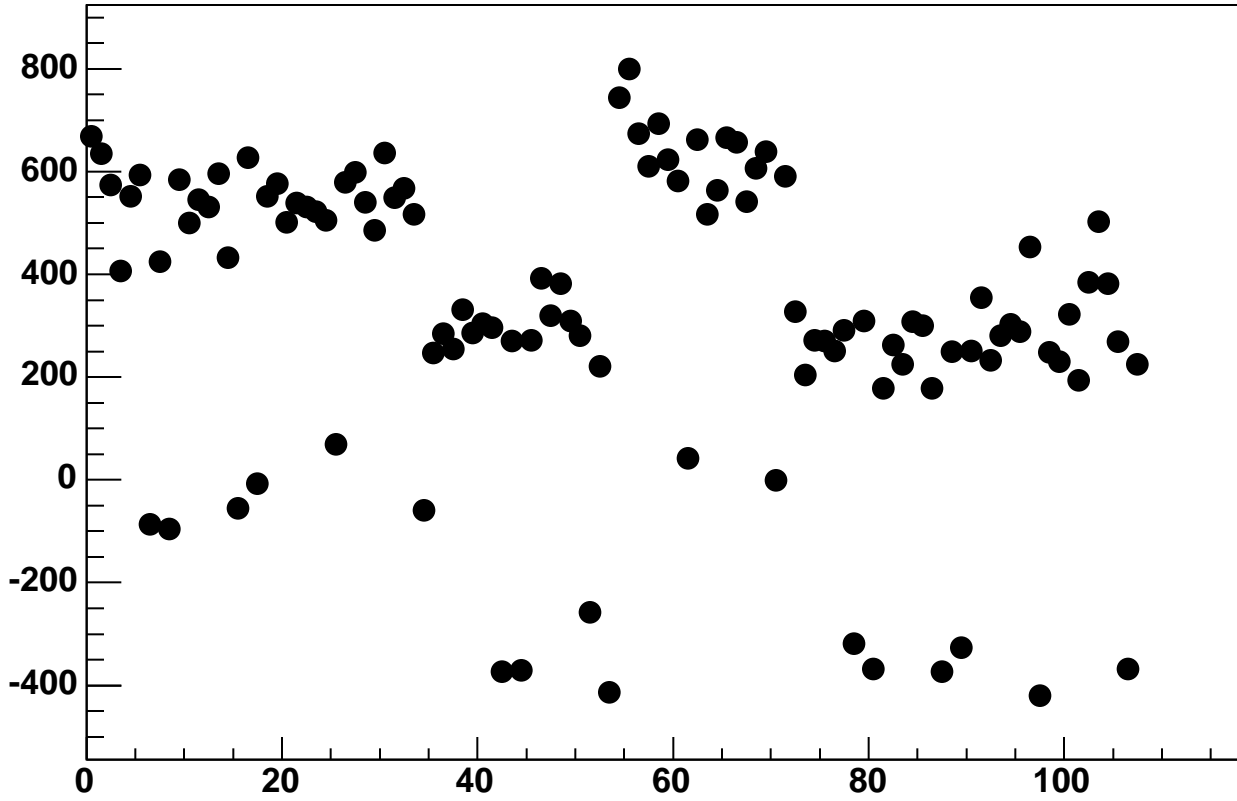
Enable 2, DAC=4000, Hold=205, ADC Mean vs 18\*Chip+Chan



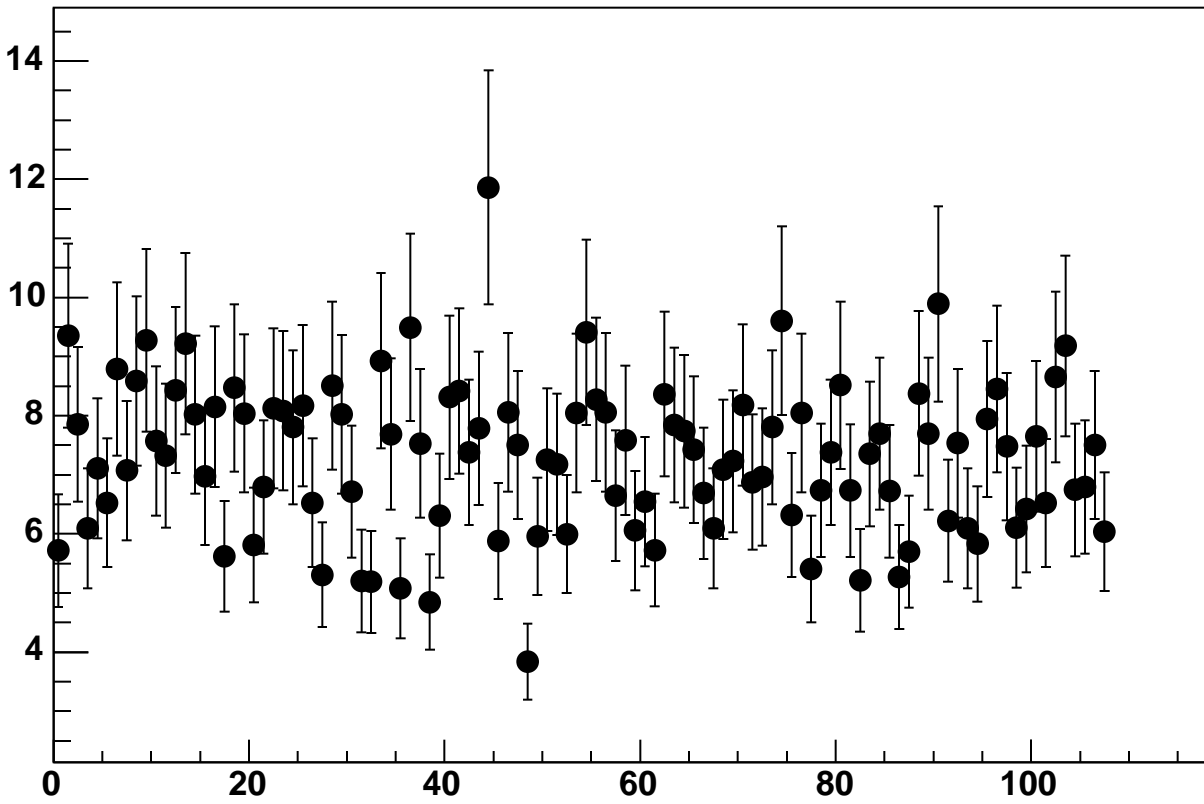
Enable 2, DAC=4000, Hold=205, ADC Noise vs 18\*Chip+Chan



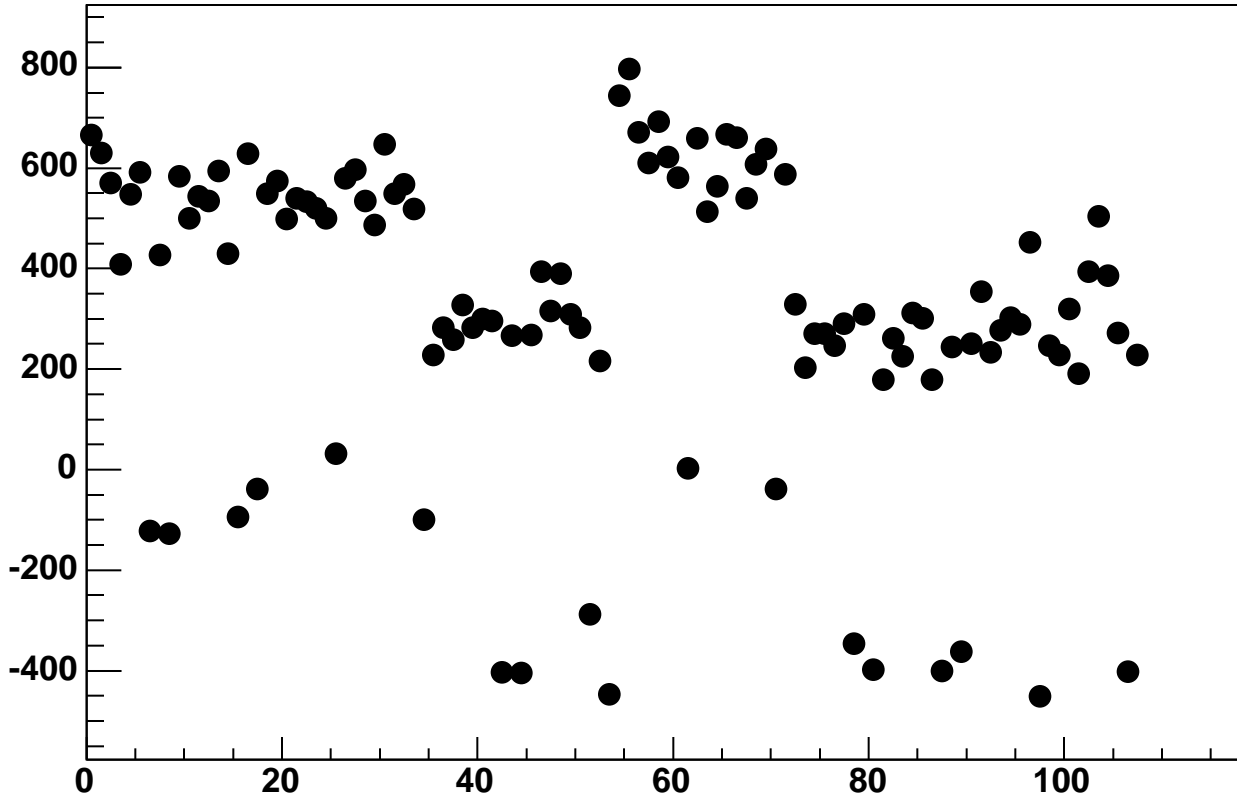
Enable 2, DAC=4000, Hold=210, ADC Mean vs 18\*Chip+Chan



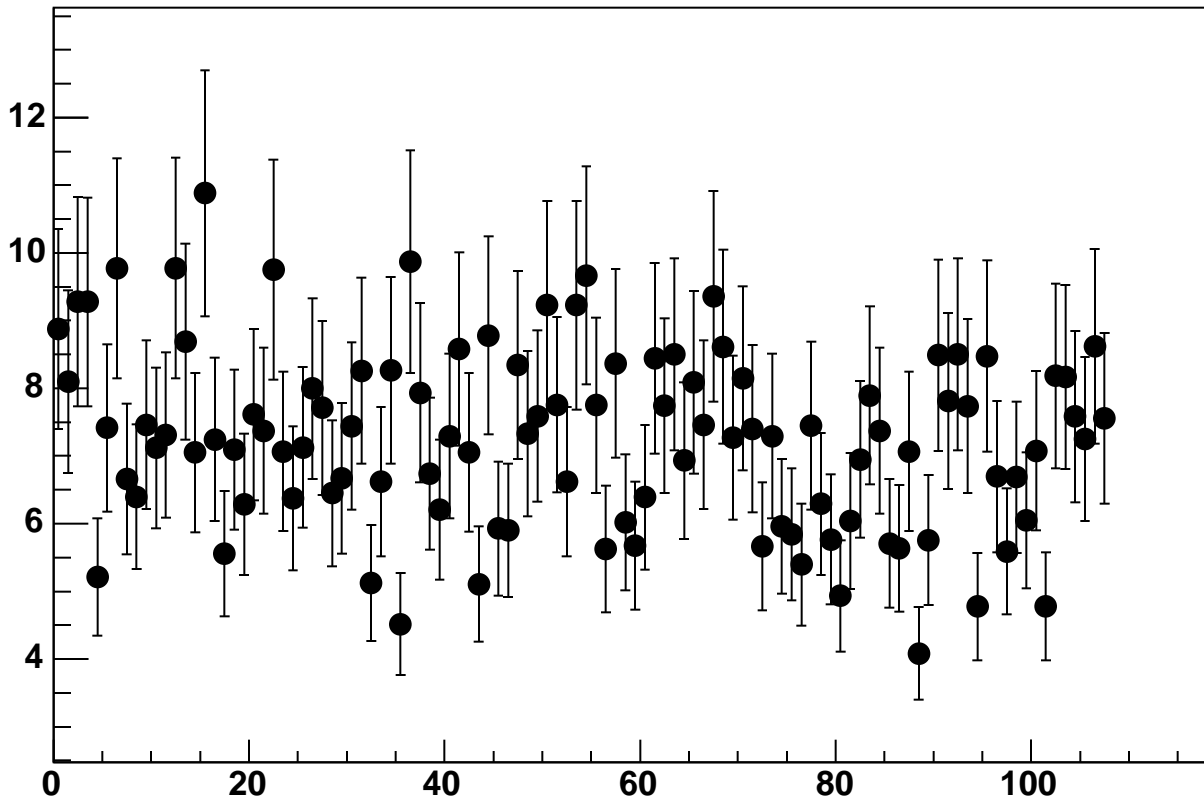
Enable 2, DAC=4000, Hold=210, ADC Noise vs 18\*Chip+Chan



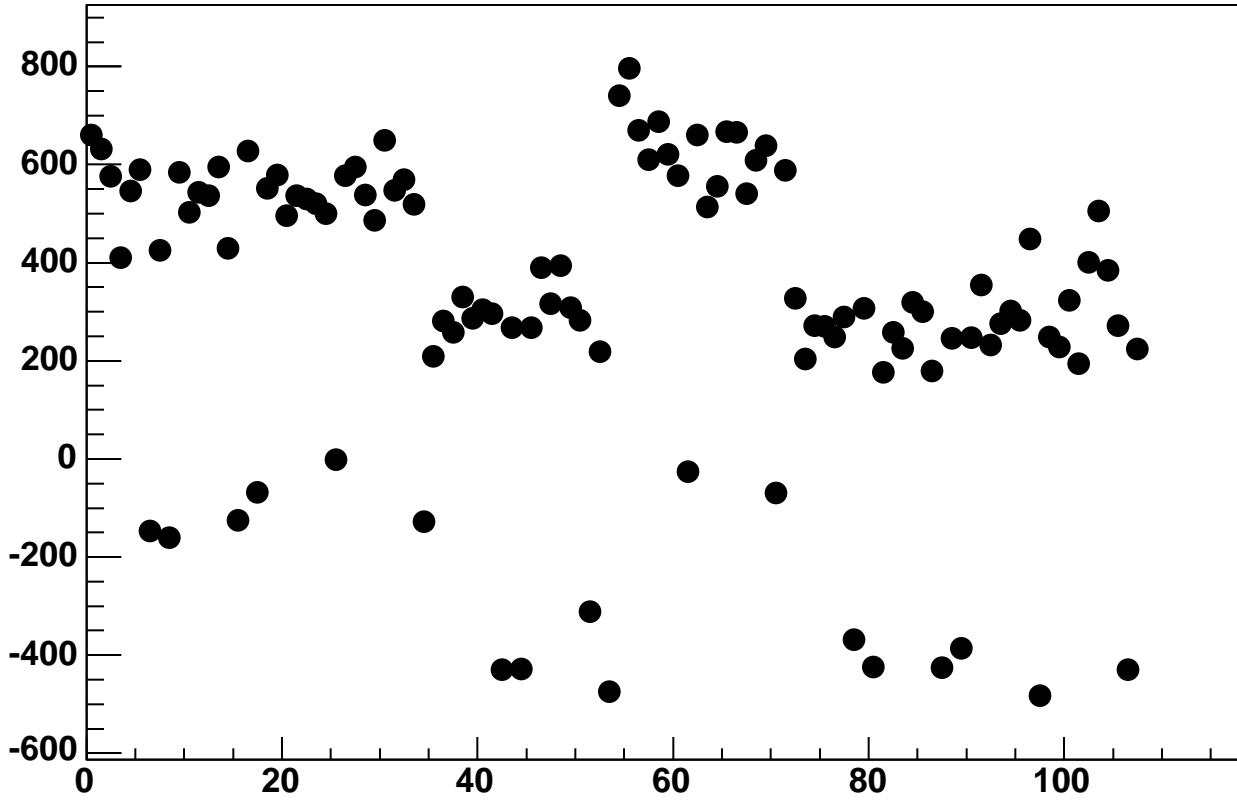
Enable 2, DAC=4000, Hold=215, ADC Mean vs 18\*Chip+Chan



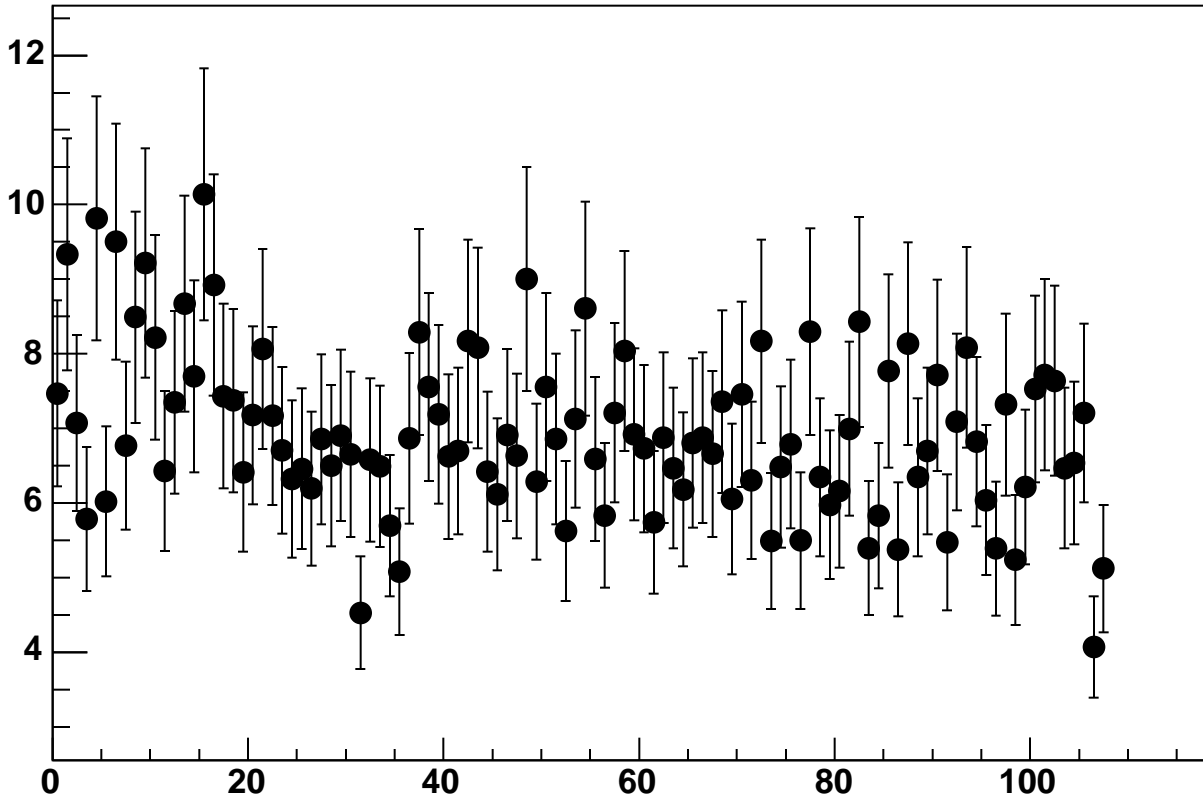
Enable 2, DAC=4000, Hold=215, ADC Noise vs 18\*Chip+Chan



Enable 2, DAC=4000, Hold=220, ADC Mean vs 18\*Chip+Chan

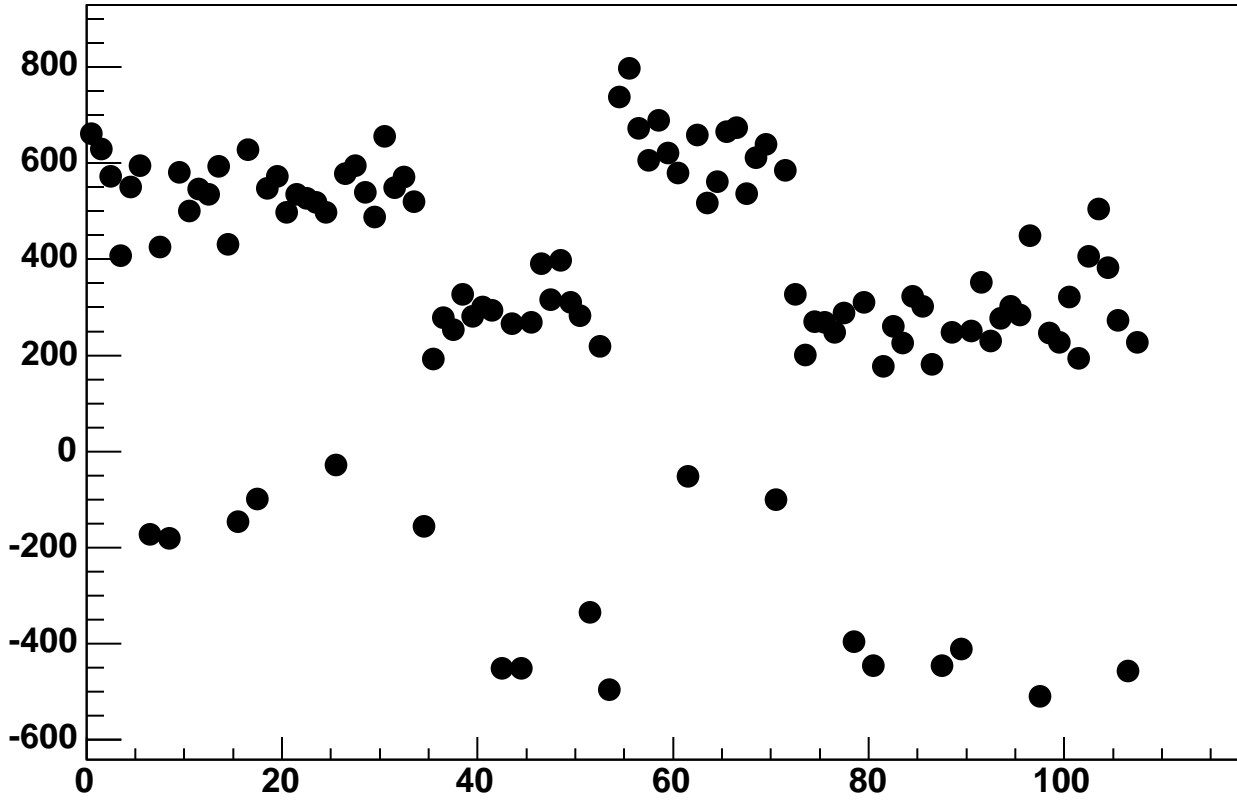


Enable 2, DAC=4000, Hold=220, ADC Noise vs 18\*Chip+Chan

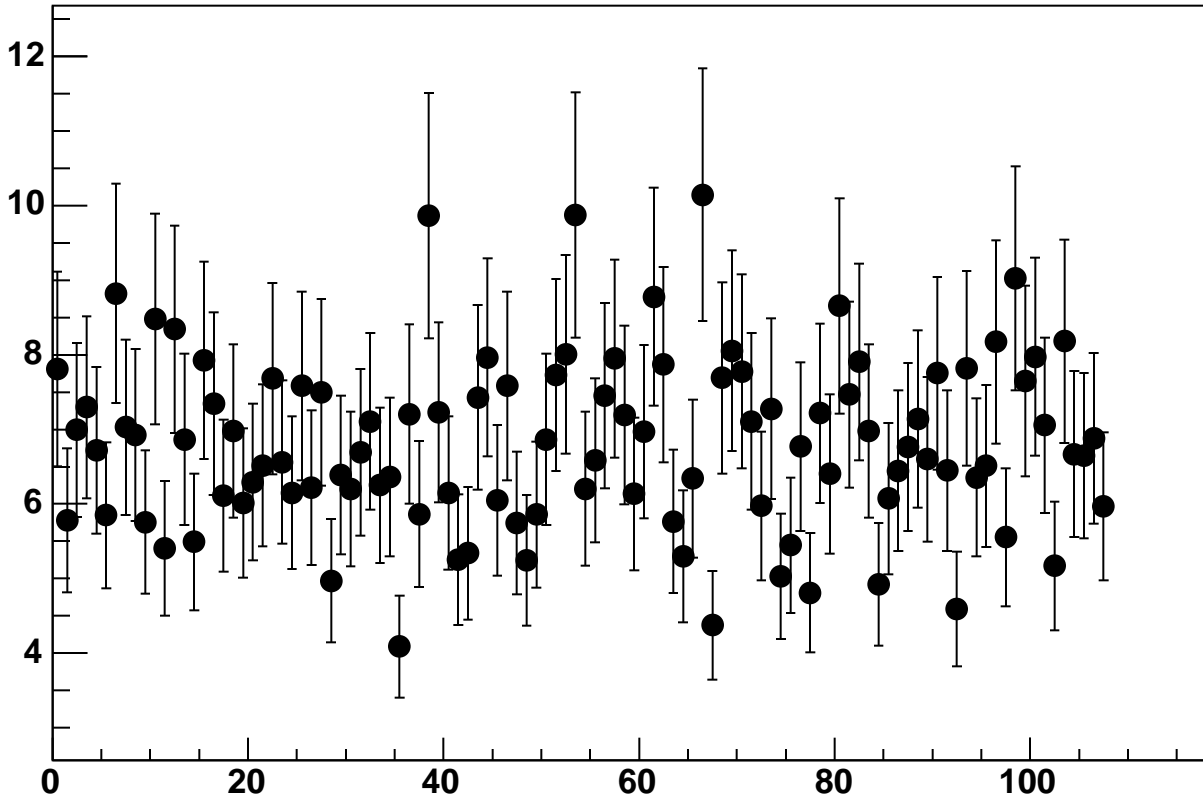




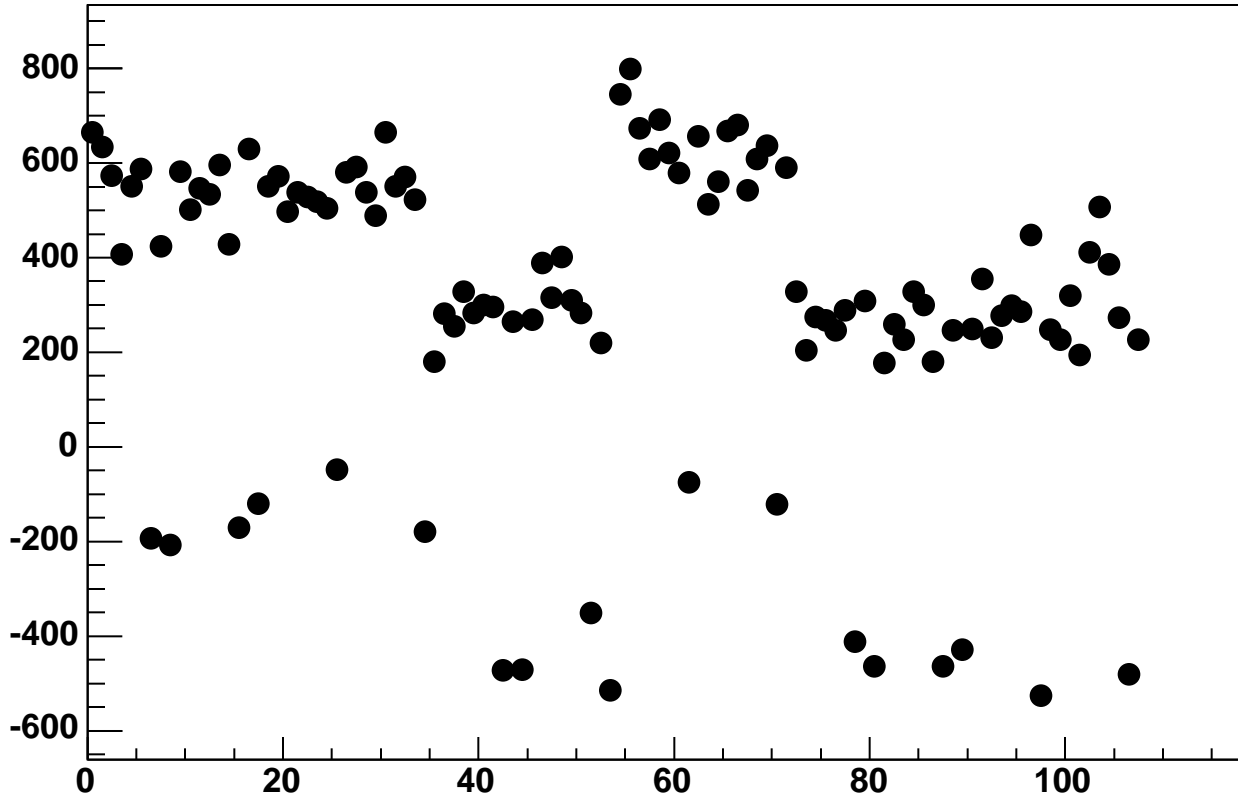
Enable 2, DAC=4000, Hold=225, ADC Mean vs 18\*Chip+Chan



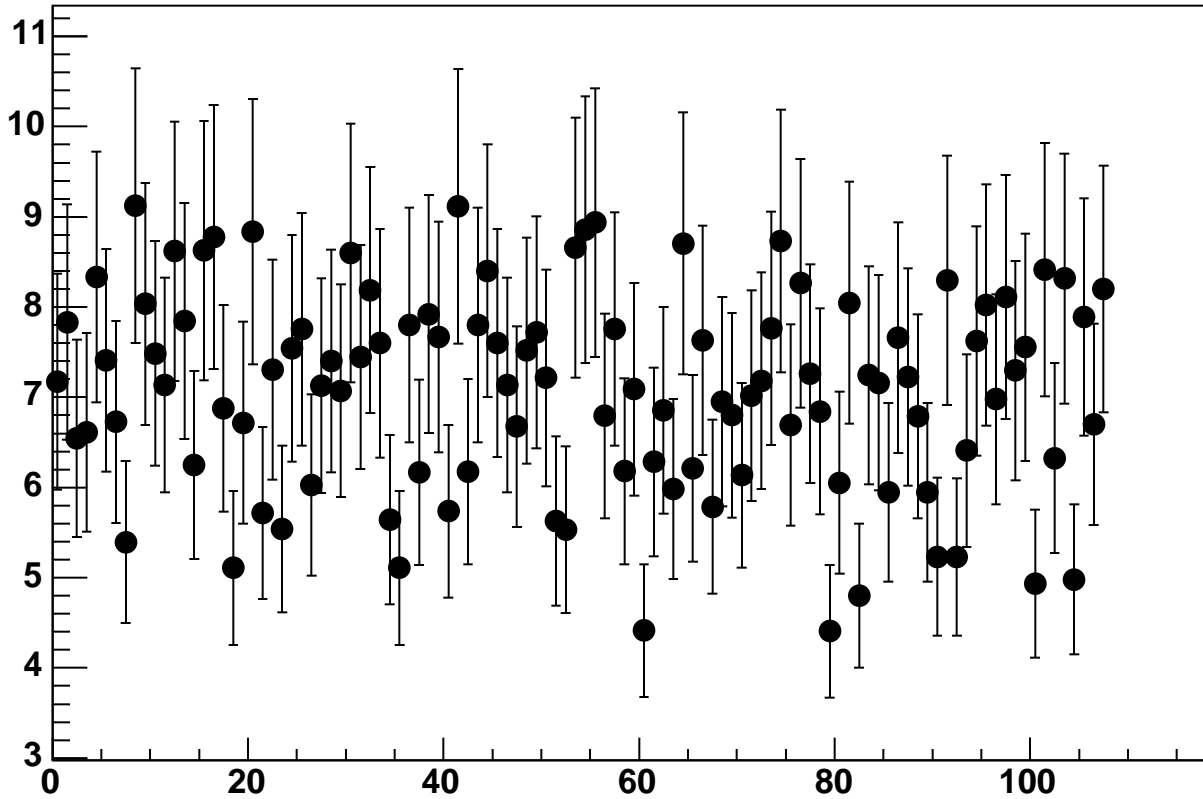
Enable 2, DAC=4000, Hold=225, ADC Noise vs 18\*Chip+Chan



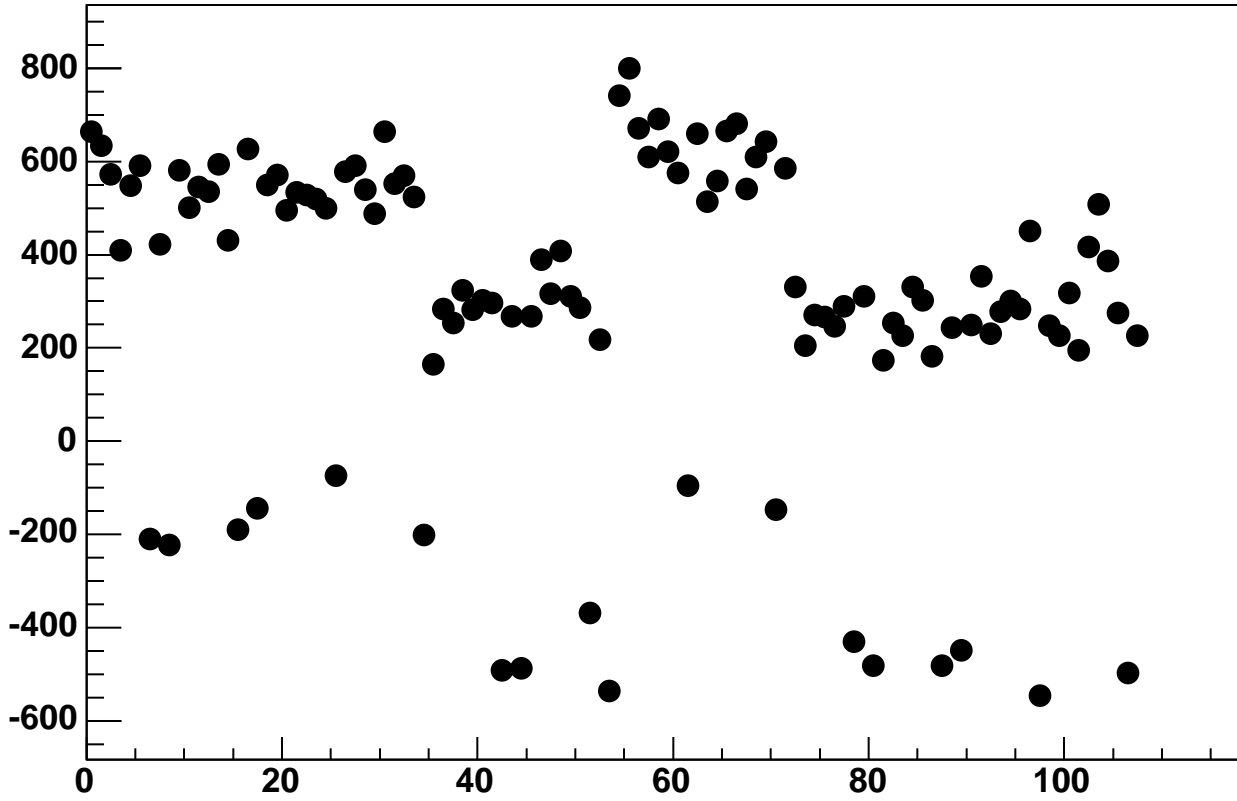
Enable 2, DAC=4000, Hold=230, ADC Mean vs 18\*Chip+Chan



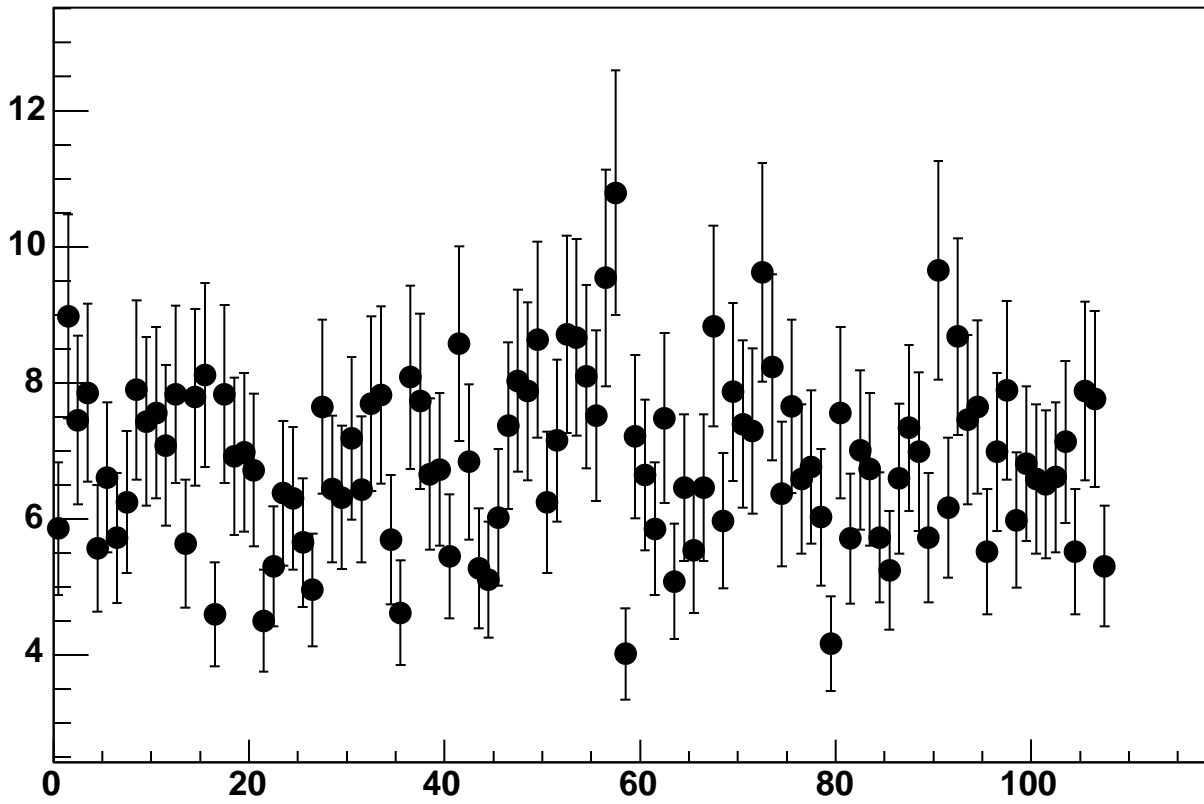
Enable 2, DAC=4000, Hold=230, ADC Noise vs 18\*Chip+Chan



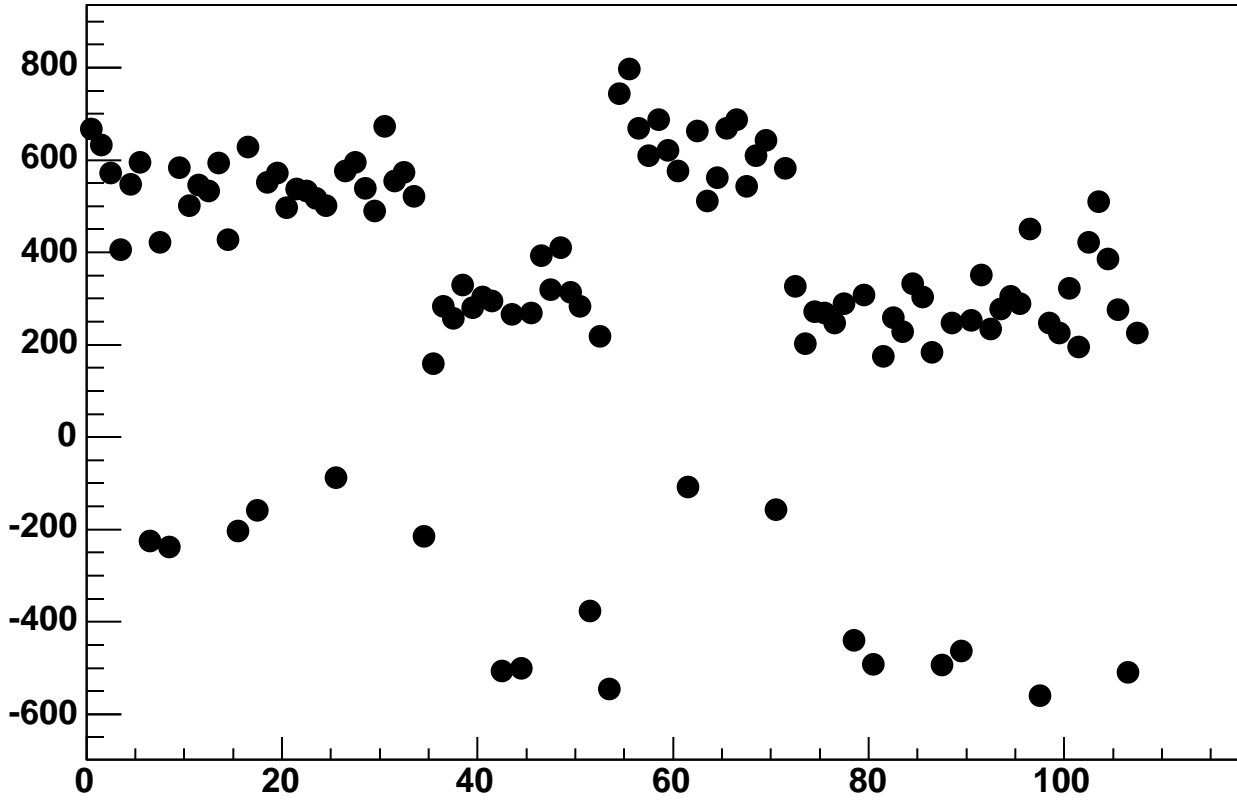
Enable 2, DAC=4000, Hold=235, ADC Mean vs 18\*Chip+Chan



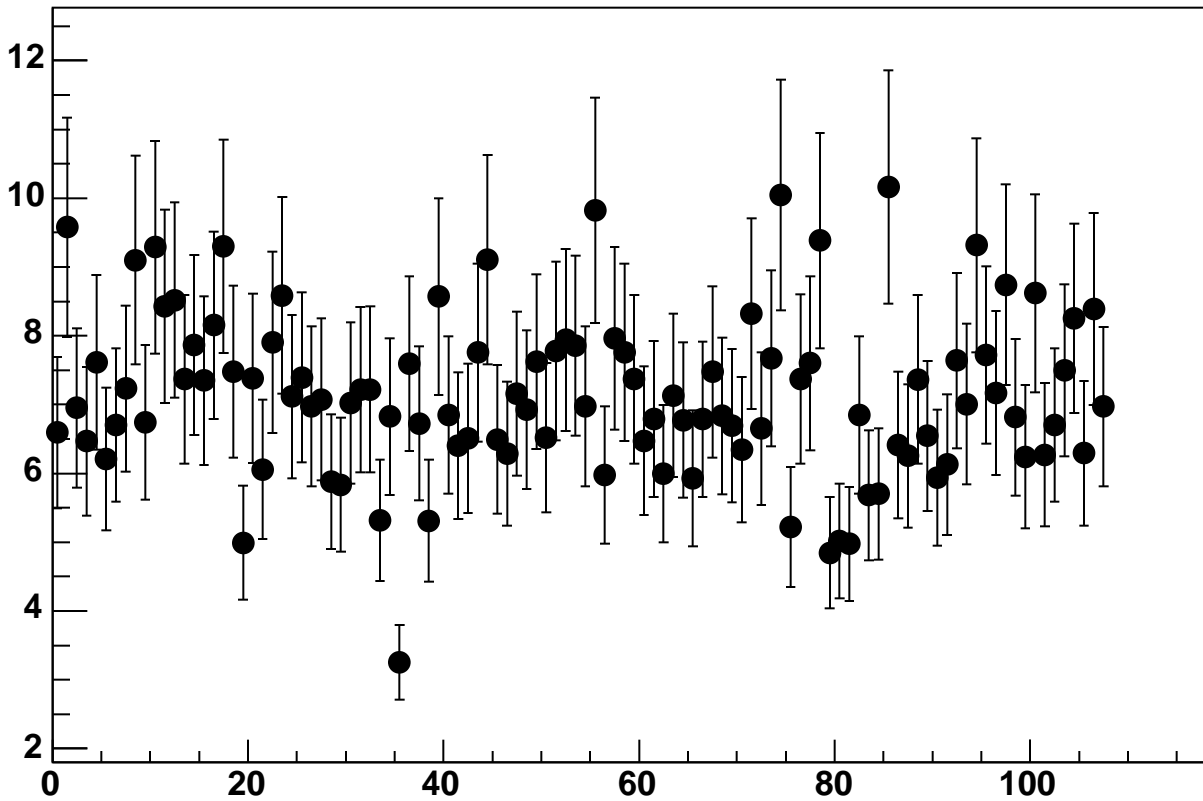
Enable 2, DAC=4000, Hold=235, ADC Noise vs 18\*Chip+Chan



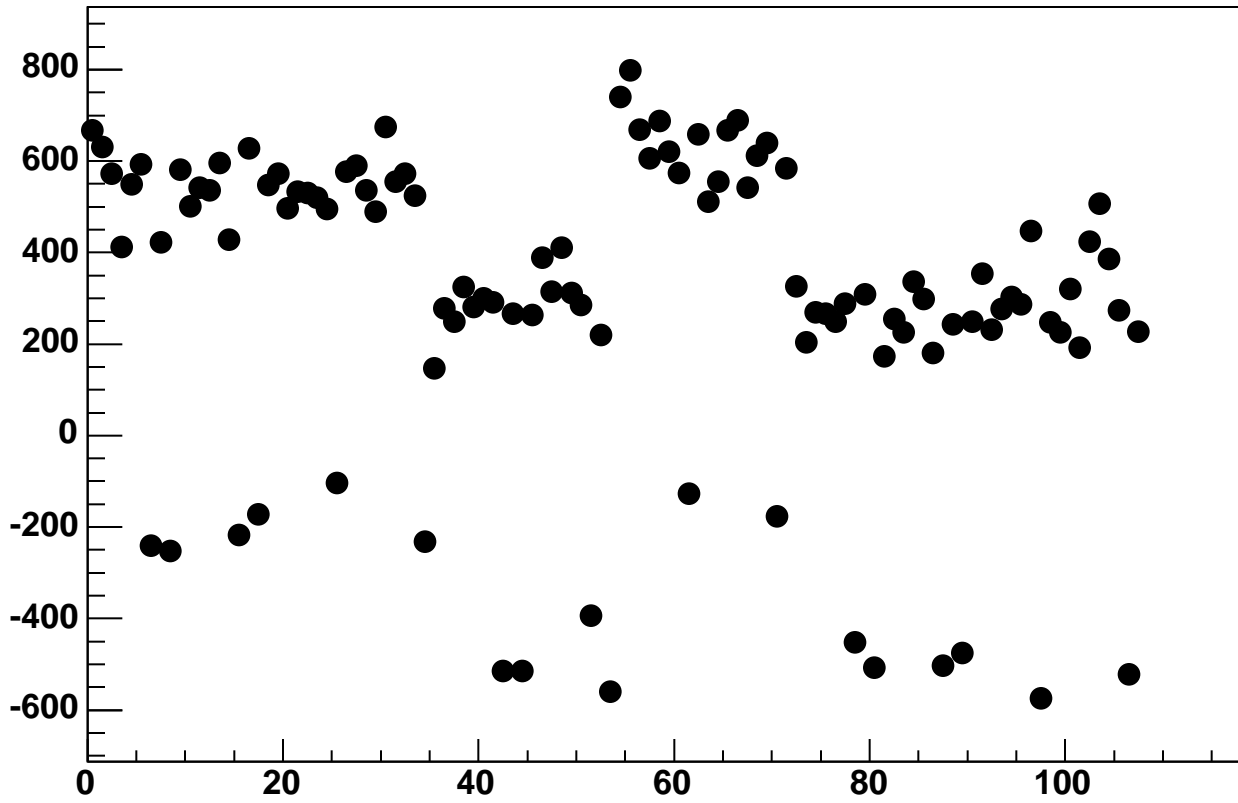
Enable 2, DAC=4000, Hold=240, ADC Mean vs 18\*Chip+Chan



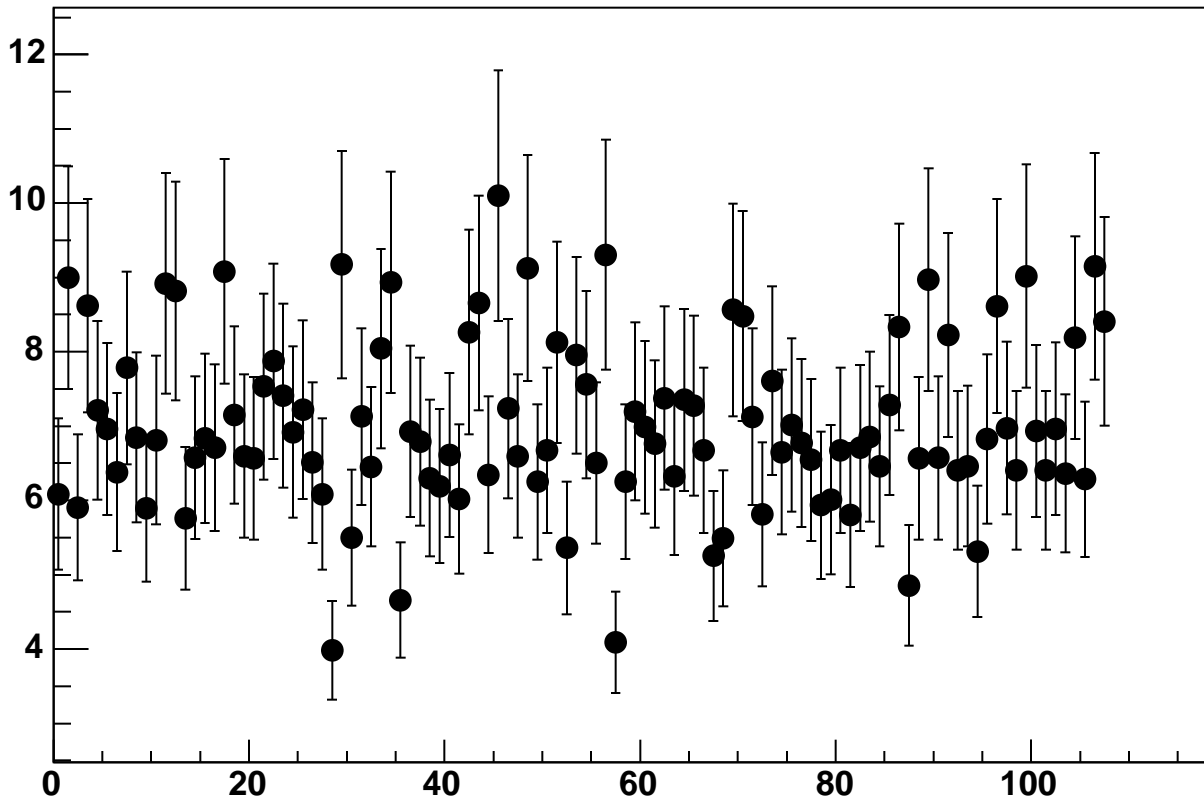
Enable 2, DAC=4000, Hold=240, ADC Noise vs 18\*Chip+Chan



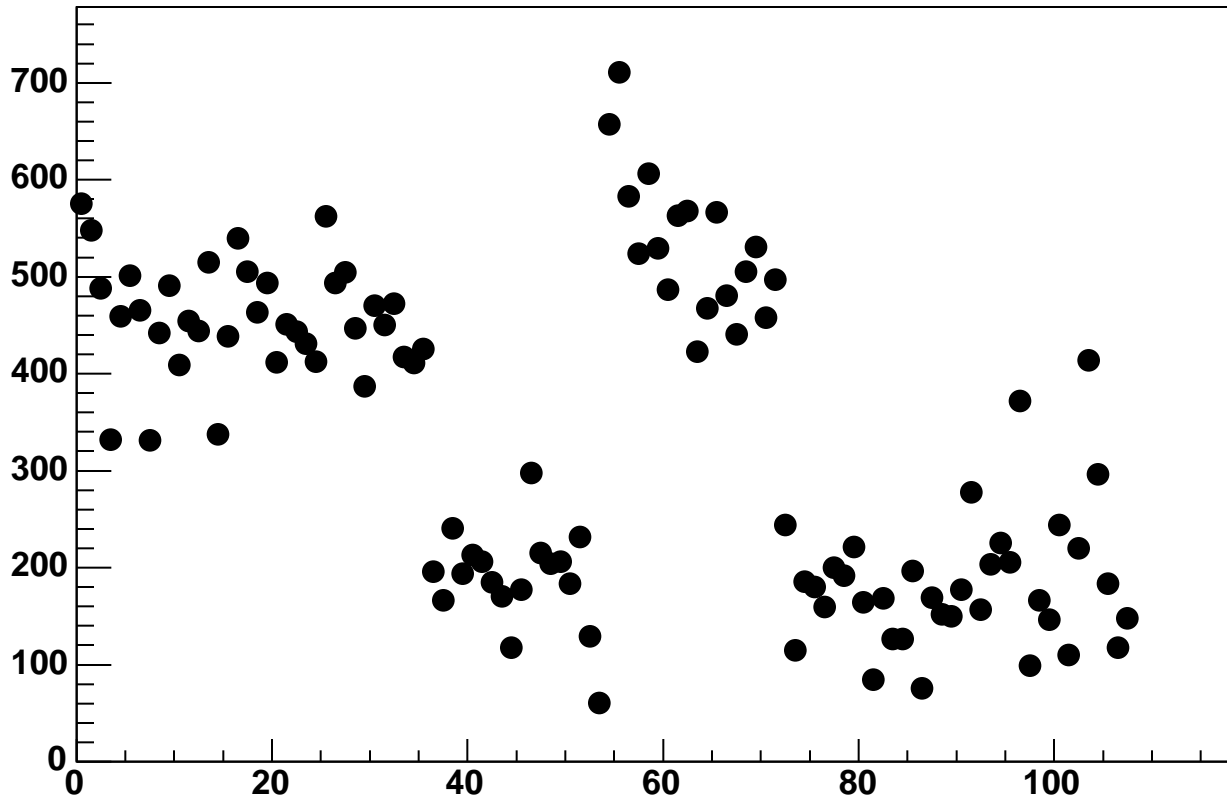
Enable 2, DAC=4000, Hold=245, ADC Mean vs 18\*Chip+Chan



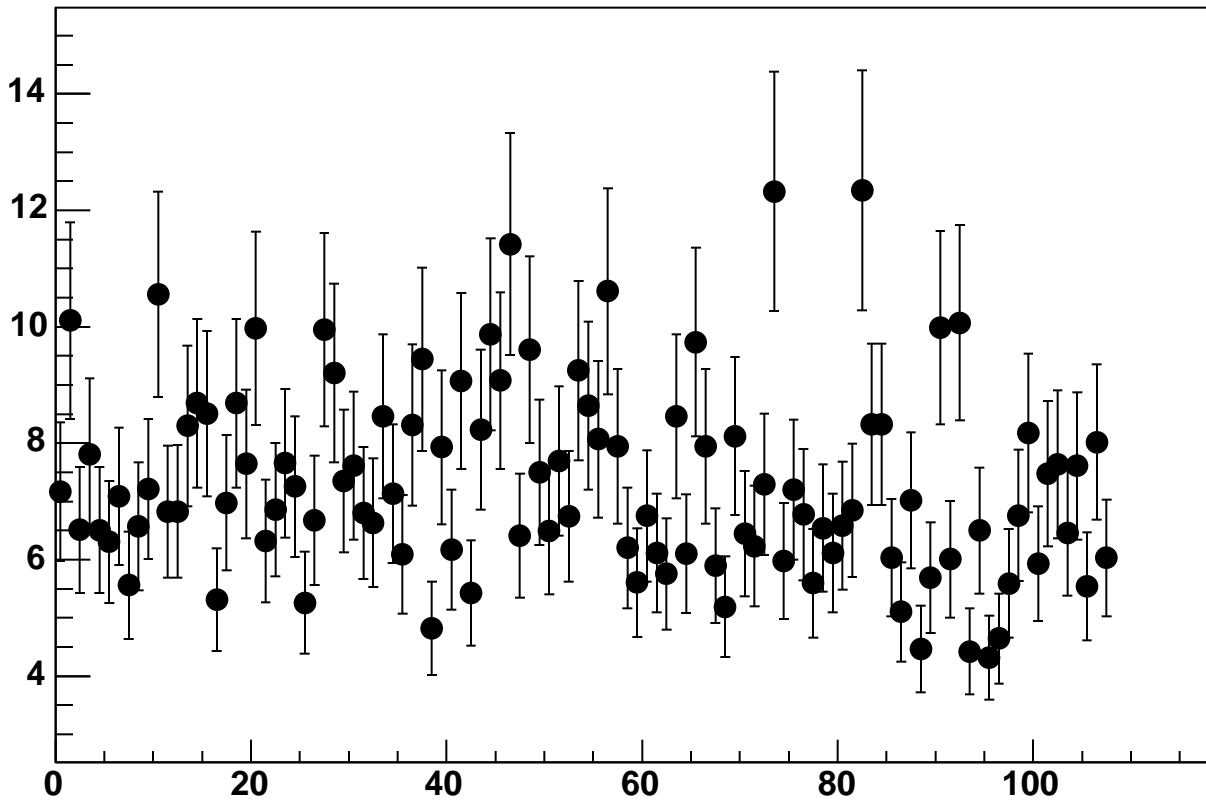
Enable 2, DAC=4000, Hold=245, ADC Noise vs 18\*Chip+Chan



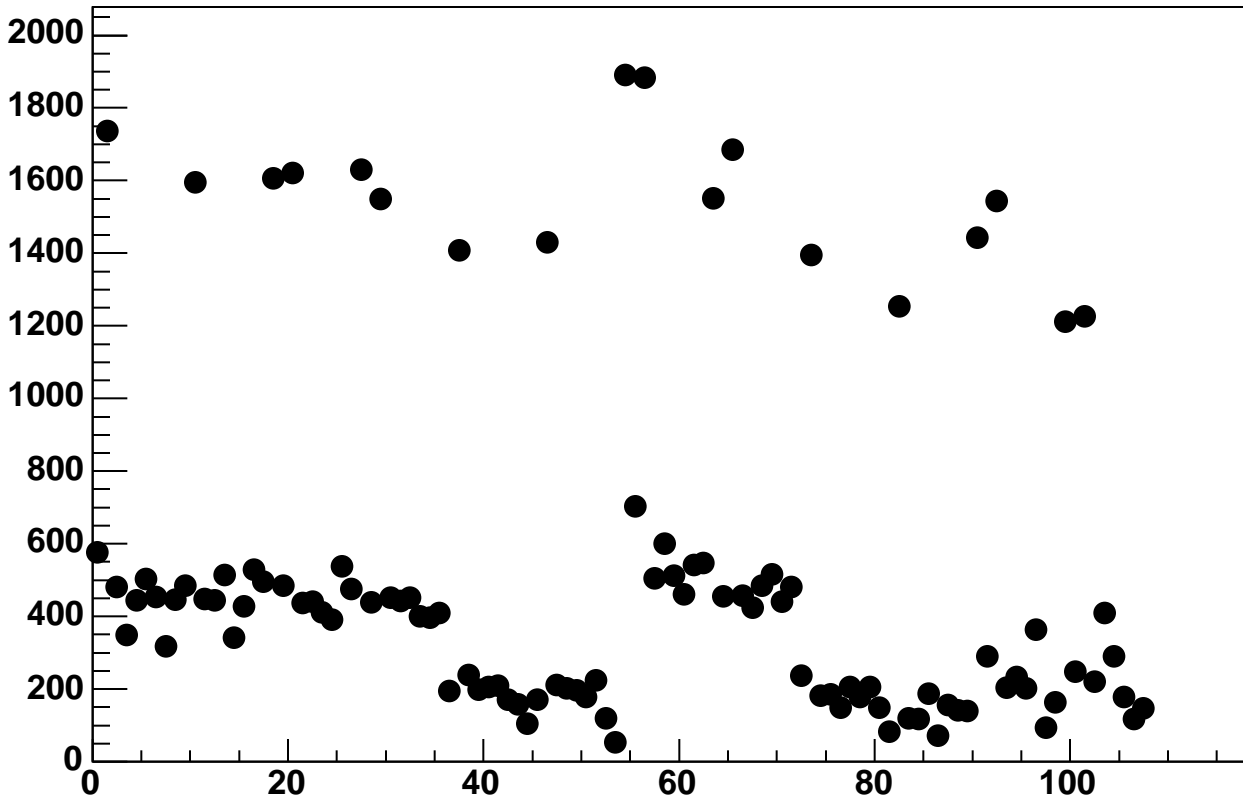
Enable 3, DAC=4000, Hold=0, ADC Mean vs 18\*Chip+Chan



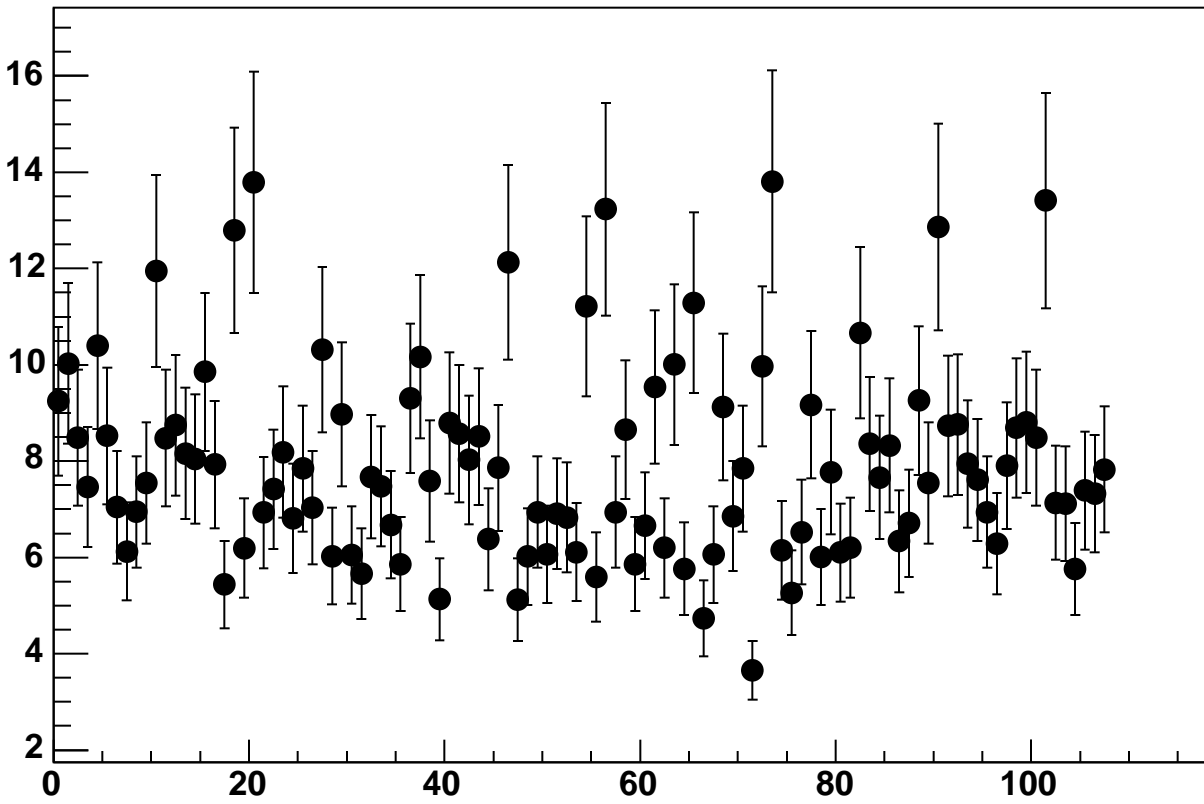
Enable 3, DAC=4000, Hold=0, ADC Noise vs 18\*Chip+Chan



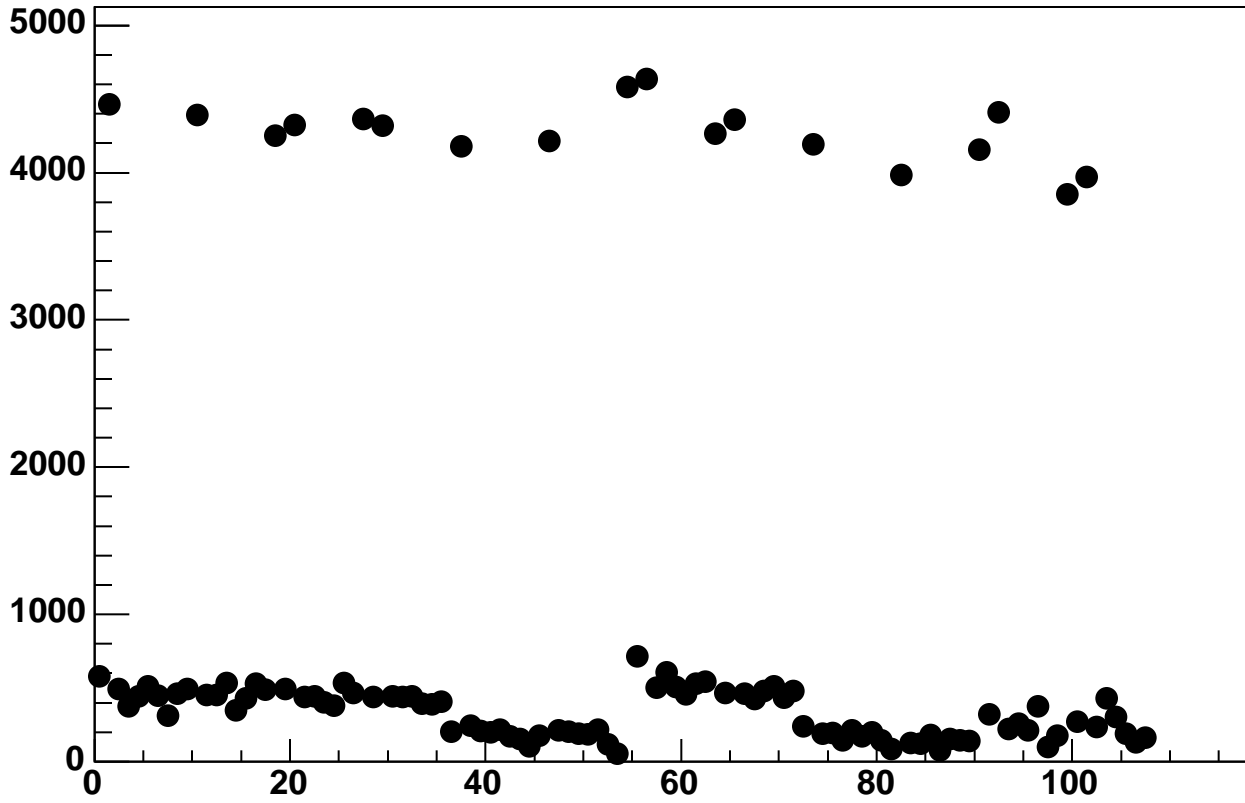
Enable 3, DAC=4000, Hold=5, ADC Mean vs 18\*Chip+Chan



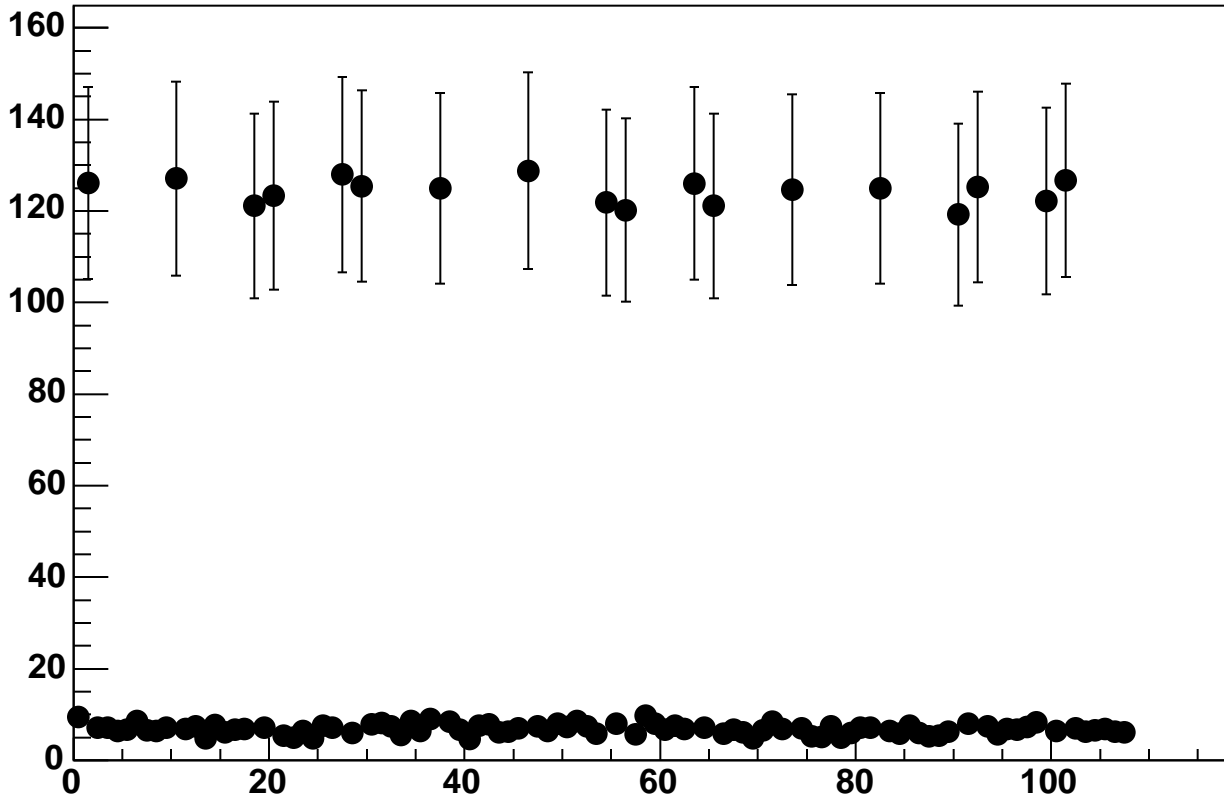
Enable 3, DAC=4000, Hold=5, ADC Noise vs 18\*Chip+Chan



Enable 3, DAC=4000, Hold=10, ADC Mean vs 18\*Chip+Chan

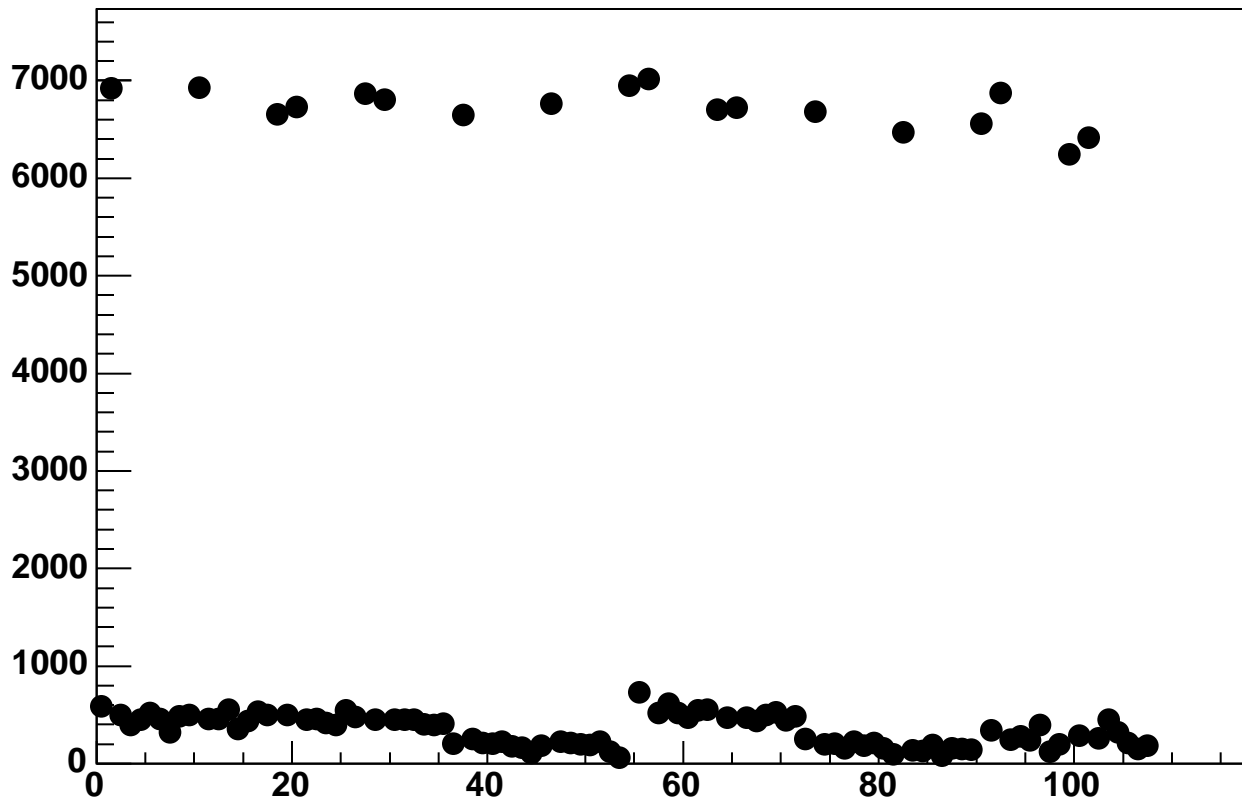


Enable 3, DAC=4000, Hold=10, ADC Noise vs 18\*Chip+Chan

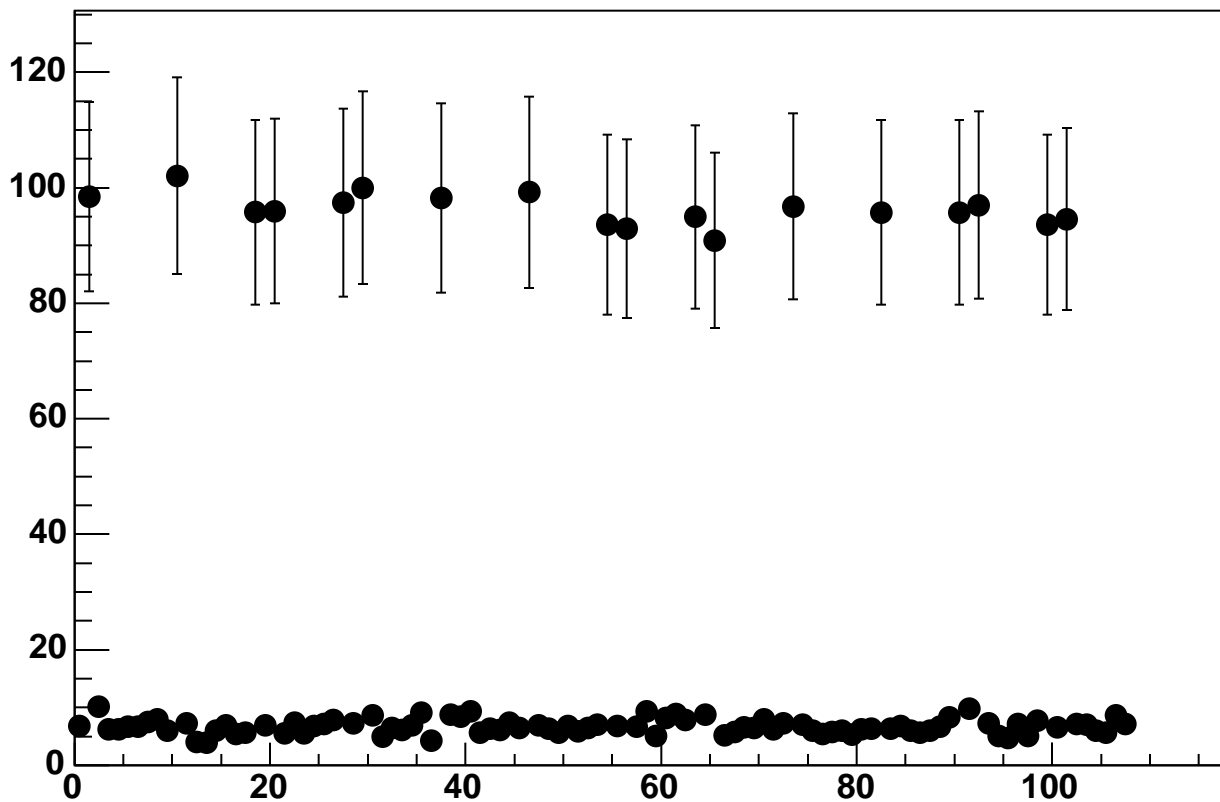




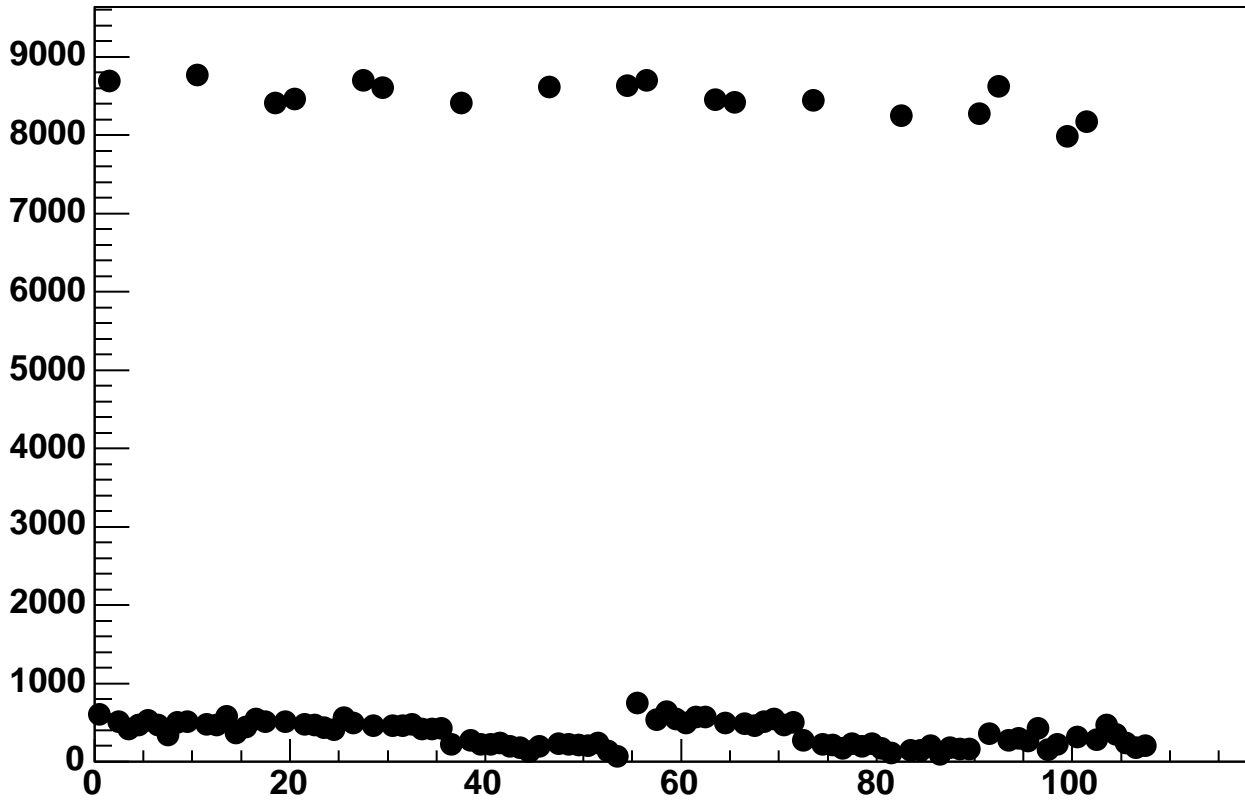
Enable 3, DAC=4000, Hold=15, ADC Mean vs 18\*Chip+Chan



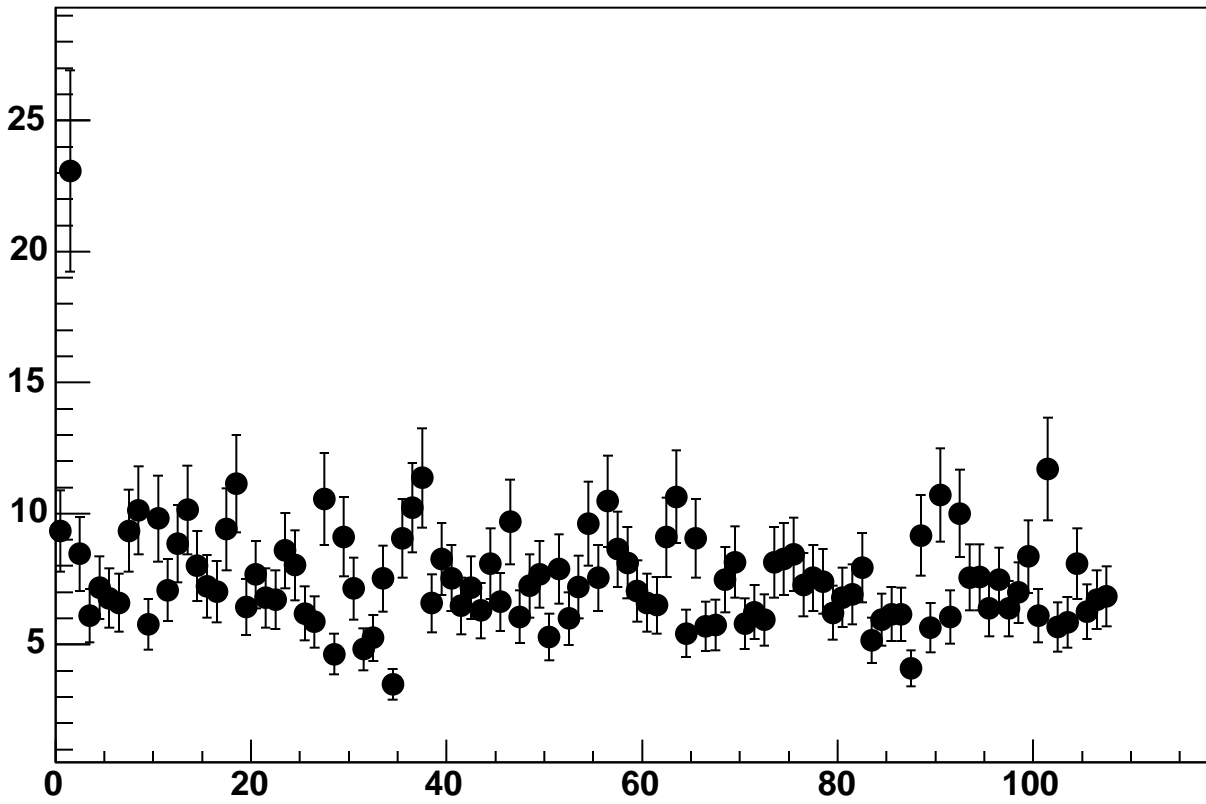
Enable 3, DAC=4000, Hold=15, ADC Noise vs 18\*Chip+Chan



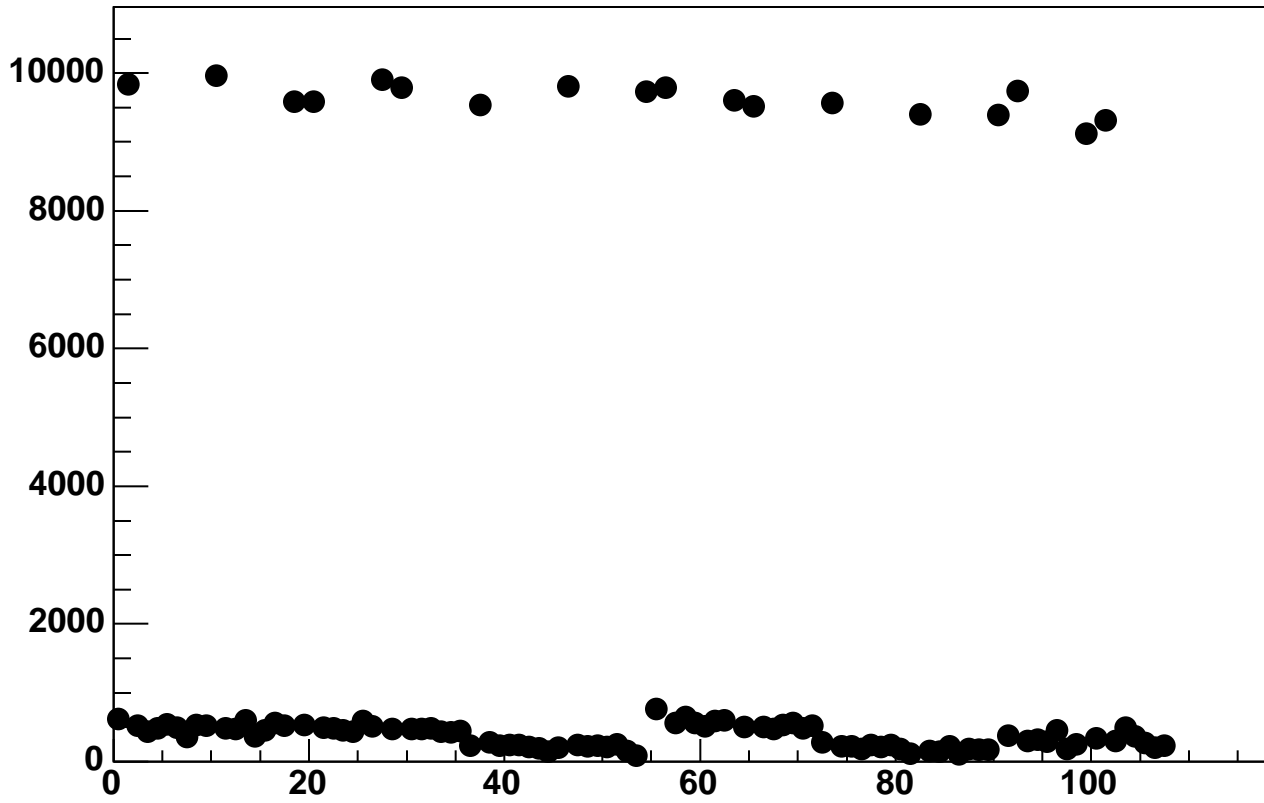
Enable 3, DAC=4000, Hold=20, ADC Mean vs 18\*Chip+Chan



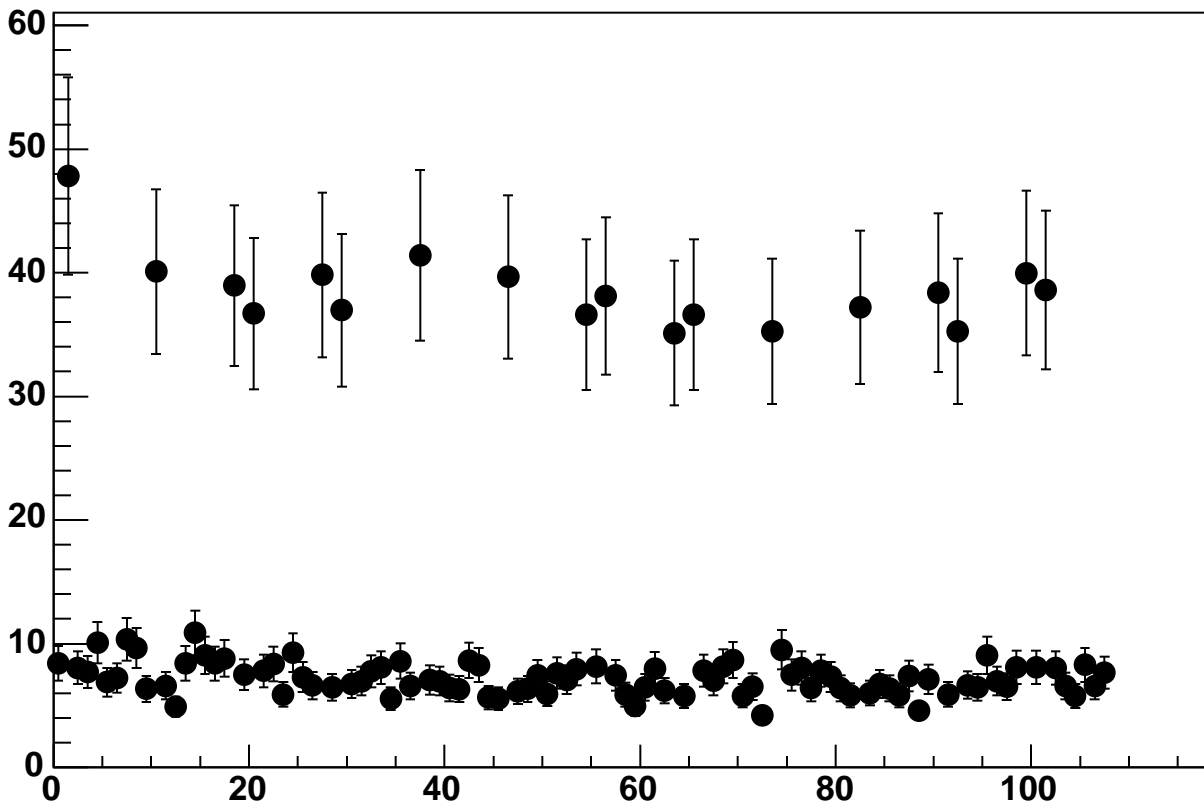
Enable 3, DAC=4000, Hold=20, ADC Noise vs 18\*Chip+Chan



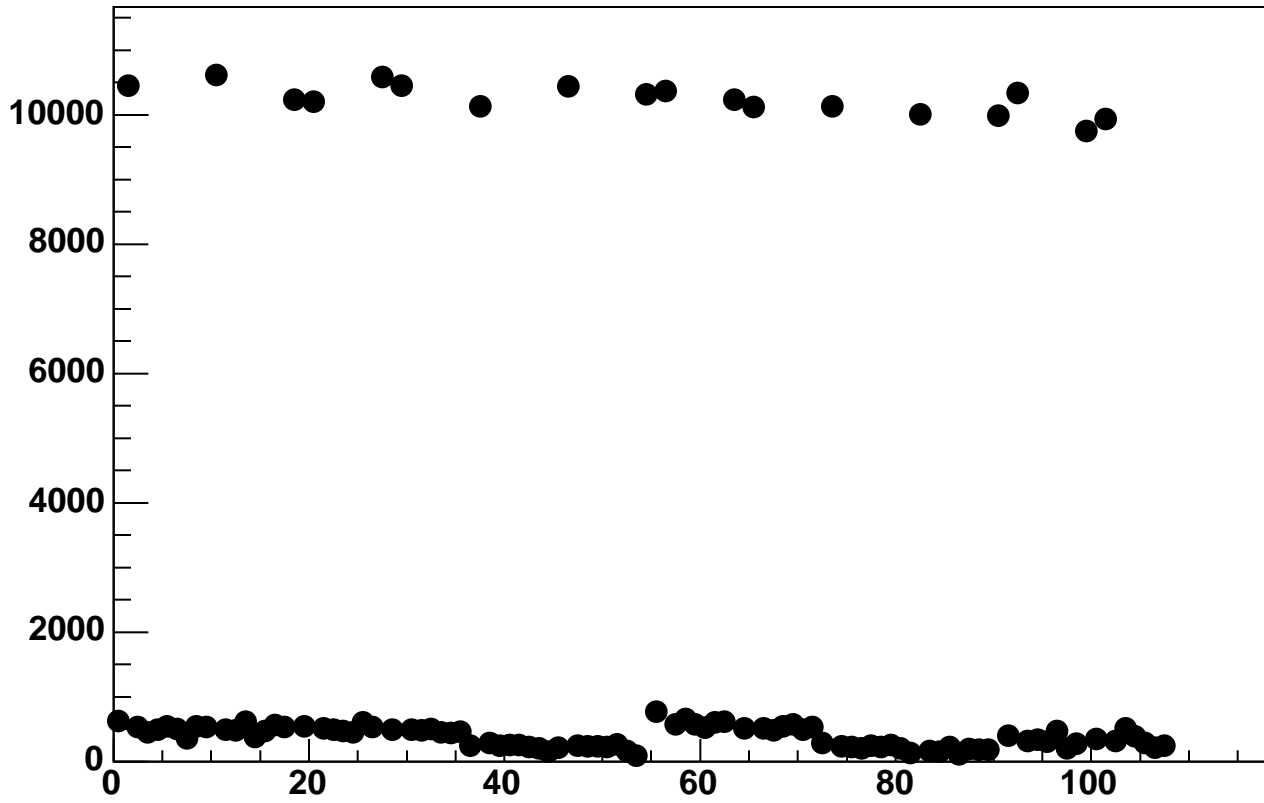
Enable 3, DAC=4000, Hold=25, ADC Mean vs 18\*Chip+Chan



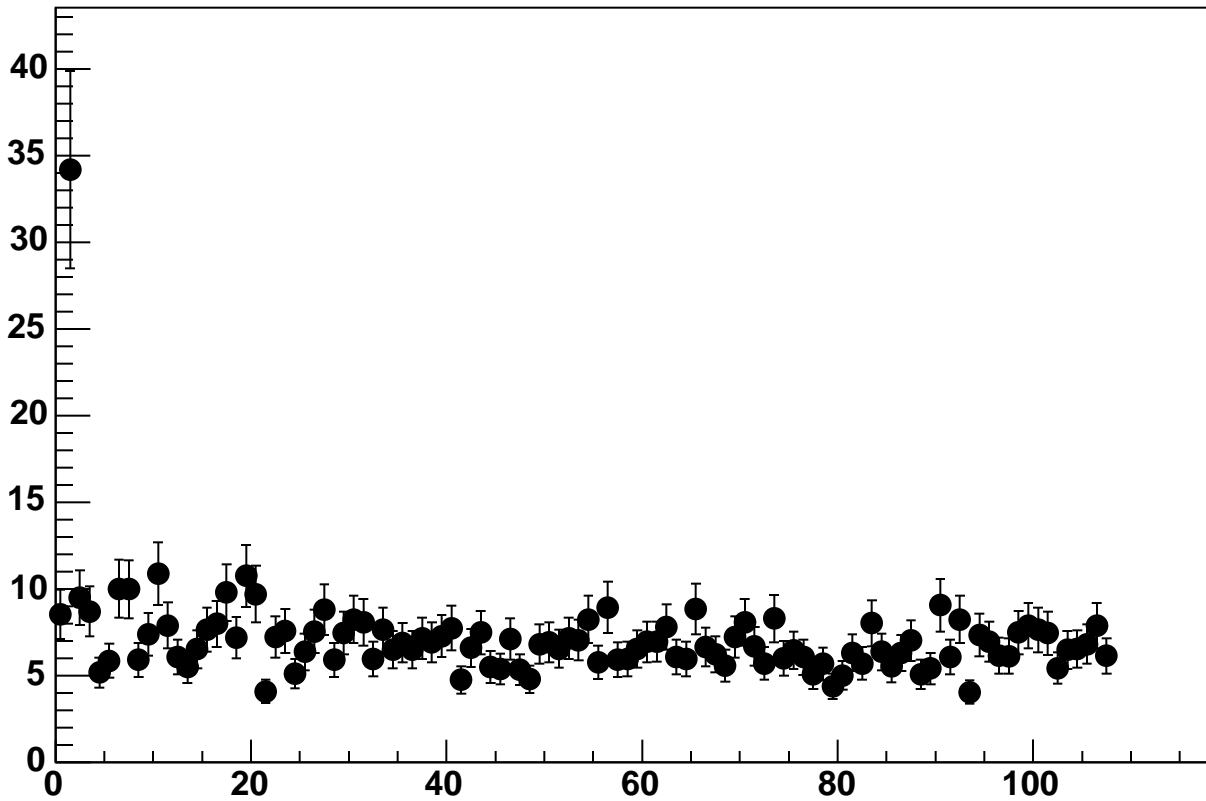
Enable 3, DAC=4000, Hold=25, ADC Noise vs 18\*Chip+Chan



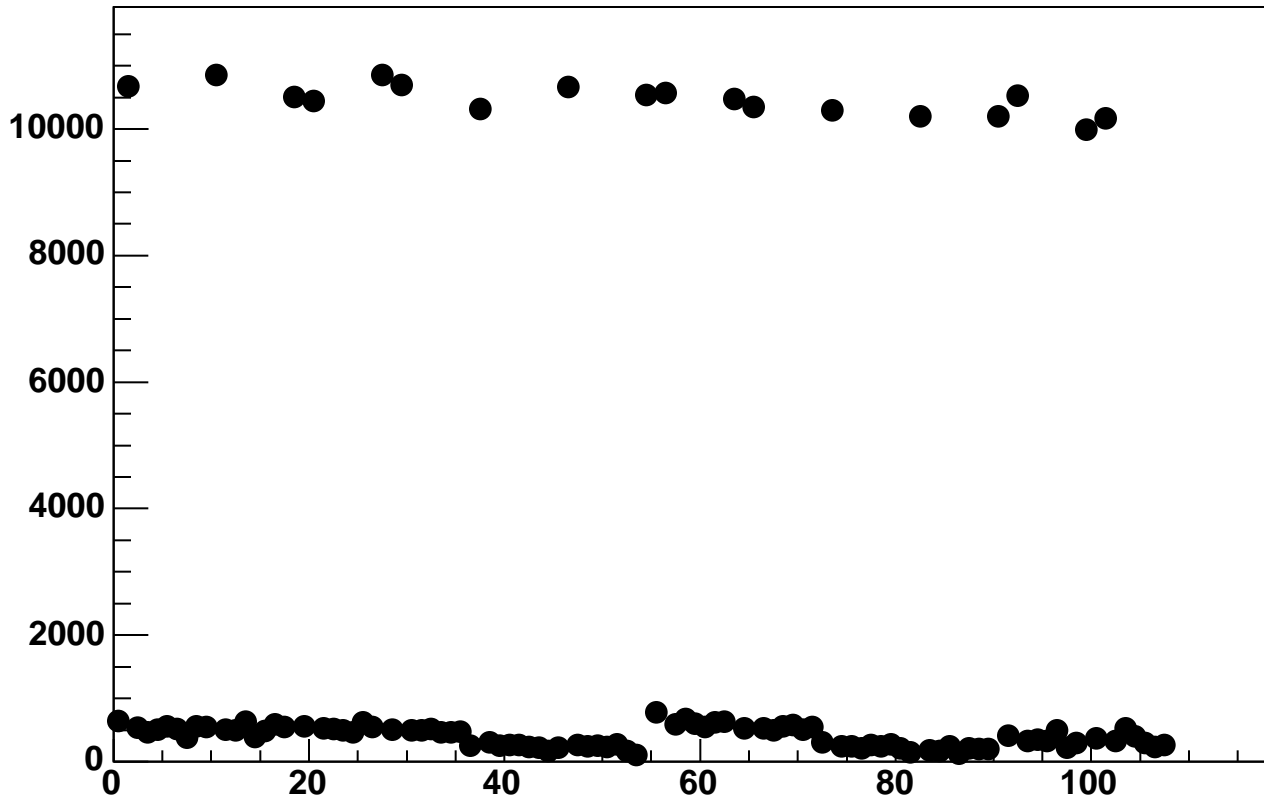
Enable 3, DAC=4000, Hold=30, ADC Mean vs 18\*Chip+Chan



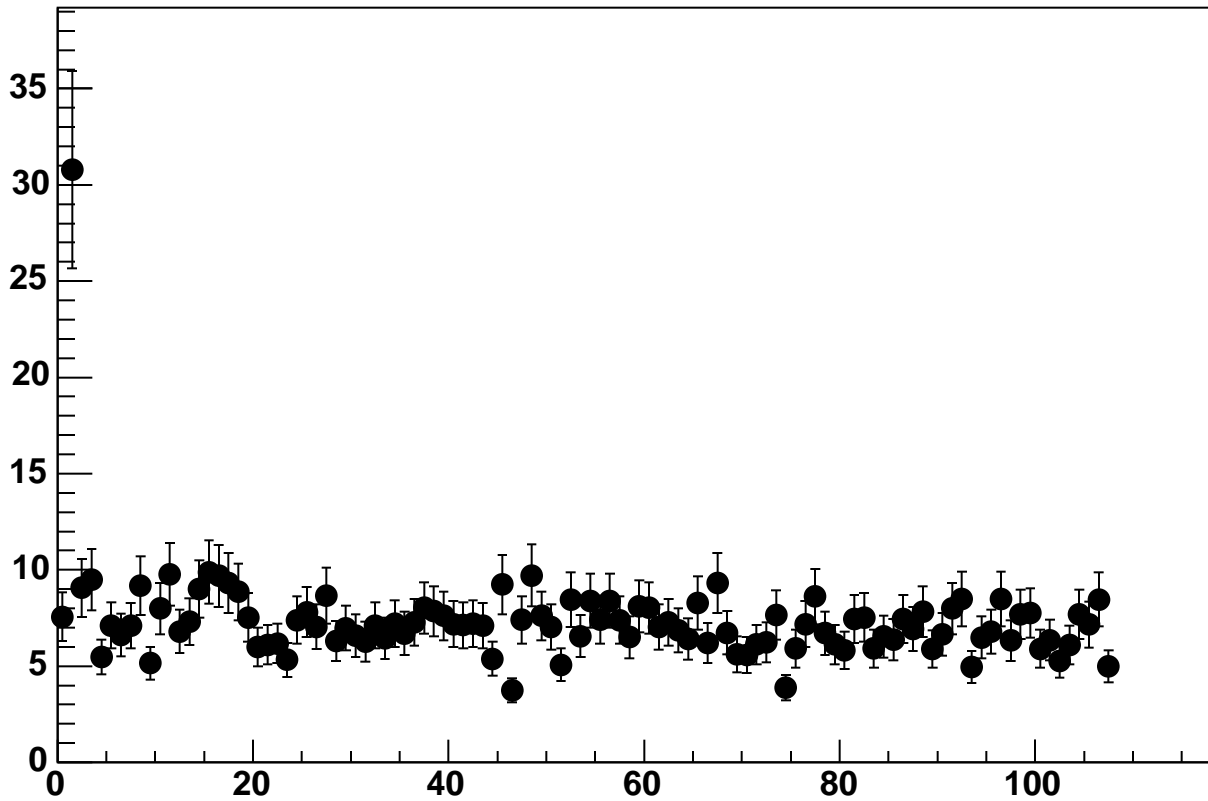
Enable 3, DAC=4000, Hold=30, ADC Noise vs 18\*Chip+Chan



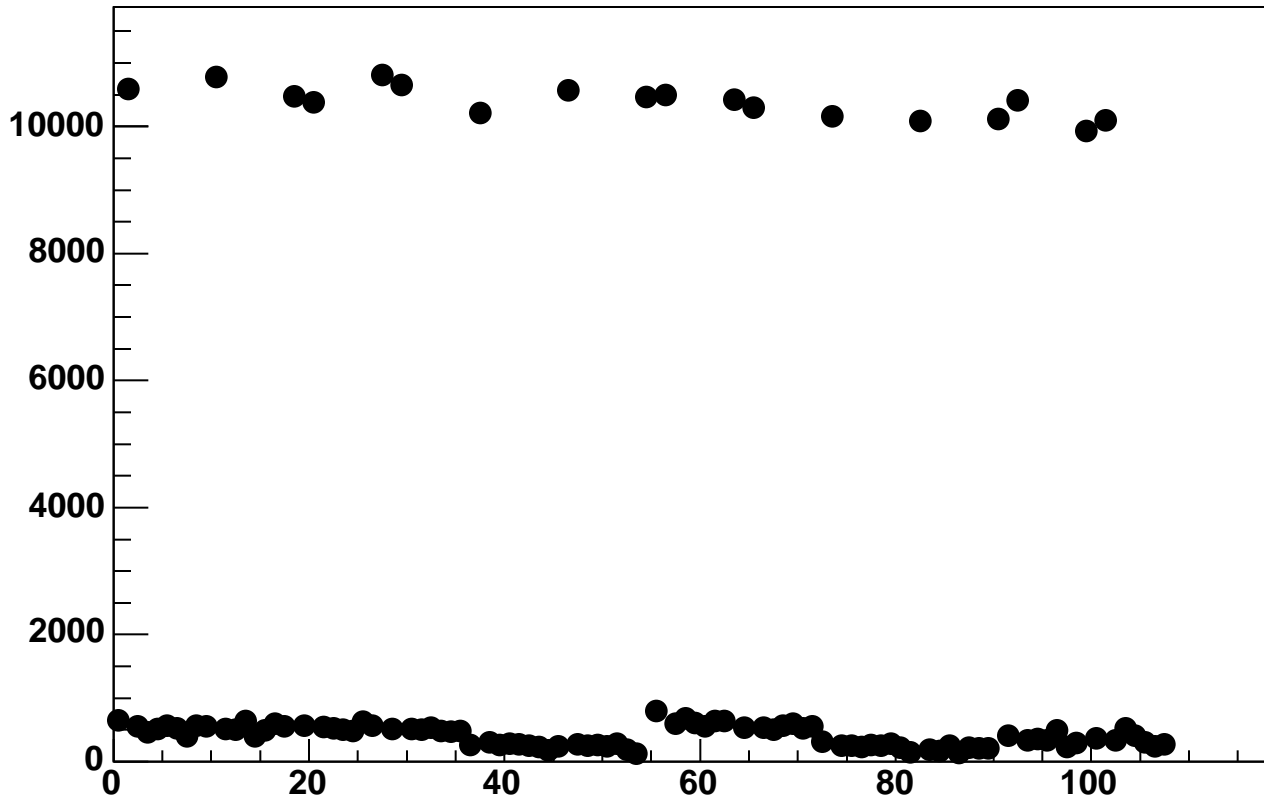
Enable 3, DAC=4000, Hold=35, ADC Mean vs 18\*Chip+Chan



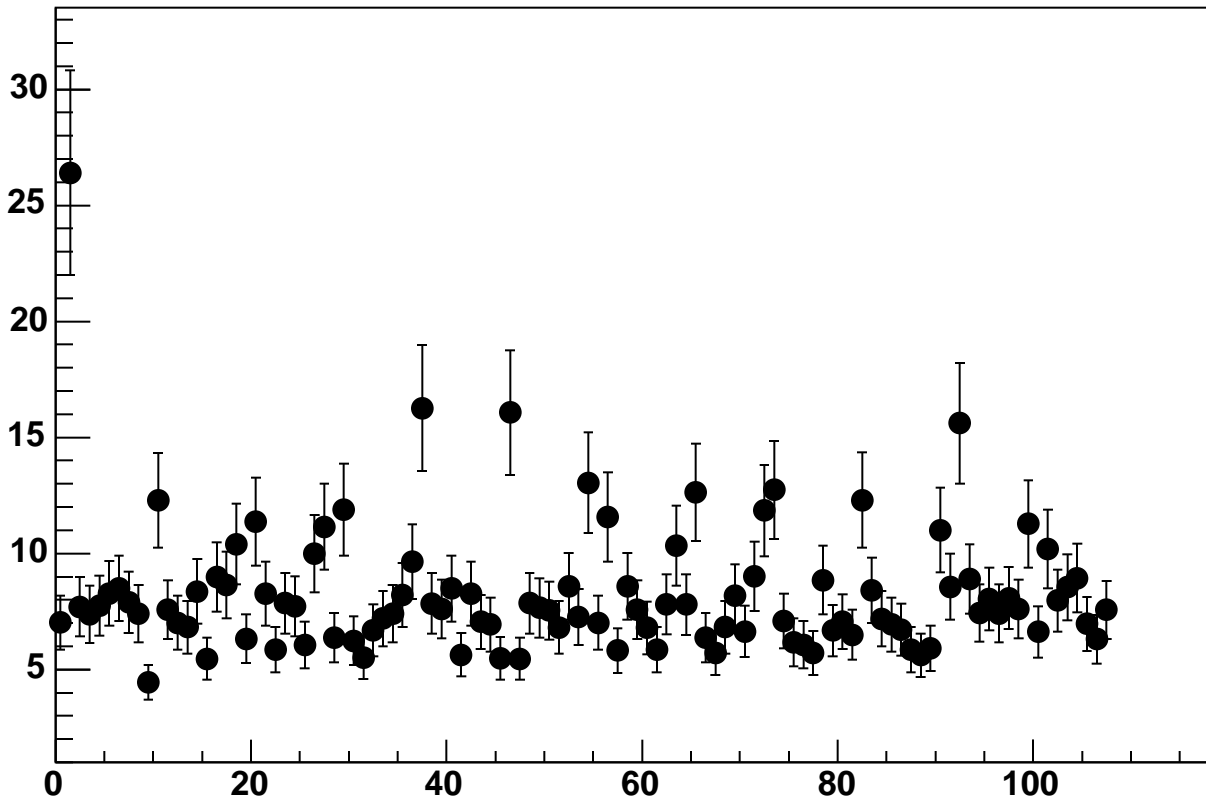
Enable 3, DAC=4000, Hold=35, ADC Noise vs 18\*Chip+Chan



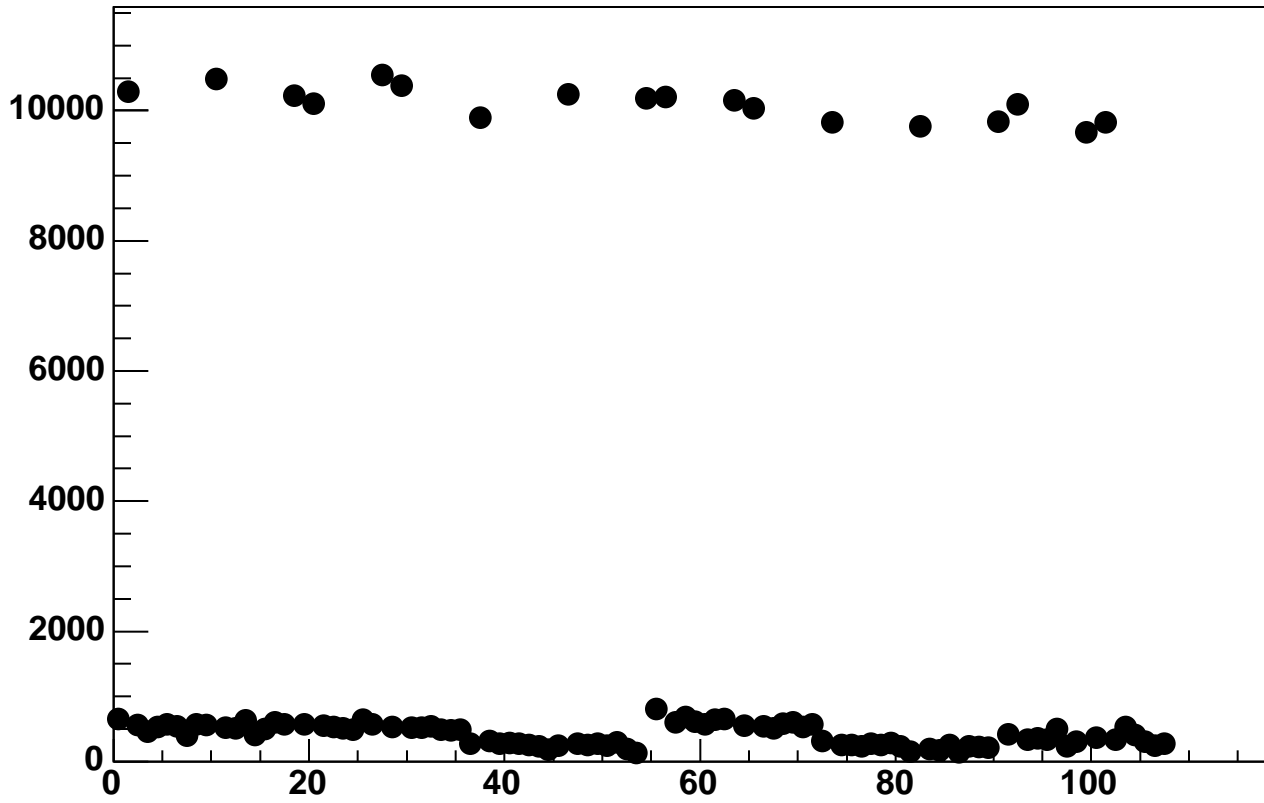
Enable 3, DAC=4000, Hold=40, ADC Mean vs 18\*Chip+Chan



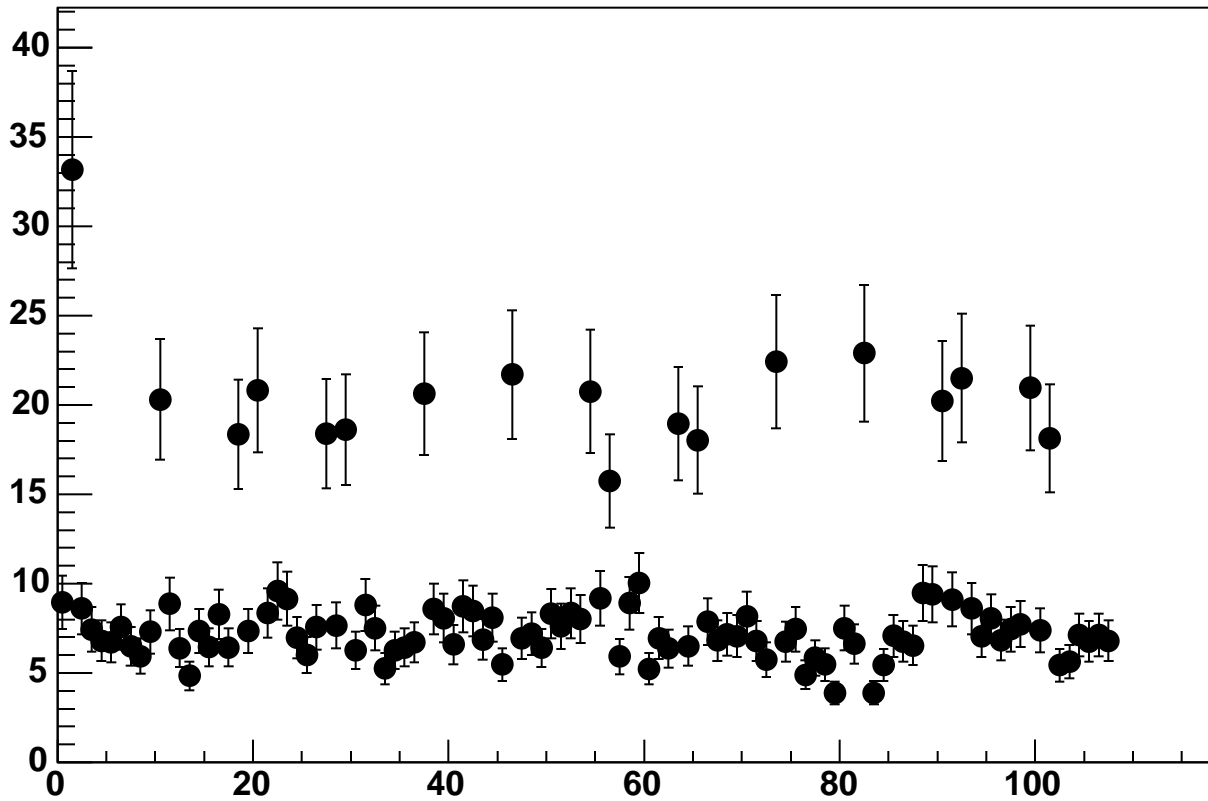
Enable 3, DAC=4000, Hold=40, ADC Noise vs 18\*Chip+Chan



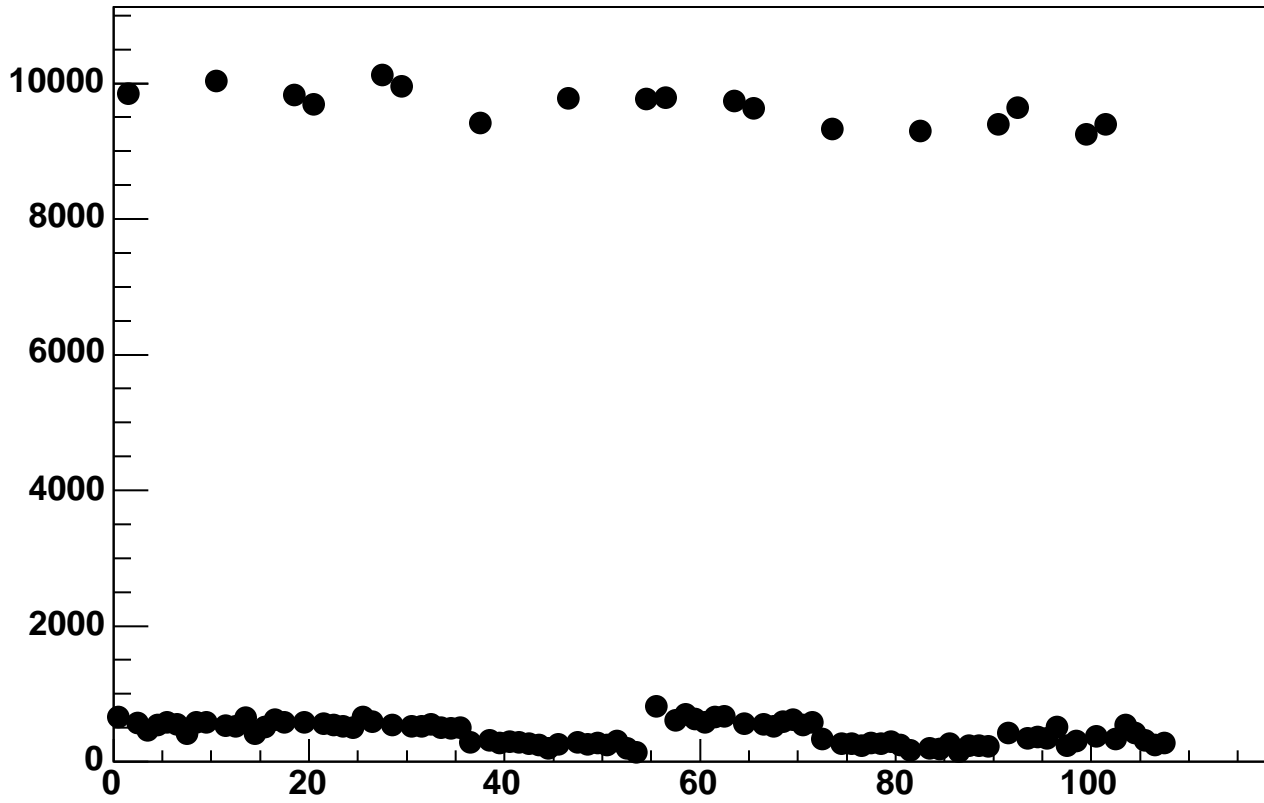
Enable 3, DAC=4000, Hold=45, ADC Mean vs 18\*Chip+Chan



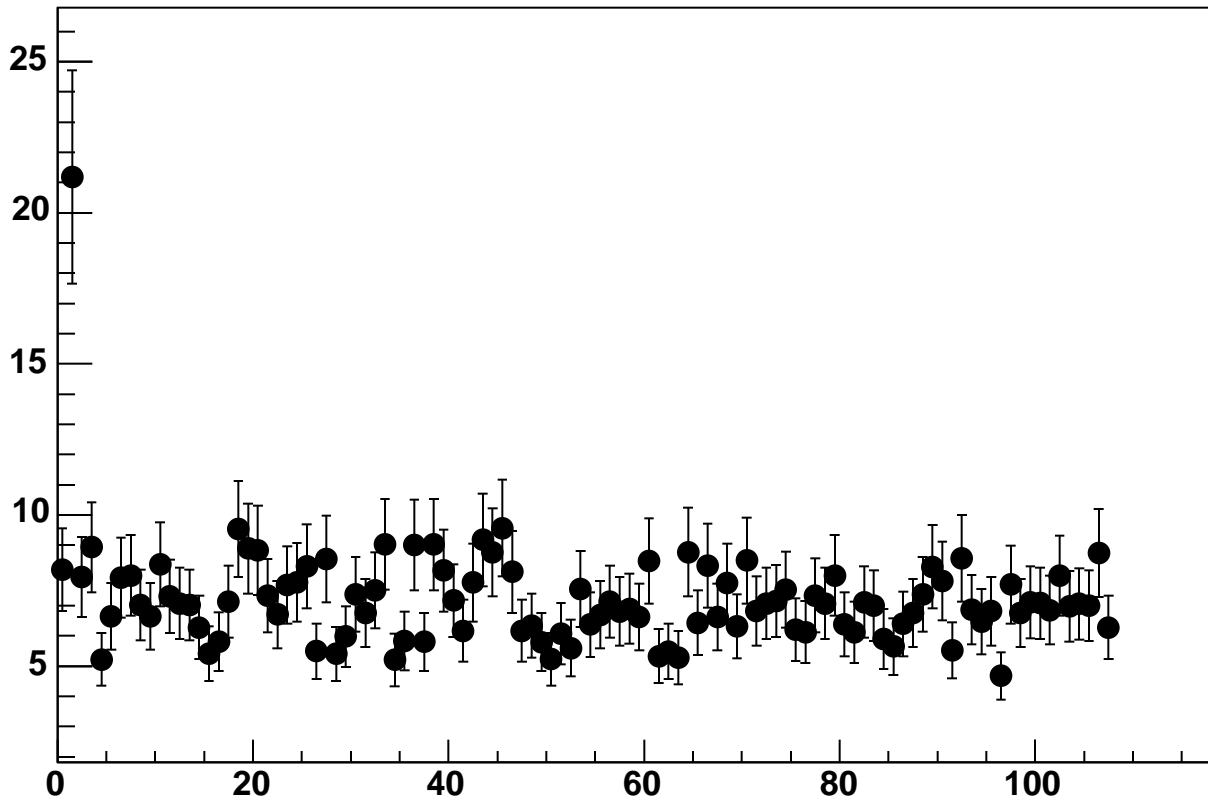
Enable 3, DAC=4000, Hold=45, ADC Noise vs 18\*Chip+Chan



Enable 3, DAC=4000, Hold=50, ADC Mean vs 18\*Chip+Chan

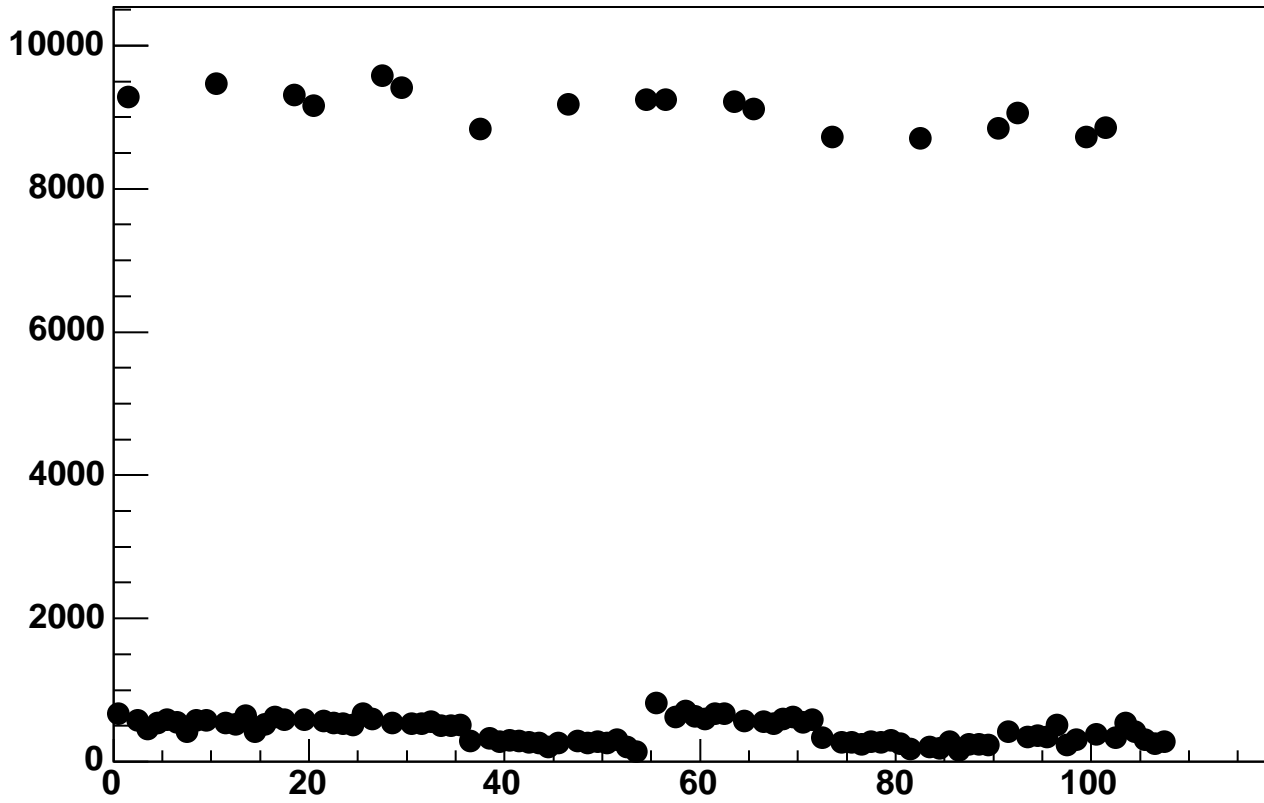


Enable 3, DAC=4000, Hold=50, ADC Noise vs 18\*Chip+Chan

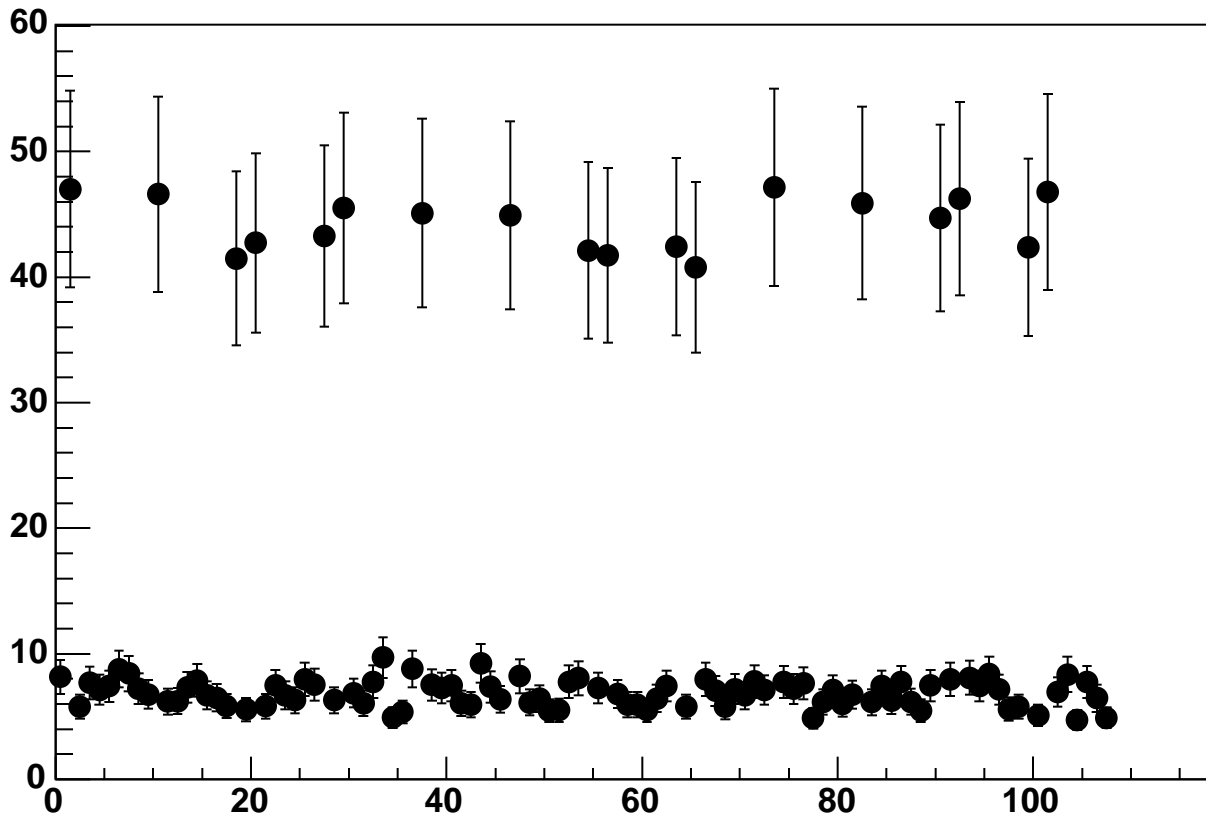




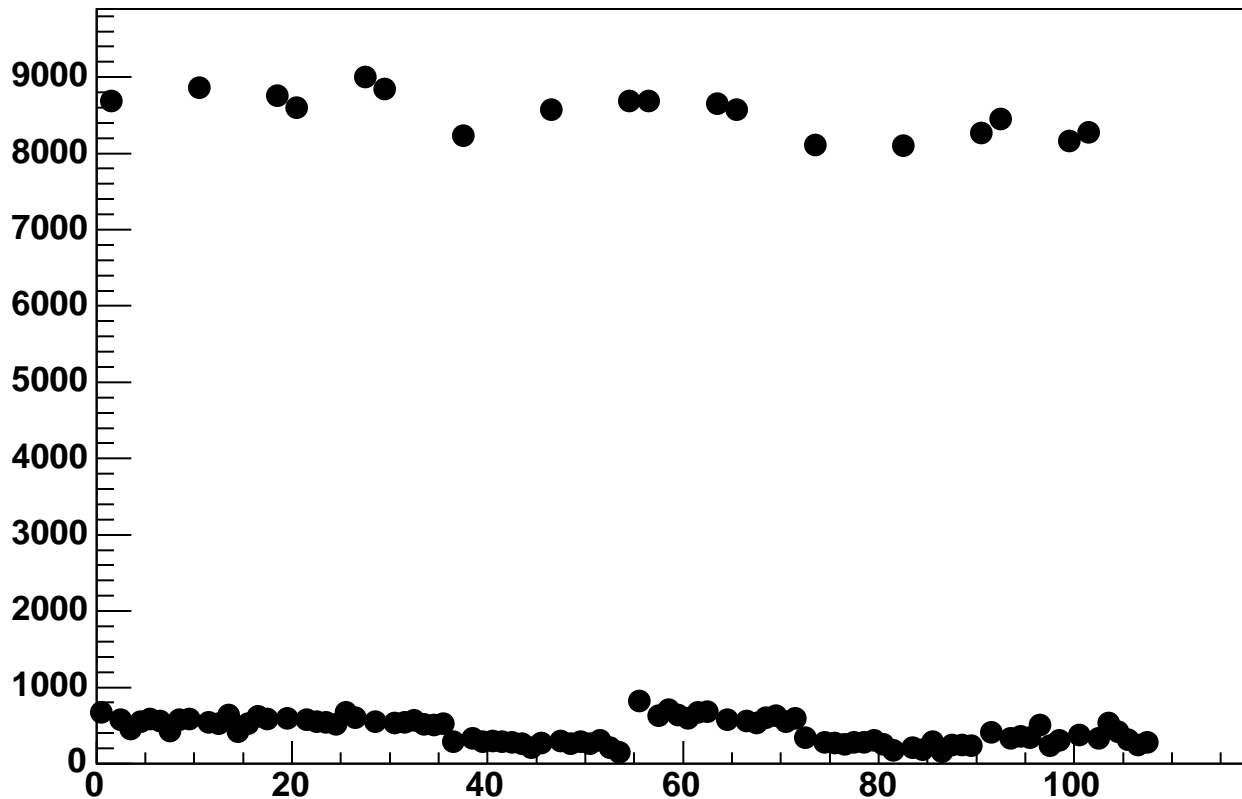
Enable 3, DAC=4000, Hold=55, ADC Mean vs 18\*Chip+Chan



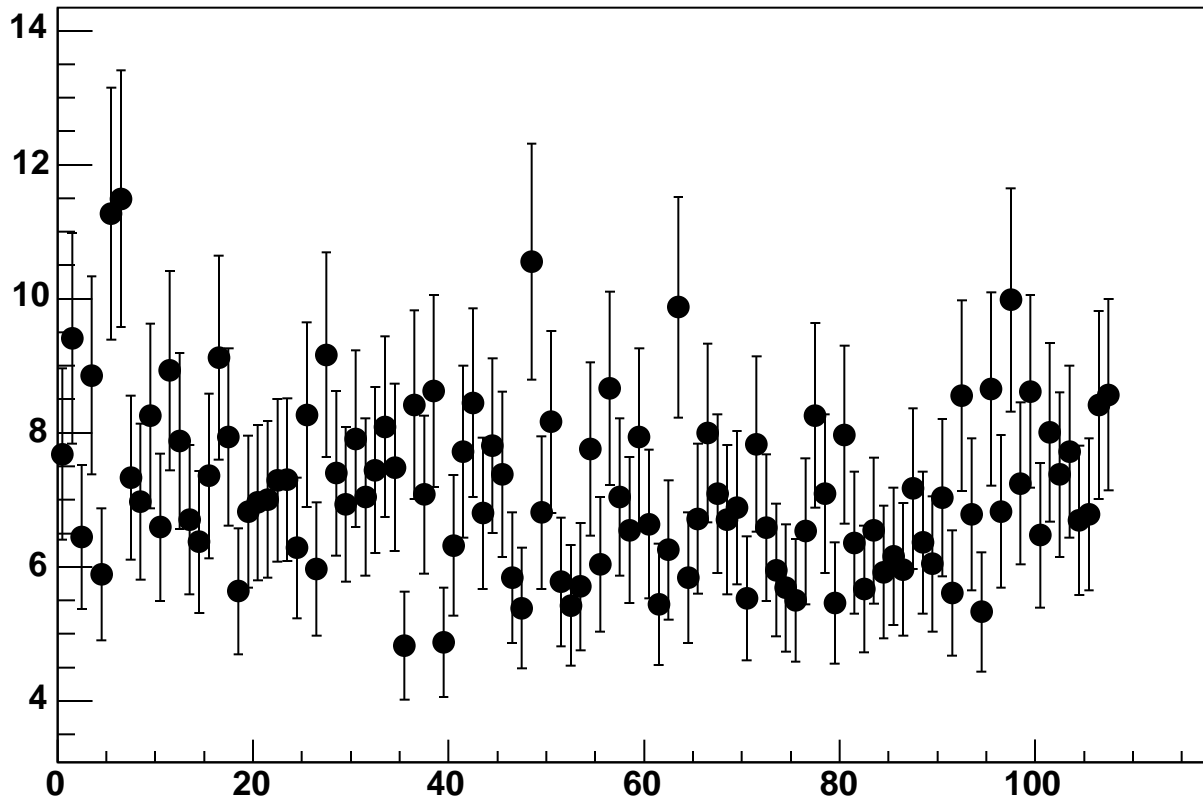
Enable 3, DAC=4000, Hold=55, ADC Noise vs 18\*Chip+Chan



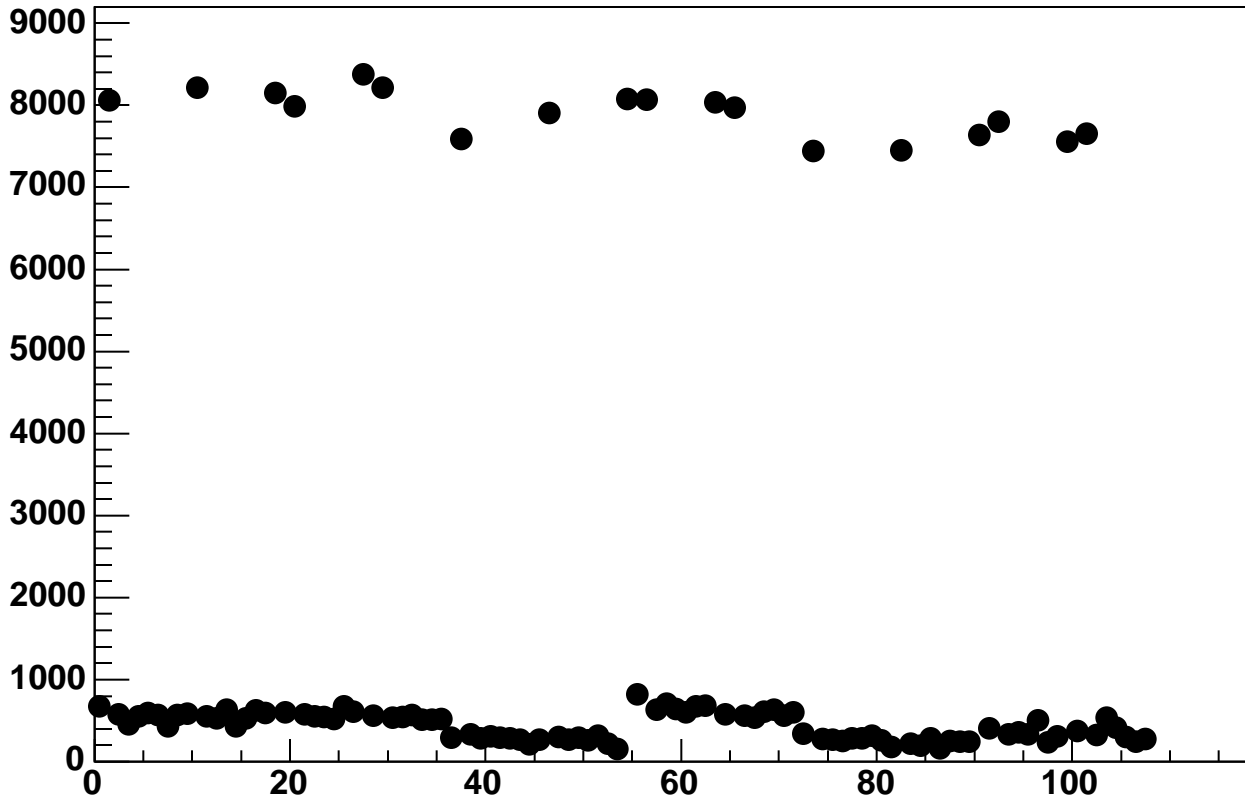
Enable 3, DAC=4000, Hold=60, ADC Mean vs 18\*Chip+Chan



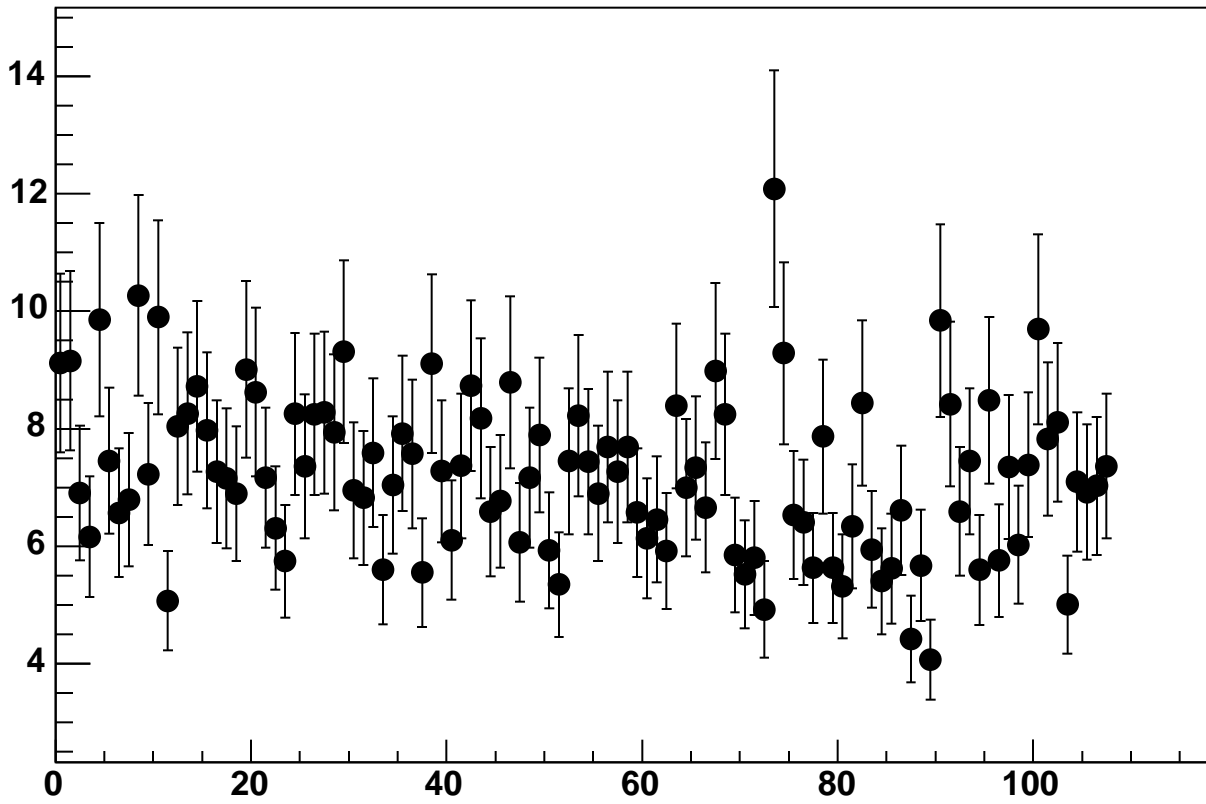
Enable 3, DAC=4000, Hold=60, ADC Noise vs 18\*Chip+Chan



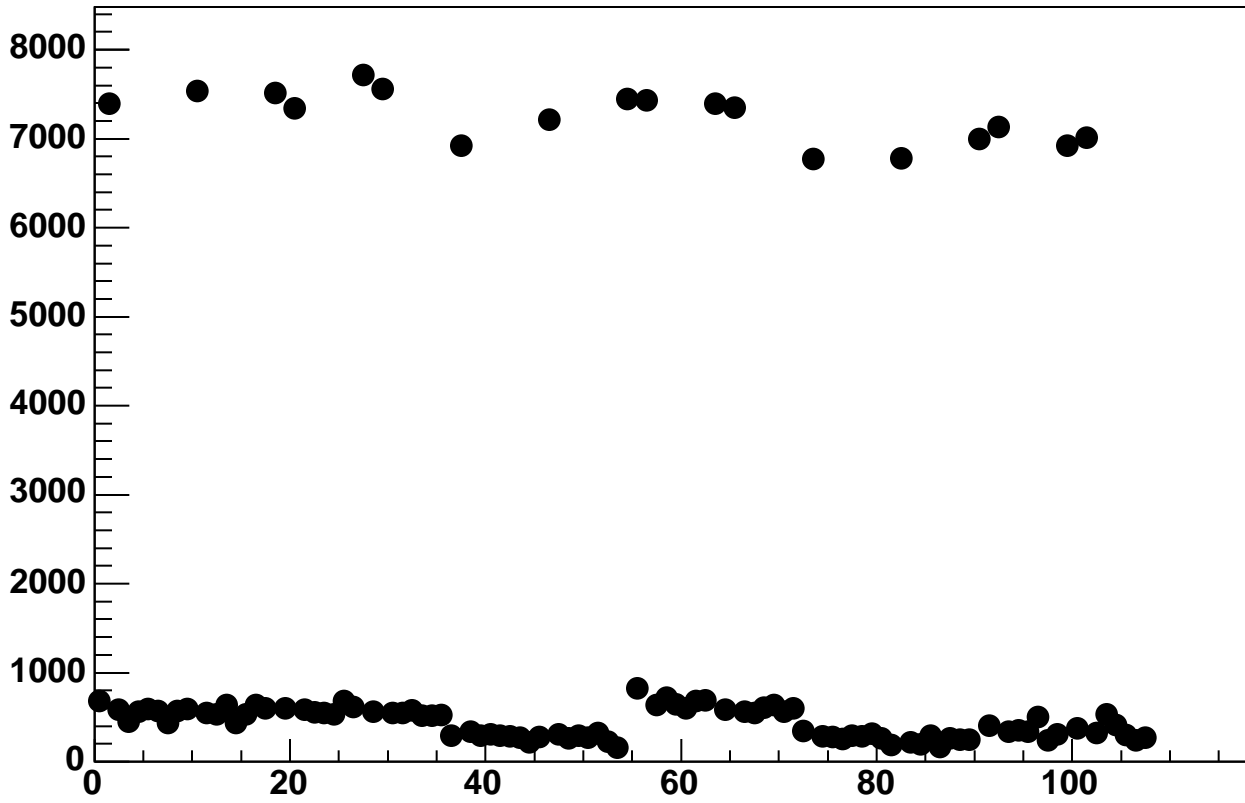
Enable 3, DAC=4000, Hold=65, ADC Mean vs 18\*Chip+Chan



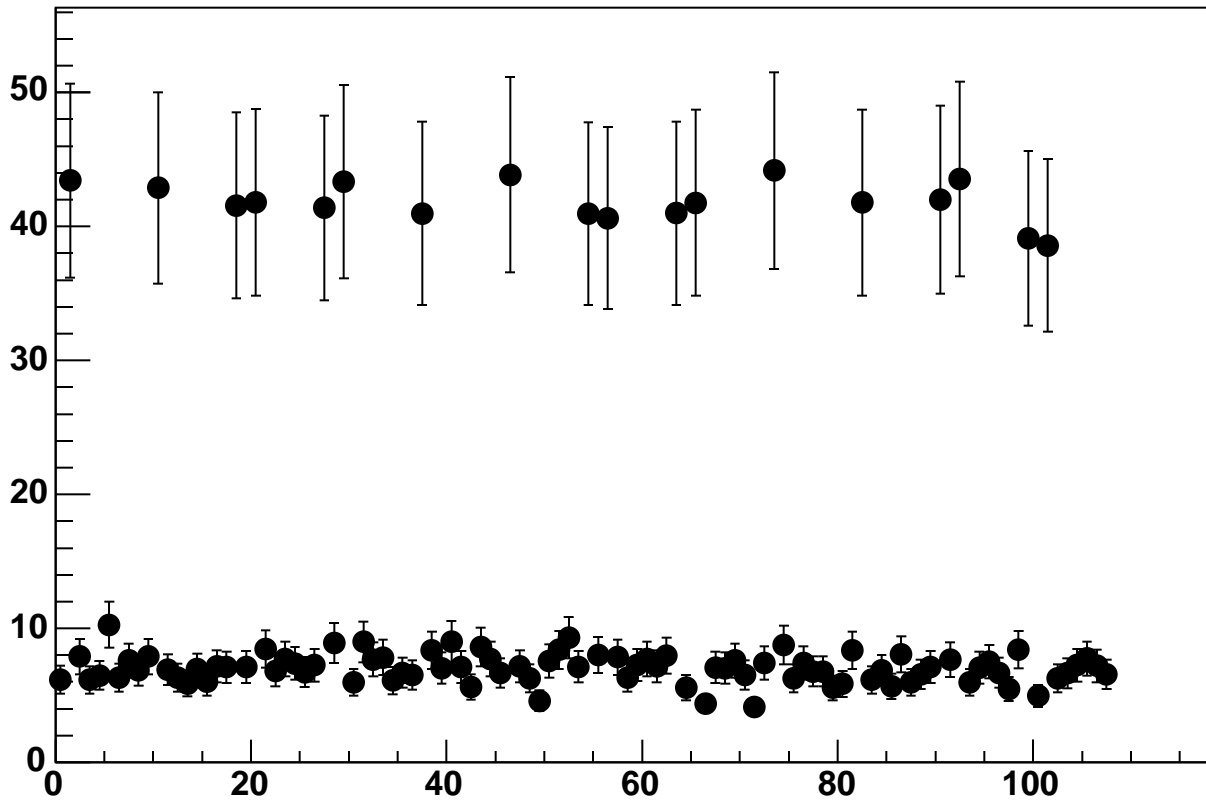
Enable 3, DAC=4000, Hold=65, ADC Noise vs 18\*Chip+Chan



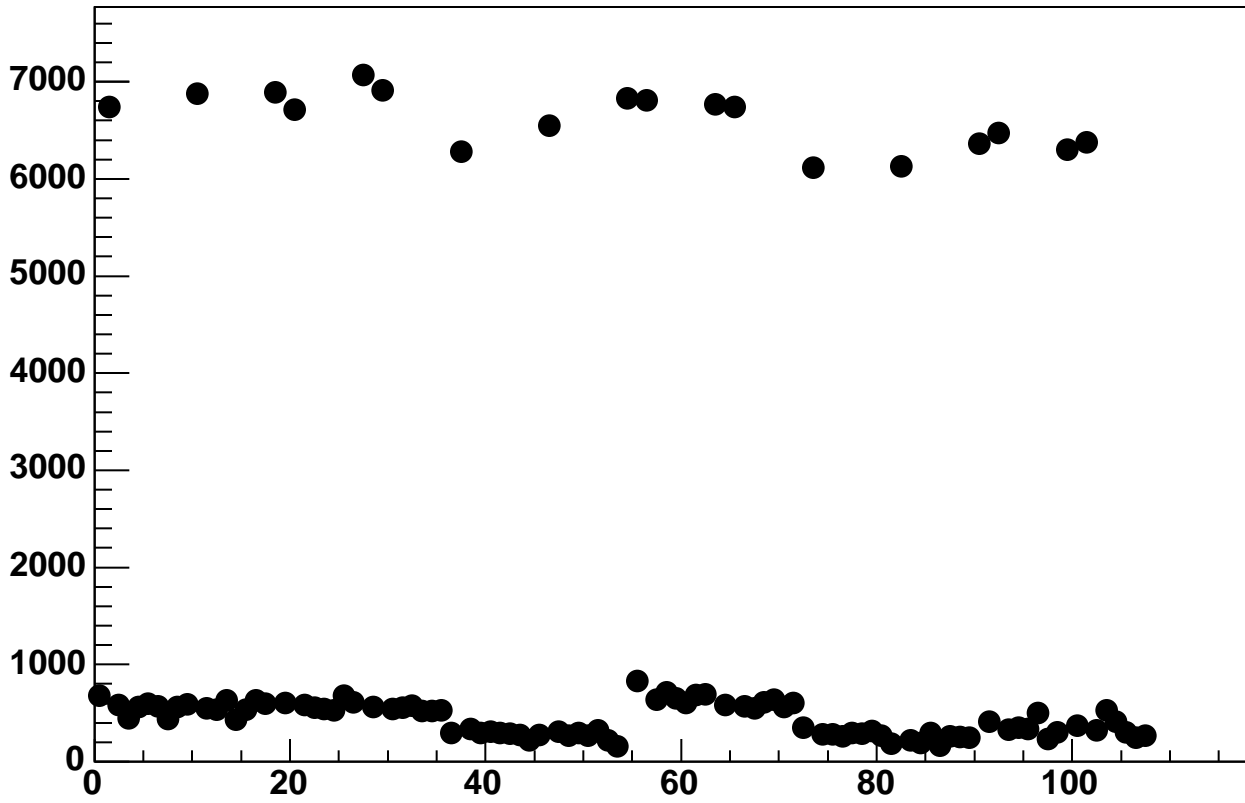
Enable 3, DAC=4000, Hold=70, ADC Mean vs 18\*Chip+Chan



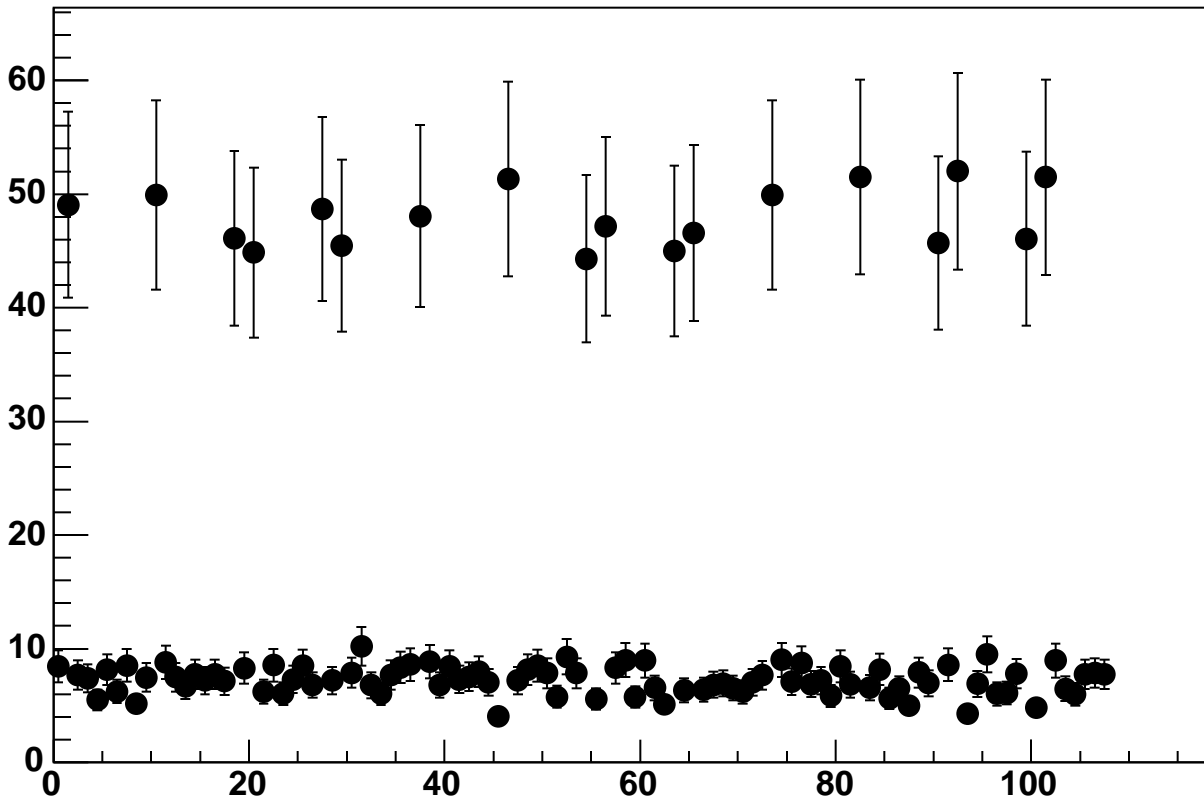
Enable 3, DAC=4000, Hold=70, ADC Noise vs 18\*Chip+Chan



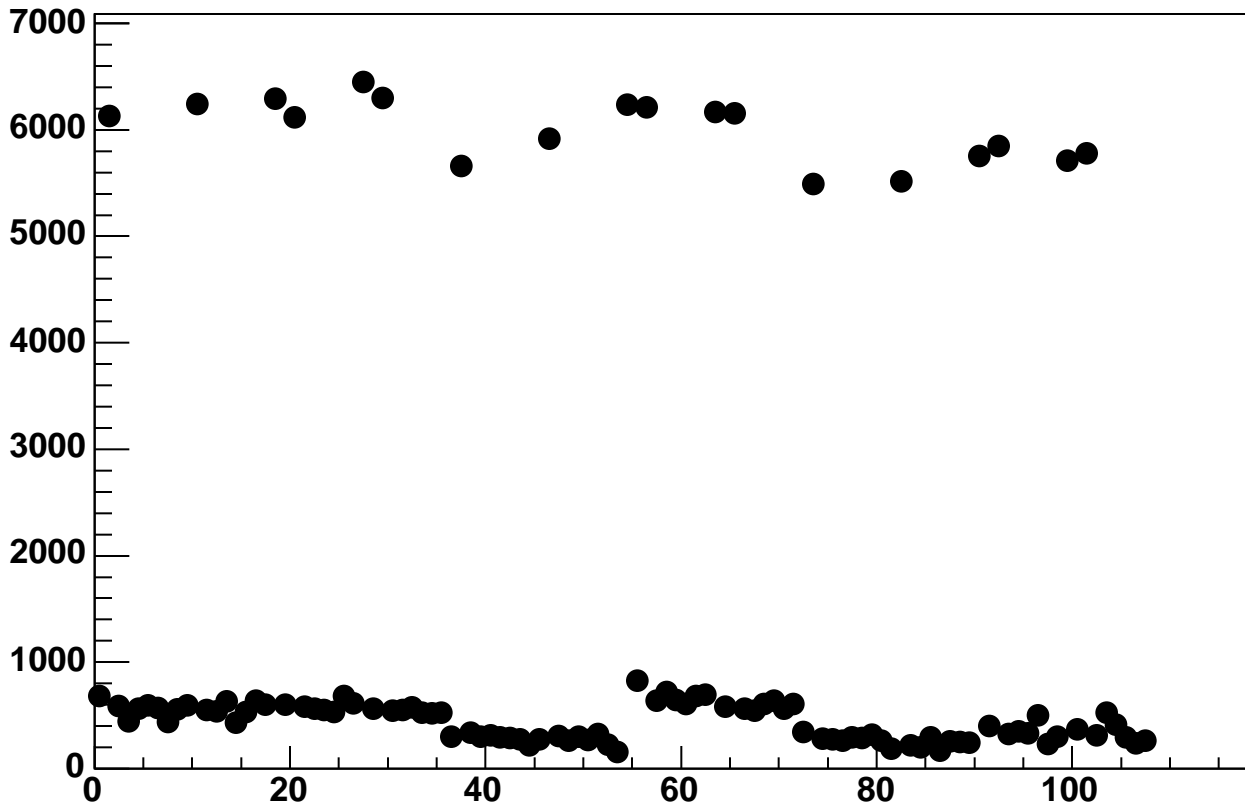
Enable 3, DAC=4000, Hold=75, ADC Mean vs 18\*Chip+Chan



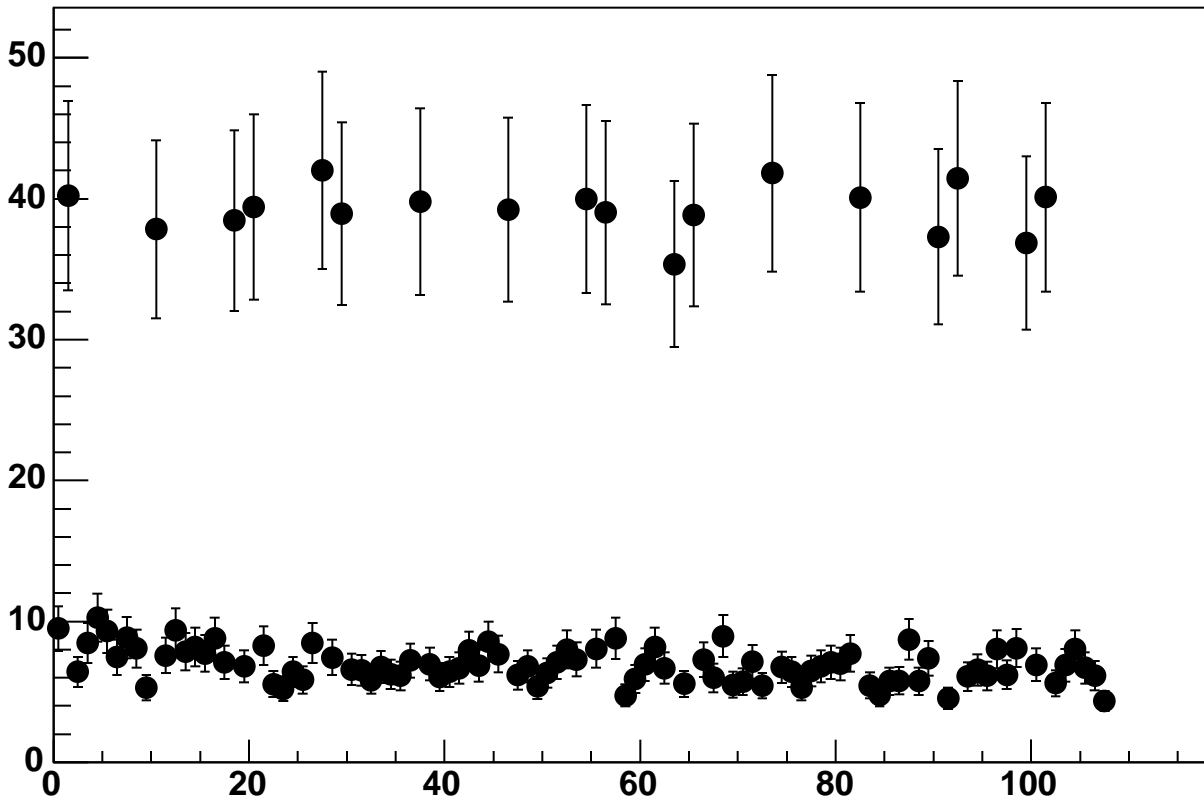
Enable 3, DAC=4000, Hold=75, ADC Noise vs 18\*Chip+Chan



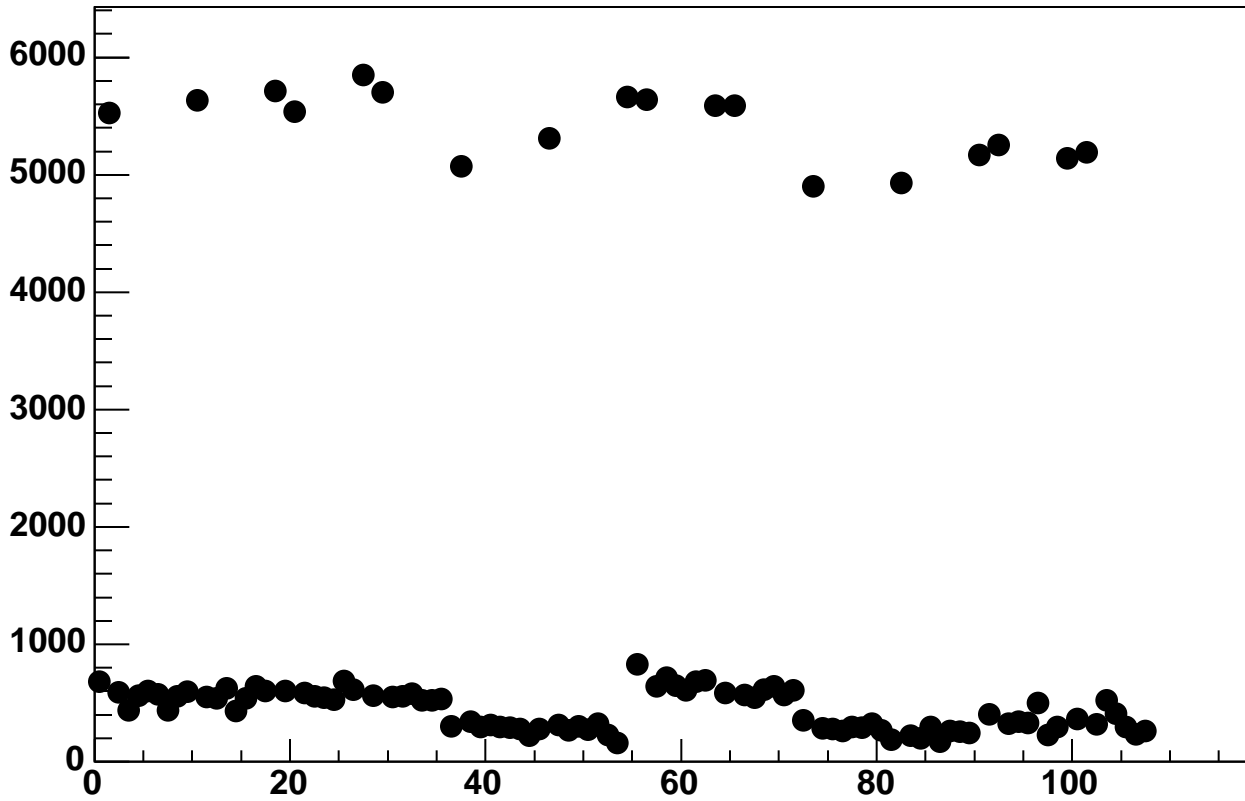
Enable 3, DAC=4000, Hold=80, ADC Mean vs 18\*Chip+Chan



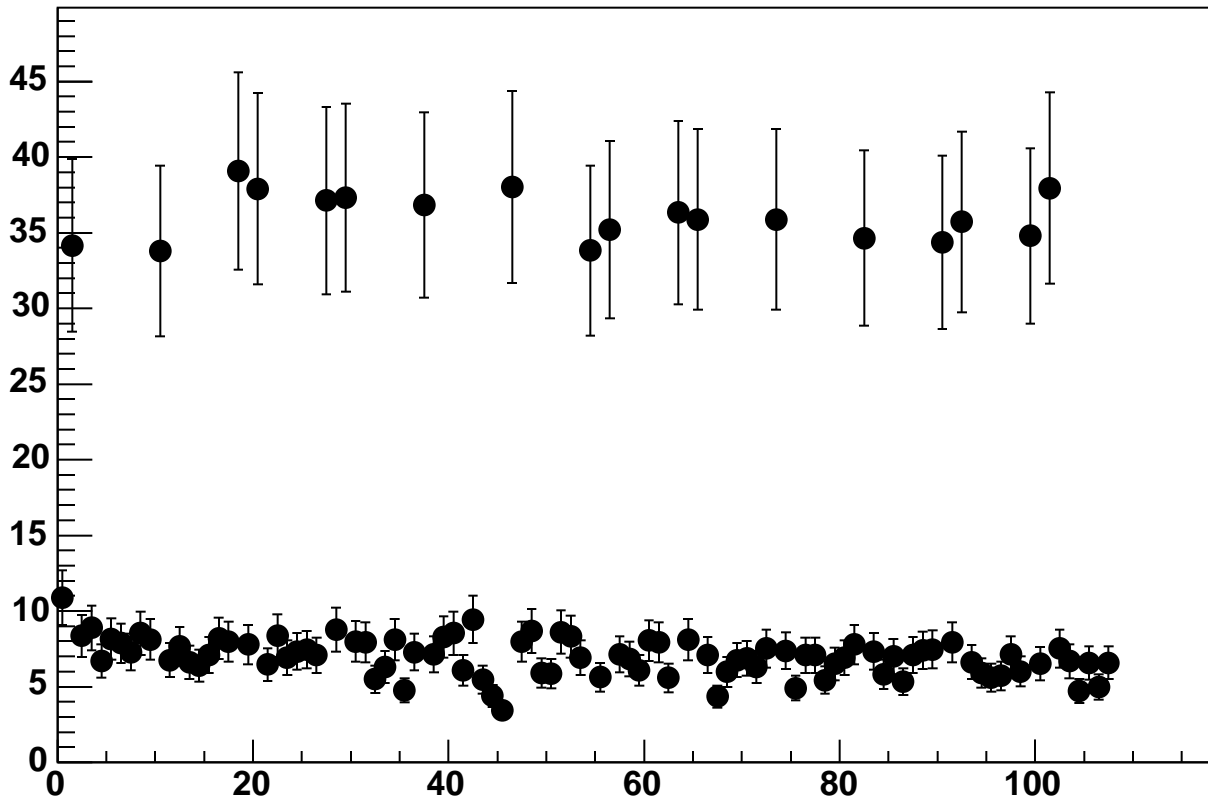
Enable 3, DAC=4000, Hold=80, ADC Noise vs 18\*Chip+Chan



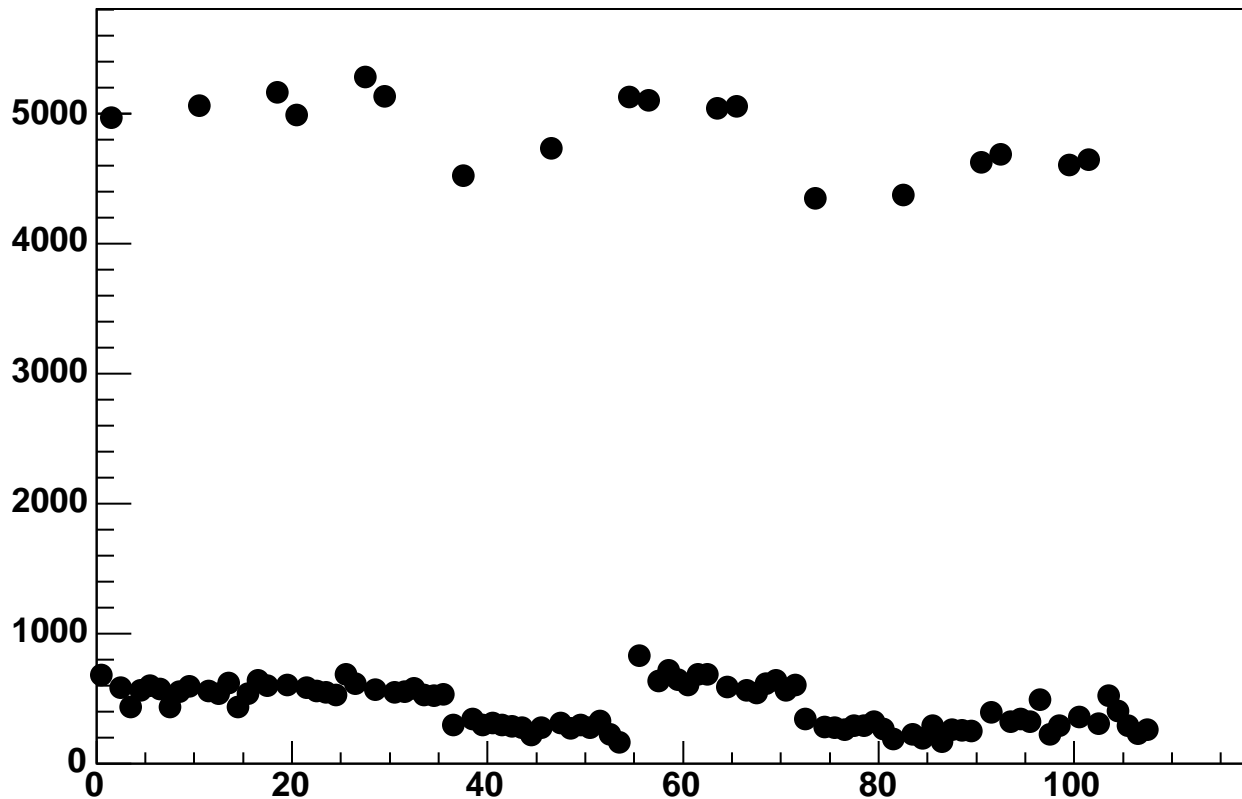
Enable 3, DAC=4000, Hold=85, ADC Mean vs 18\*Chip+Chan



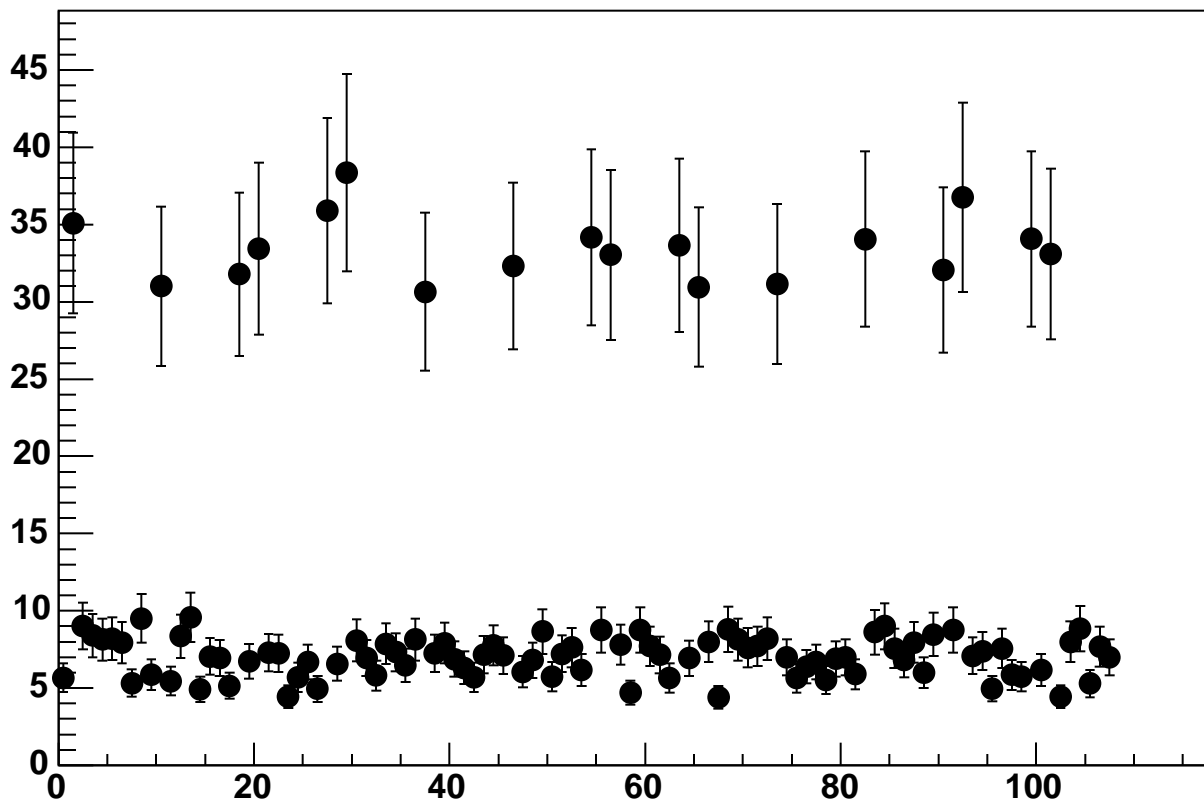
Enable 3, DAC=4000, Hold=85, ADC Noise vs 18\*Chip+Chan



Enable 3, DAC=4000, Hold=90, ADC Mean vs 18\*Chip+Chan

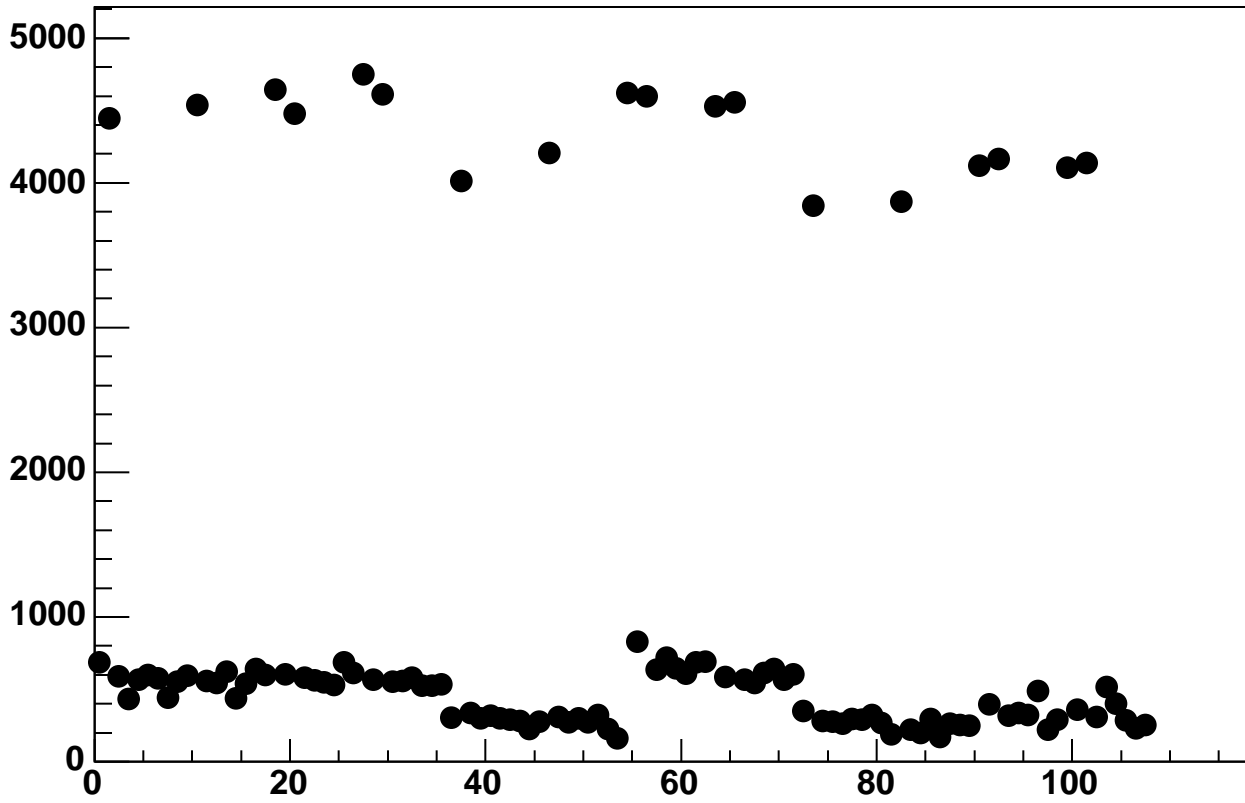


Enable 3, DAC=4000, Hold=90, ADC Noise vs 18\*Chip+Chan

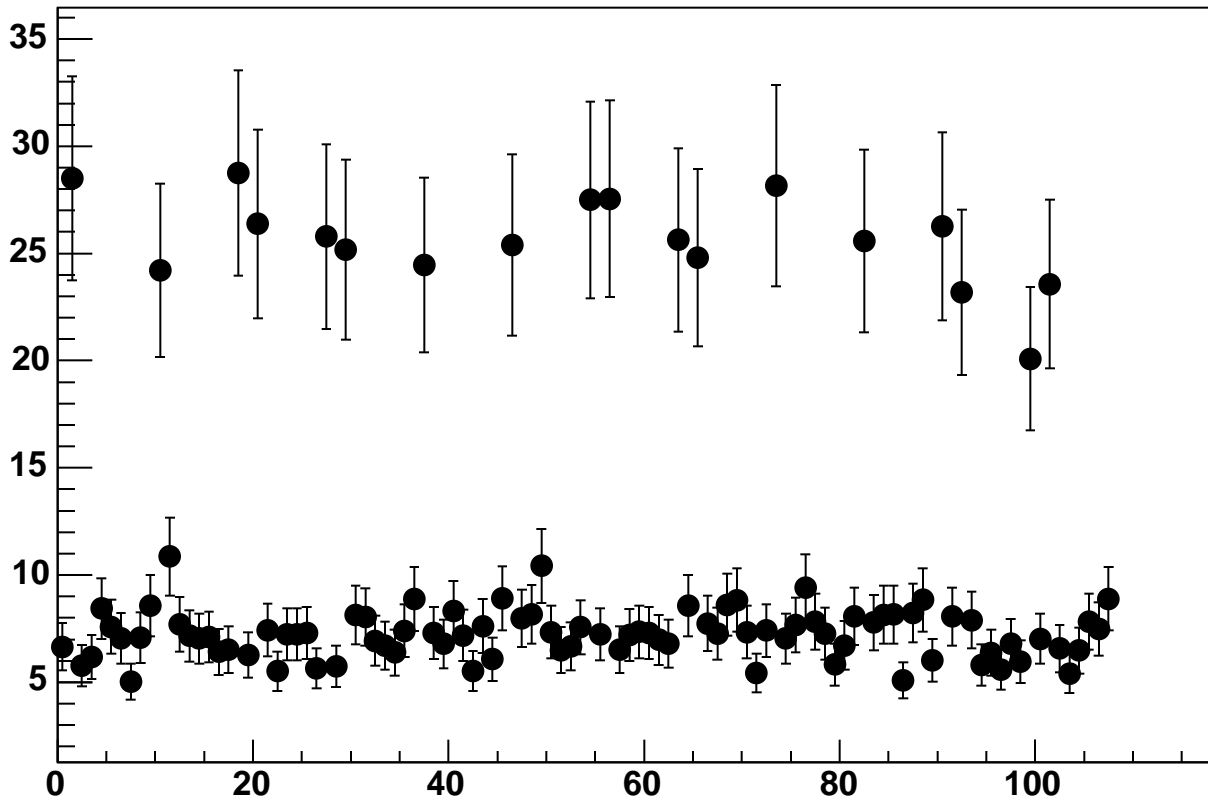




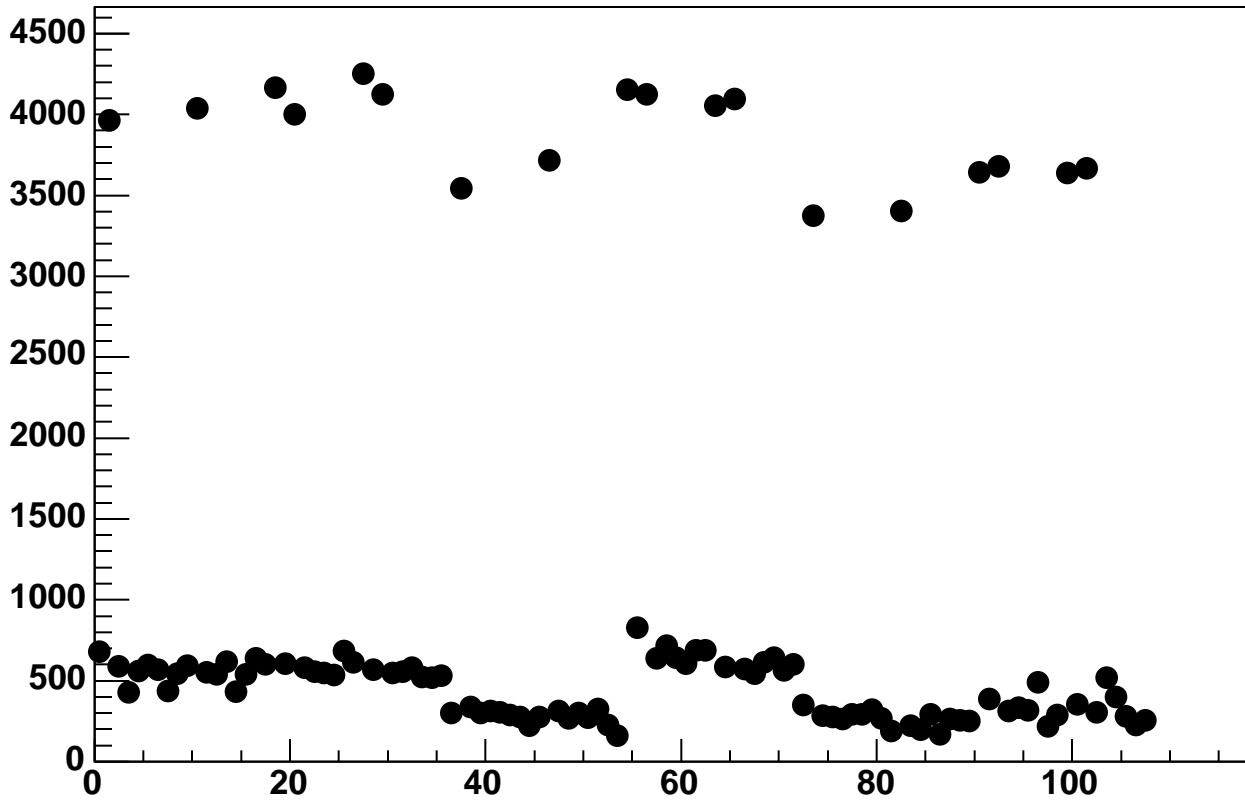
Enable 3, DAC=4000, Hold=95, ADC Mean vs 18\*Chip+Chan



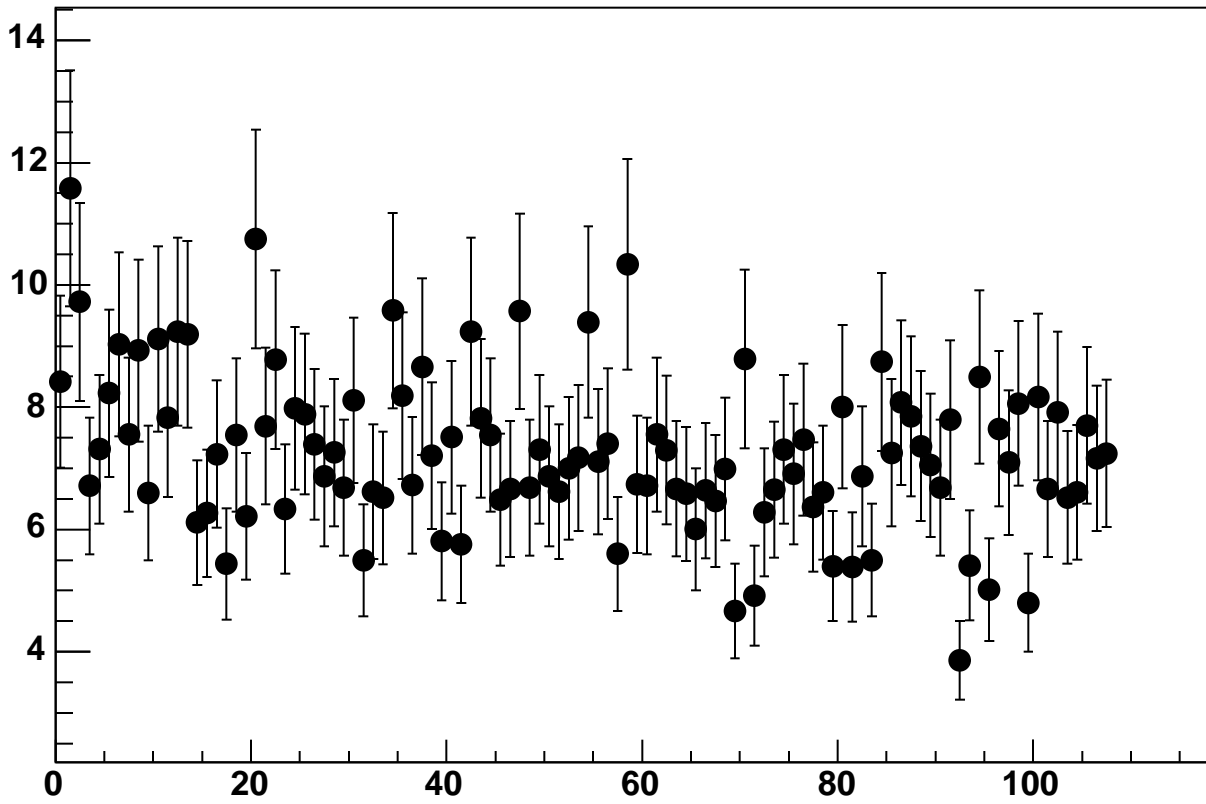
Enable 3, DAC=4000, Hold=95, ADC Noise vs 18\*Chip+Chan



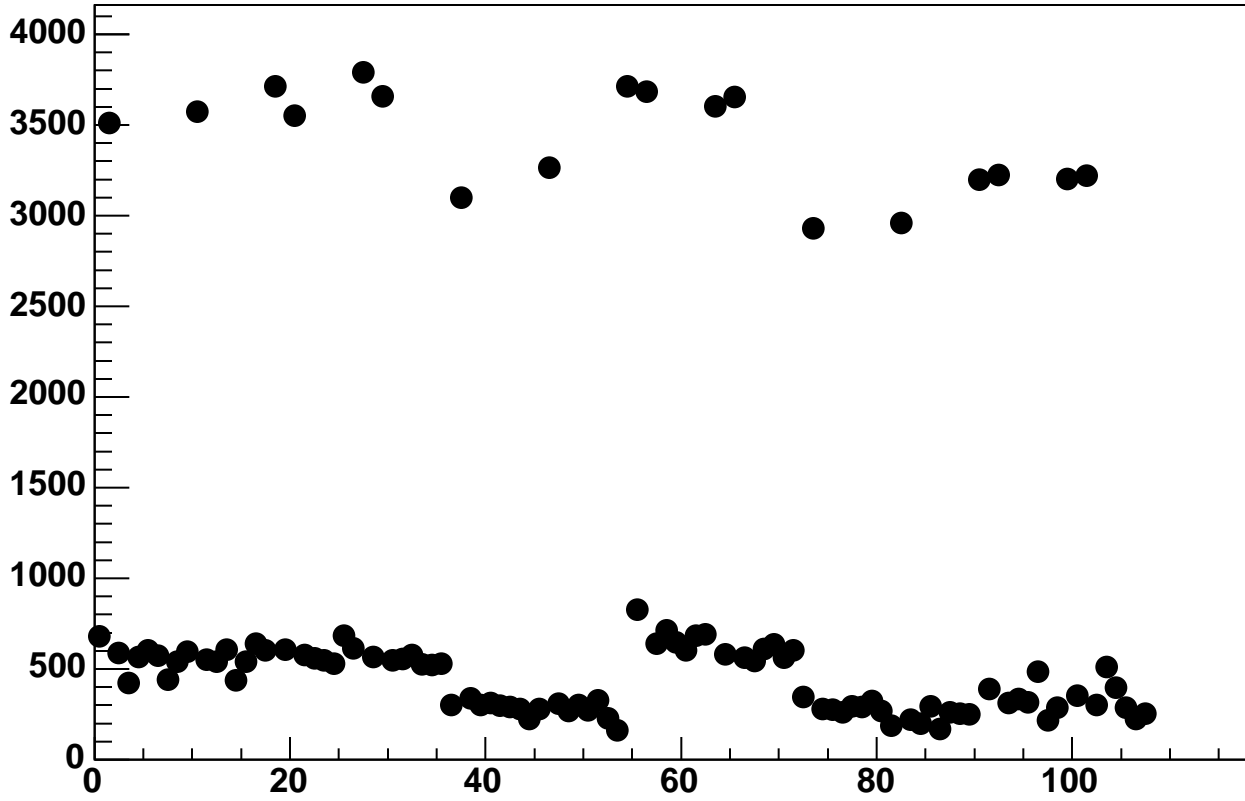
Enable 3, DAC=4000, Hold=100, ADC Mean vs 18\*Chip+Chan



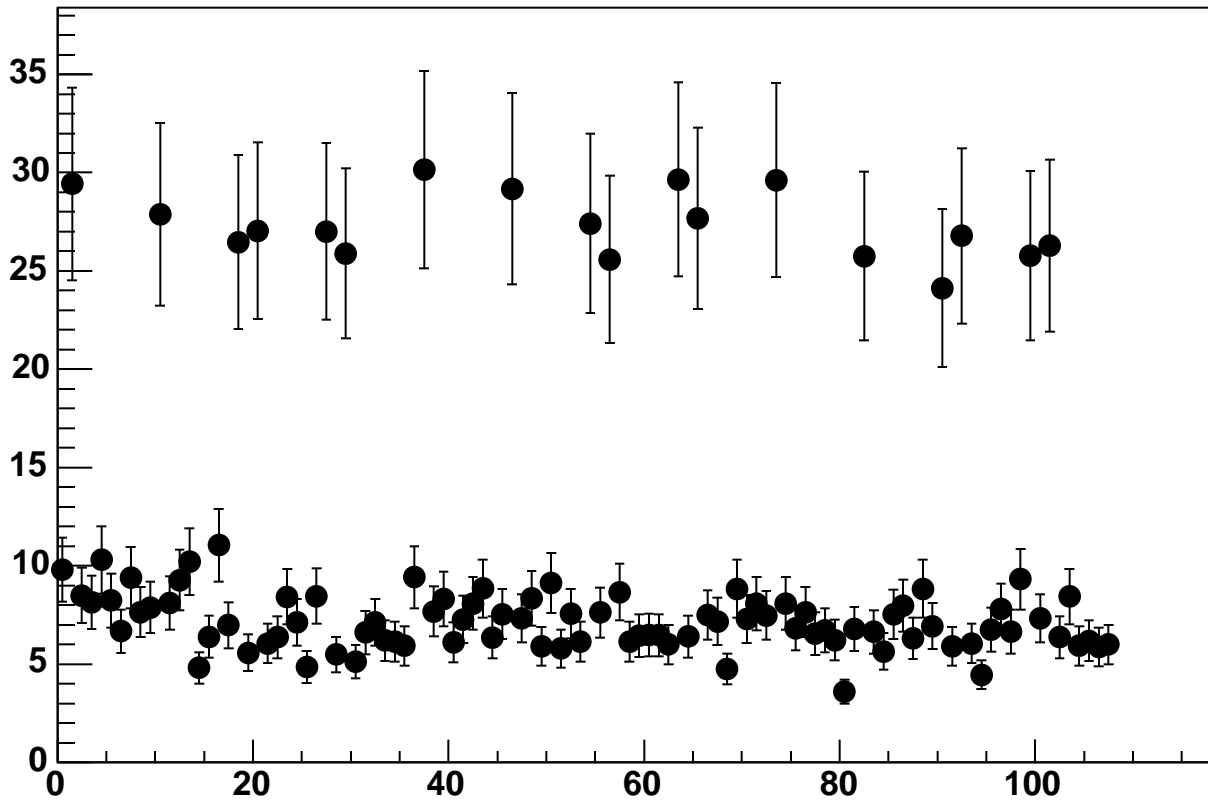
Enable 3, DAC=4000, Hold=100, ADC Noise vs 18\*Chip+Chan



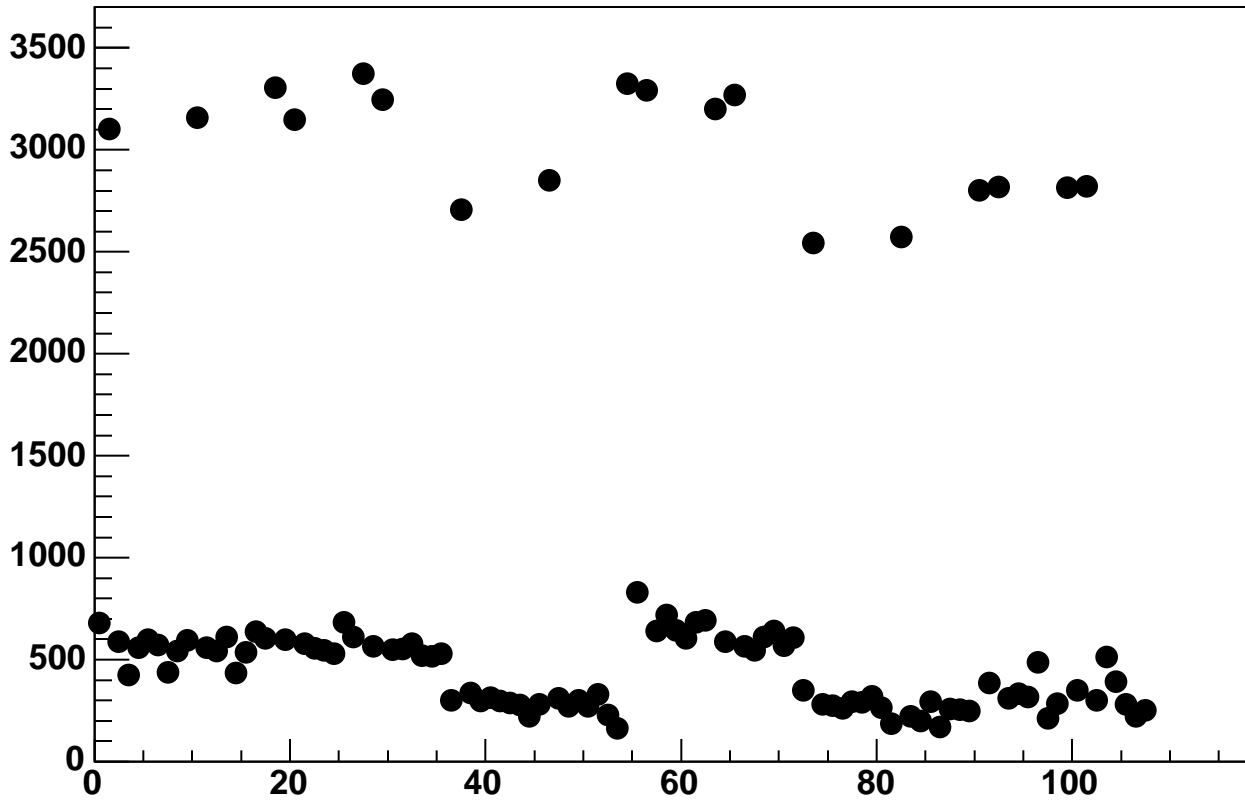
Enable 3, DAC=4000, Hold=105, ADC Mean vs 18\*Chip+Chan



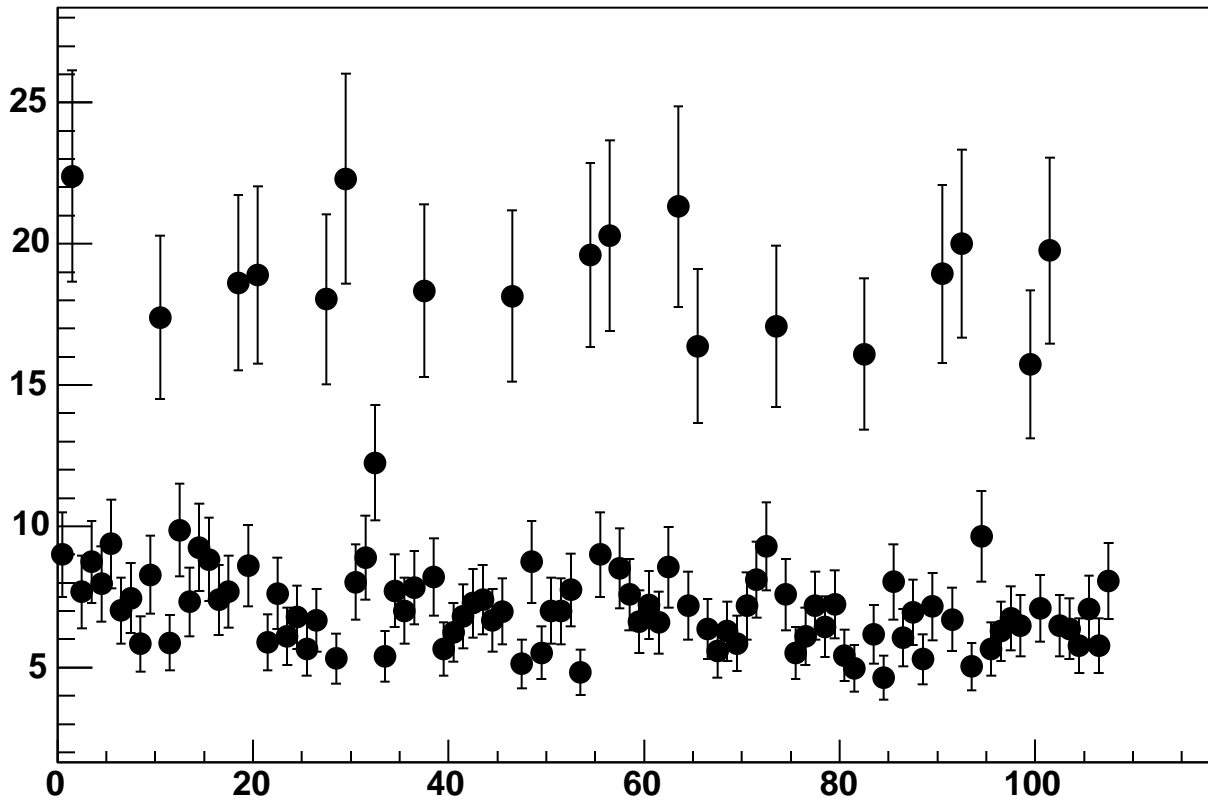
Enable 3, DAC=4000, Hold=105, ADC Noise vs 18\*Chip+Chan



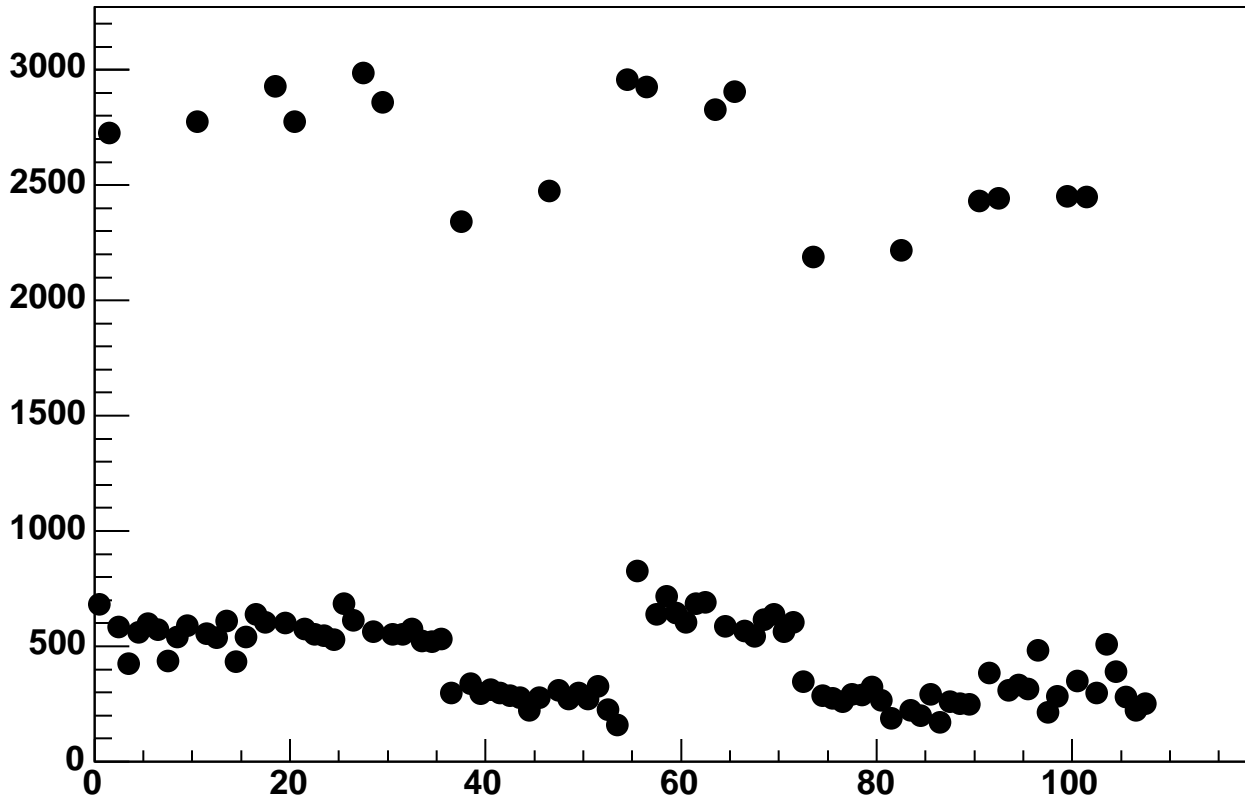
Enable 3, DAC=4000, Hold=110, ADC Mean vs 18\*Chip+Chan



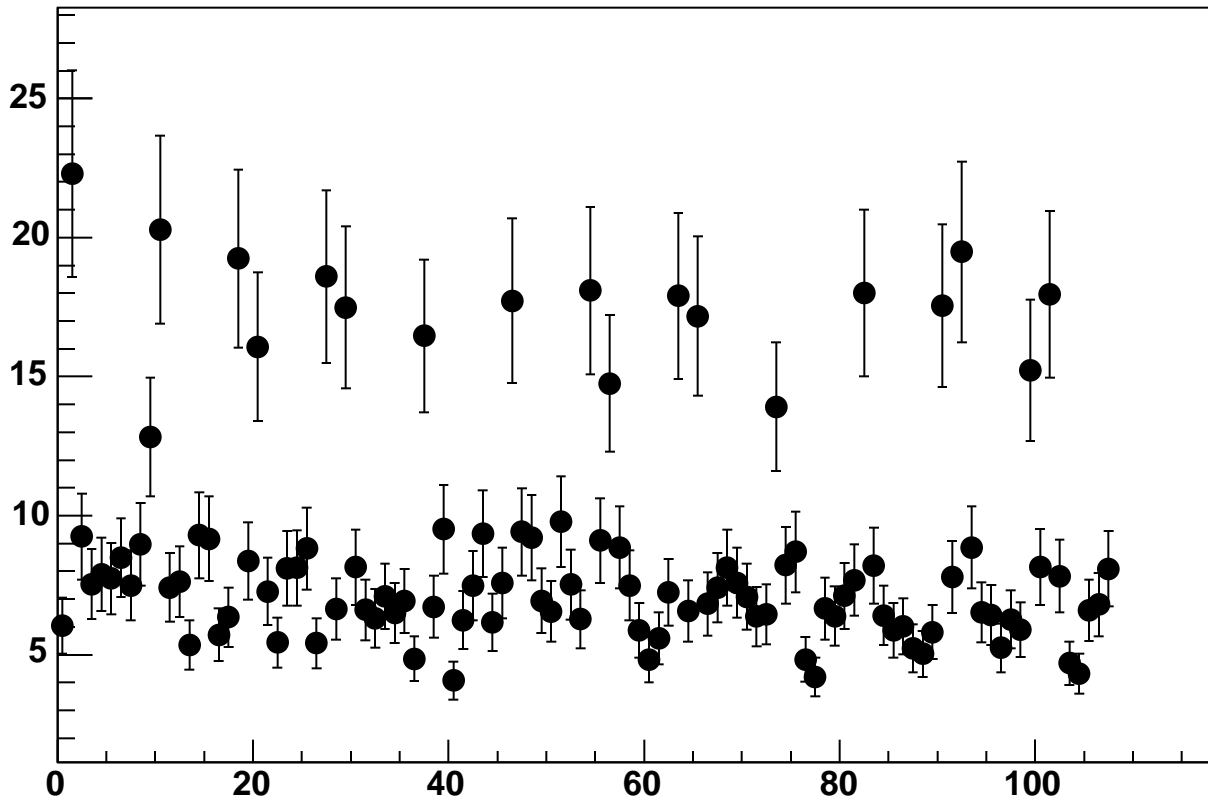
Enable 3, DAC=4000, Hold=110, ADC Noise vs 18\*Chip+Chan



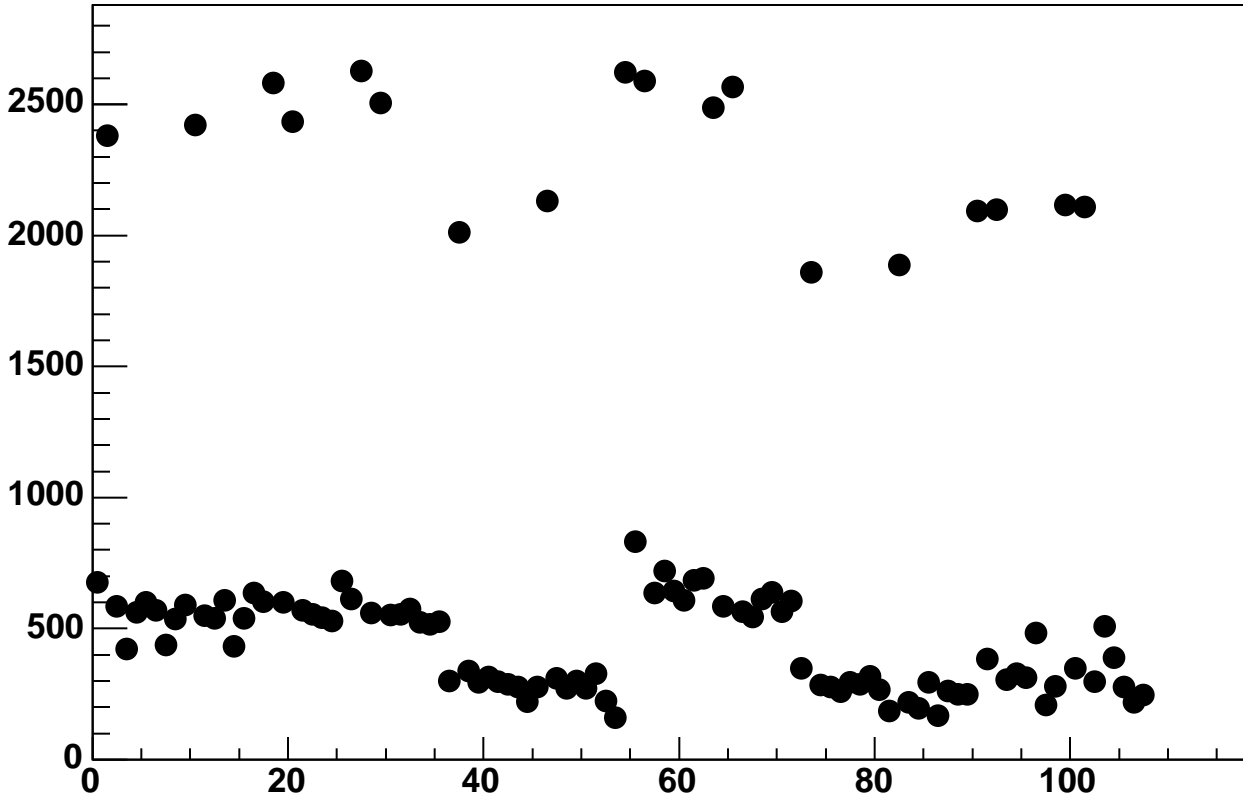
Enable 3, DAC=4000, Hold=115, ADC Mean vs 18\*Chip+Chan



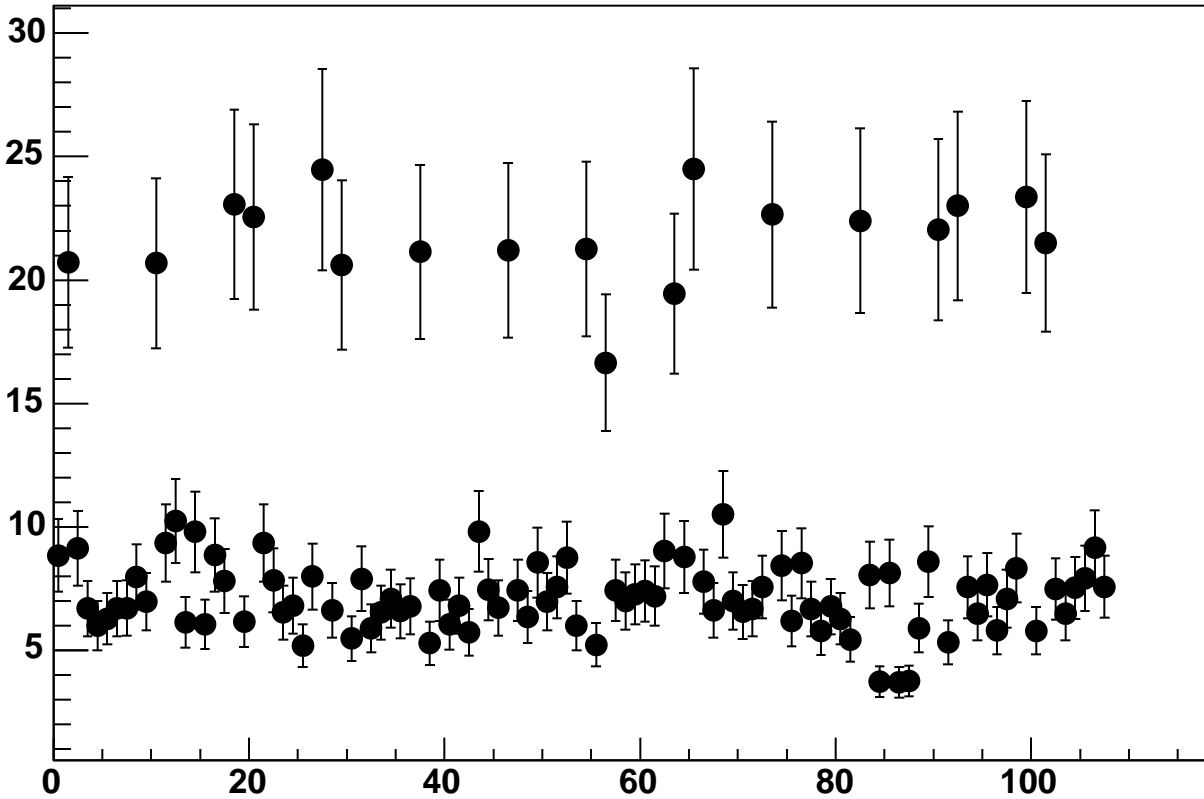
Enable 3, DAC=4000, Hold=115, ADC Noise vs 18\*Chip+Chan



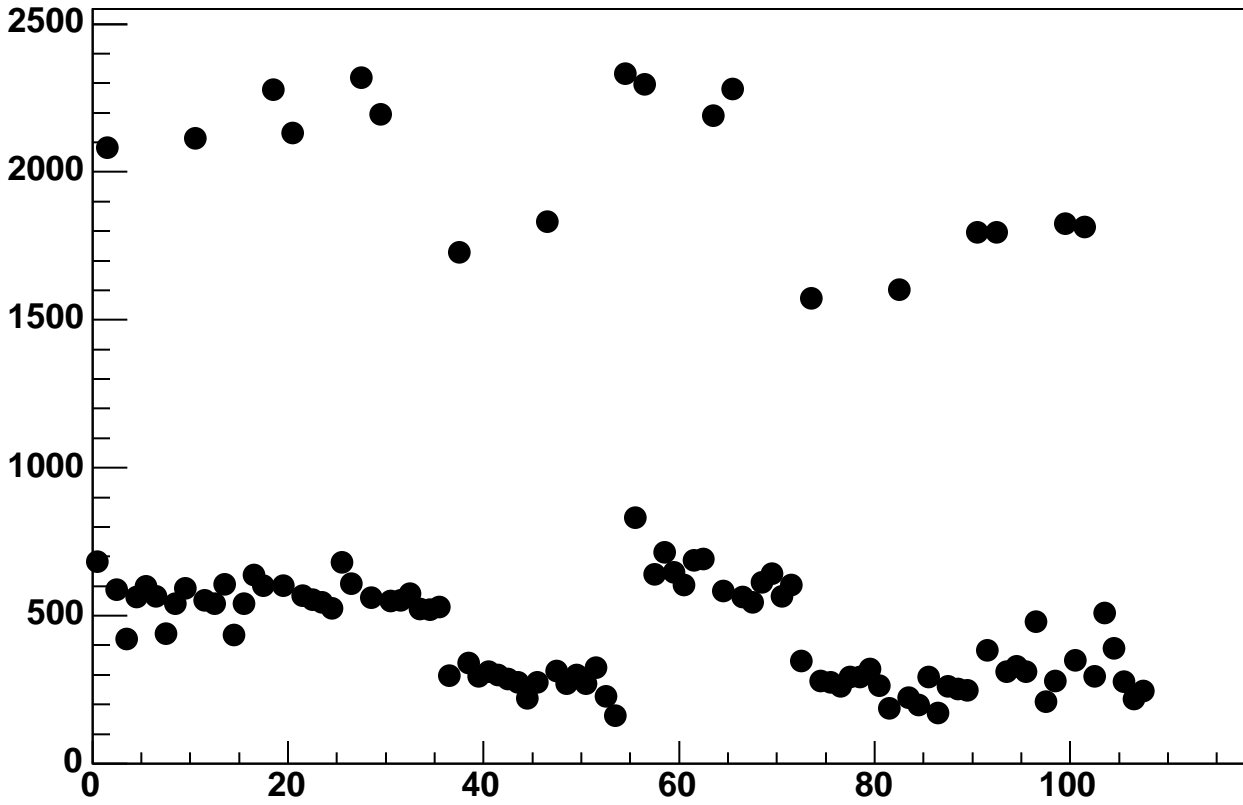
Enable 3, DAC=4000, Hold=120, ADC Mean vs 18\*Chip+Chan



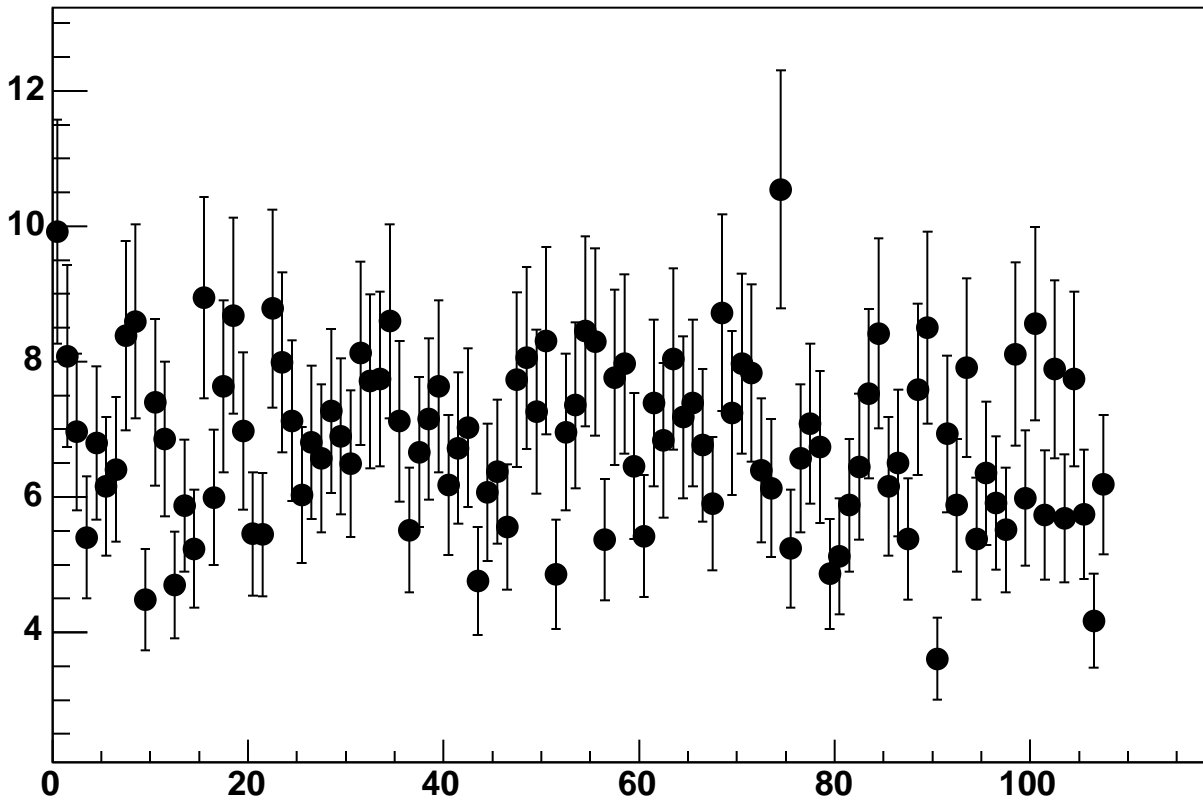
Enable 3, DAC=4000, Hold=120, ADC Noise vs 18\*Chip+Chan



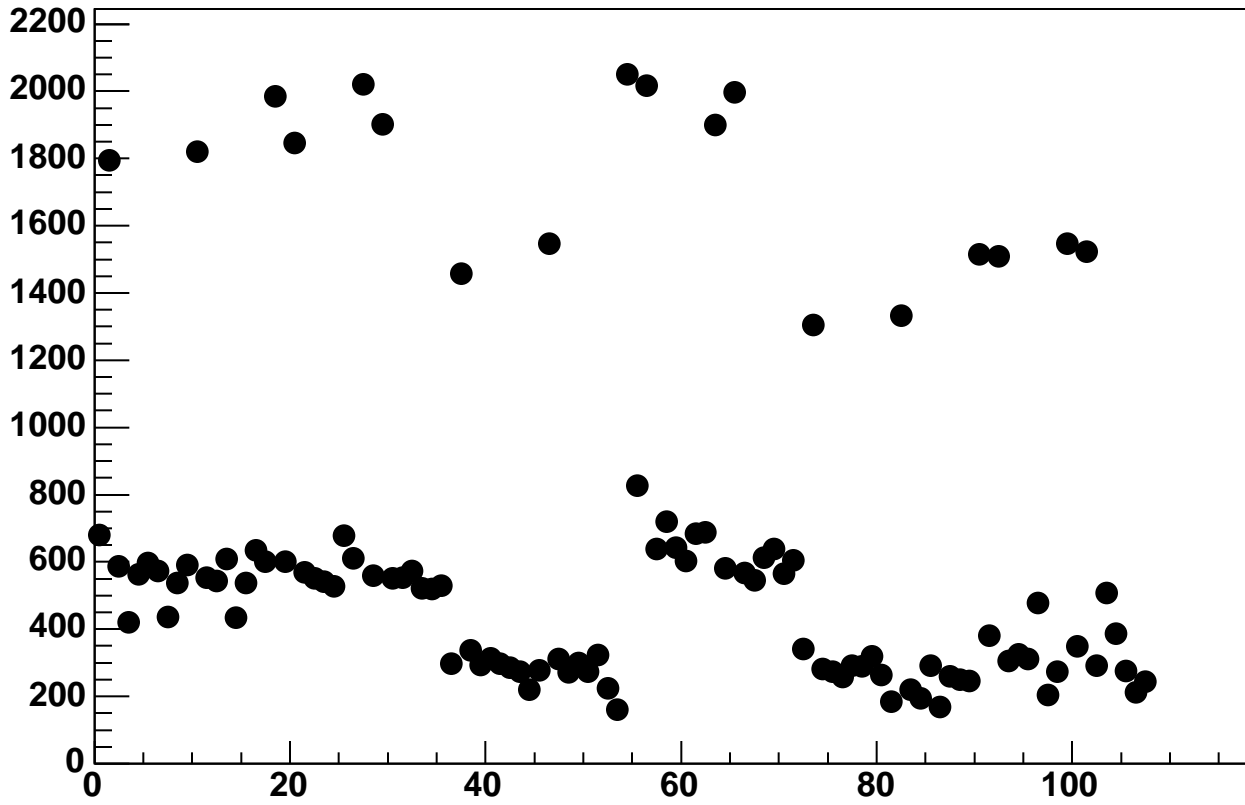
Enable 3, DAC=4000, Hold=125, ADC Mean vs 18\*Chip+Chan



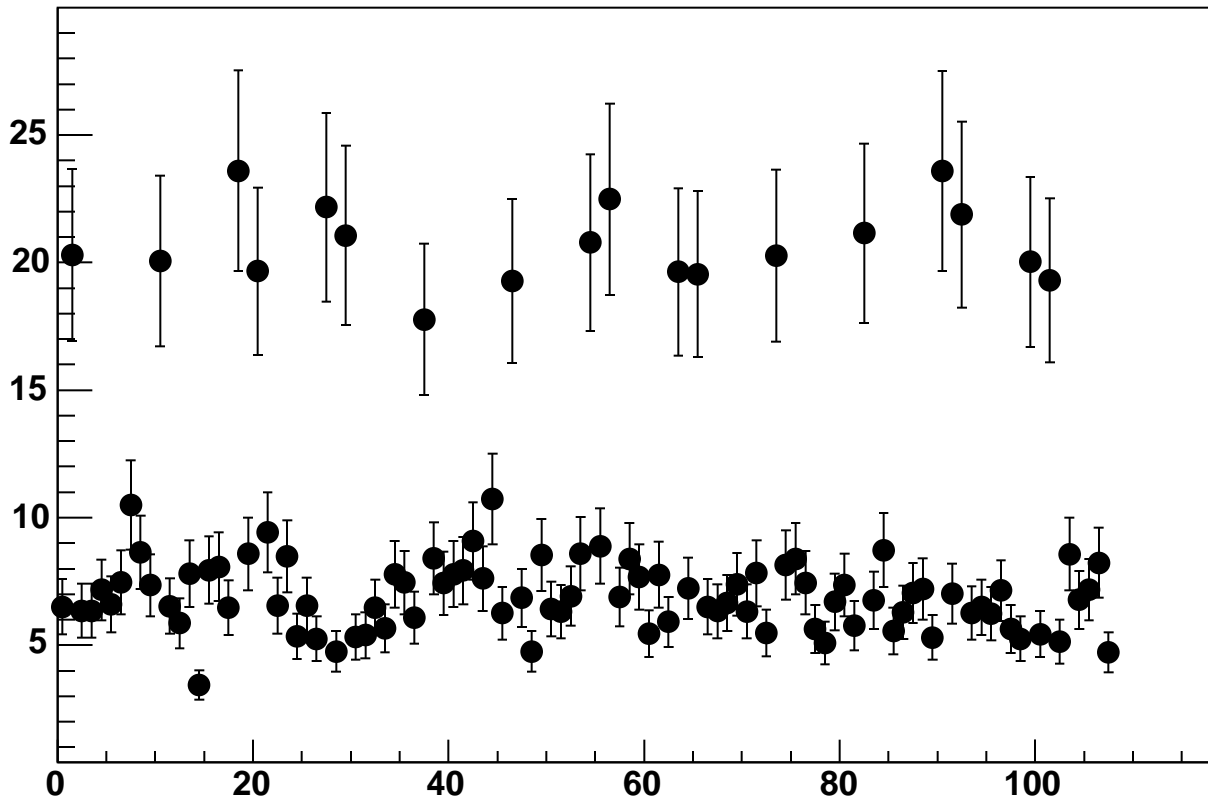
Enable 3, DAC=4000, Hold=125, ADC Noise vs 18\*Chip+Chan



Enable 3, DAC=4000, Hold=130, ADC Mean vs 18\*Chip+Chan

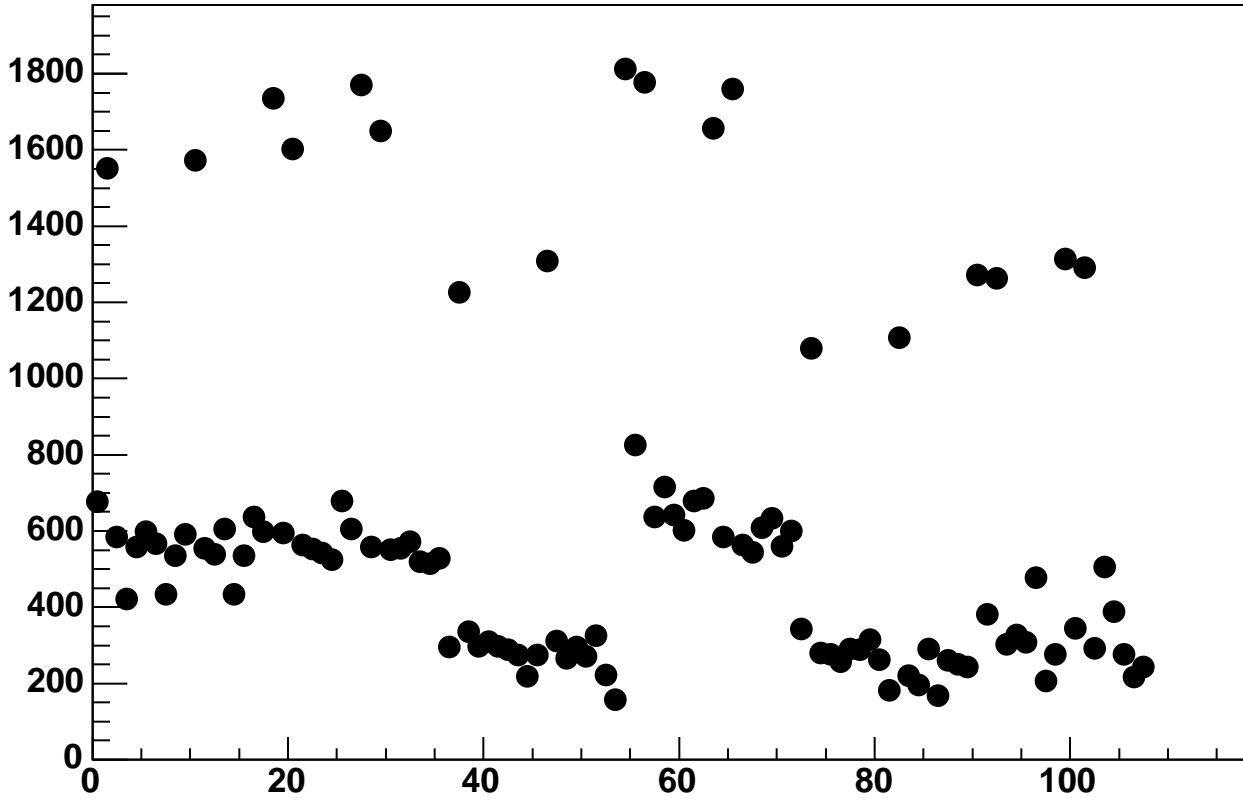


Enable 3, DAC=4000, Hold=130, ADC Noise vs 18\*Chip+Chan

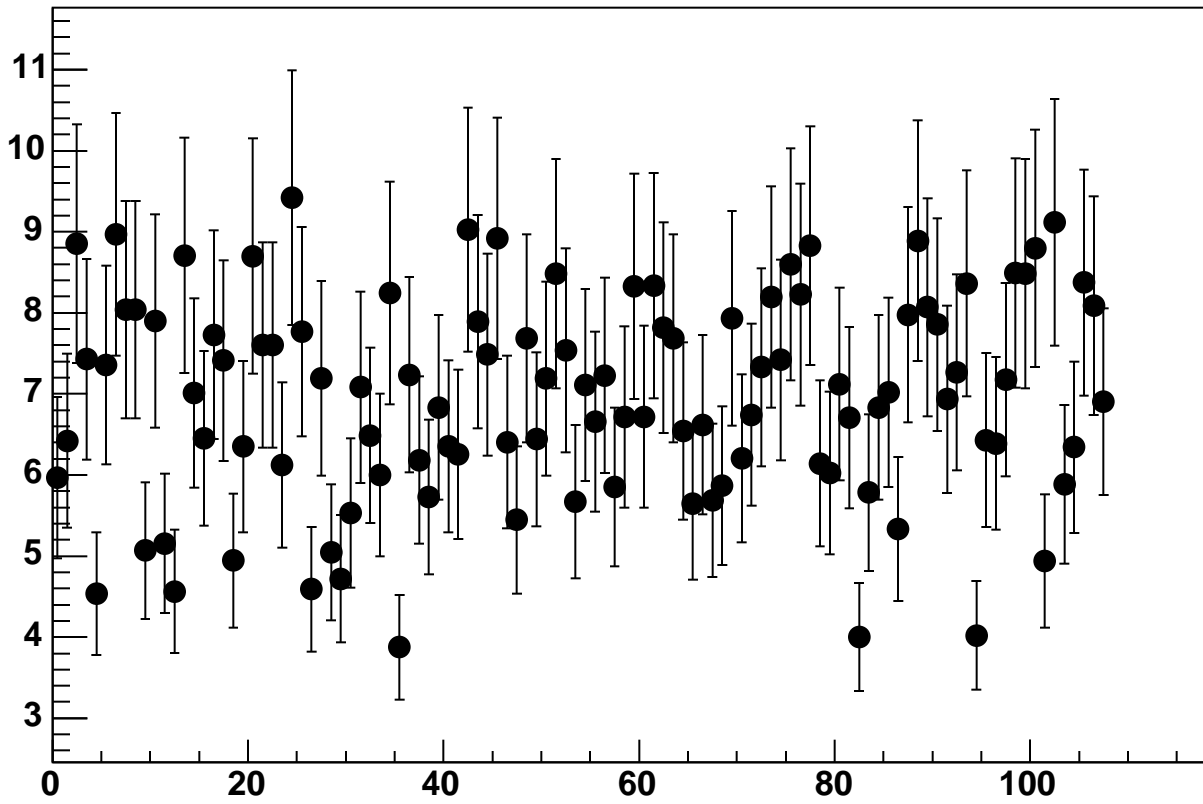




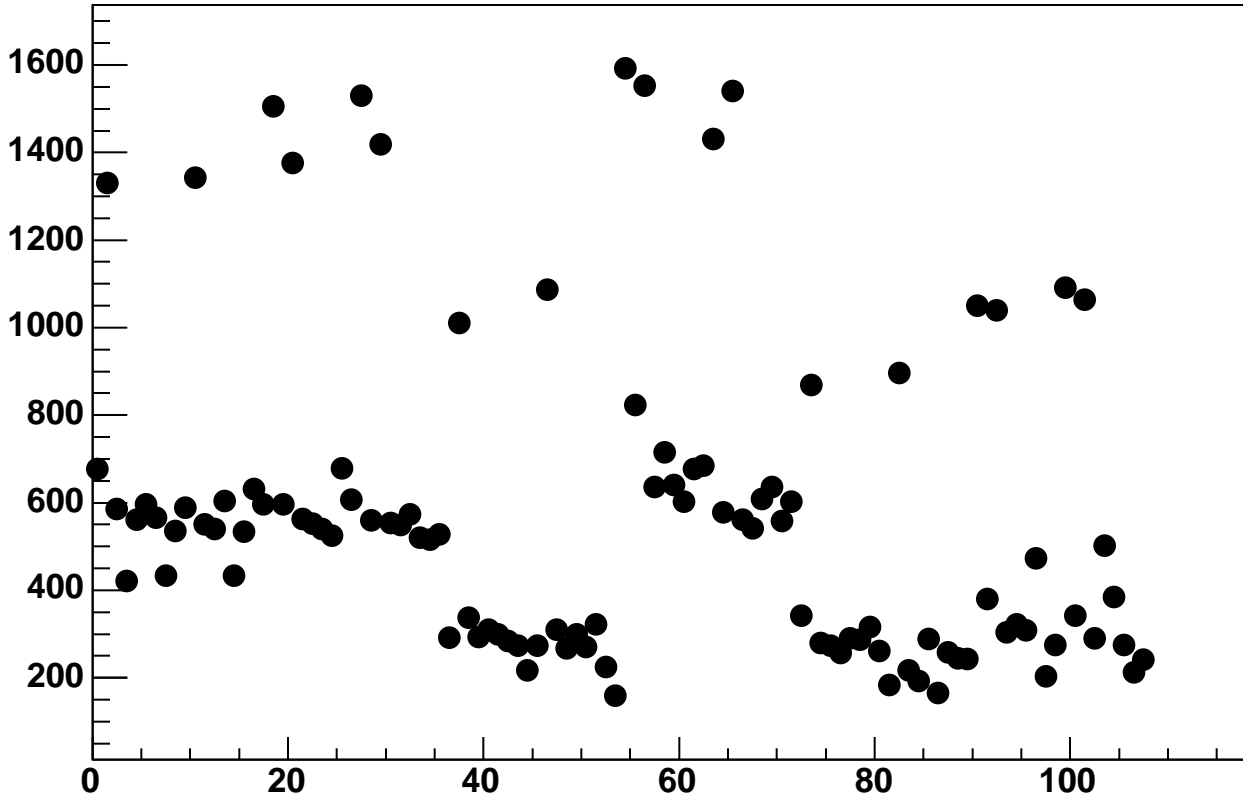
Enable 3, DAC=4000, Hold=135, ADC Mean vs 18\*Chip+Chan



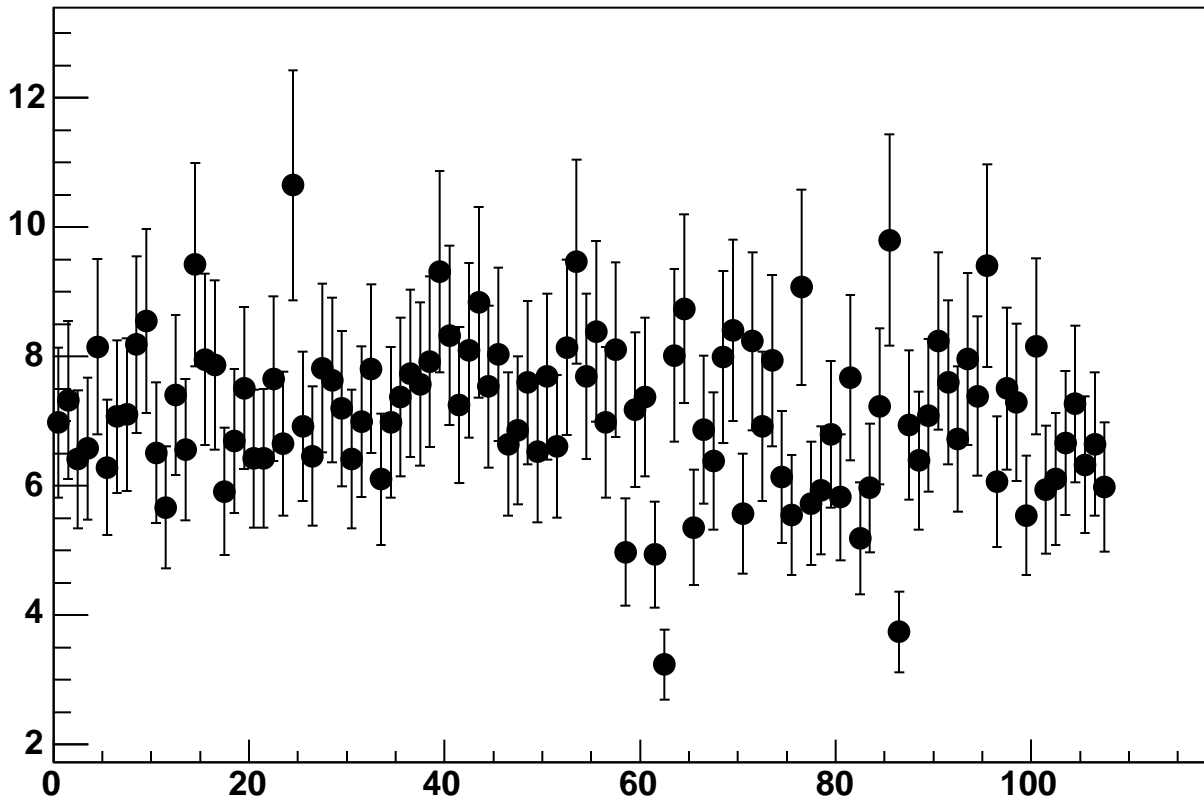
Enable 3, DAC=4000, Hold=135, ADC Noise vs 18\*Chip+Chan



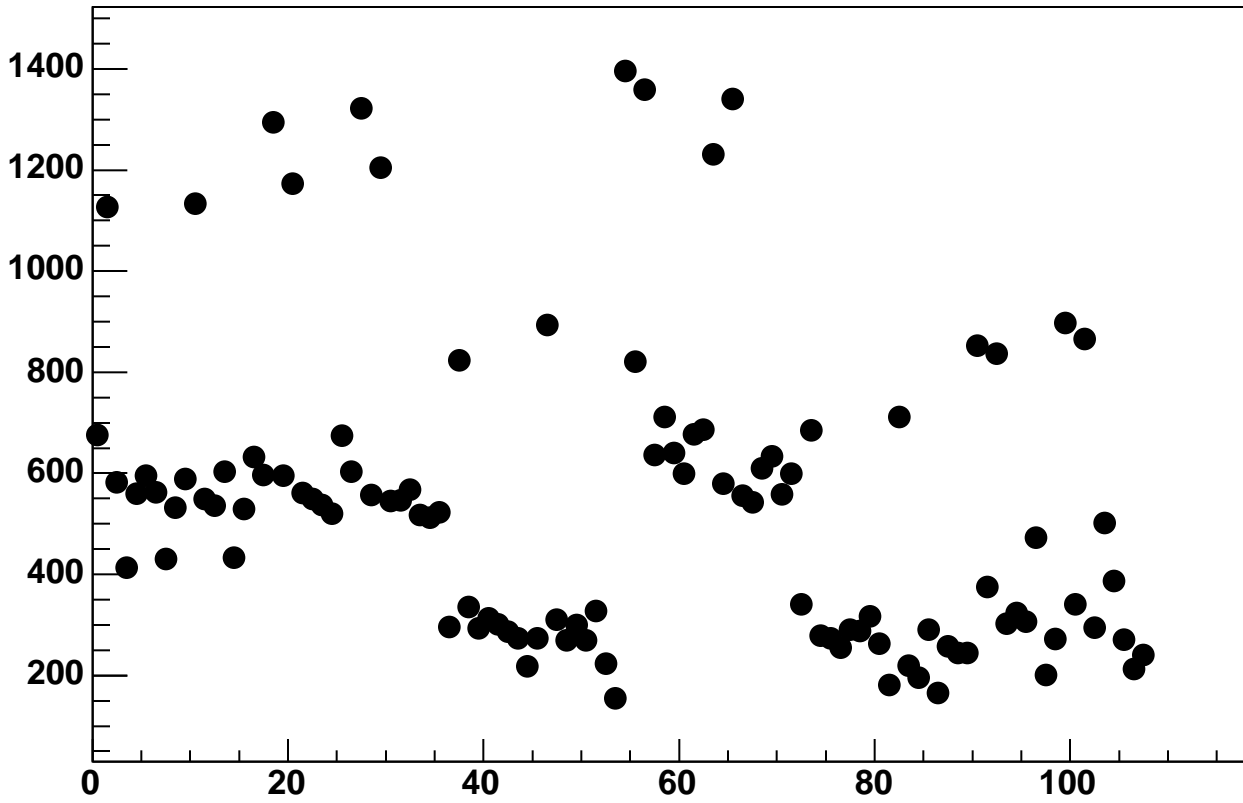
Enable 3, DAC=4000, Hold=140, ADC Mean vs 18\*Chip+Chan



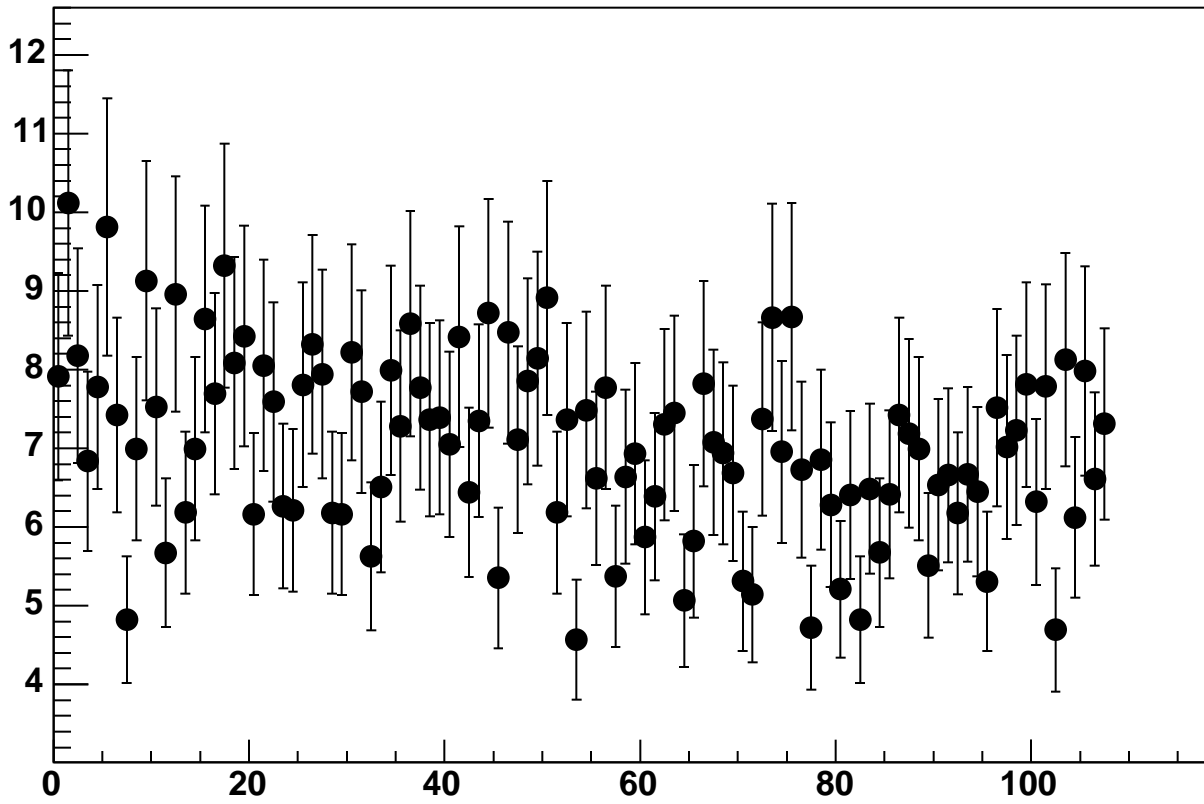
Enable 3, DAC=4000, Hold=140, ADC Noise vs 18\*Chip+Chan



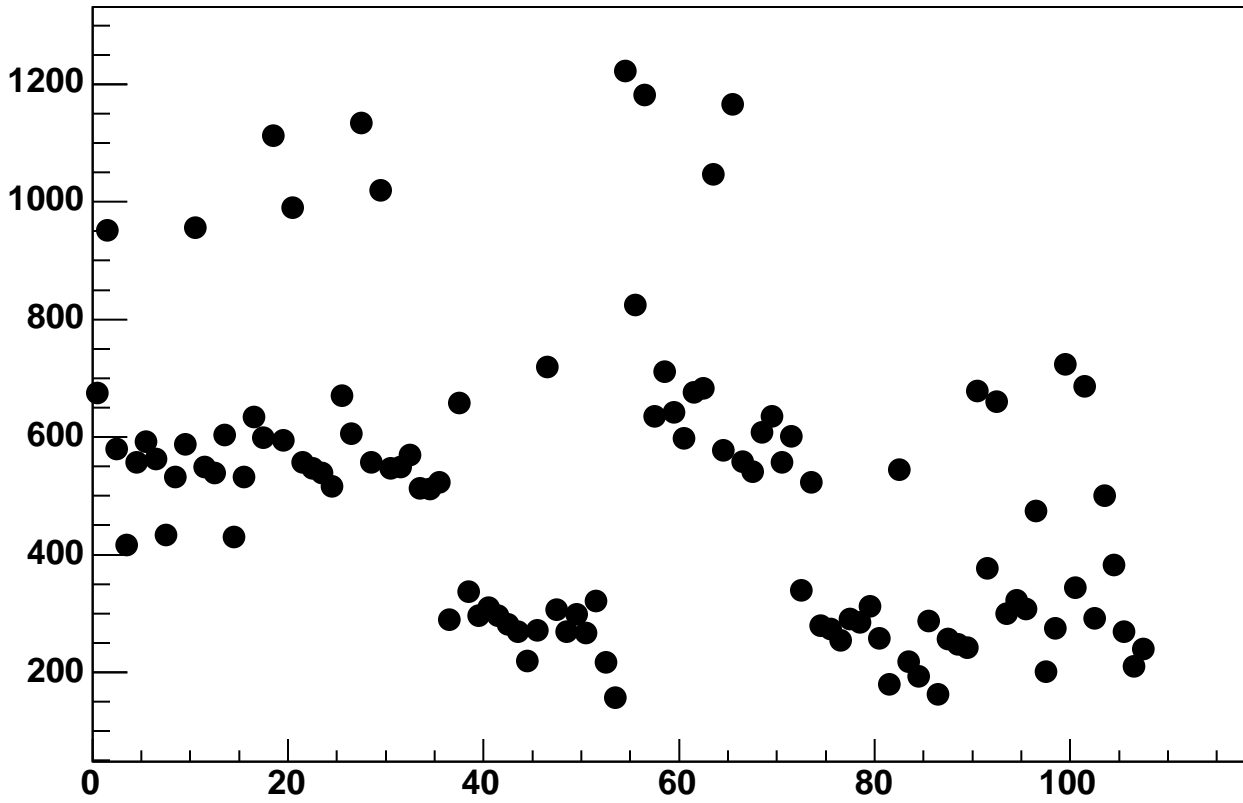
Enable 3, DAC=4000, Hold=145, ADC Mean vs 18\*Chip+Chan



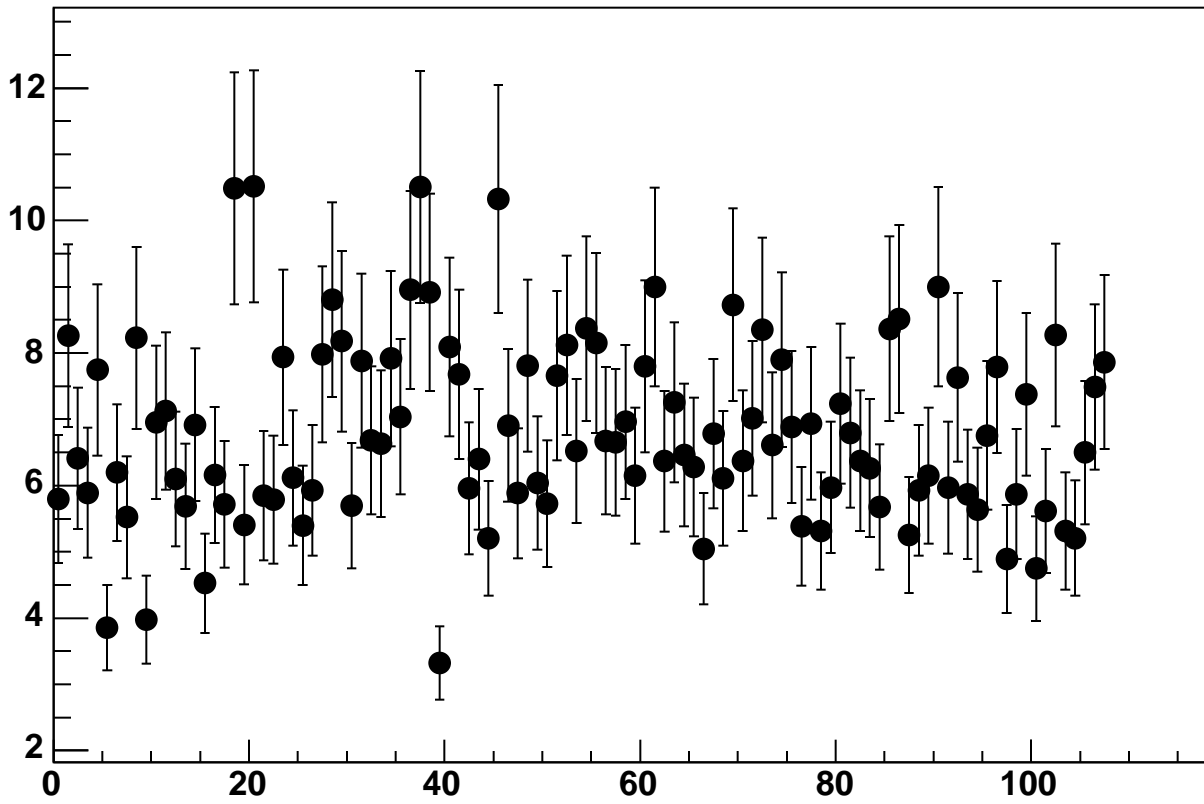
Enable 3, DAC=4000, Hold=145, ADC Noise vs 18\*Chip+Chan



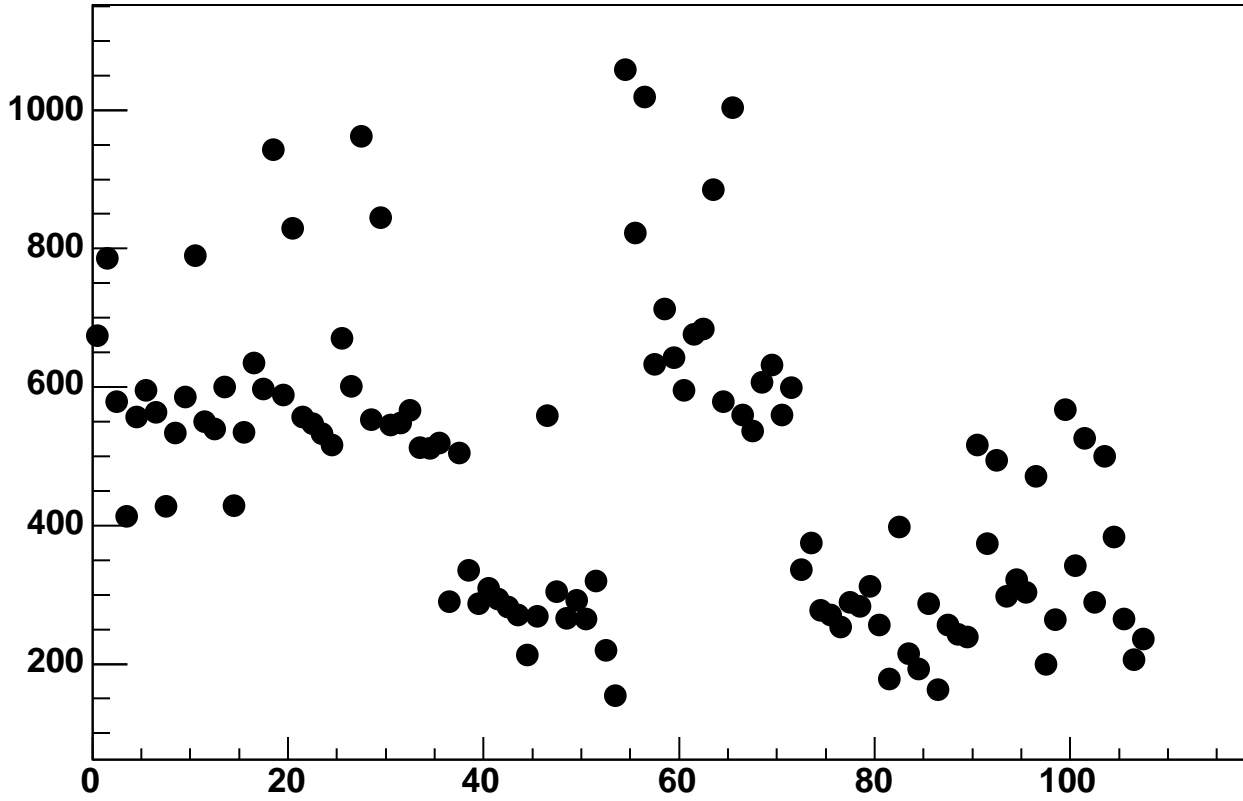
Enable 3, DAC=4000, Hold=150, ADC Mean vs 18\*Chip+Chan



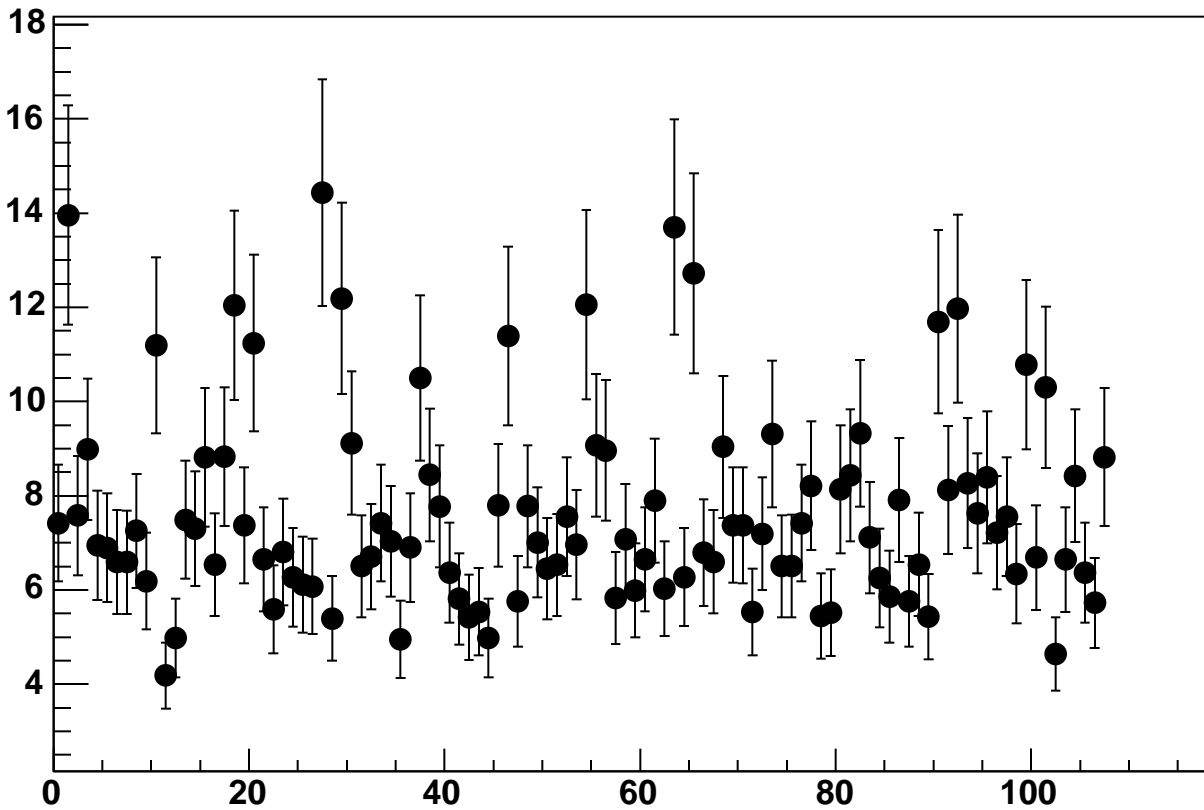
Enable 3, DAC=4000, Hold=150, ADC Noise vs 18\*Chip+Chan



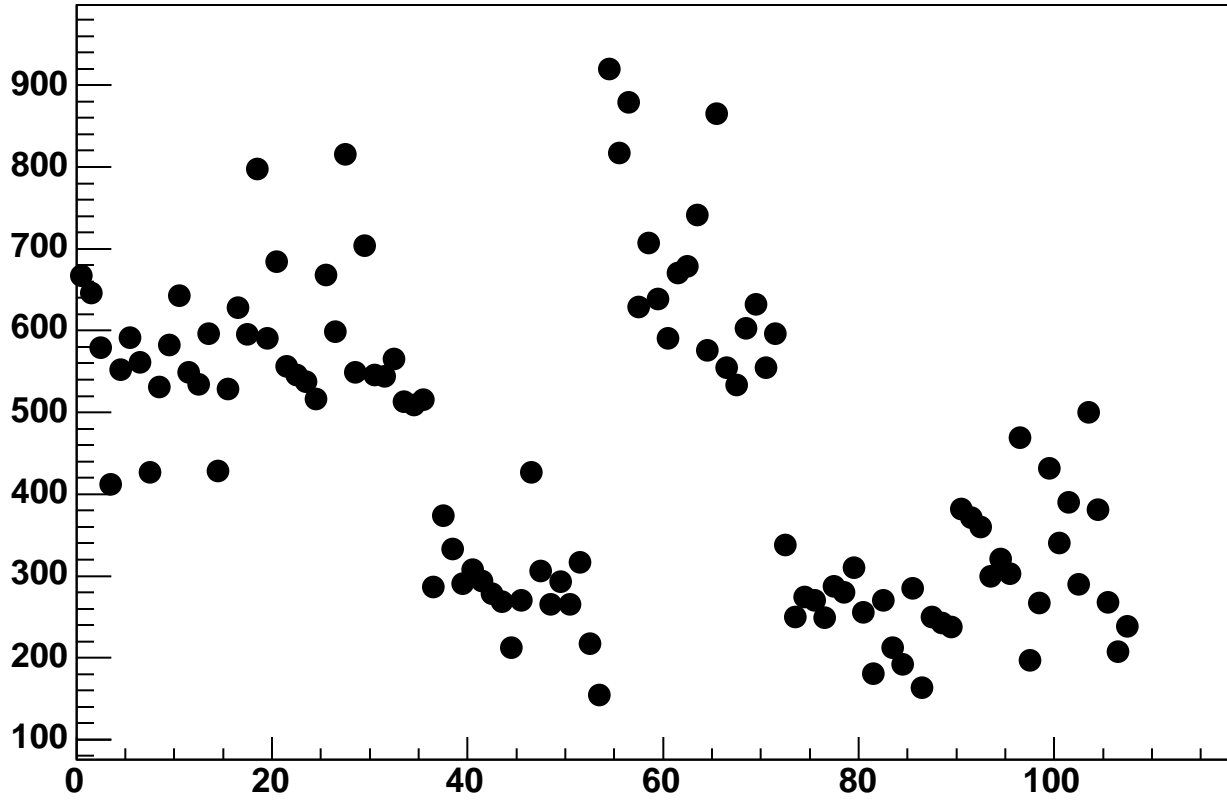
Enable 3, DAC=4000, Hold=155, ADC Mean vs 18\*Chip+Chan



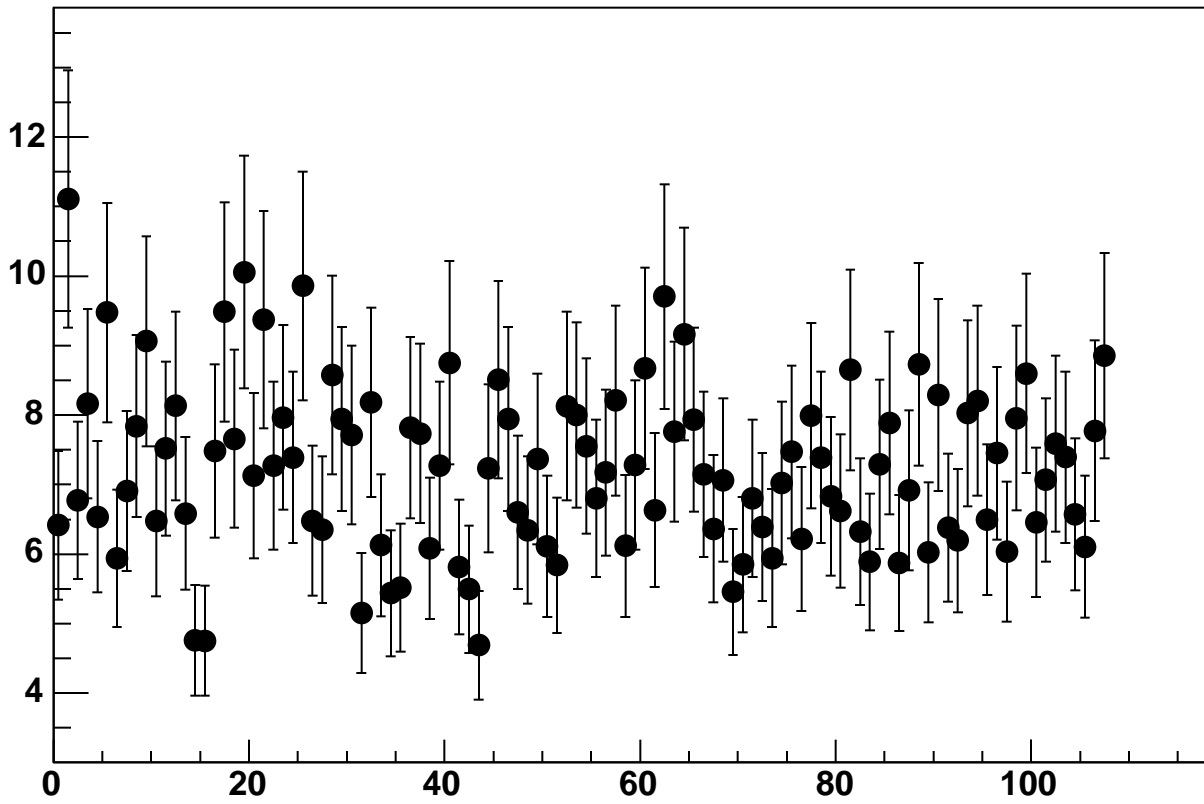
Enable 3, DAC=4000, Hold=155, ADC Noise vs 18\*Chip+Chan



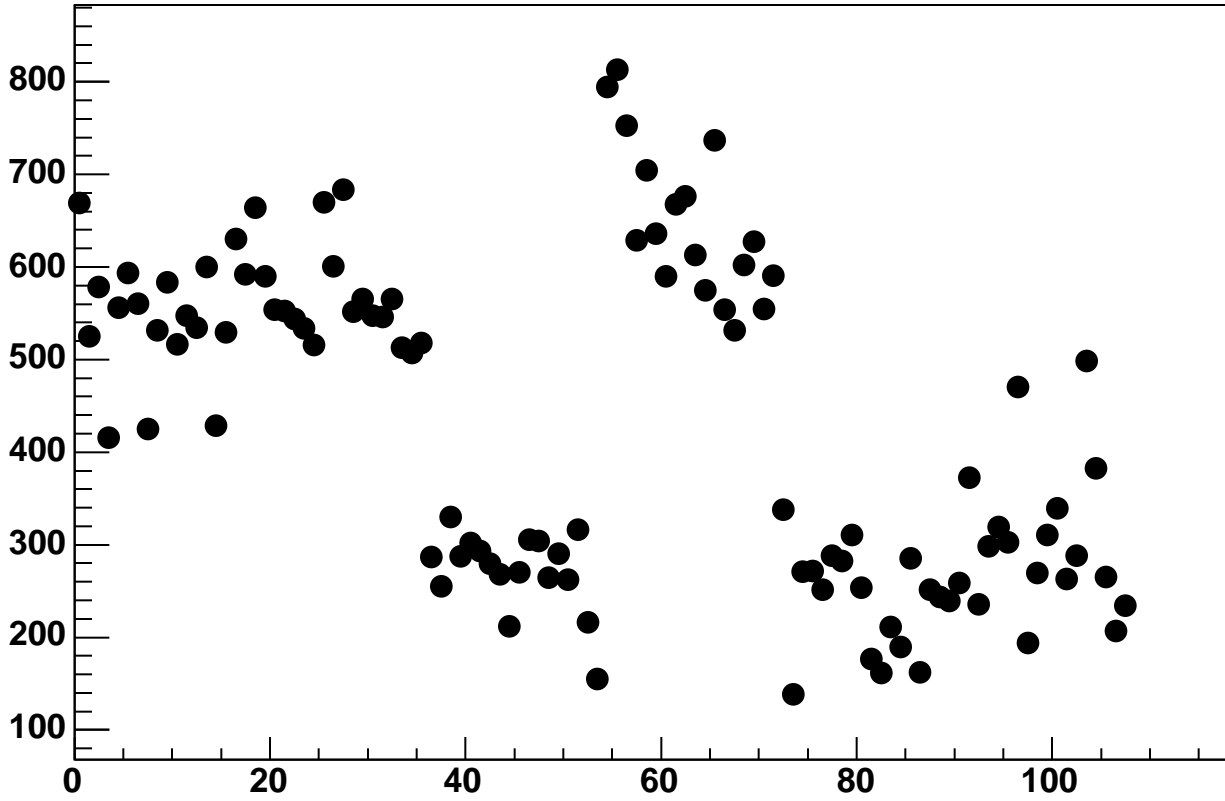
Enable 3, DAC=4000, Hold=160, ADC Mean vs 18\*Chip+Chan



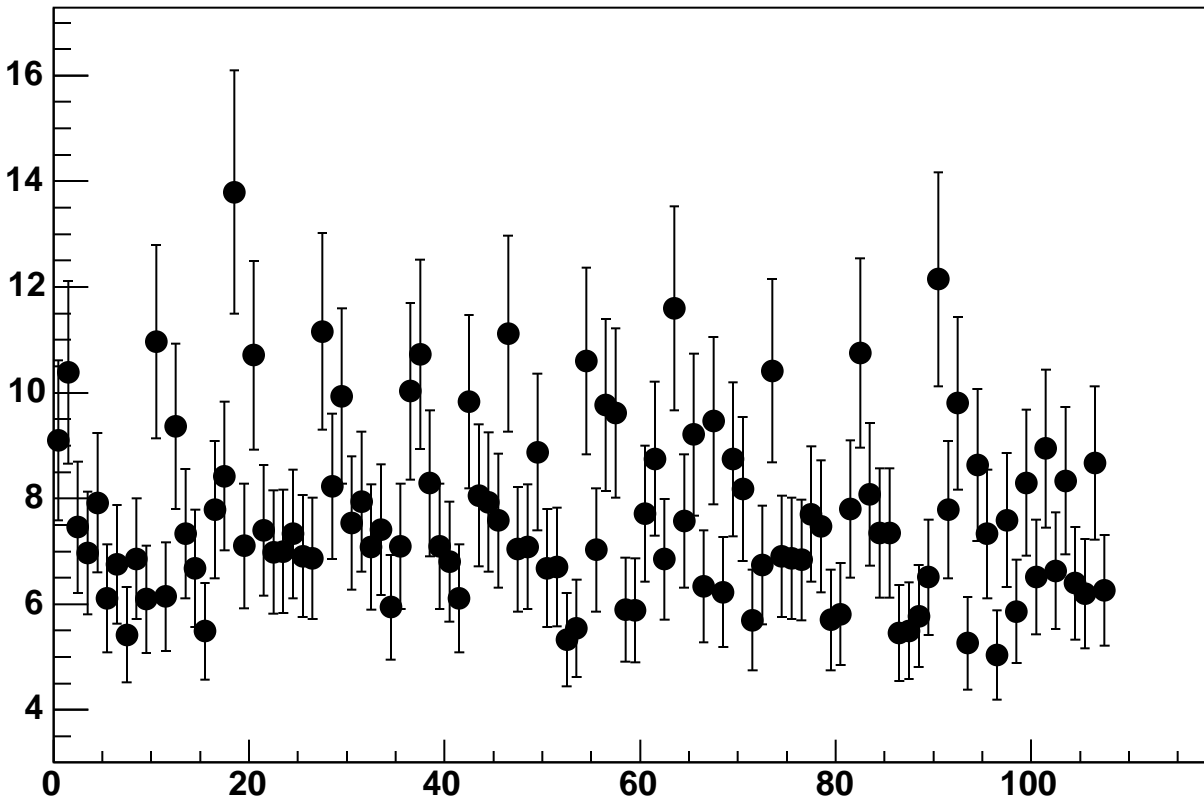
Enable 3, DAC=4000, Hold=160, ADC Noise vs 18\*Chip+Chan



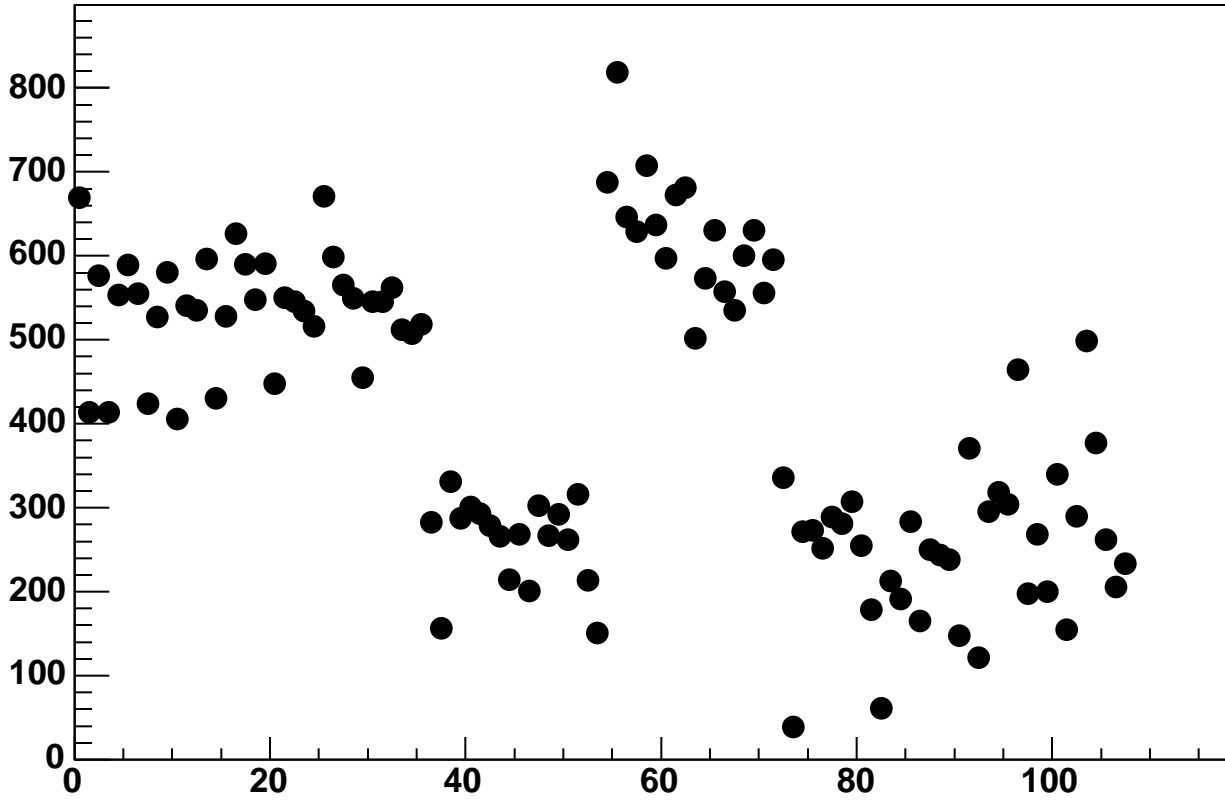
Enable 3, DAC=4000, Hold=165, ADC Mean vs 18\*Chip+Chan



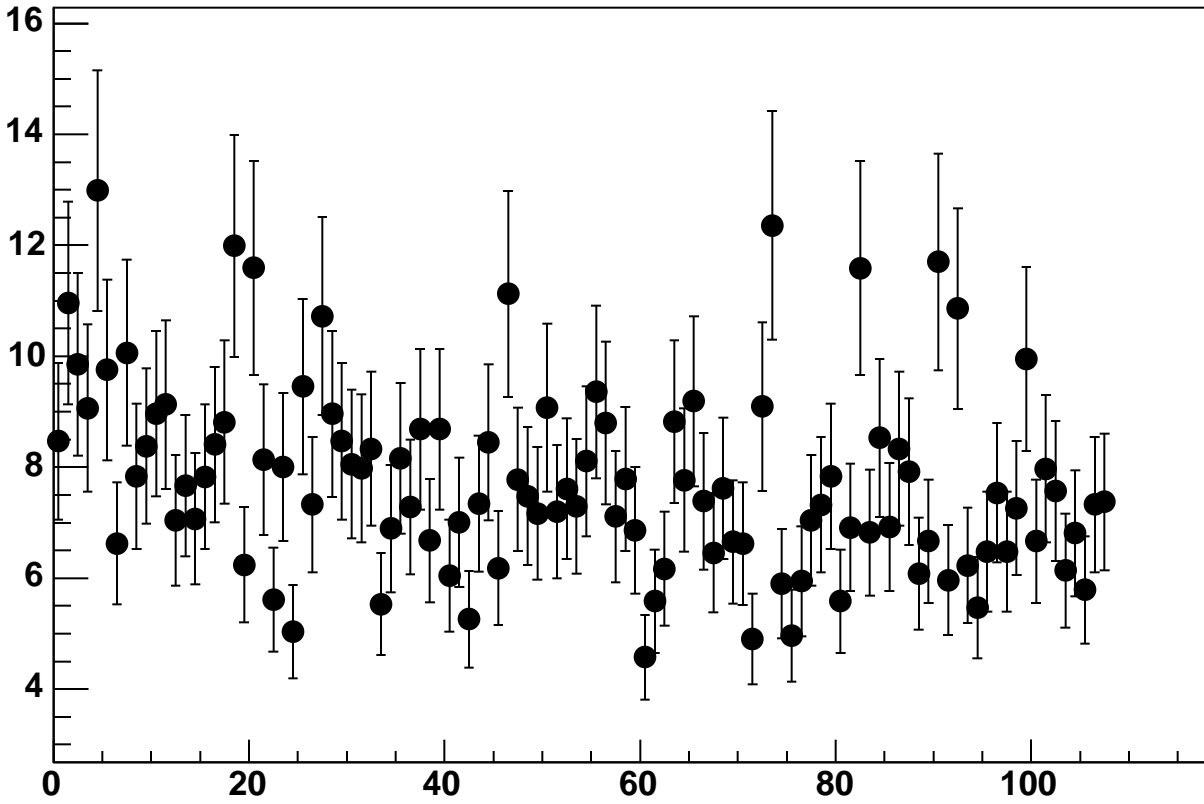
Enable 3, DAC=4000, Hold=165, ADC Noise vs 18\*Chip+Chan



Enable 3, DAC=4000, Hold=170, ADC Mean vs 18\*Chip+Chan

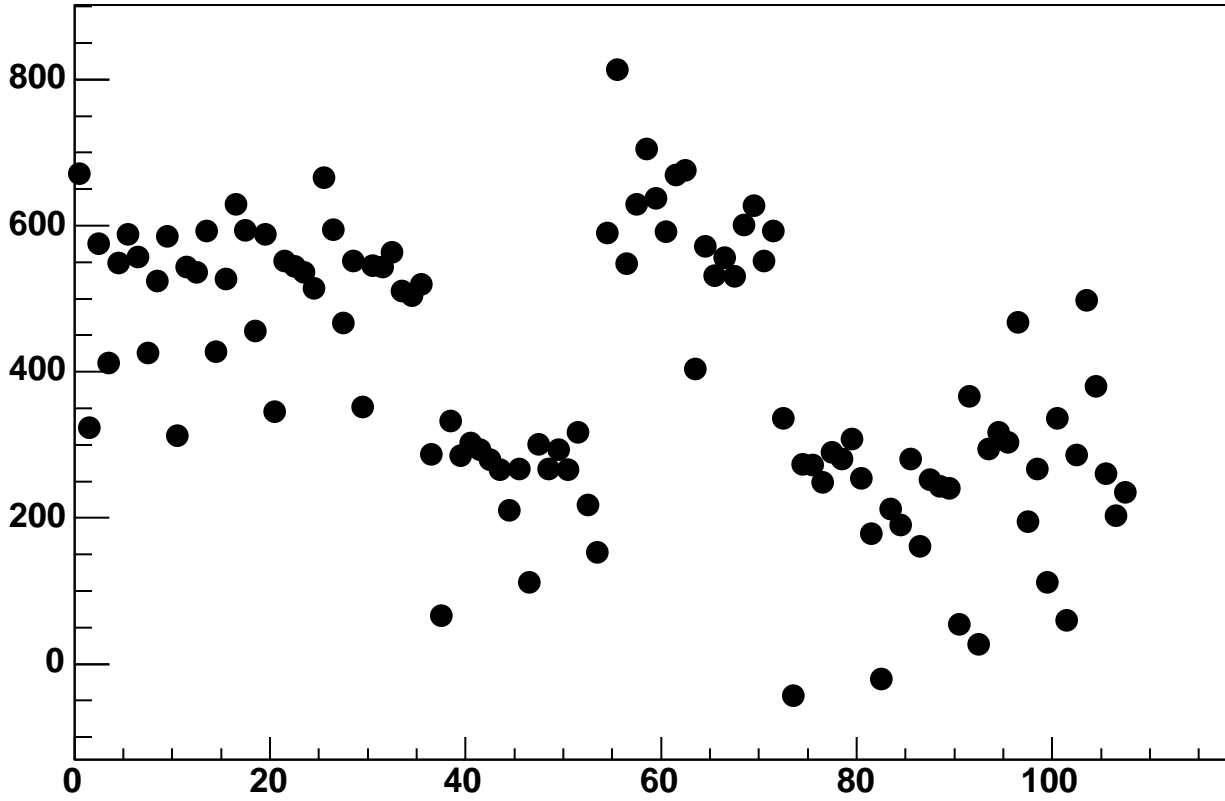


Enable 3, DAC=4000, Hold=170, ADC Noise vs 18\*Chip+Chan

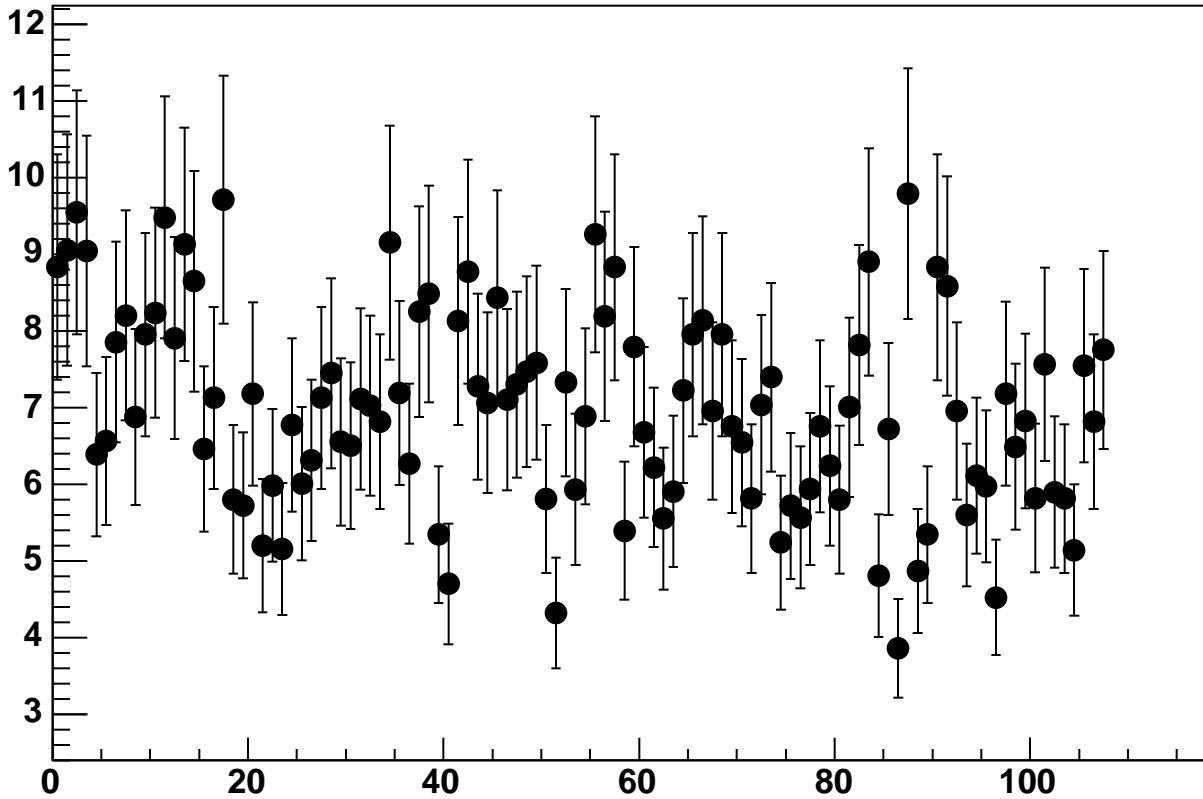




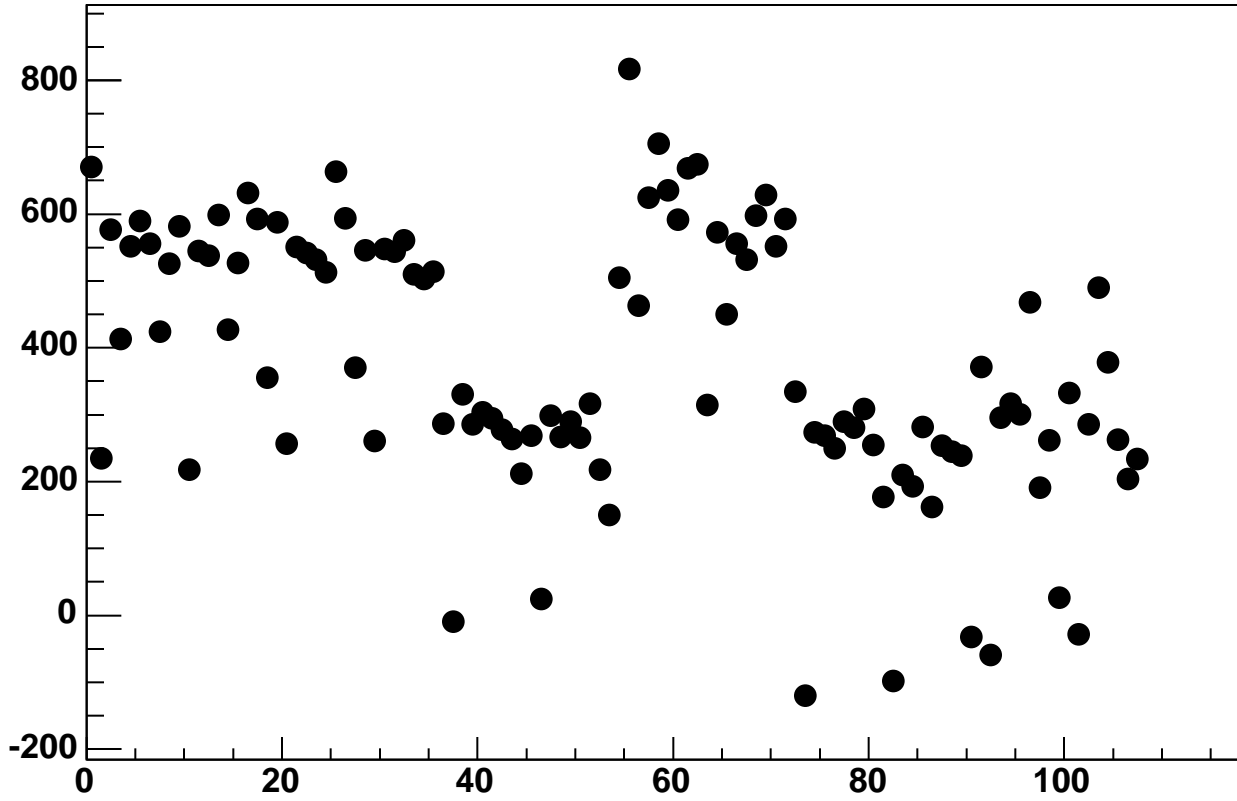
Enable 3, DAC=4000, Hold=175, ADC Mean vs 18\*Chip+Chan



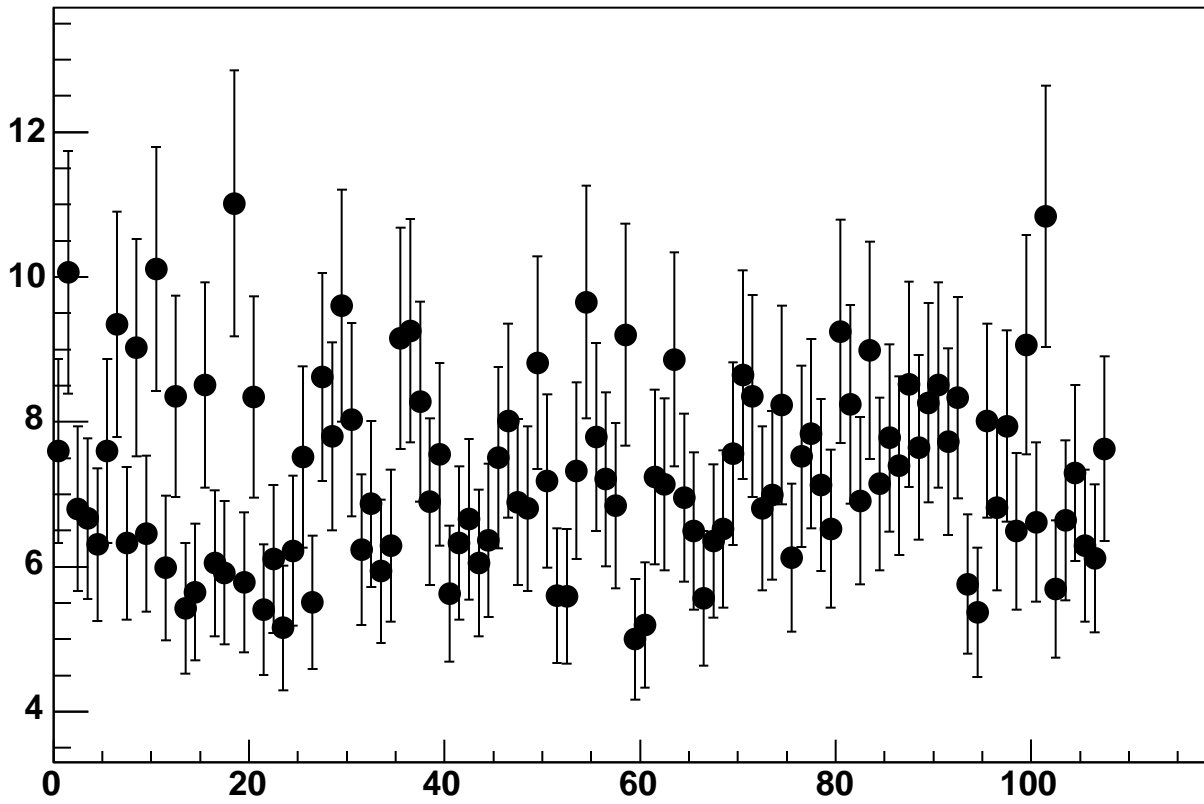
Enable 3, DAC=4000, Hold=175, ADC Noise vs 18\*Chip+Chan



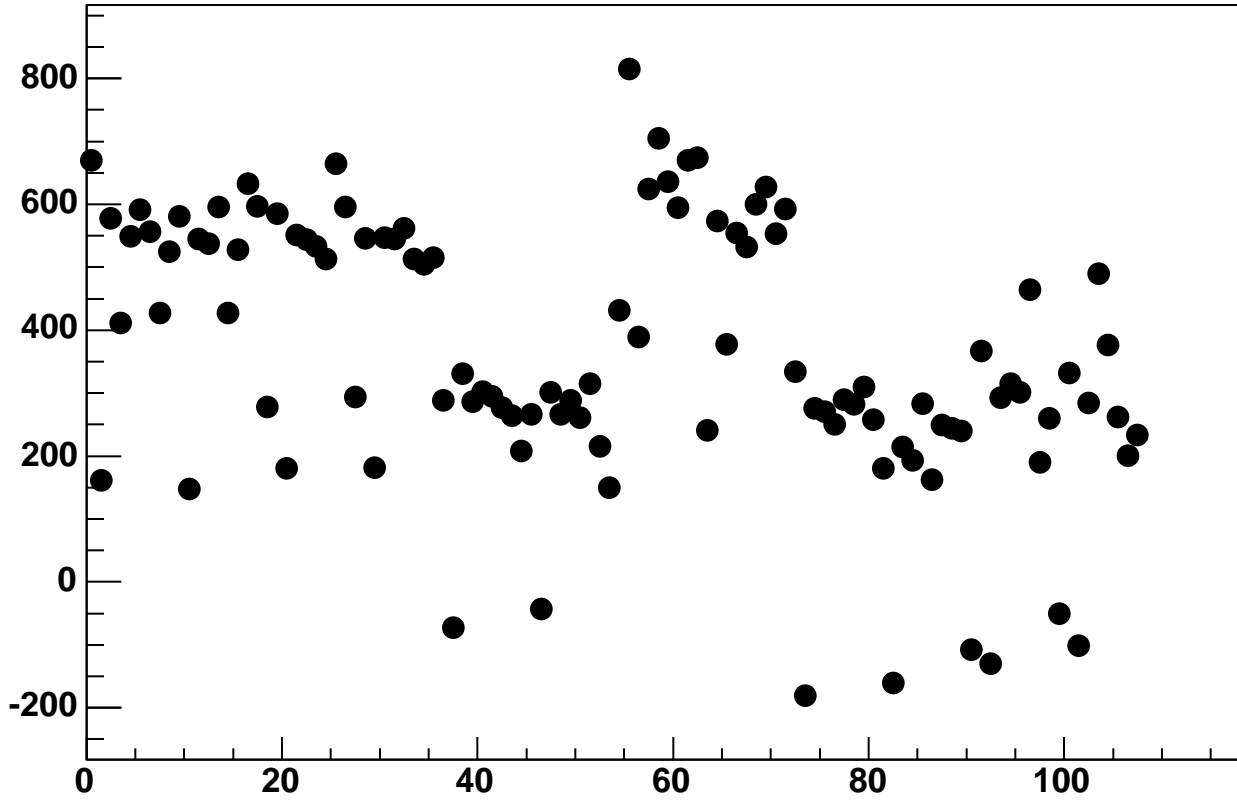
Enable 3, DAC=4000, Hold=180, ADC Mean vs 18\*Chip+Chan



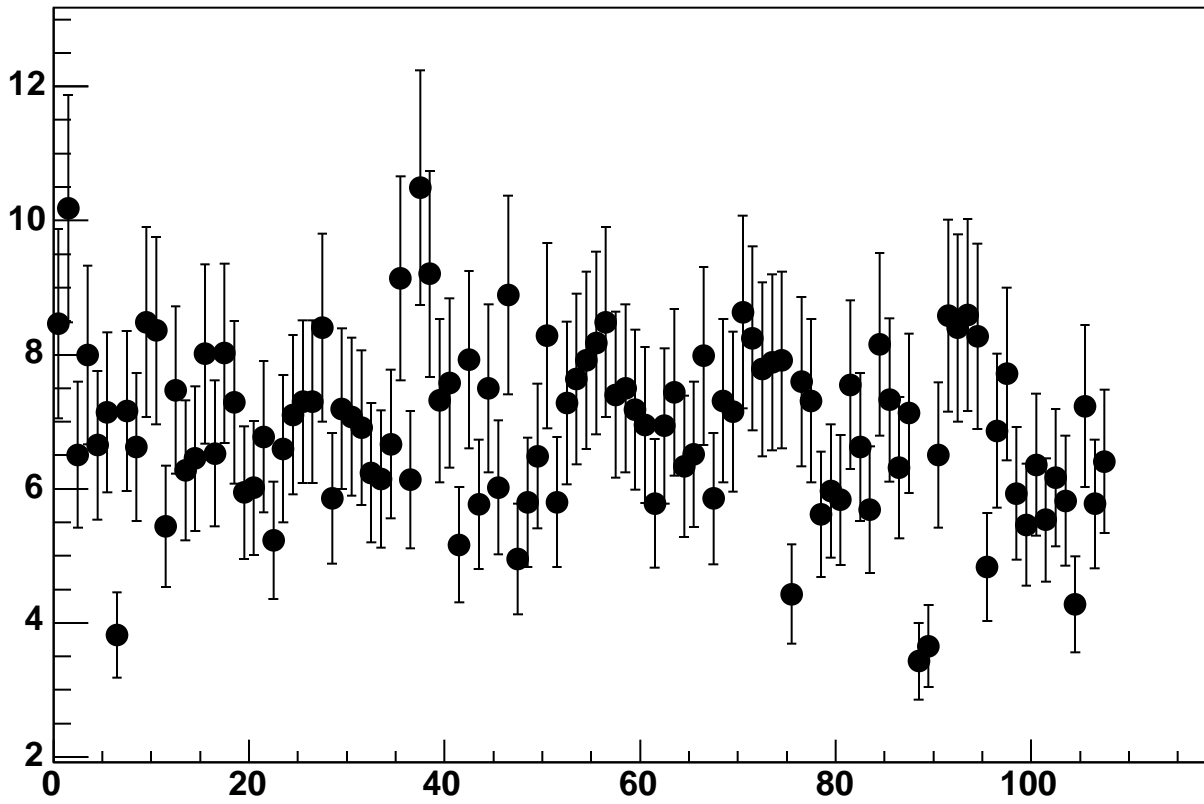
Enable 3, DAC=4000, Hold=180, ADC Noise vs 18\*Chip+Chan



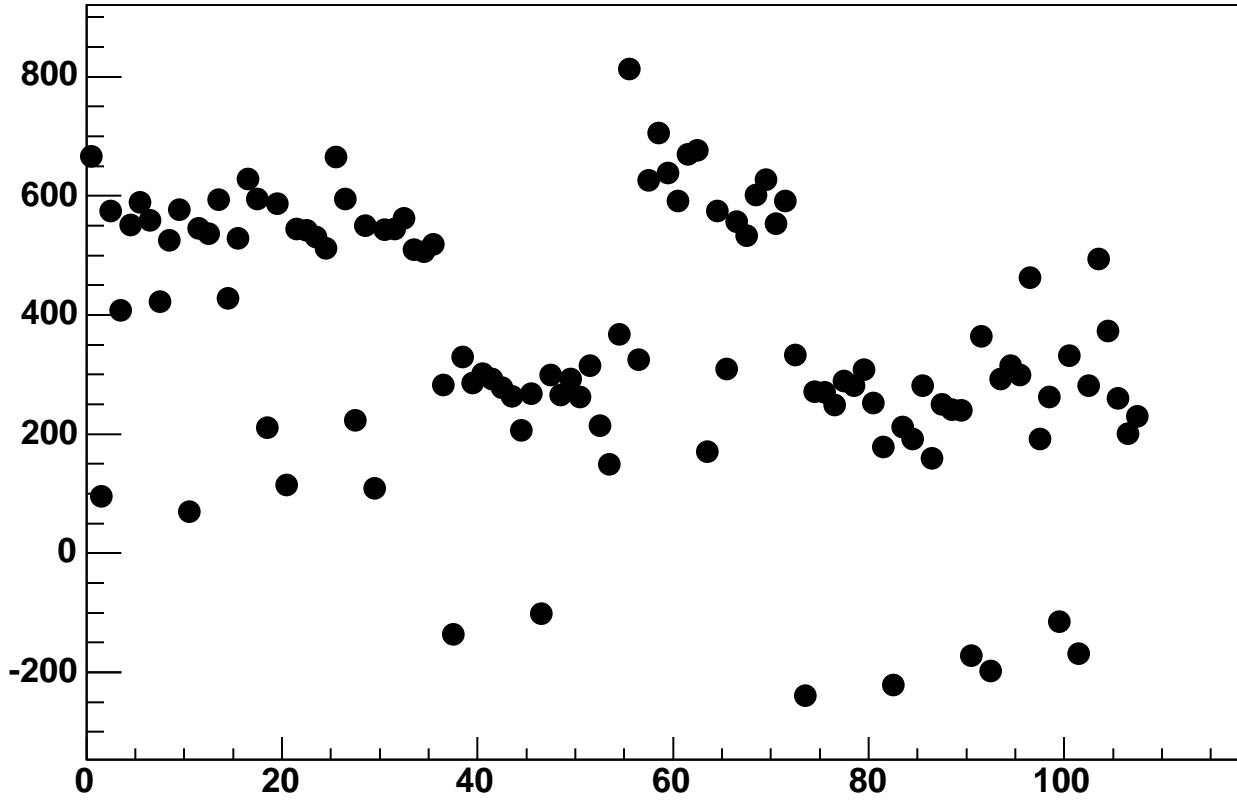
Enable 3, DAC=4000, Hold=185, ADC Mean vs 18\*Chip+Chan



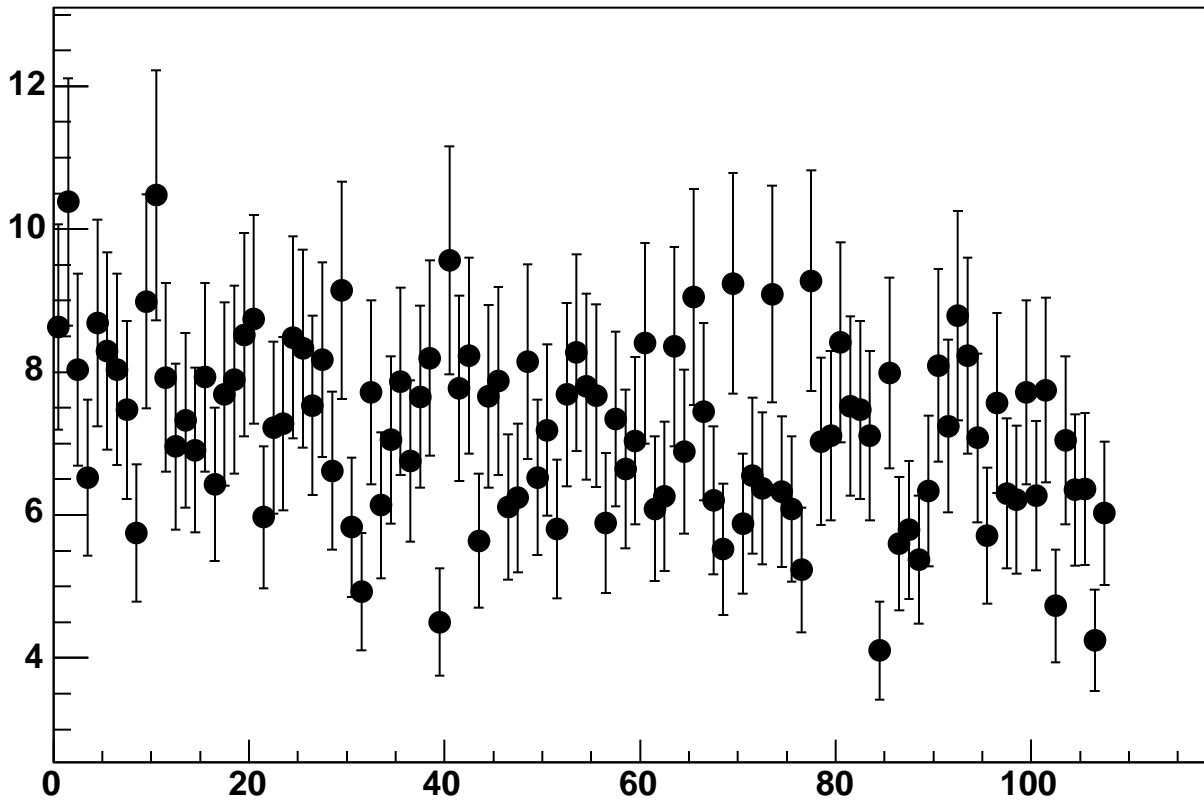
Enable 3, DAC=4000, Hold=185, ADC Noise vs 18\*Chip+Chan



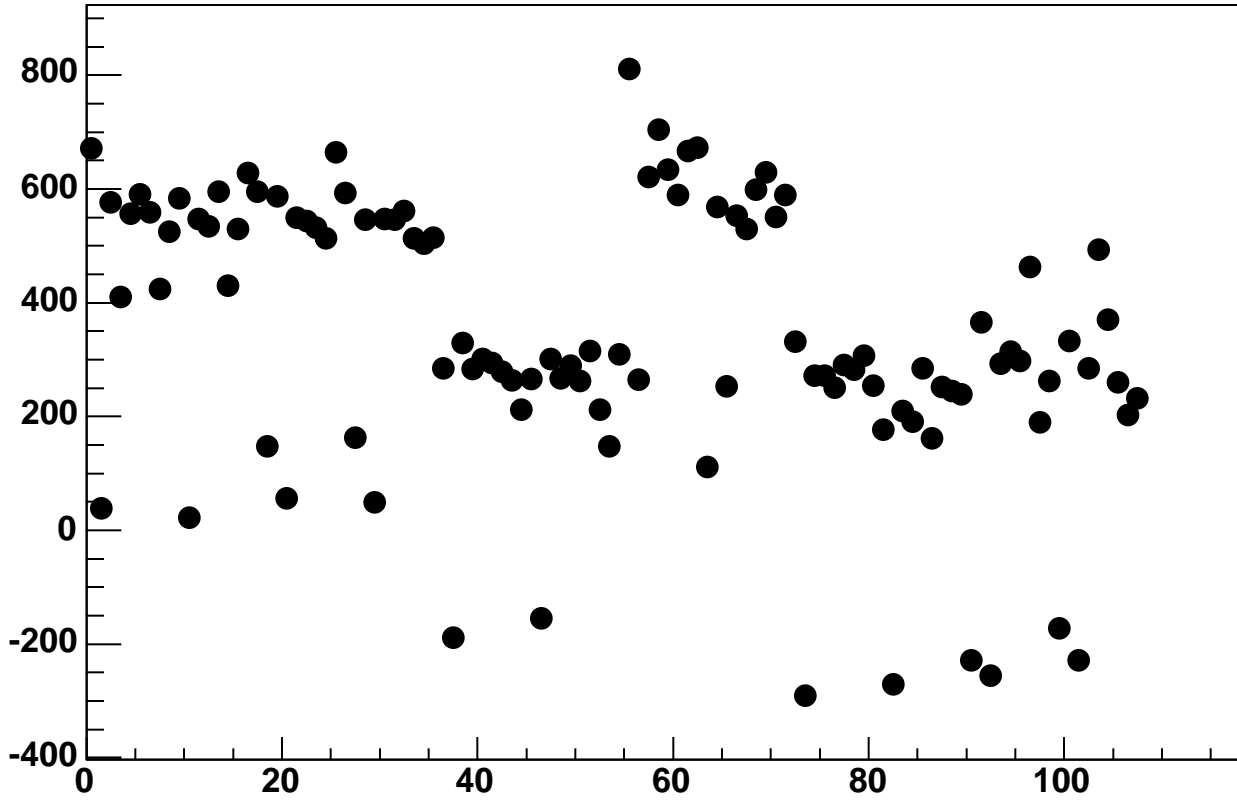
Enable 3, DAC=4000, Hold=190, ADC Mean vs 18\*Chip+Chan



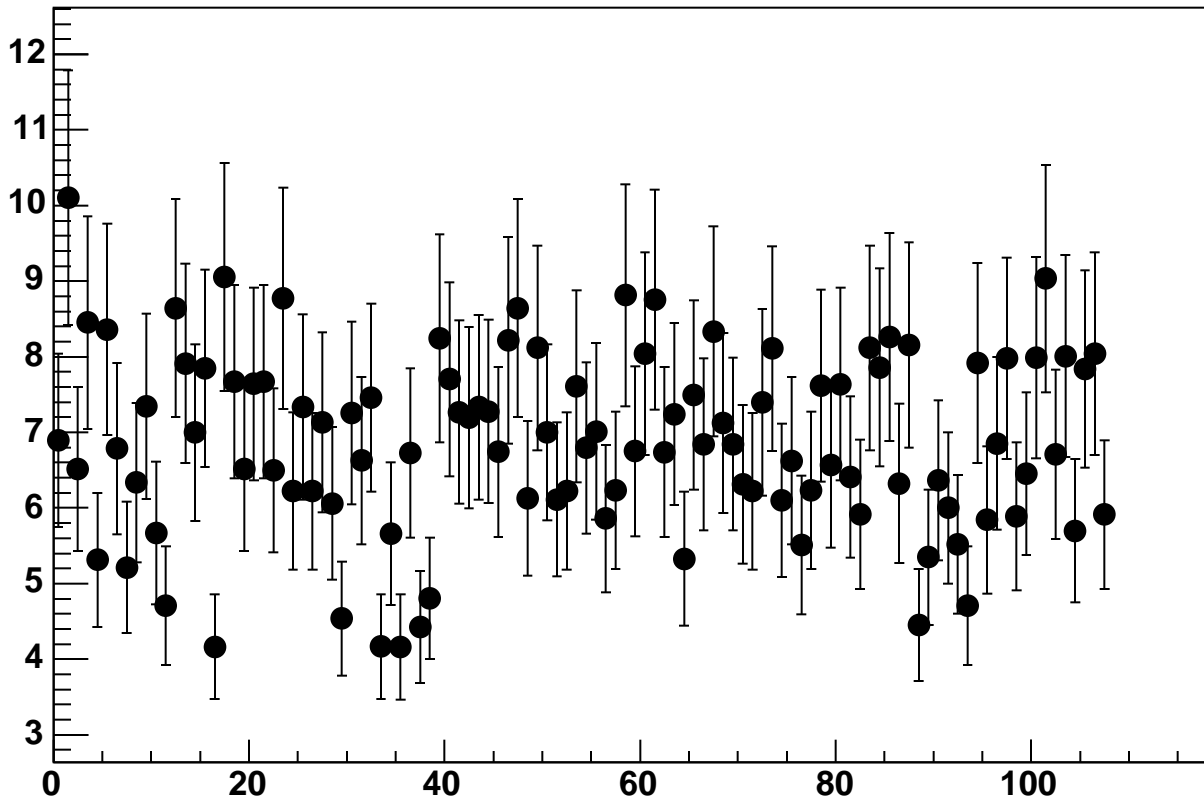
Enable 3, DAC=4000, Hold=190, ADC Noise vs 18\*Chip+Chan



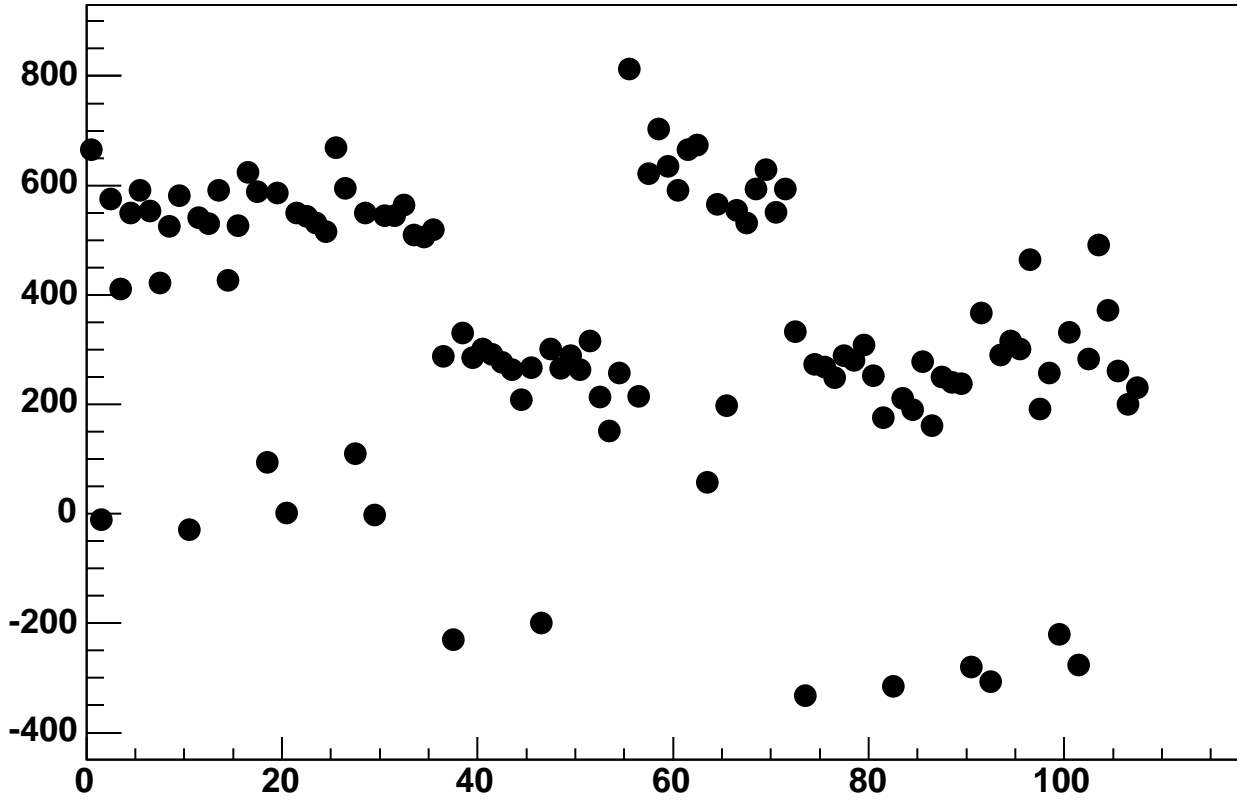
Enable 3, DAC=4000, Hold=195, ADC Mean vs 18\*Chip+Chan



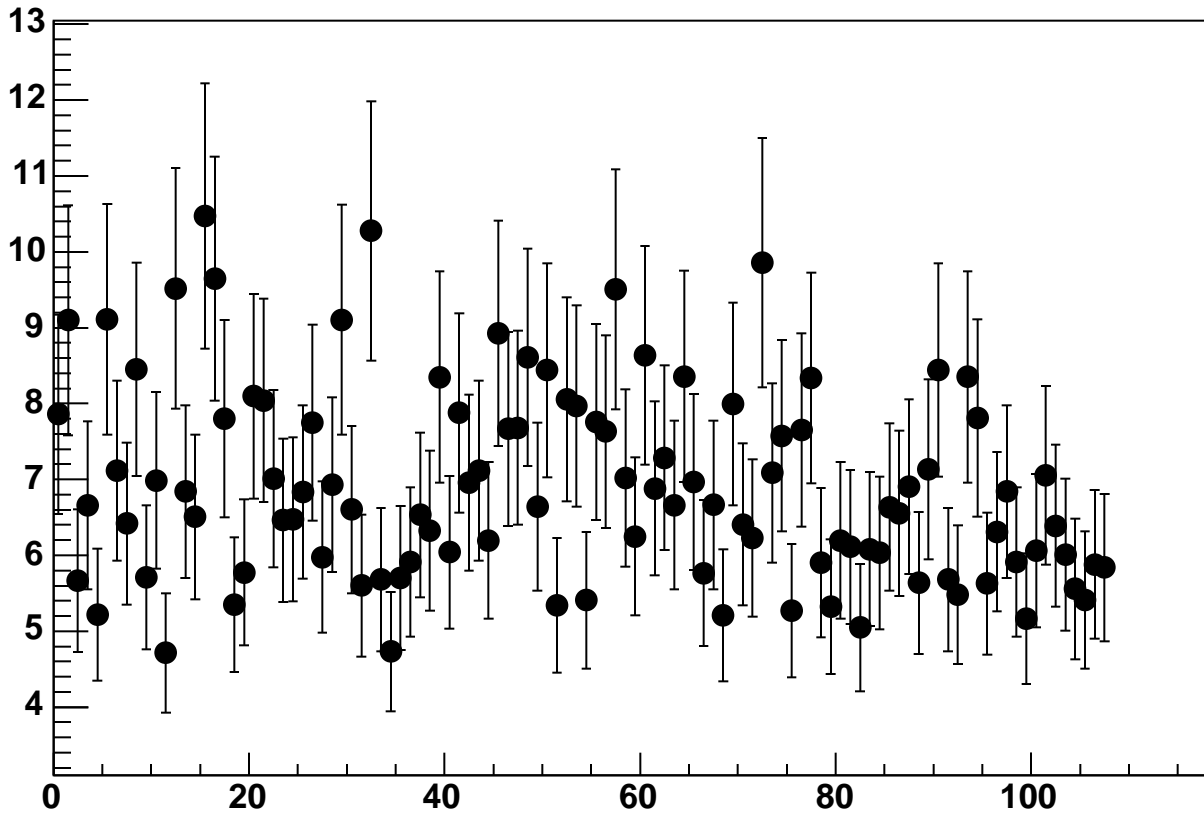
Enable 3, DAC=4000, Hold=195, ADC Noise vs 18\*Chip+Chan



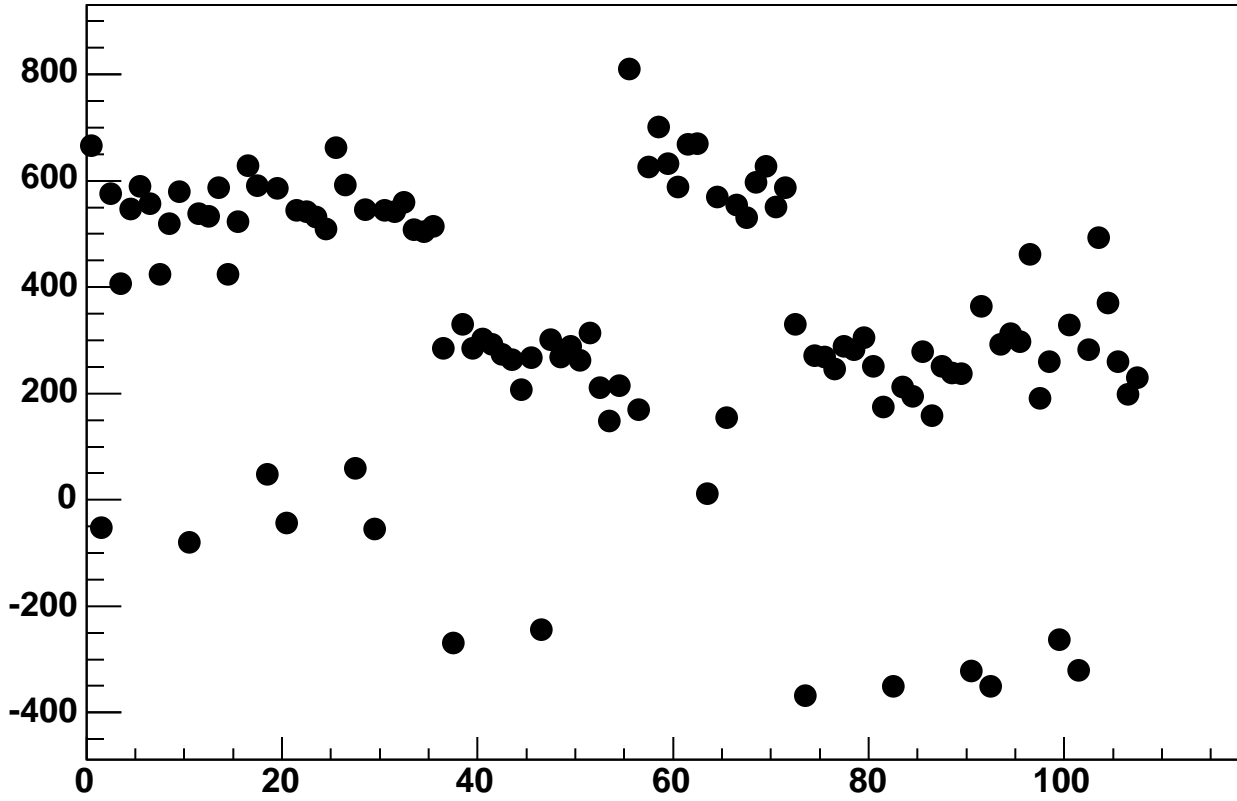
Enable 3, DAC=4000, Hold=200, ADC Mean vs 18\*Chip+Chan



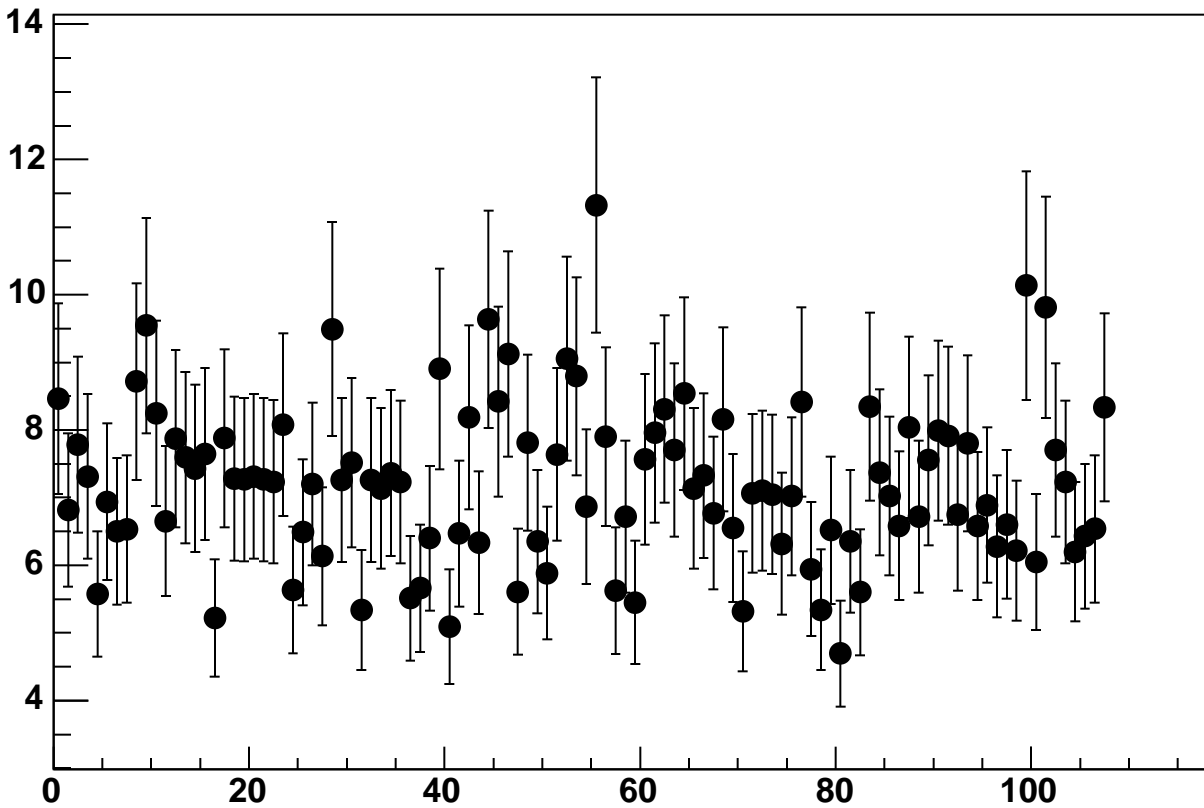
Enable 3, DAC=4000, Hold=200, ADC Noise vs 18\*Chip+Chan



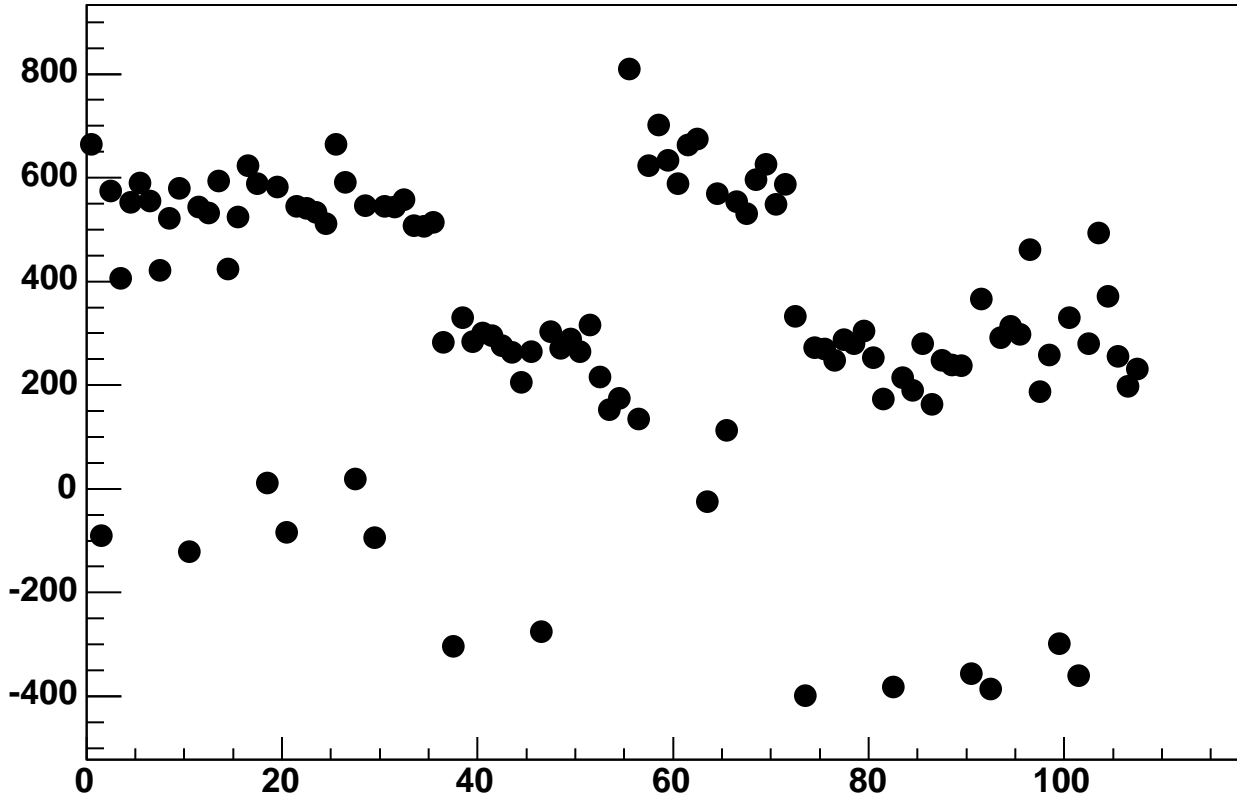
Enable 3, DAC=4000, Hold=205, ADC Mean vs 18\*Chip+Chan



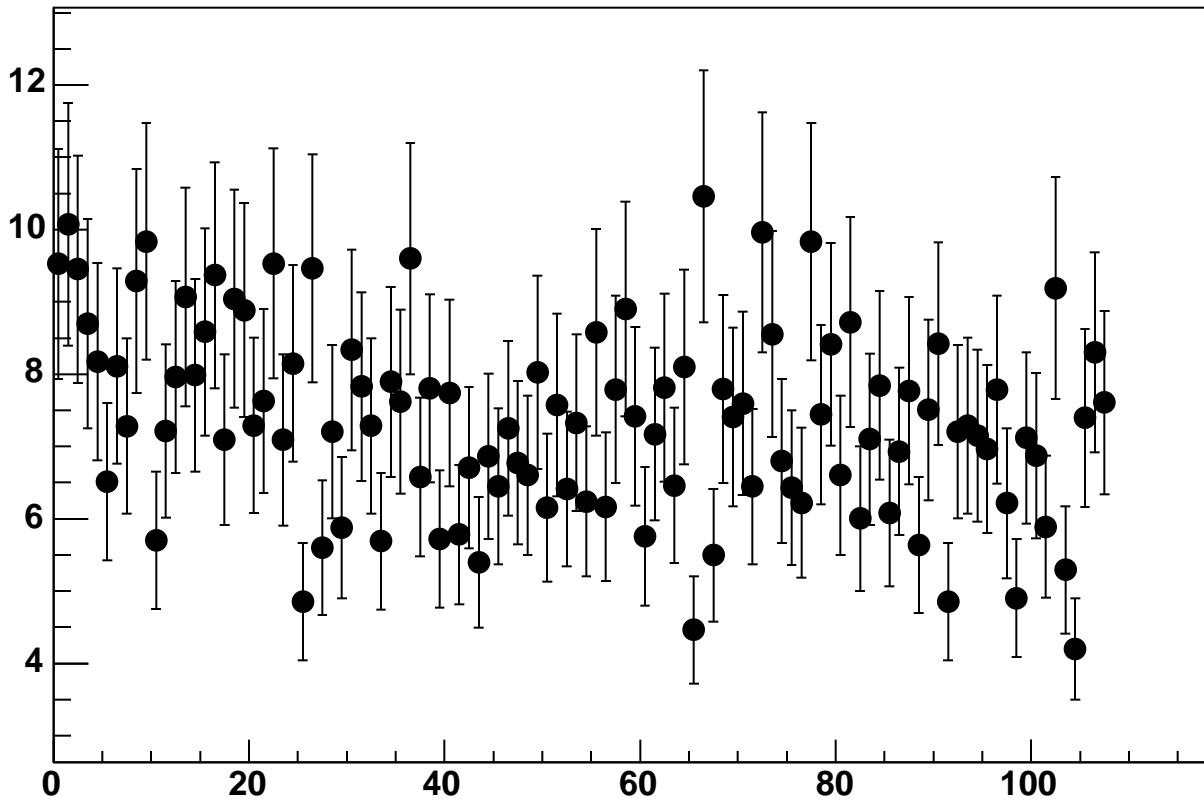
Enable 3, DAC=4000, Hold=205, ADC Noise vs 18\*Chip+Chan



Enable 3, DAC=4000, Hold=210, ADC Mean vs 18\*Chip+Chan

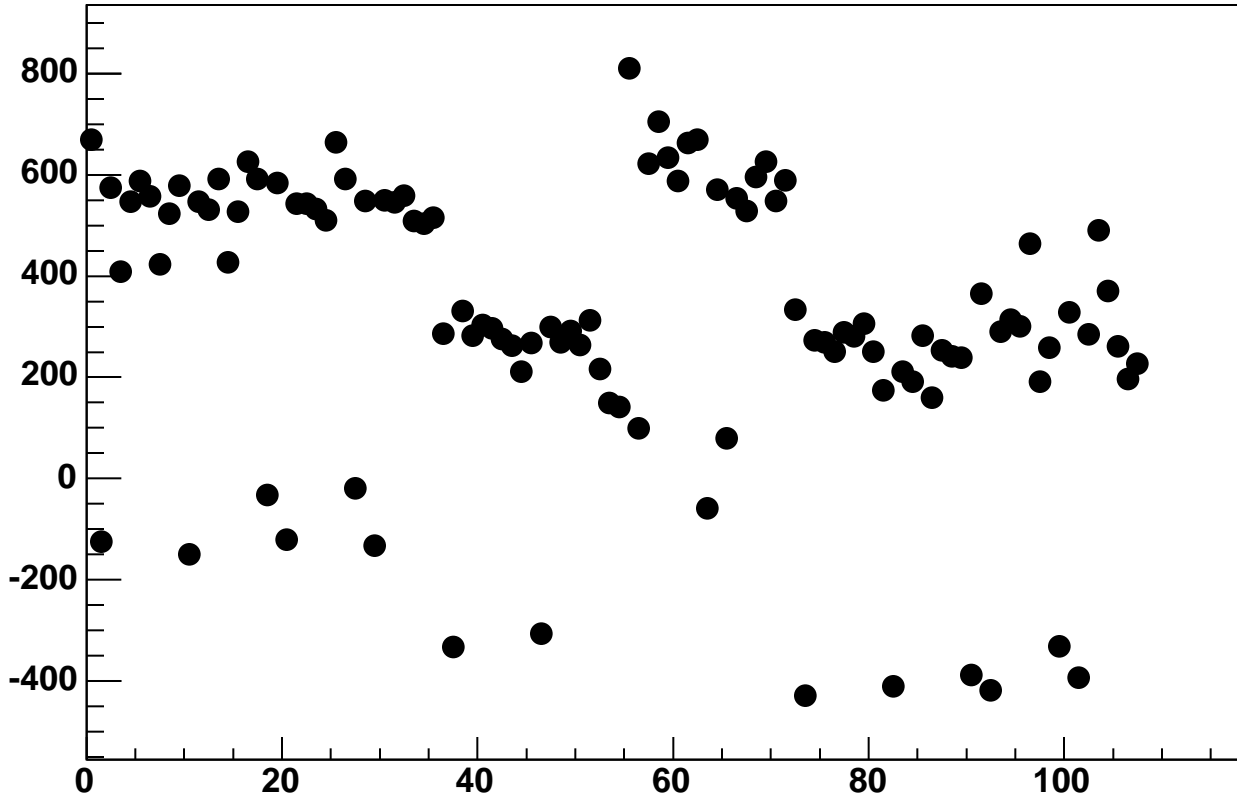


Enable 3, DAC=4000, Hold=210, ADC Noise vs 18\*Chip+Chan

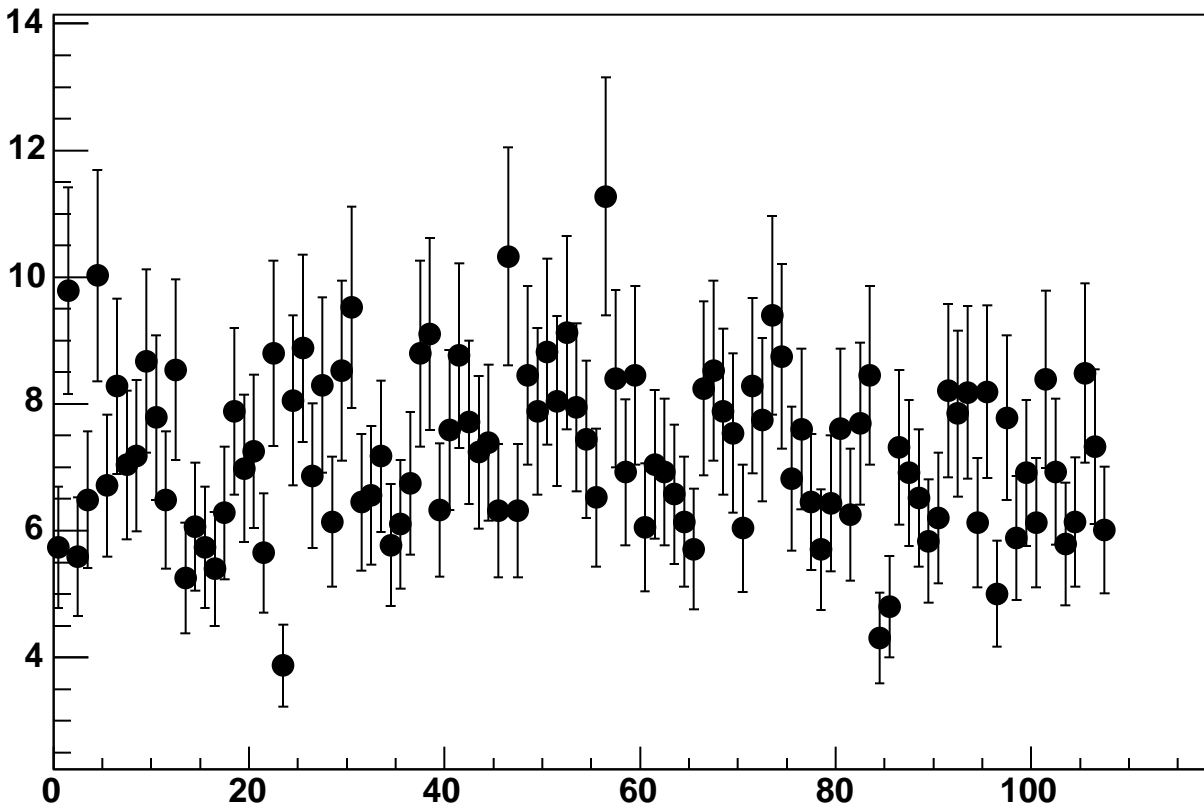




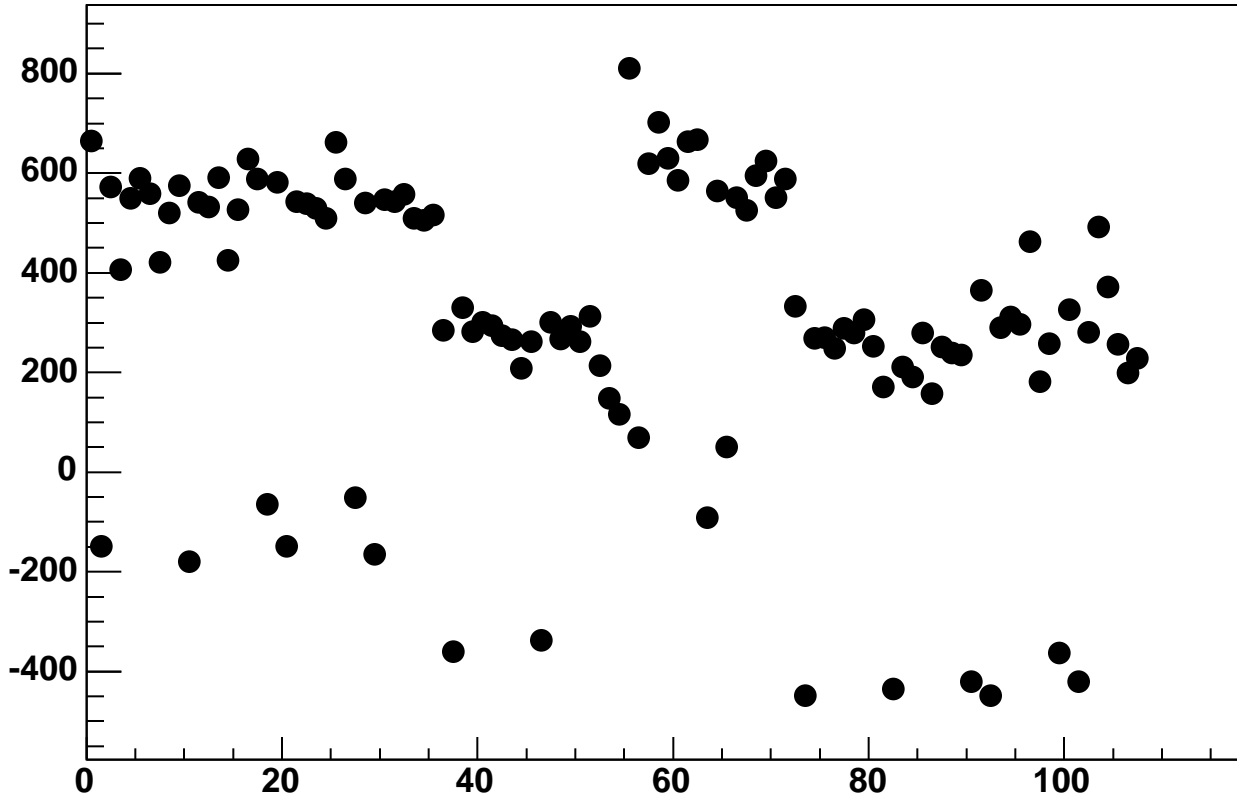
Enable 3, DAC=4000, Hold=215, ADC Mean vs 18\*Chip+Chan



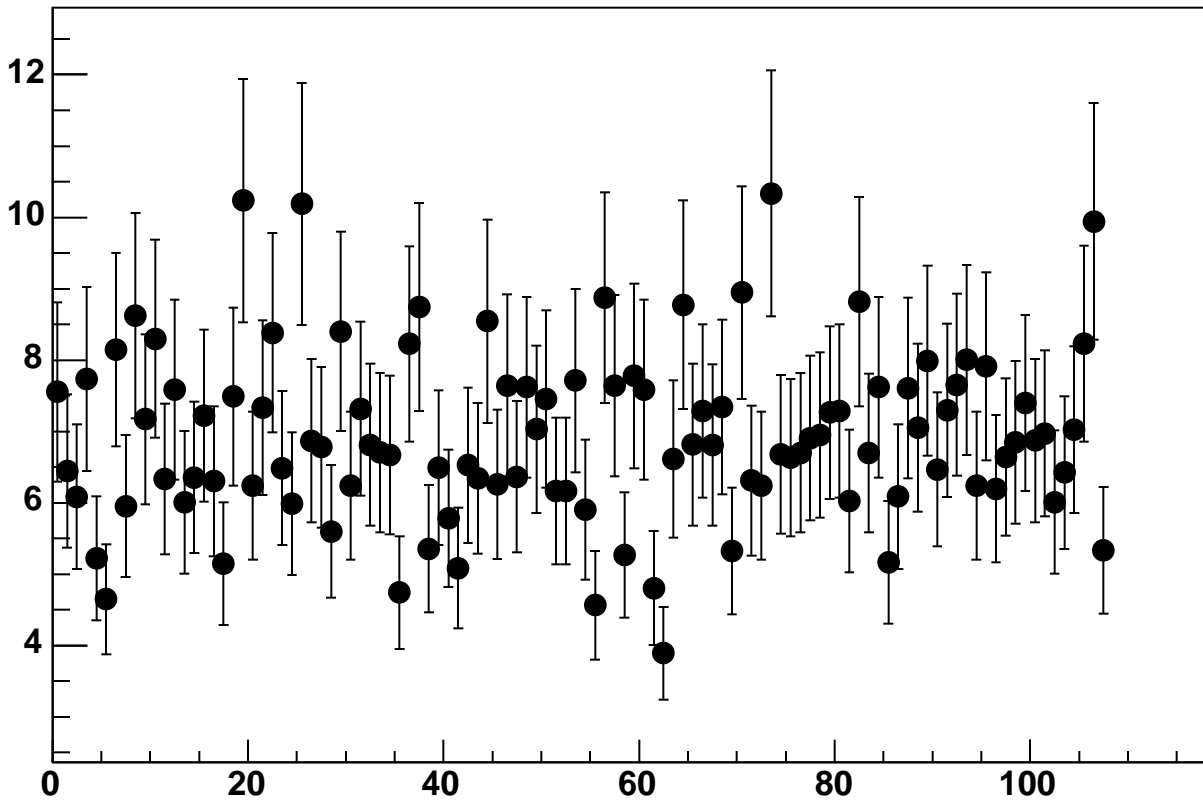
Enable 3, DAC=4000, Hold=215, ADC Noise vs 18\*Chip+Chan



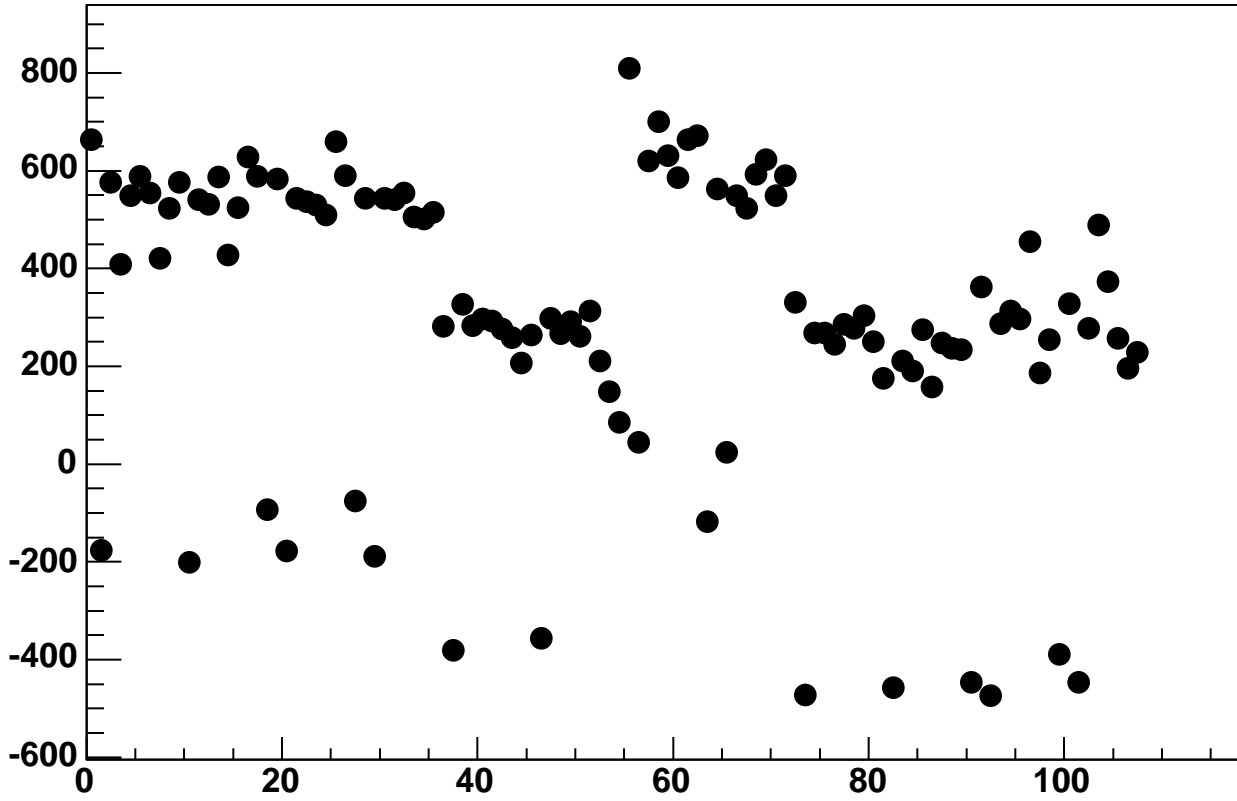
Enable 3, DAC=4000, Hold=220, ADC Mean vs 18\*Chip+Chan



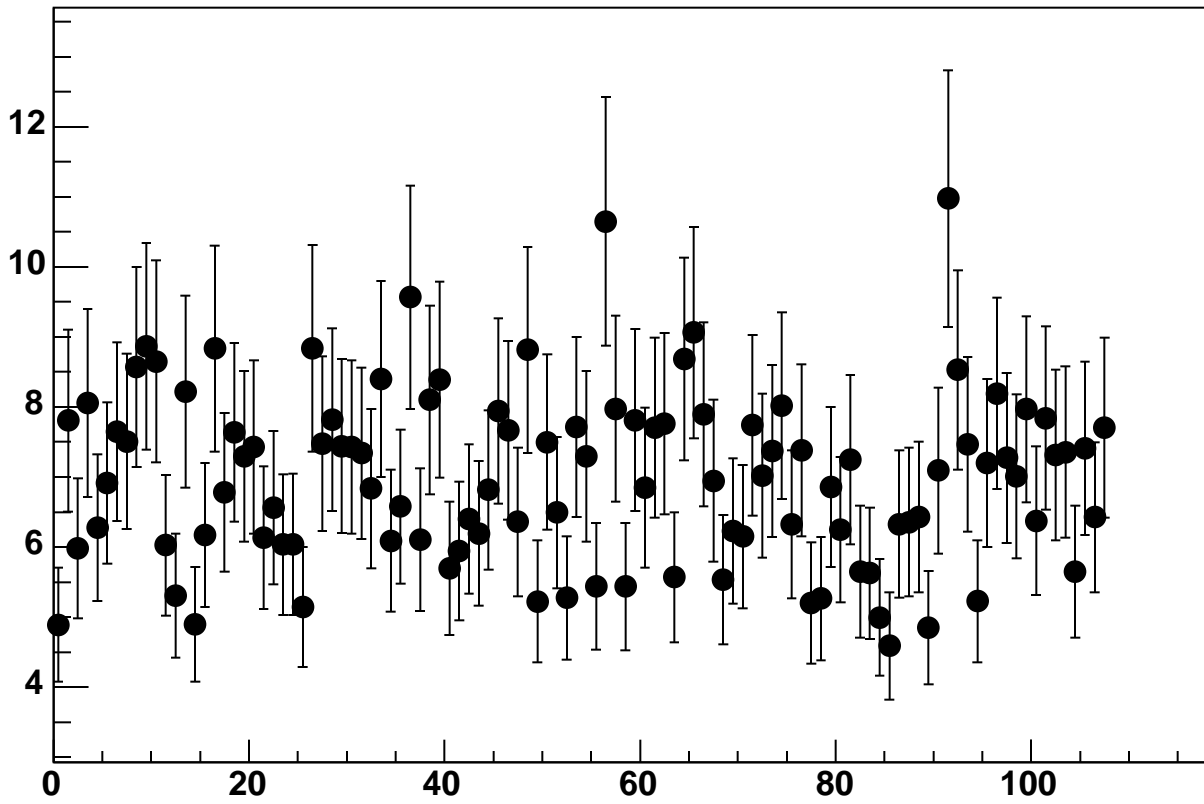
Enable 3, DAC=4000, Hold=220, ADC Noise vs 18\*Chip+Chan



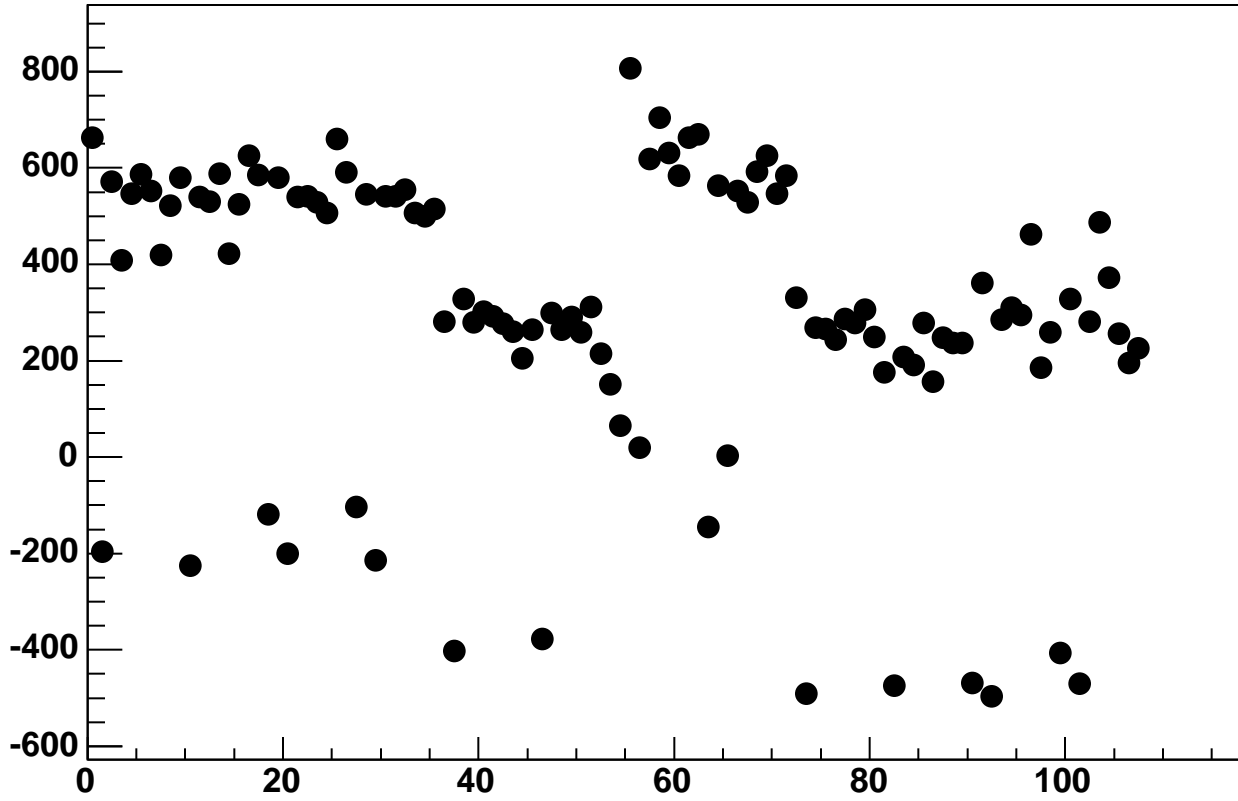
Enable 3, DAC=4000, Hold=225, ADC Mean vs 18\*Chip+Chan



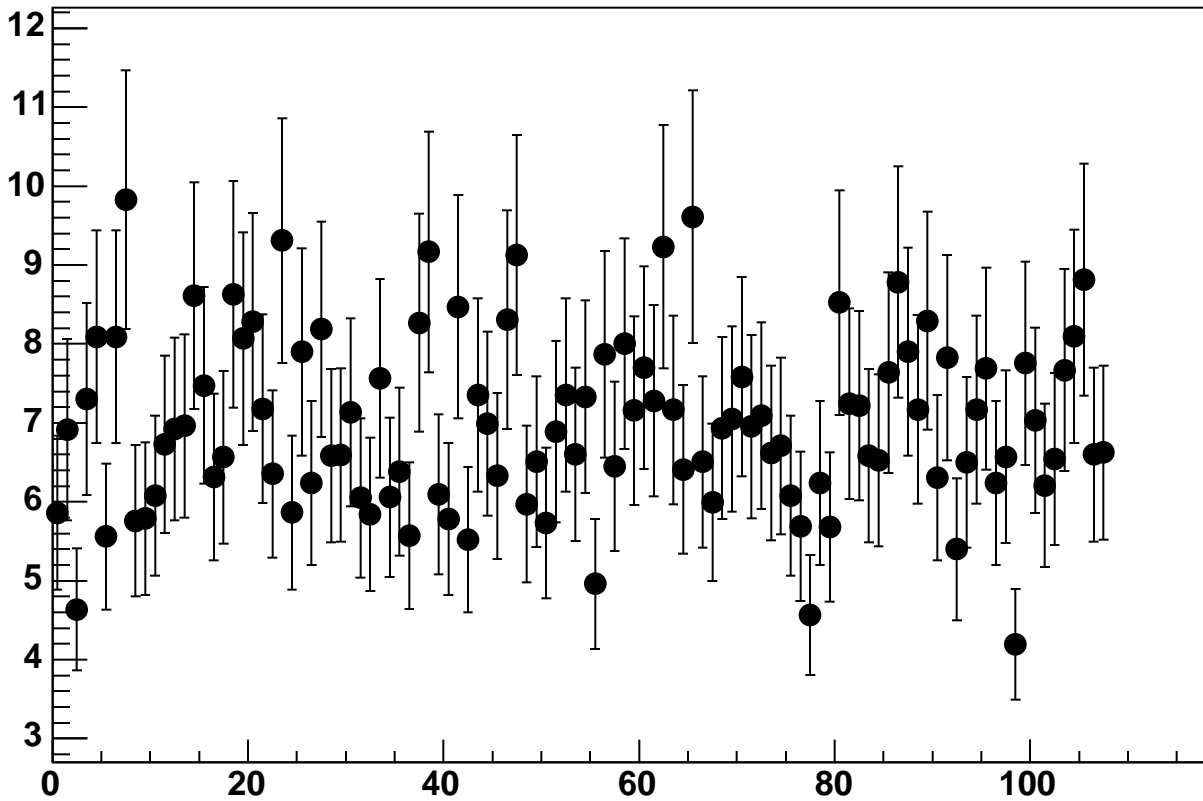
Enable 3, DAC=4000, Hold=225, ADC Noise vs 18\*Chip+Chan



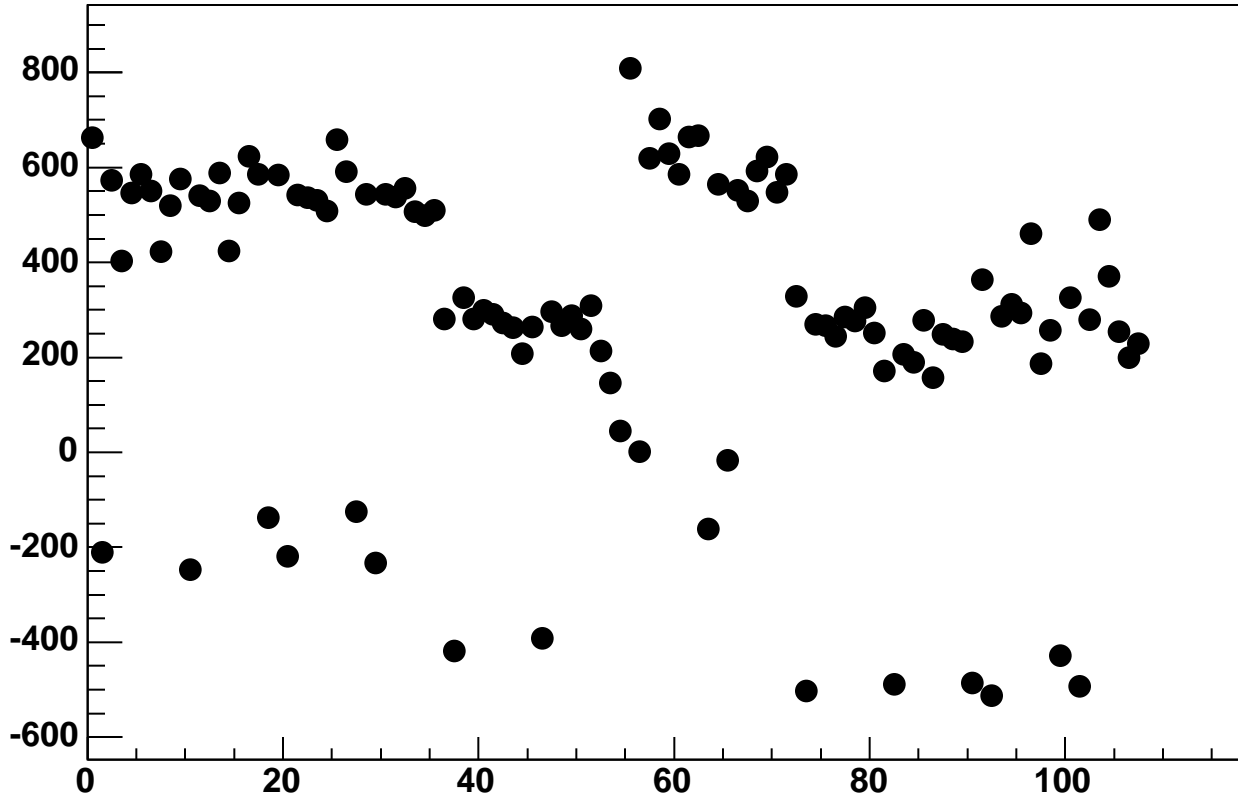
Enable 3, DAC=4000, Hold=230, ADC Mean vs 18\*Chip+Chan



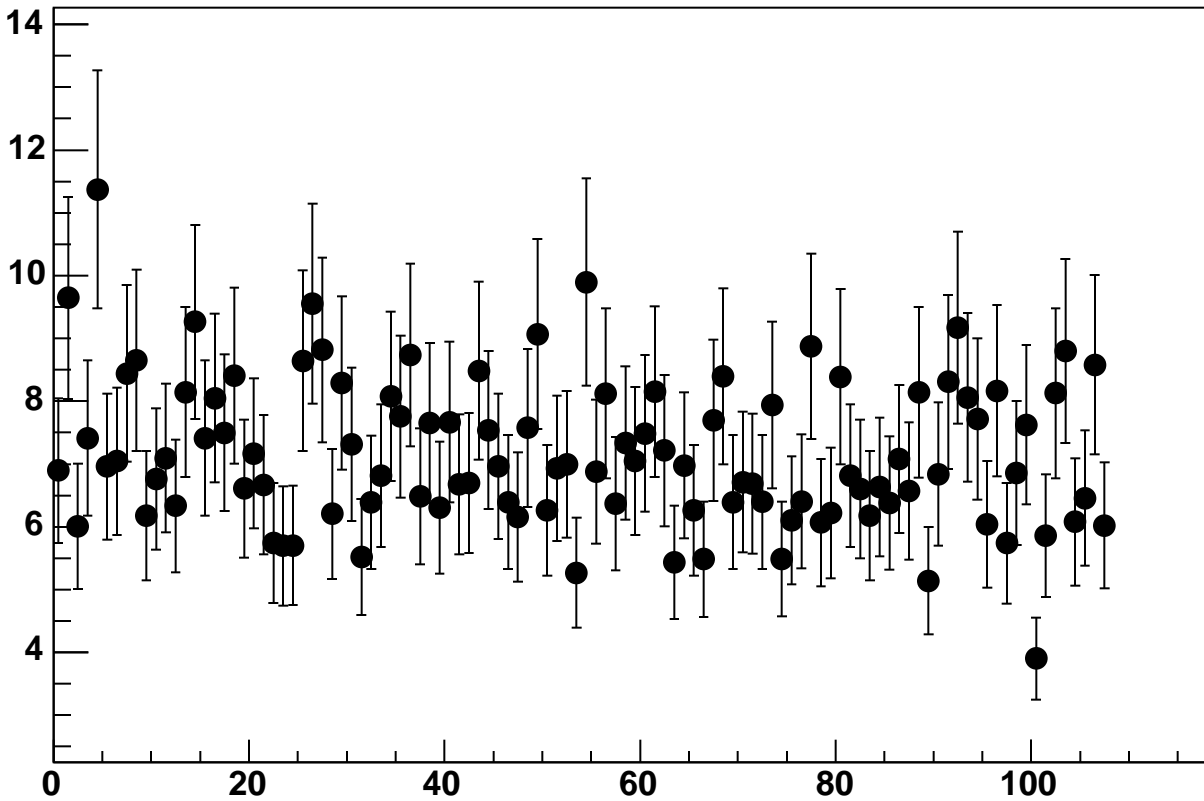
Enable 3, DAC=4000, Hold=230, ADC Noise vs 18\*Chip+Chan



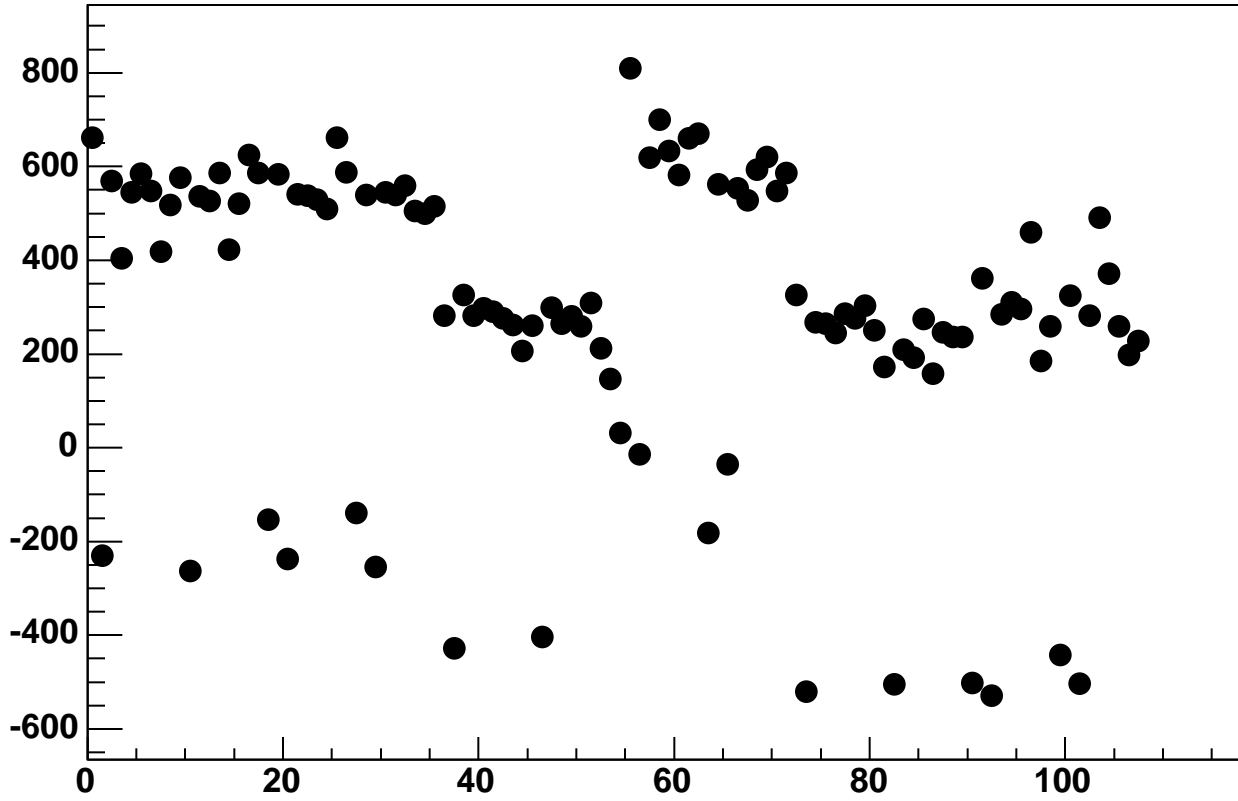
Enable 3, DAC=4000, Hold=235, ADC Mean vs 18\*Chip+Chan



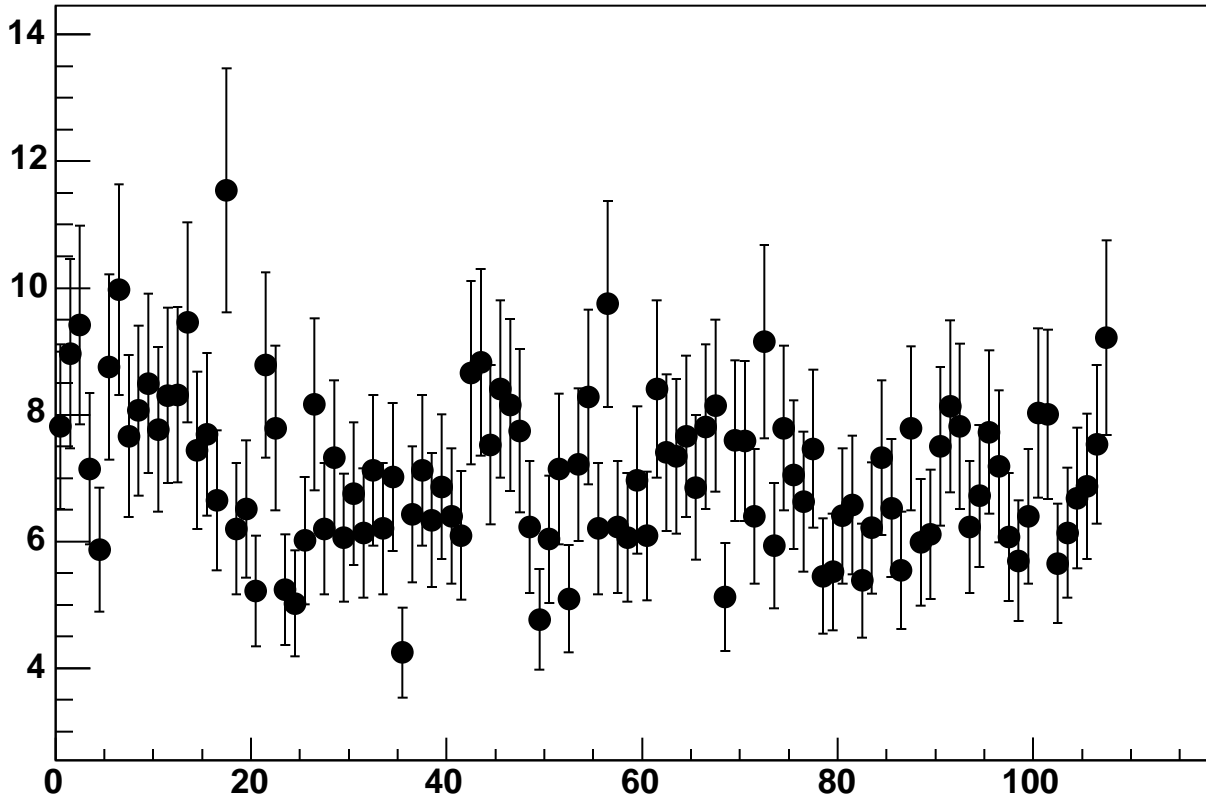
Enable 3, DAC=4000, Hold=235, ADC Noise vs 18\*Chip+Chan



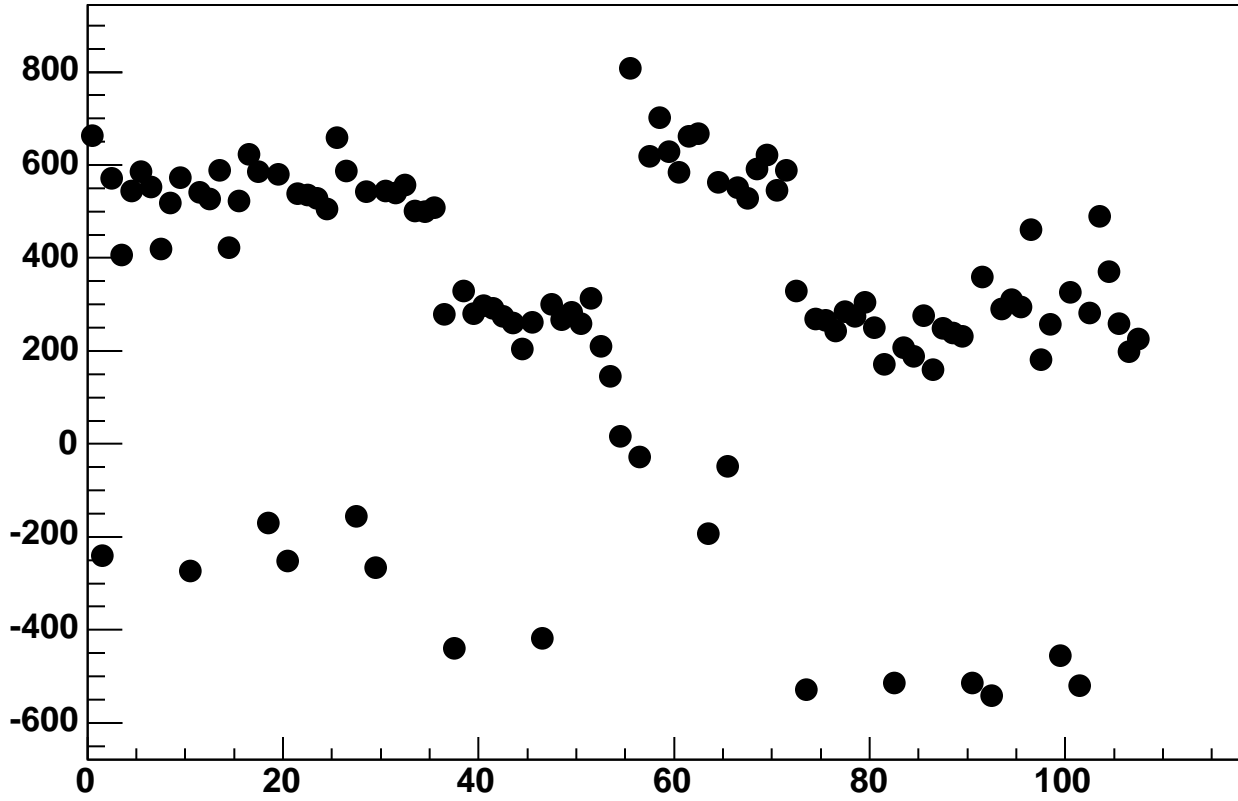
Enable 3, DAC=4000, Hold=240, ADC Mean vs 18\*Chip+Chan



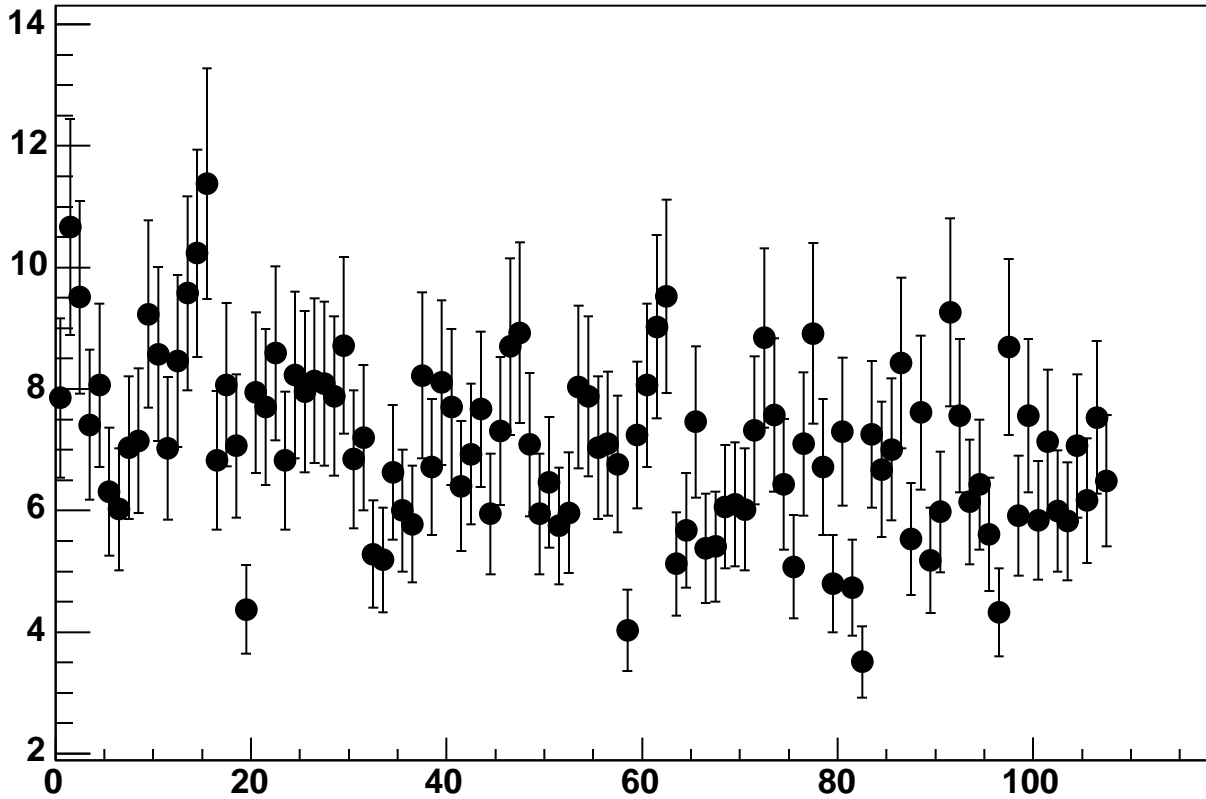
Enable 3, DAC=4000, Hold=240, ADC Noise vs 18\*Chip+Chan



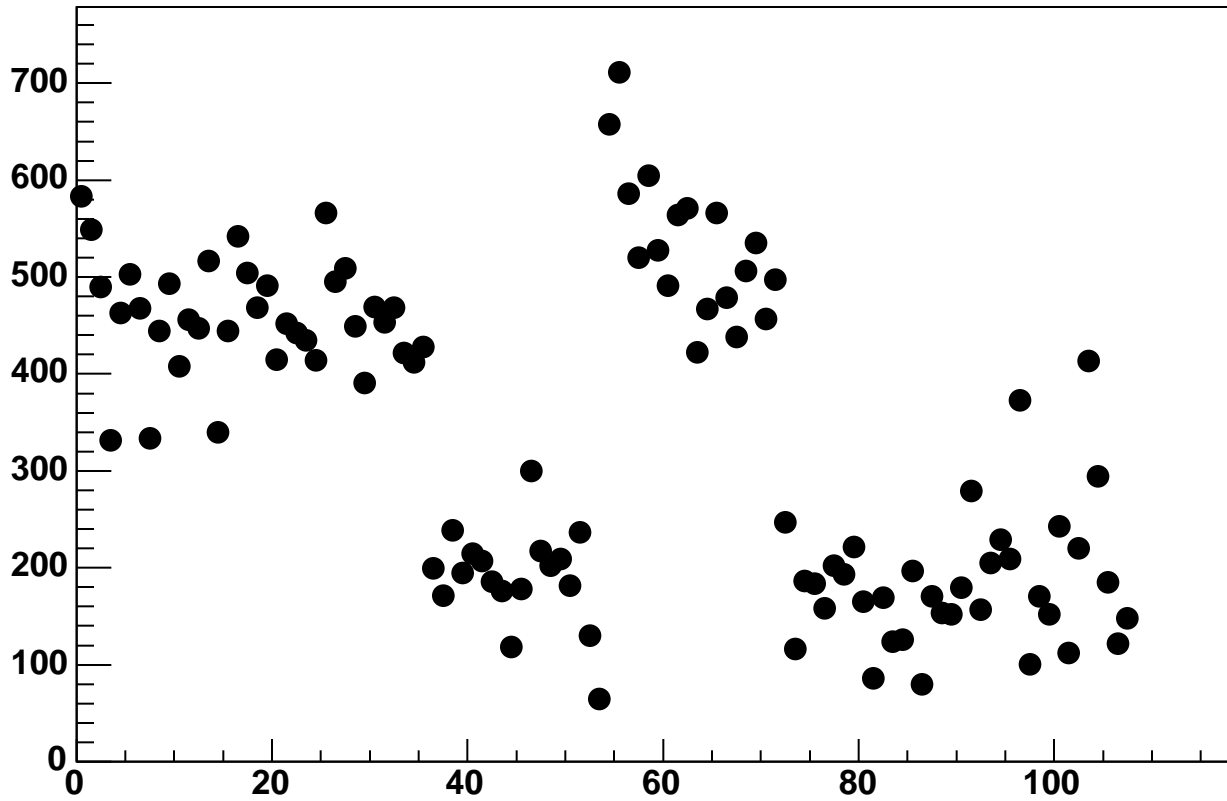
Enable 3, DAC=4000, Hold=245, ADC Mean vs 18\*Chip+Chan



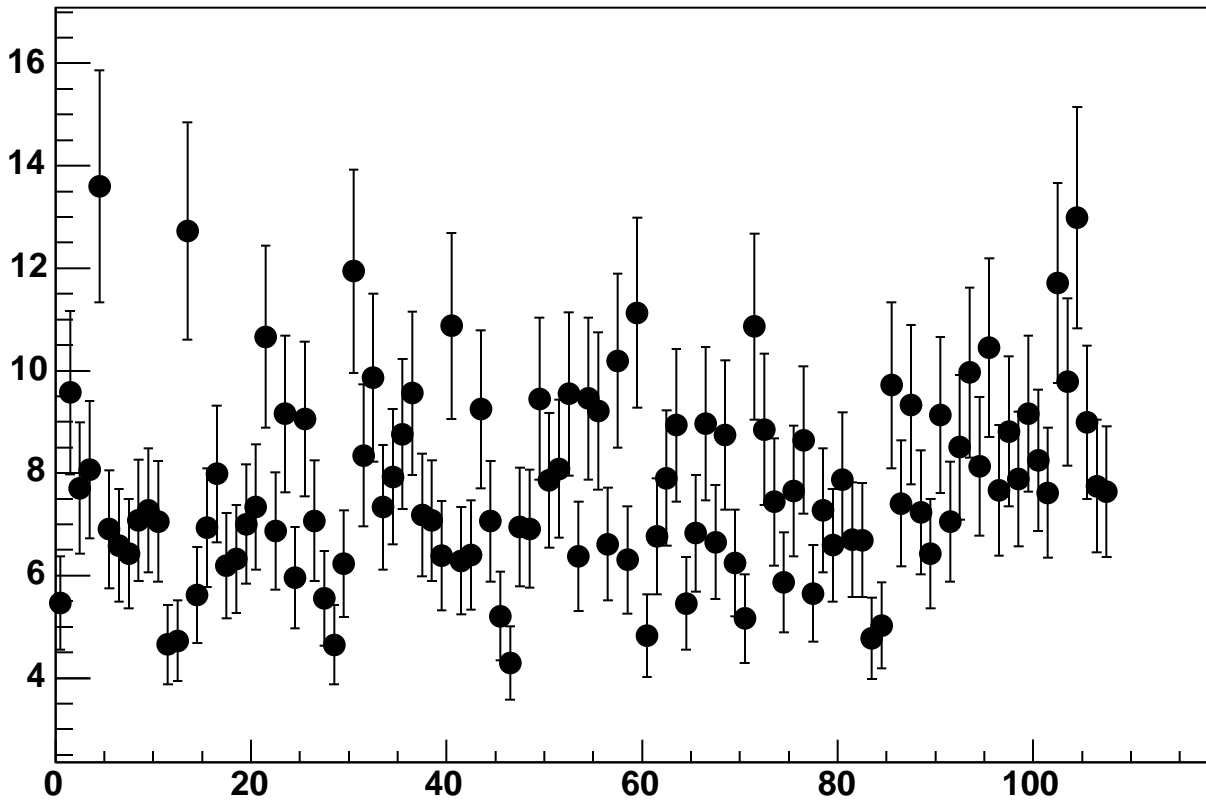
Enable 3, DAC=4000, Hold=245, ADC Noise vs 18\*Chip+Chan



Enable 4, DAC=4000, Hold=0, ADC Mean vs 18\*Chip+Chan

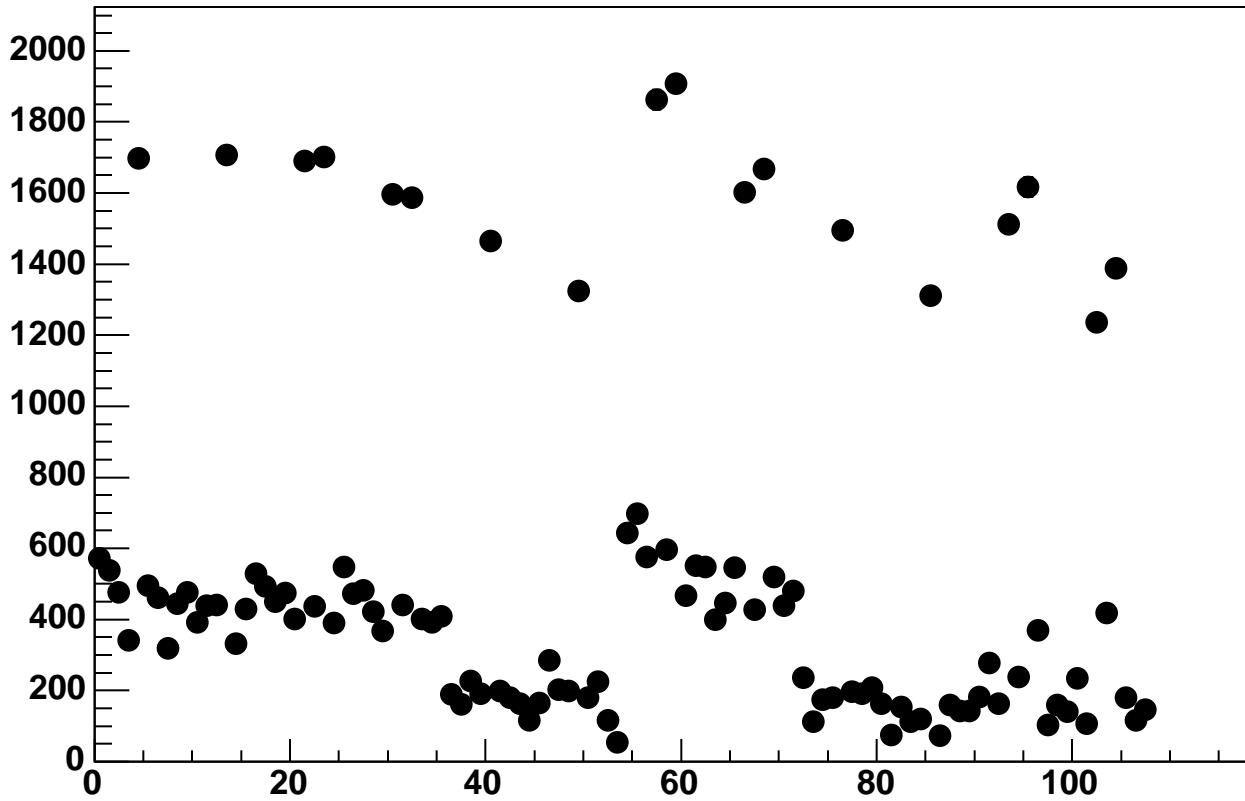


Enable 4, DAC=4000, Hold=0, ADC Noise vs 18\*Chip+Chan

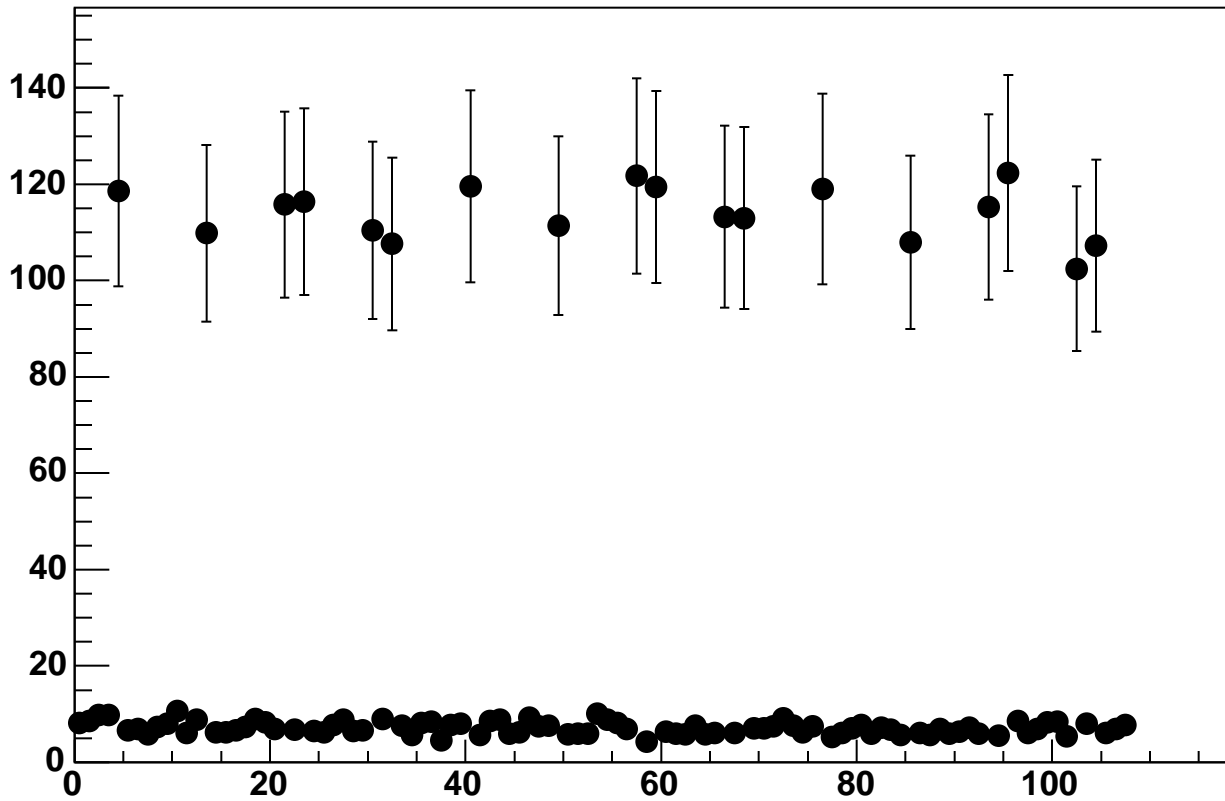




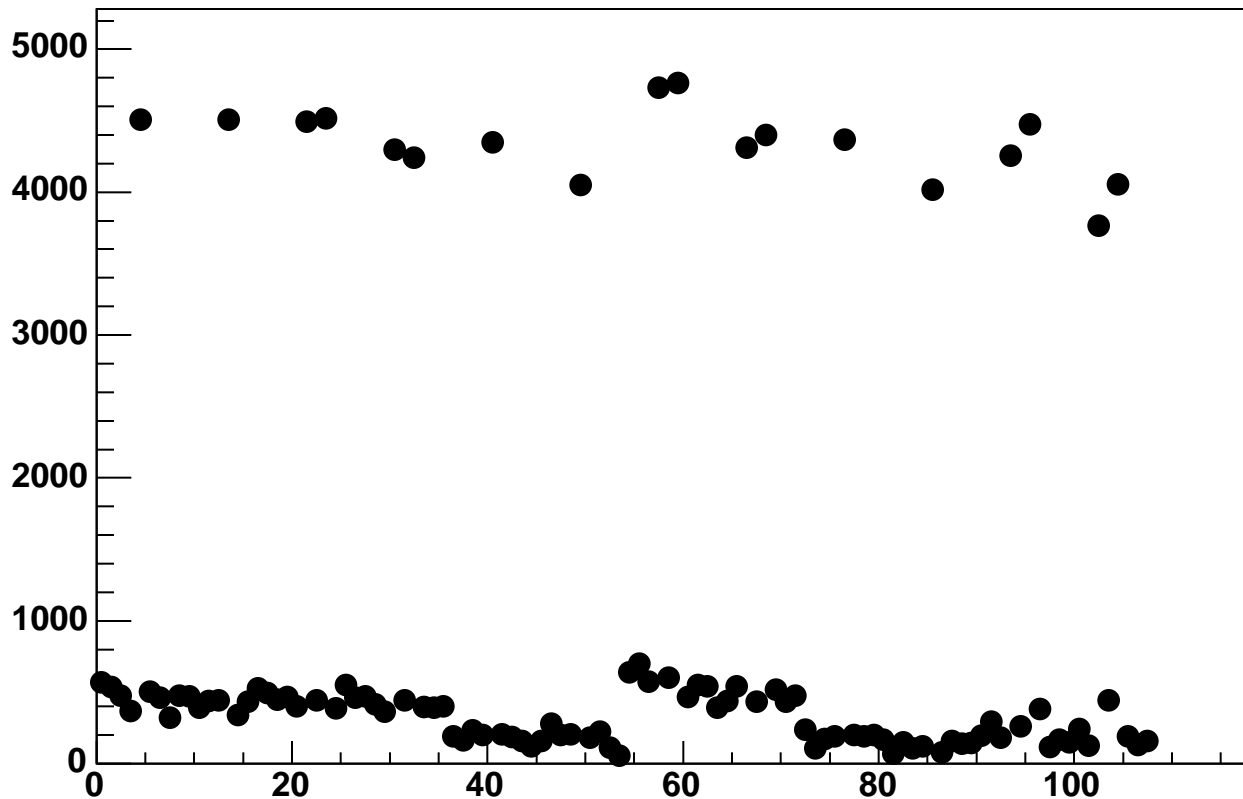
Enable 4, DAC=4000, Hold=5, ADC Mean vs 18\*Chip+Chan



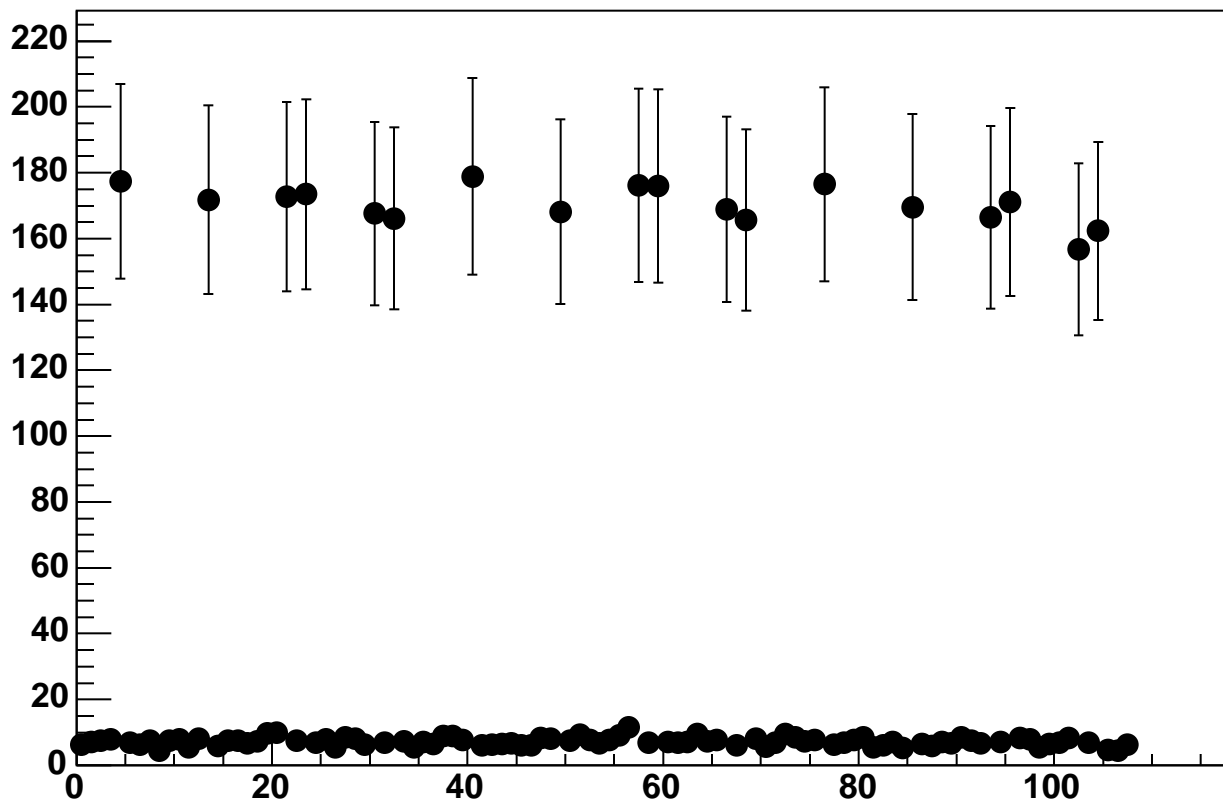
Enable 4, DAC=4000, Hold=5, ADC Noise vs 18\*Chip+Chan



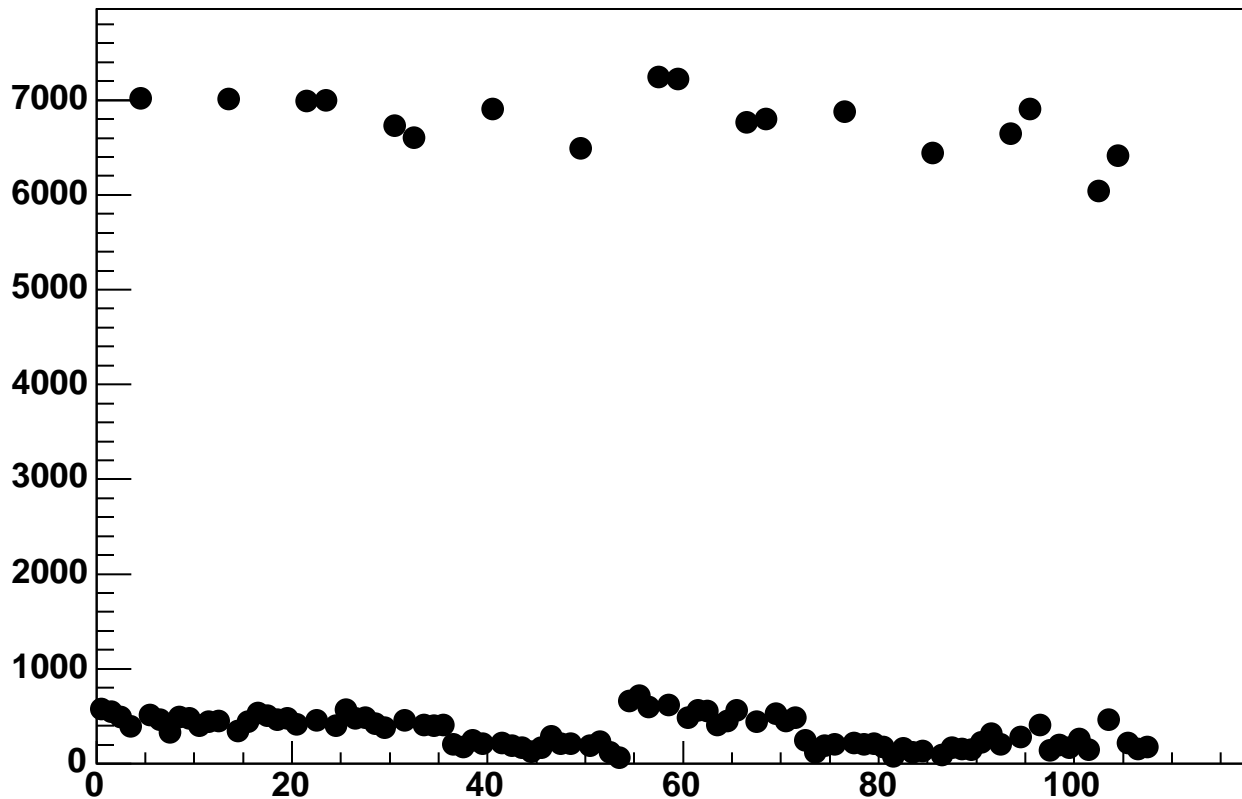
Enable 4, DAC=4000, Hold=10, ADC Mean vs 18\*Chip+Chan



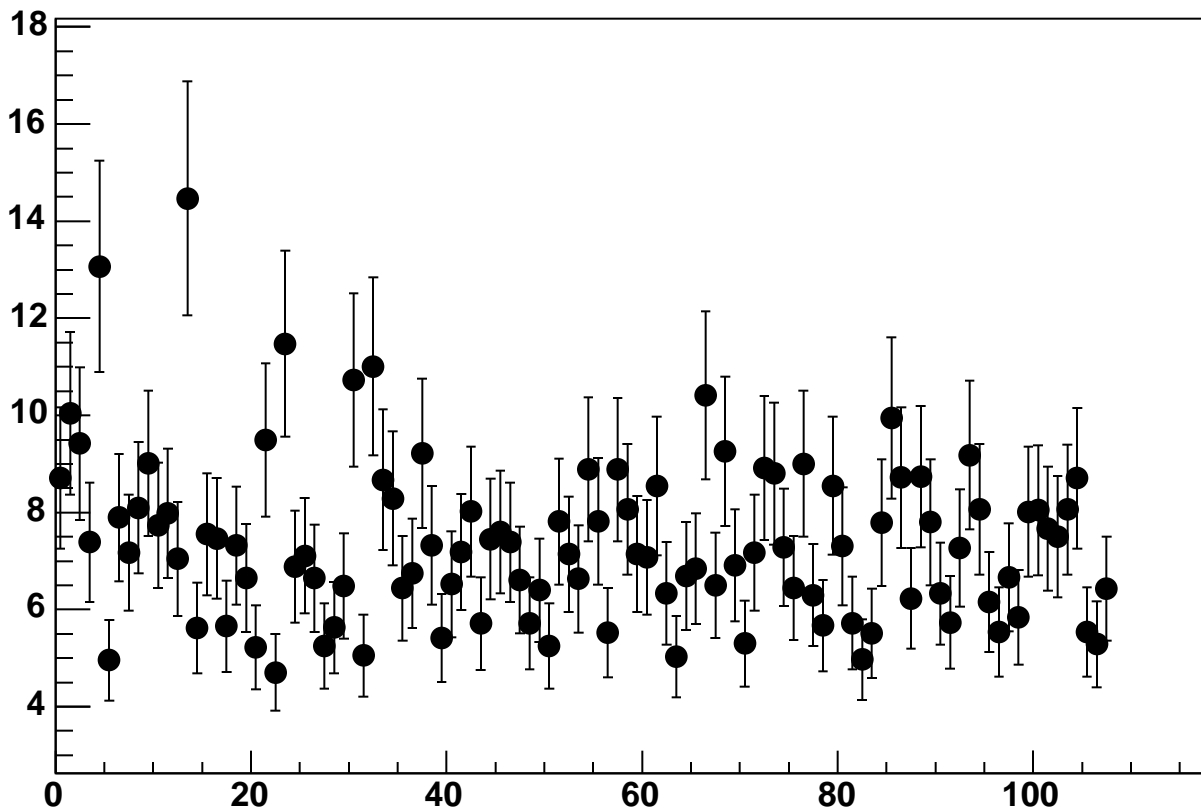
Enable 4, DAC=4000, Hold=10, ADC Noise vs 18\*Chip+Chan



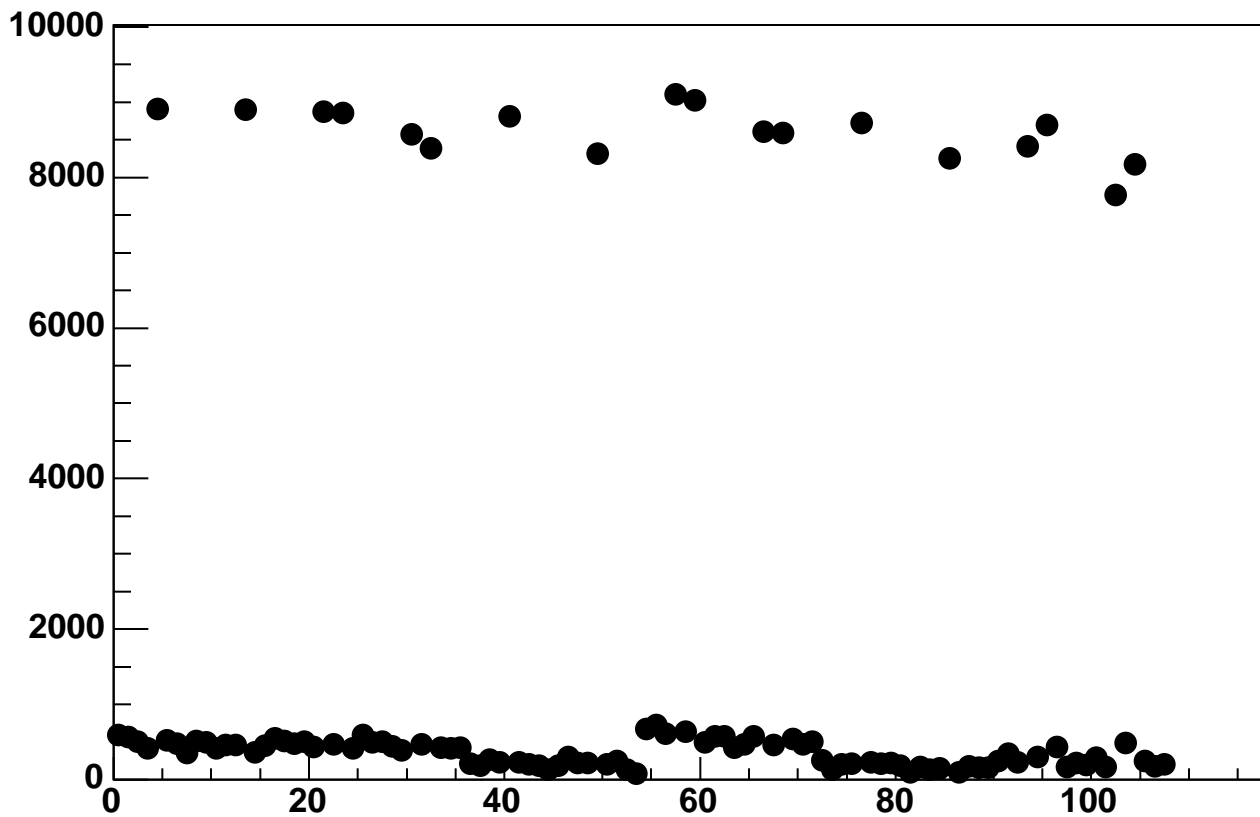
Enable 4, DAC=4000, Hold=15, ADC Mean vs 18\*Chip+Chan



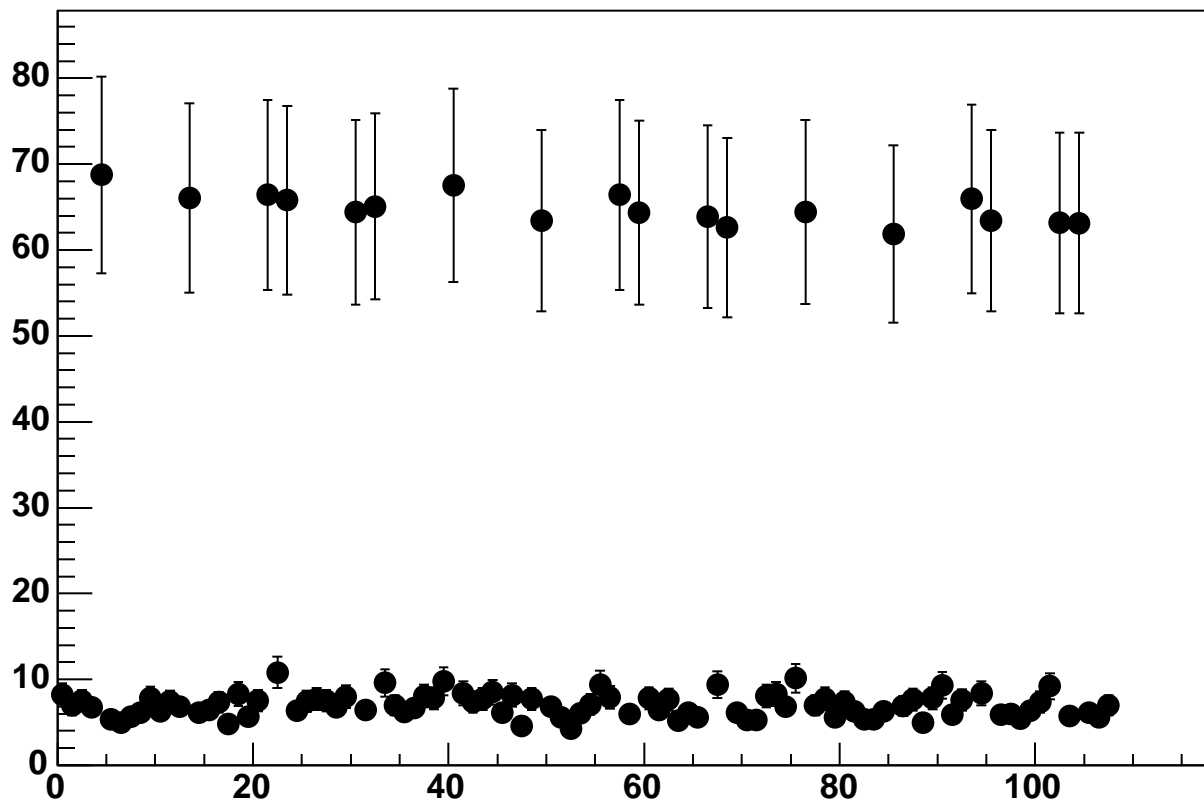
Enable 4, DAC=4000, Hold=15, ADC Noise vs 18\*Chip+Chan



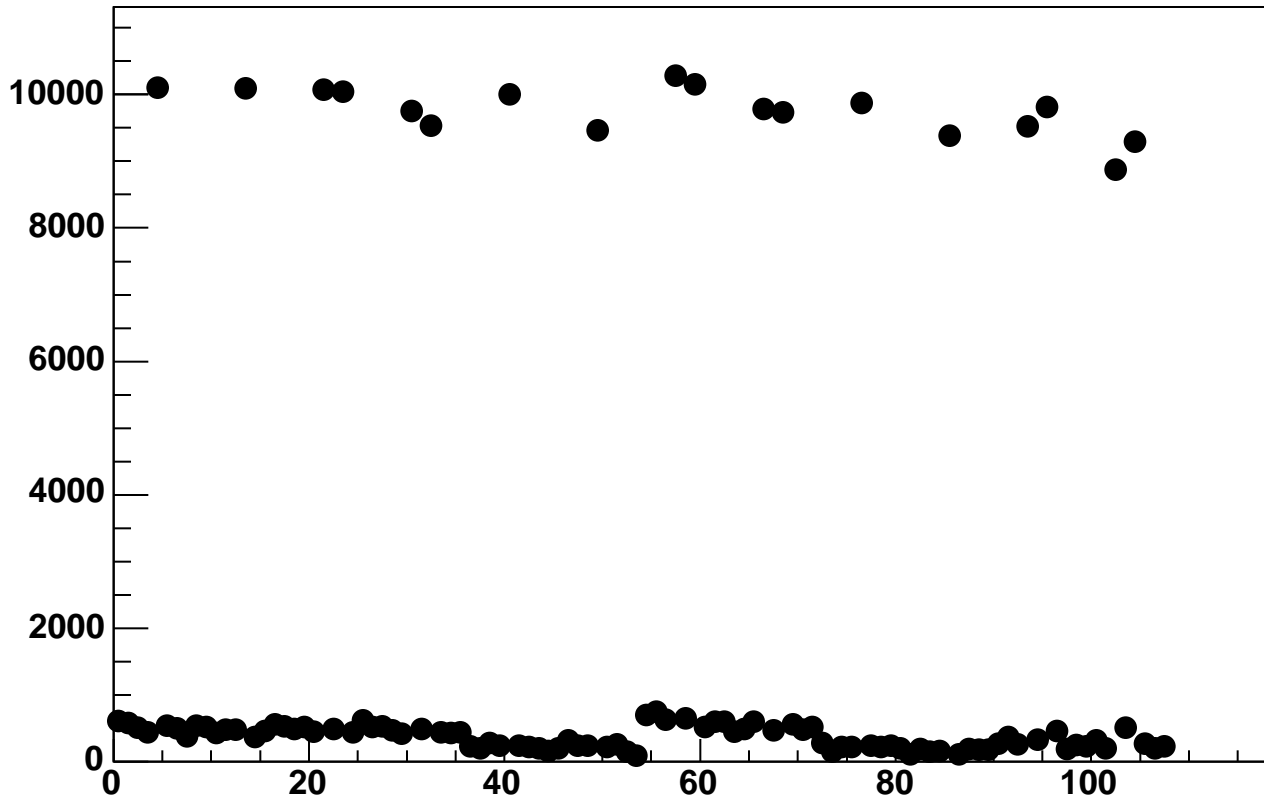
Enable 4, DAC=4000, Hold=20, ADC Mean vs 18\*Chip+Chan



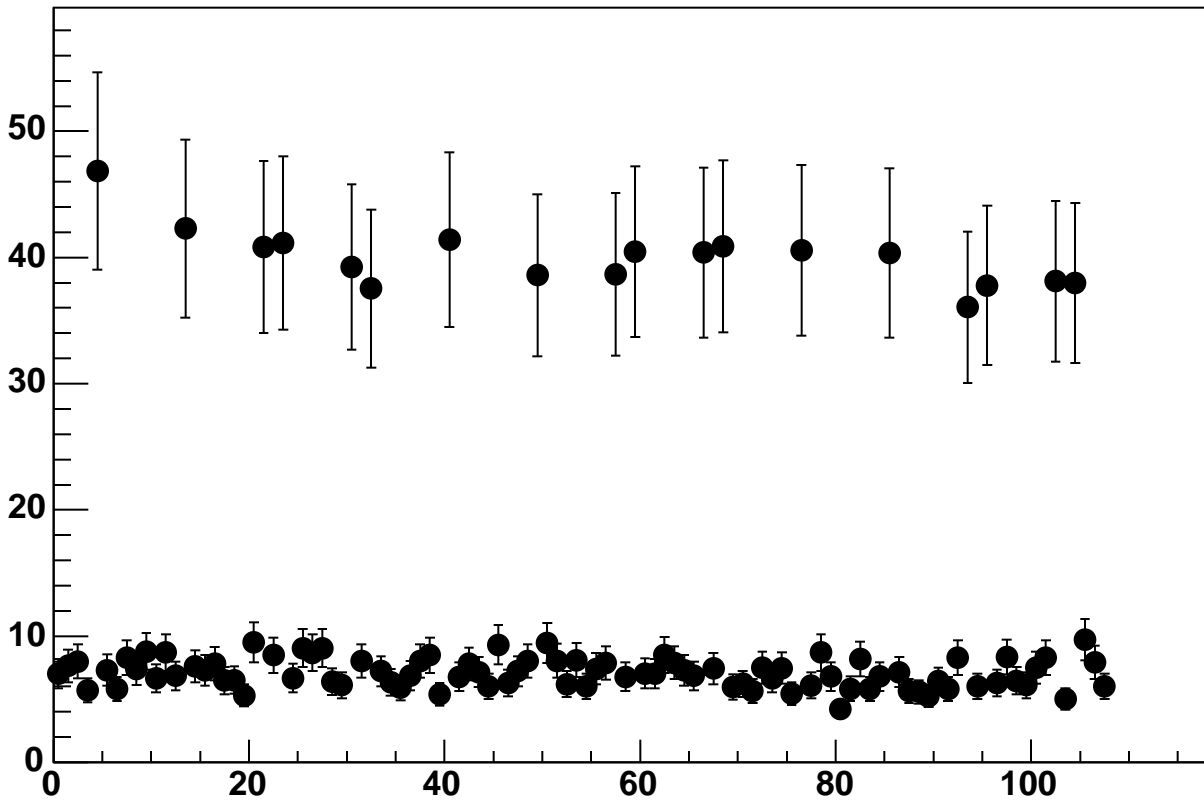
Enable 4, DAC=4000, Hold=20, ADC Noise vs 18\*Chip+Chan



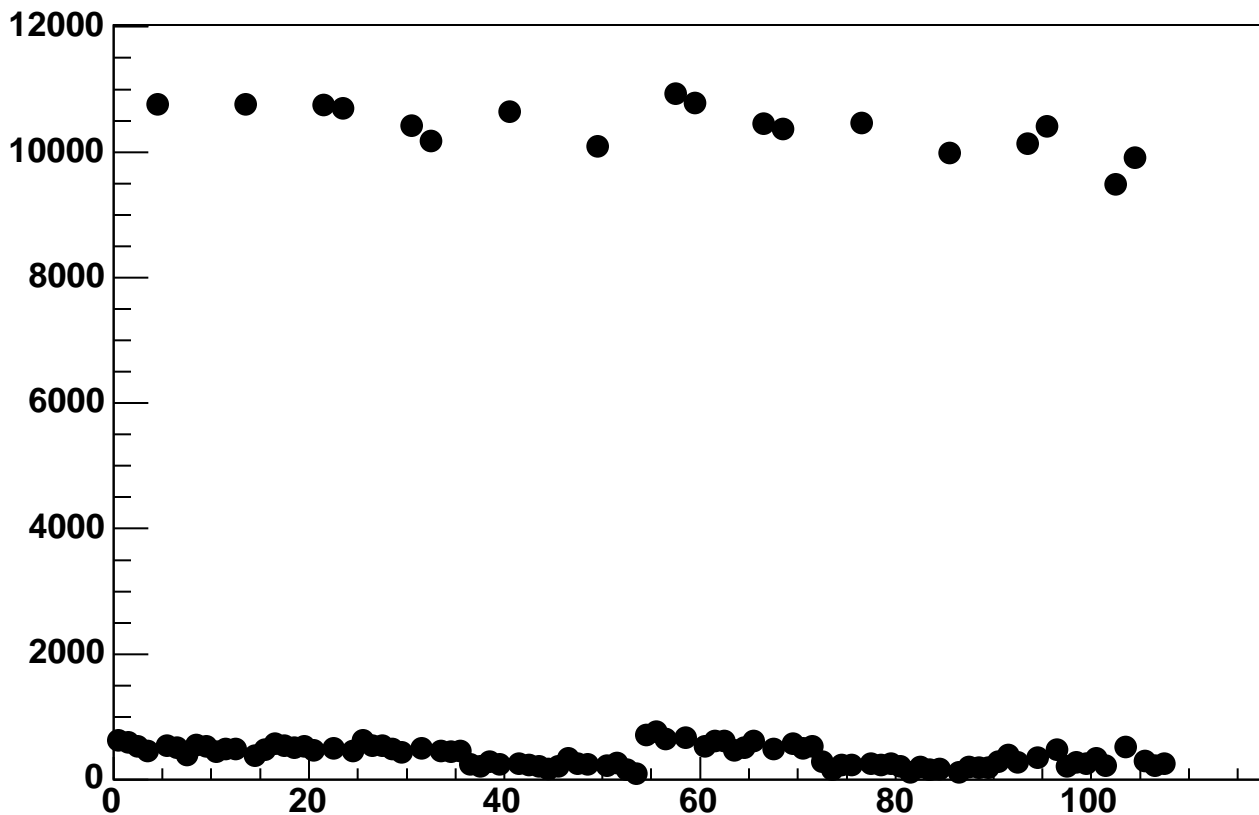
Enable 4, DAC=4000, Hold=25, ADC Mean vs 18\*Chip+Chan



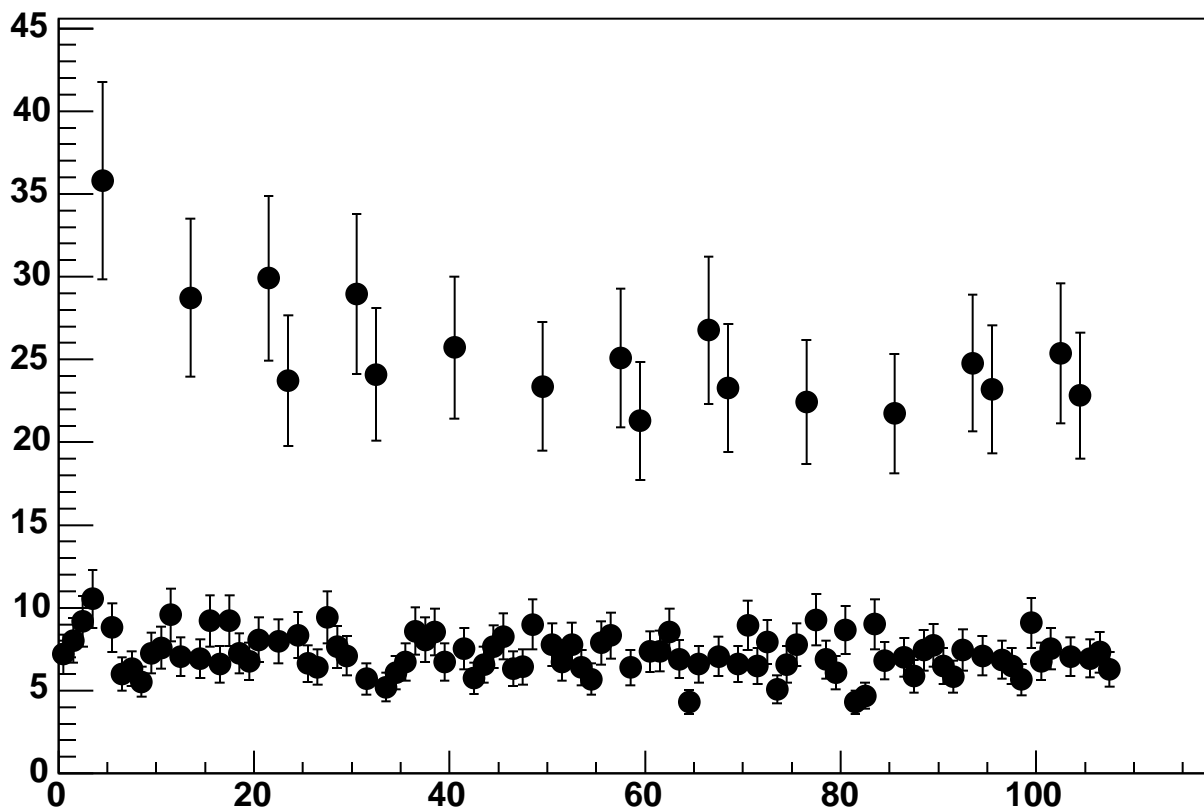
Enable 4, DAC=4000, Hold=25, ADC Noise vs 18\*Chip+Chan



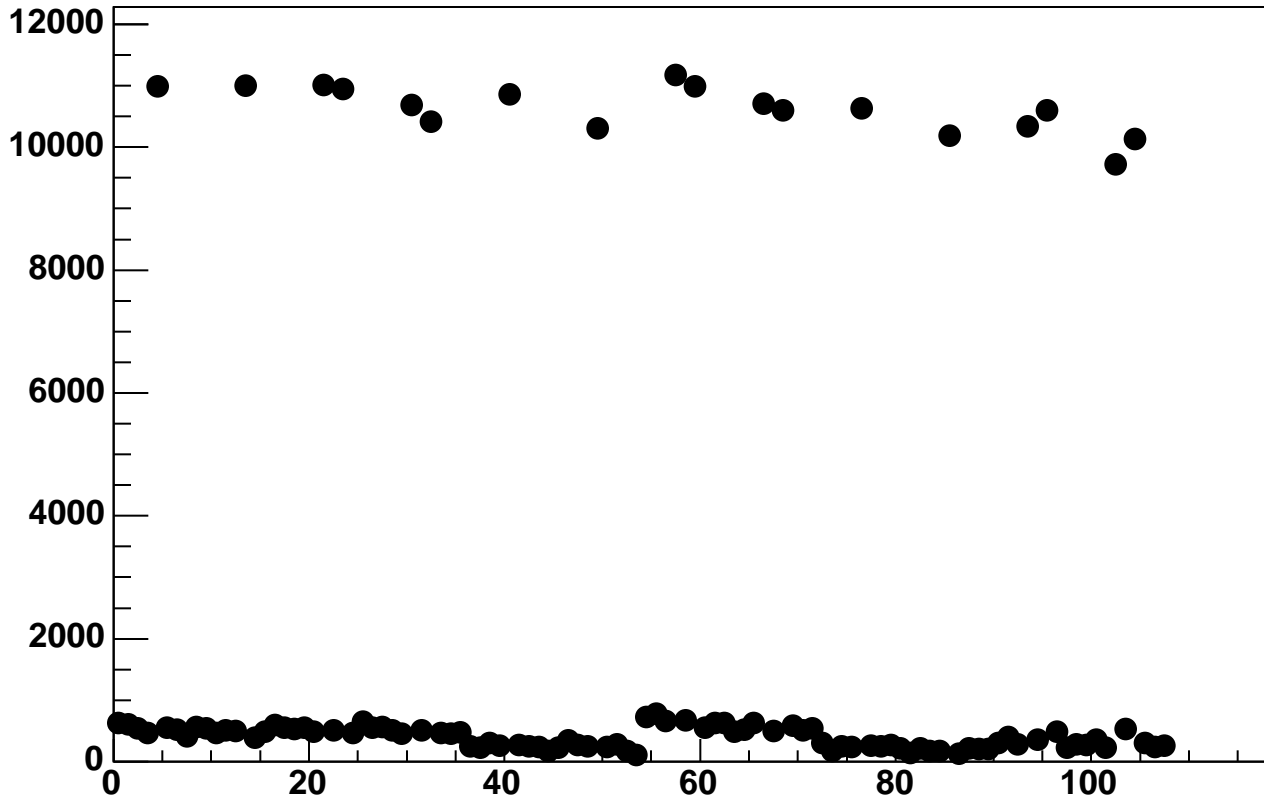
Enable 4, DAC=4000, Hold=30, ADC Mean vs 18\*Chip+Chan



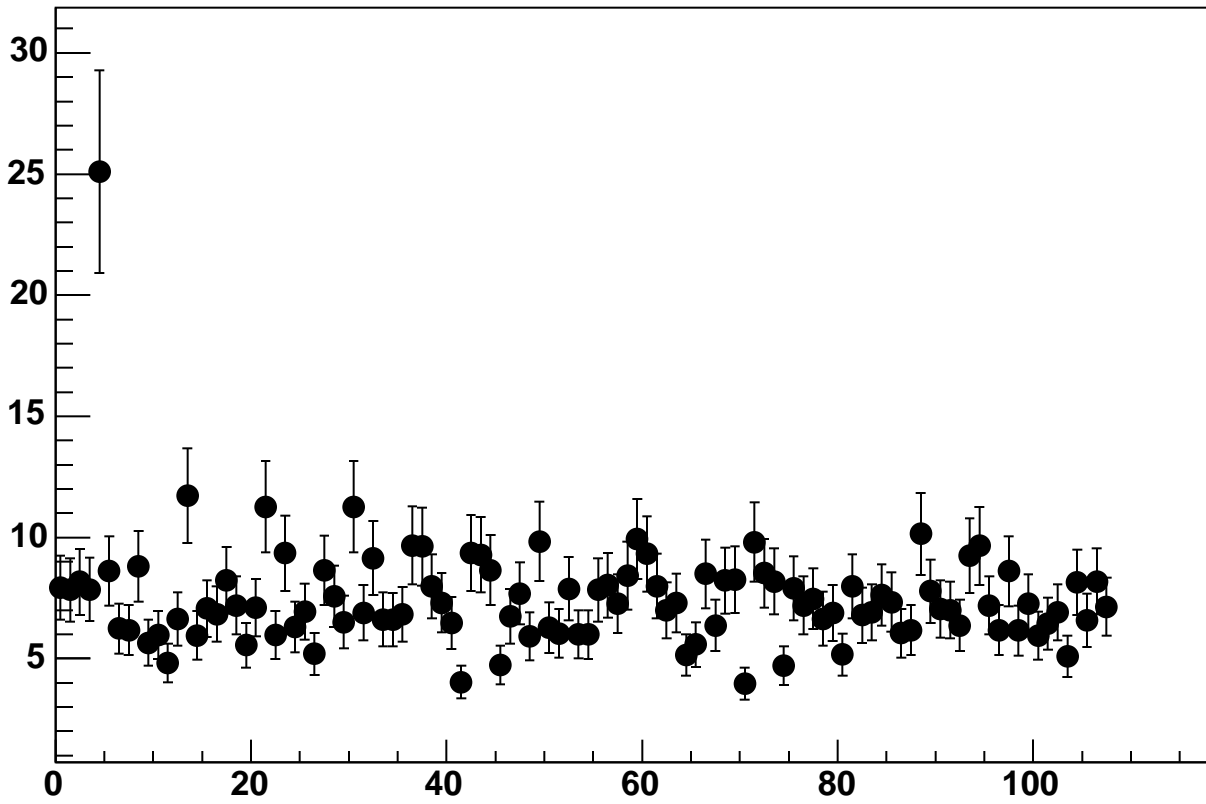
Enable 4, DAC=4000, Hold=30, ADC Noise vs 18\*Chip+Chan



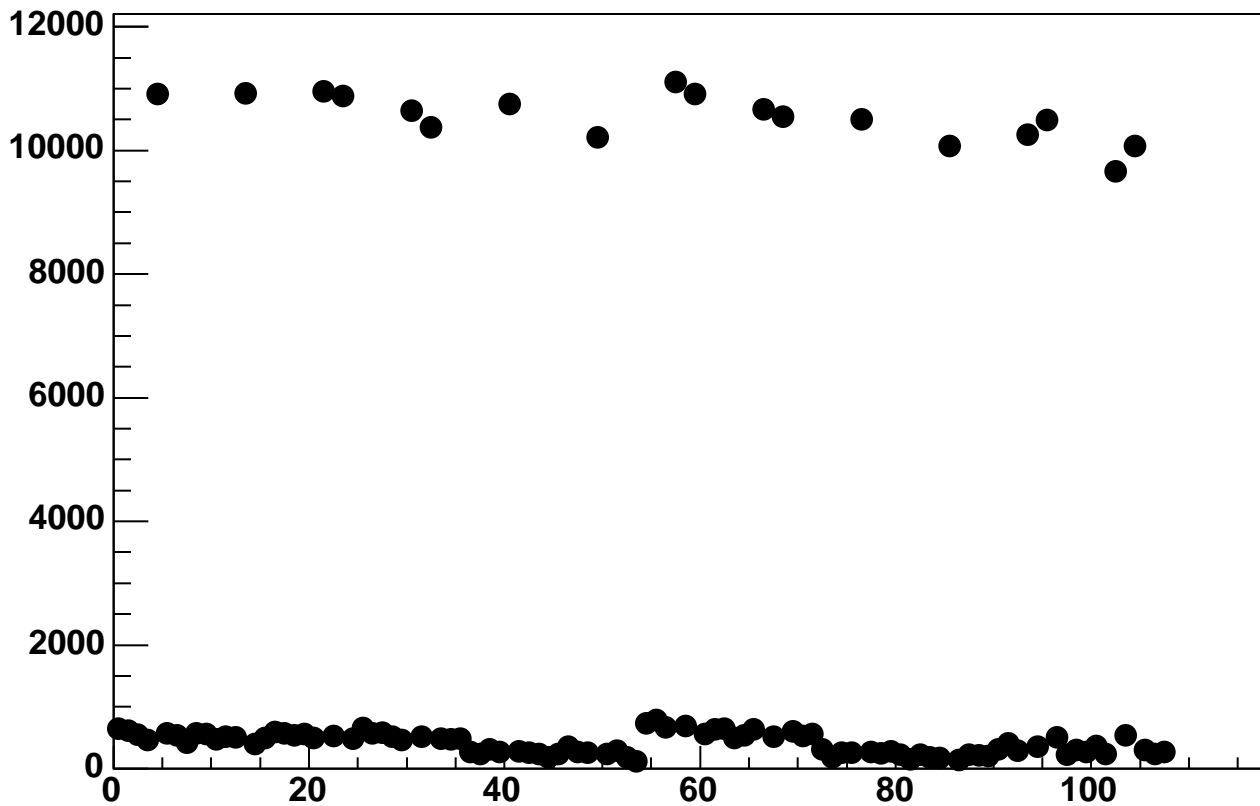
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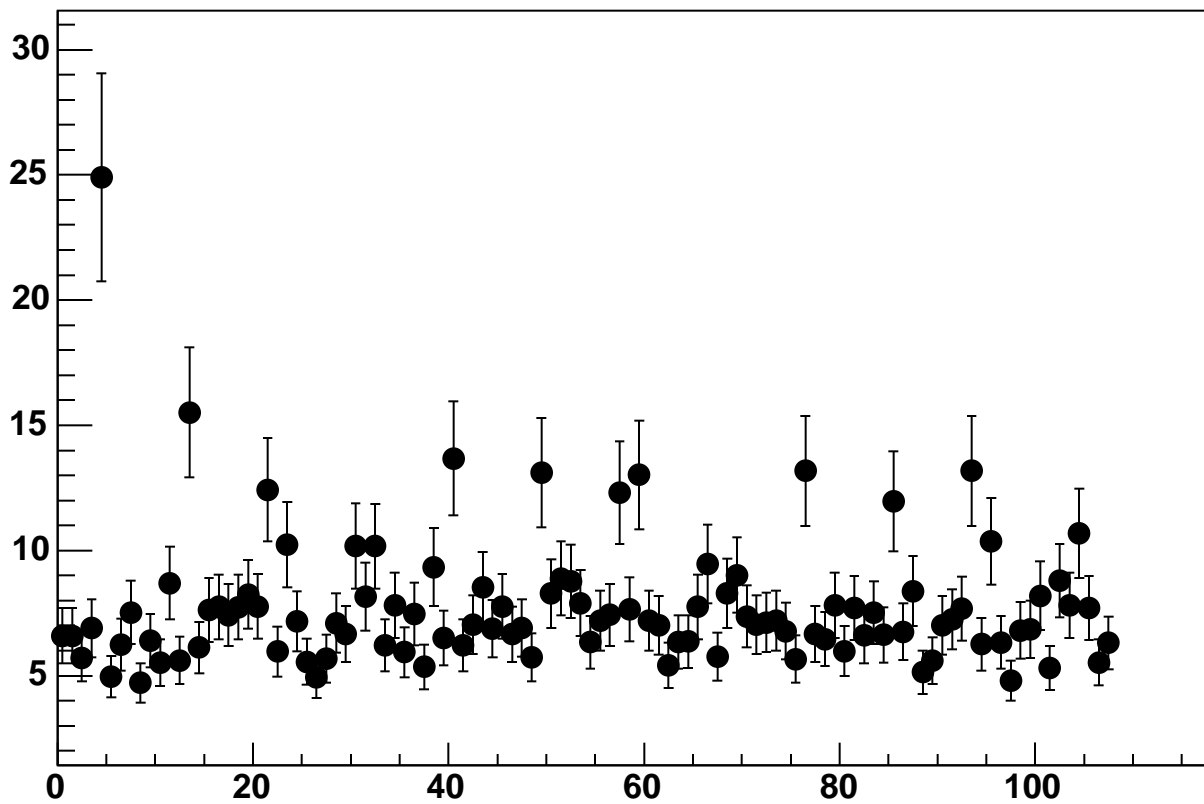
Enable 4, DAC=4000, Hold=35, ADC Noise vs 18\*Chip+Chan



Enable 4, DAC=4000, Hold=40, ADC Mean vs 18\*Chip+Chan

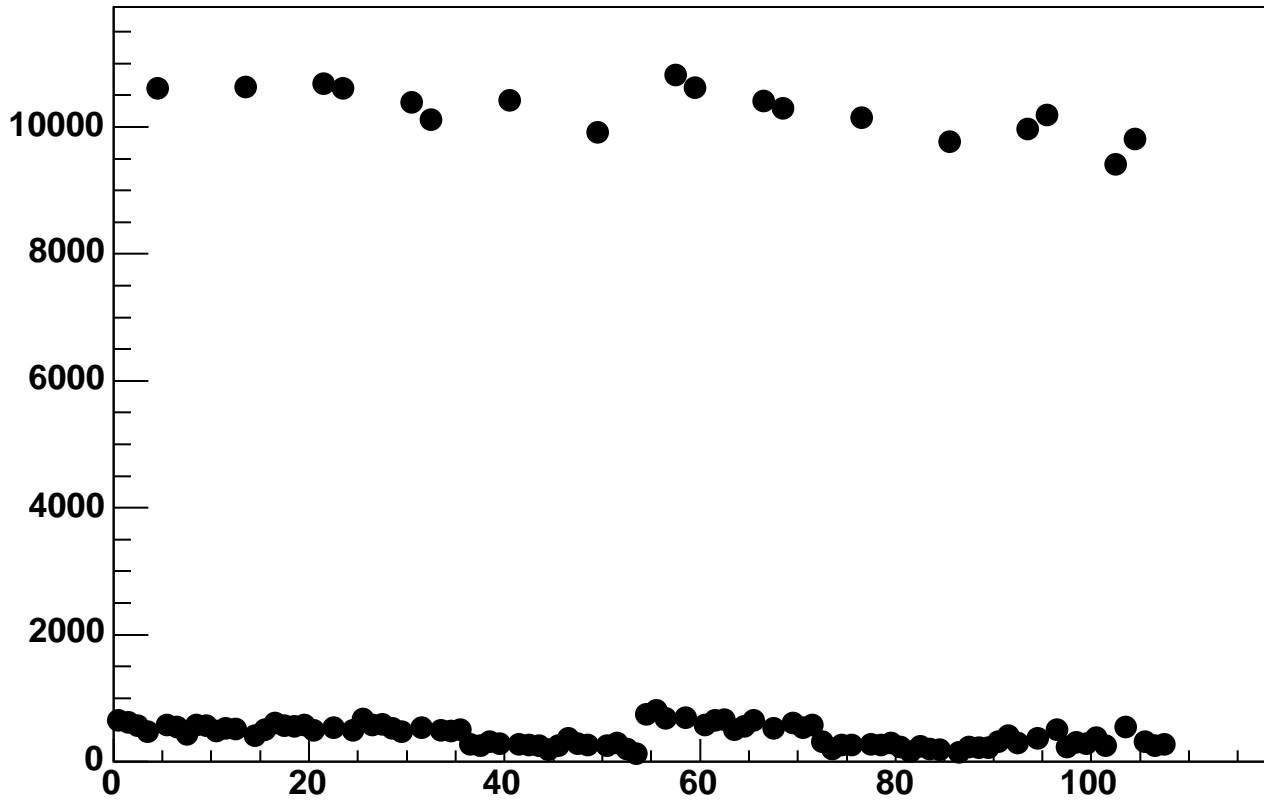


Enable 4, DAC=4000, Hold=40, ADC Noise vs 18\*Chip+Chan

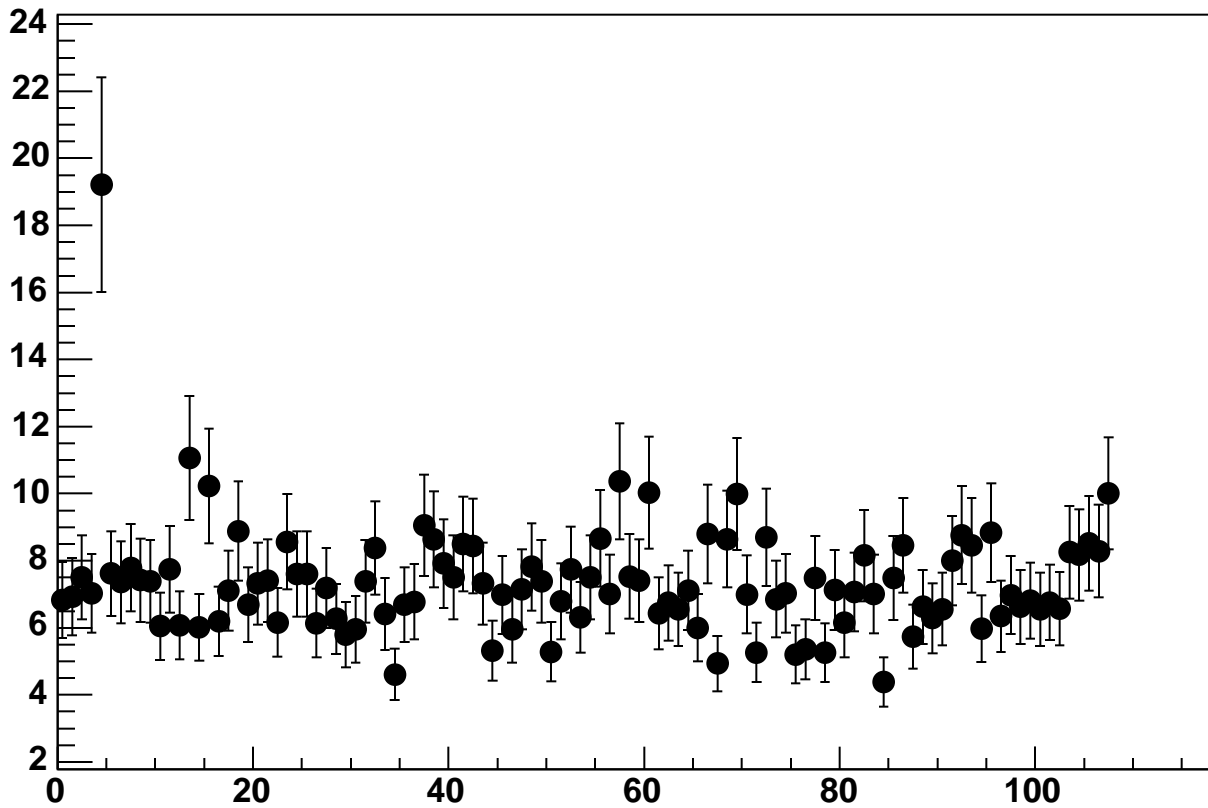




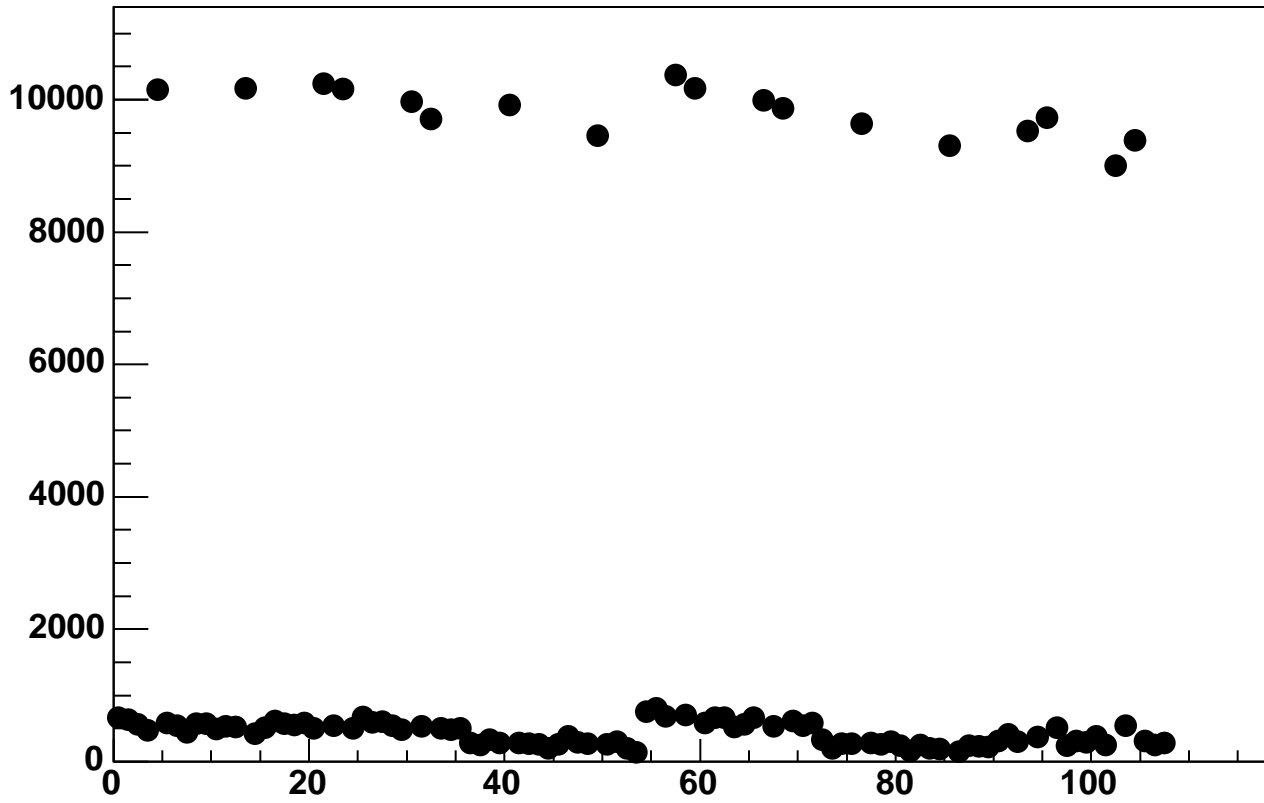
Enable 4, DAC=4000, Hold=45, ADC Mean vs 18\*Chip+Chan



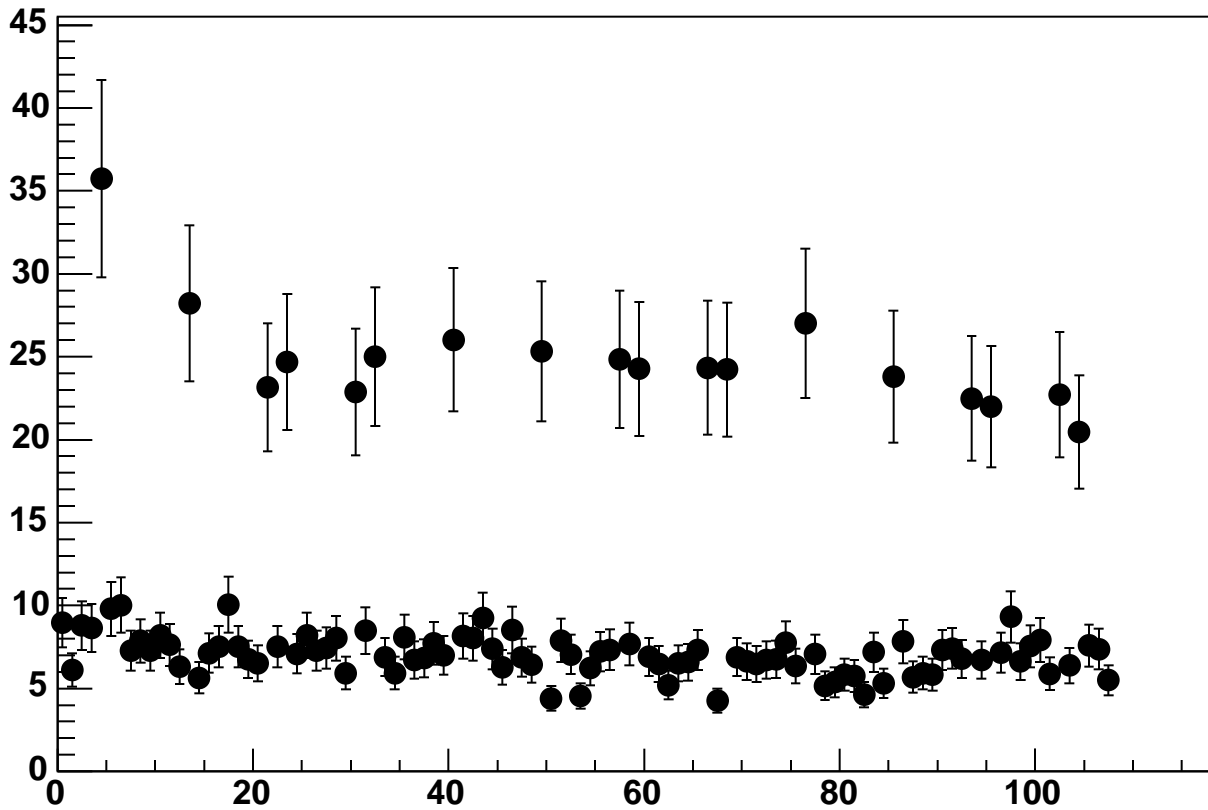
Enable 4, DAC=4000, Hold=45, ADC Noise vs 18\*Chip+Chan



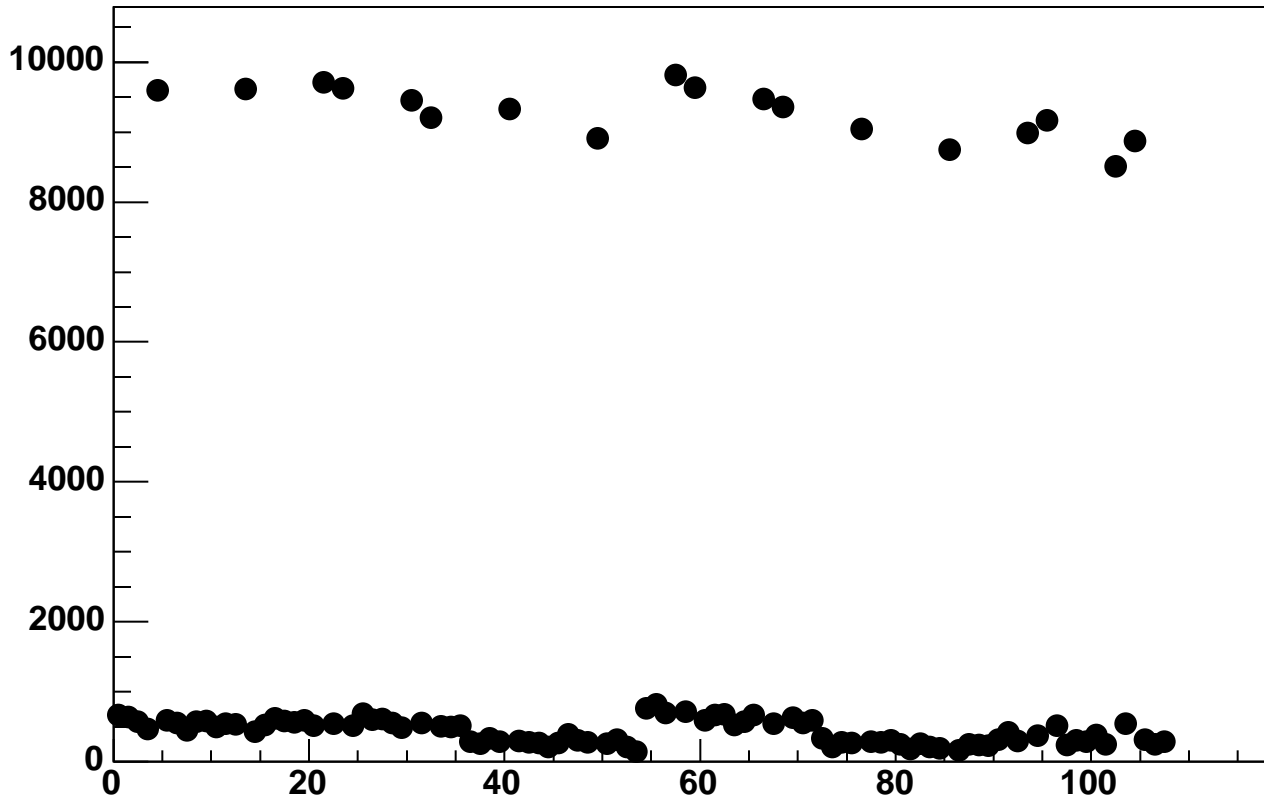
Enable 4, DAC=4000, Hold=50, ADC Mean vs 18\*Chip+Chan



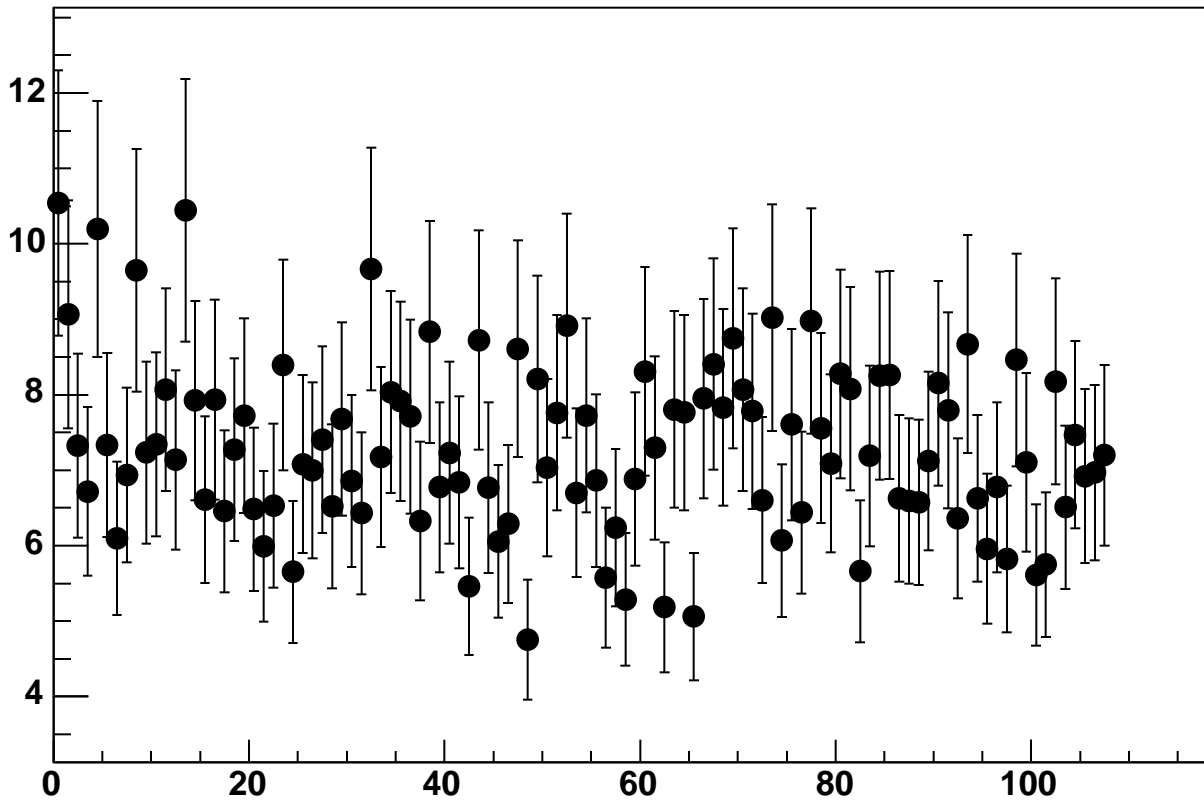
Enable 4, DAC=4000, Hold=50, ADC Noise vs 18\*Chip+Chan



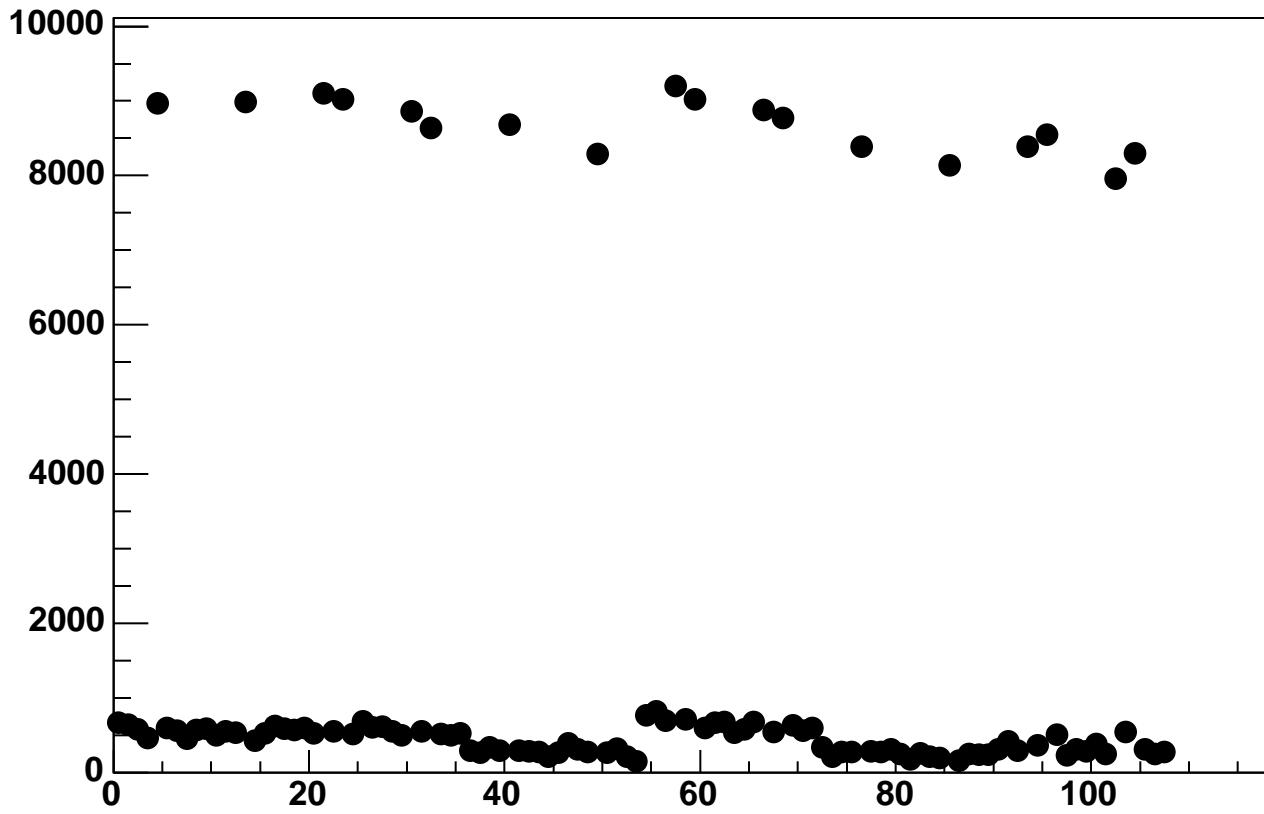
Enable 4, DAC=4000, Hold=55, ADC Mean vs 18\*Chip+Chan



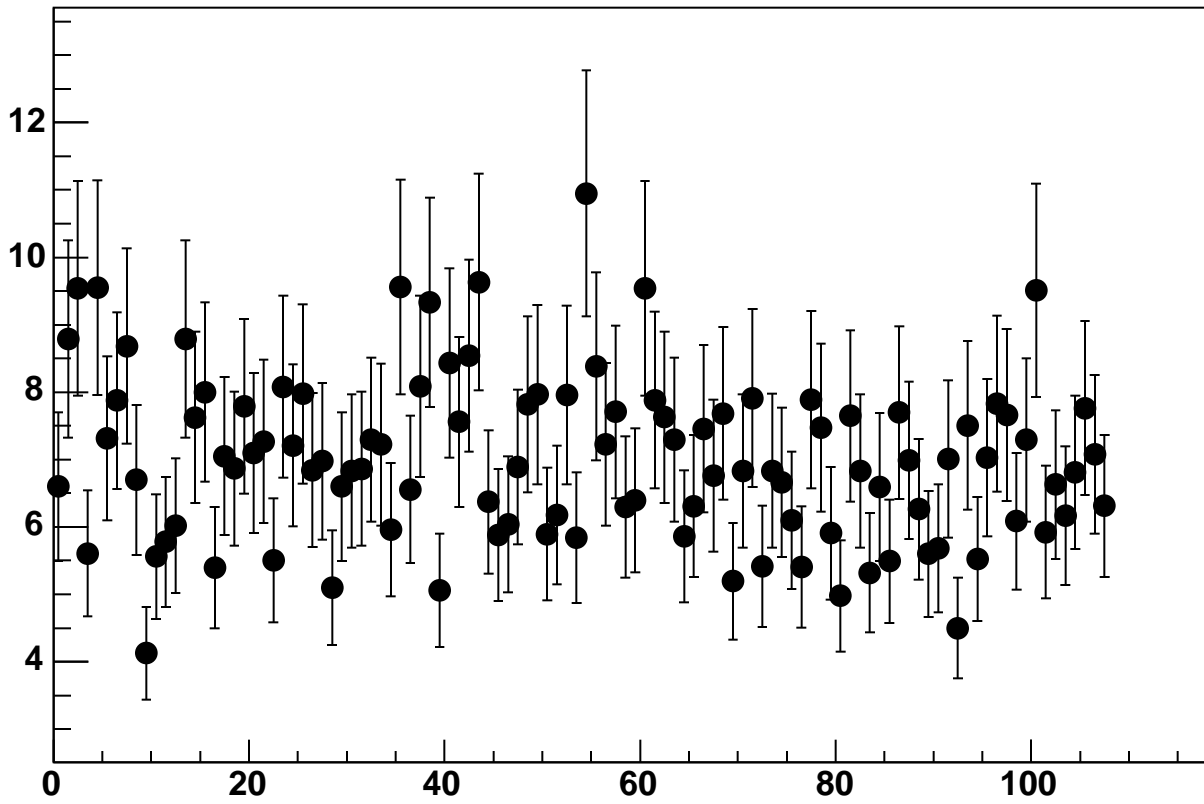
Enable 4, DAC=4000, Hold=55, ADC Noise vs 18\*Chip+Chan



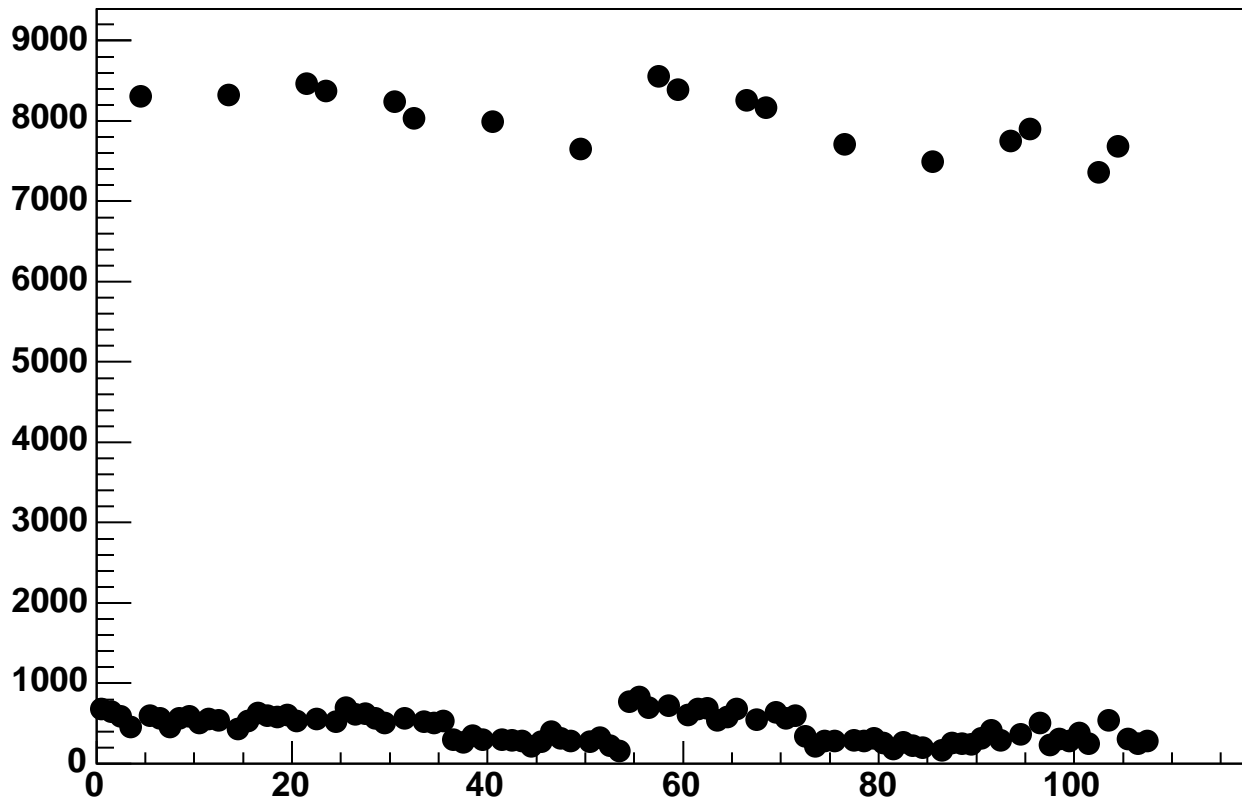
Enable 4, DAC=4000, Hold=60, ADC Mean vs 18\*Chip+Chan



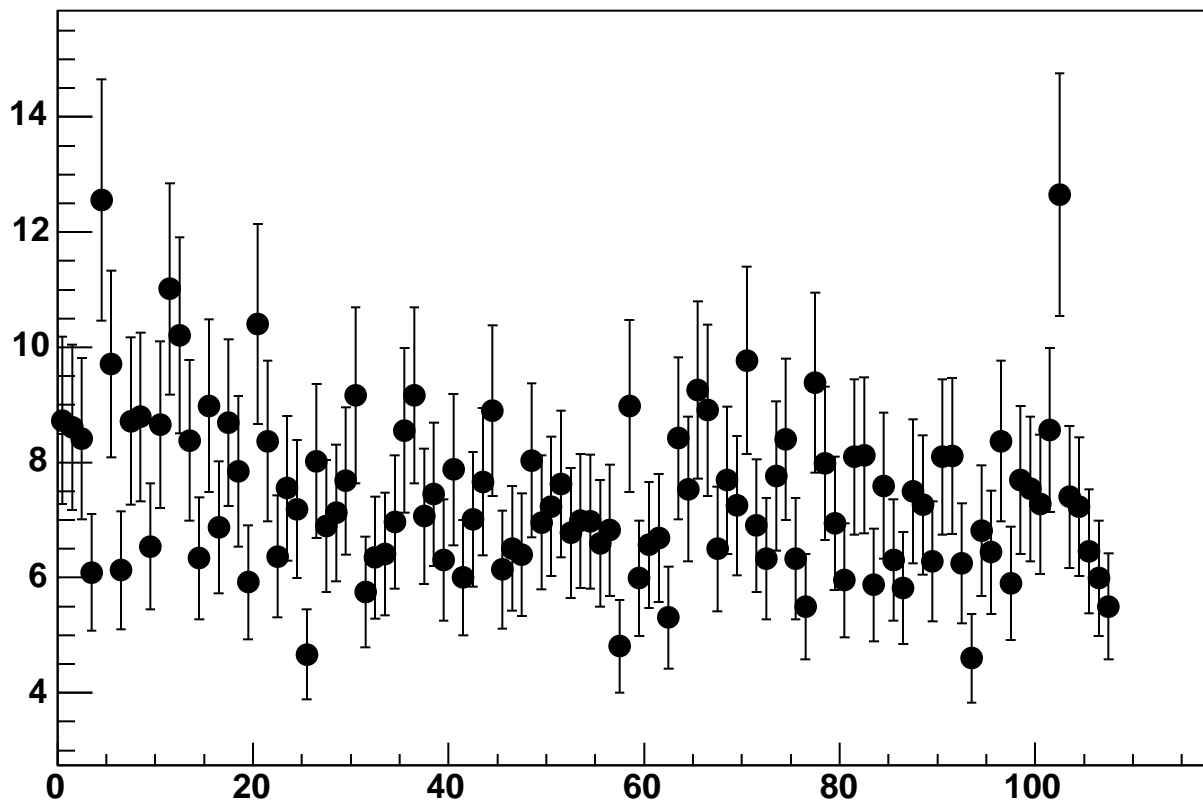
Enable 4, DAC=4000, Hold=60, ADC Noise vs 18\*Chip+Chan



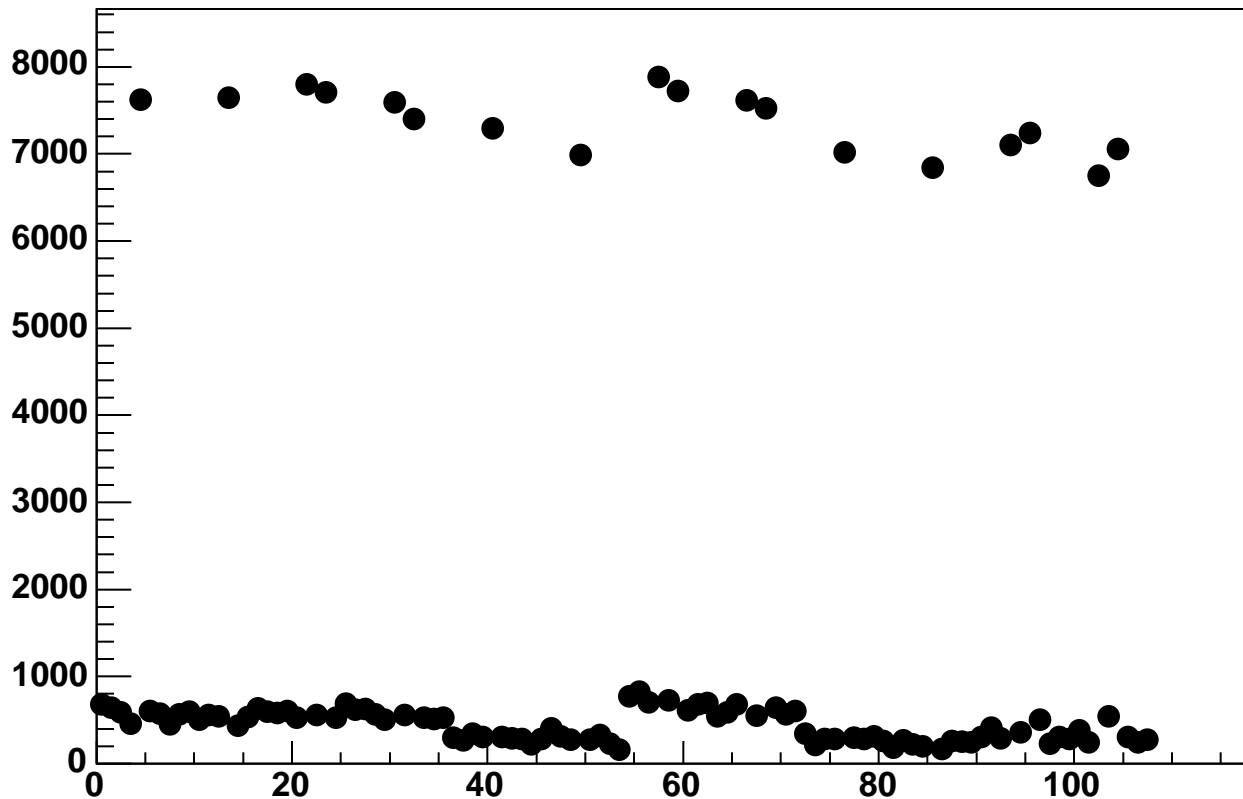
Enable 4, DAC=4000, Hold=65, ADC Mean vs 18\*Chip+Chan



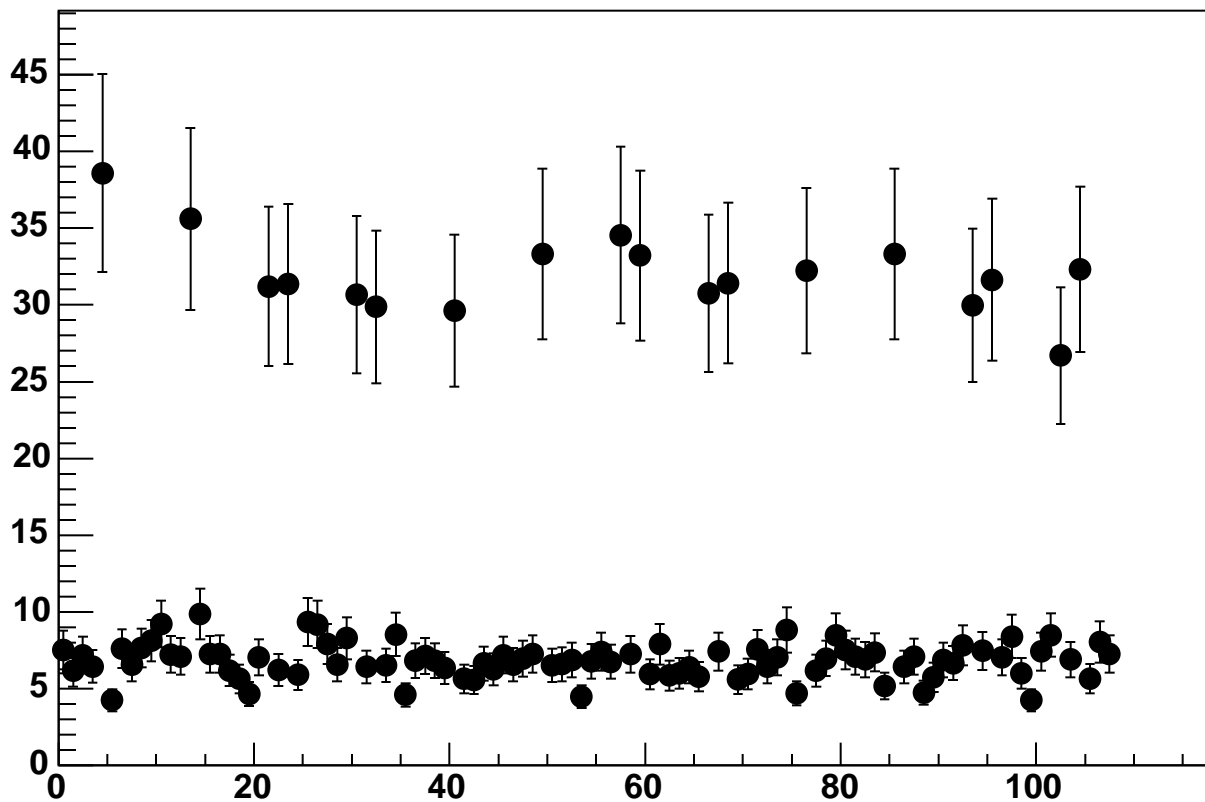
Enable 4, DAC=4000, Hold=65, ADC Noise vs 18\*Chip+Chan



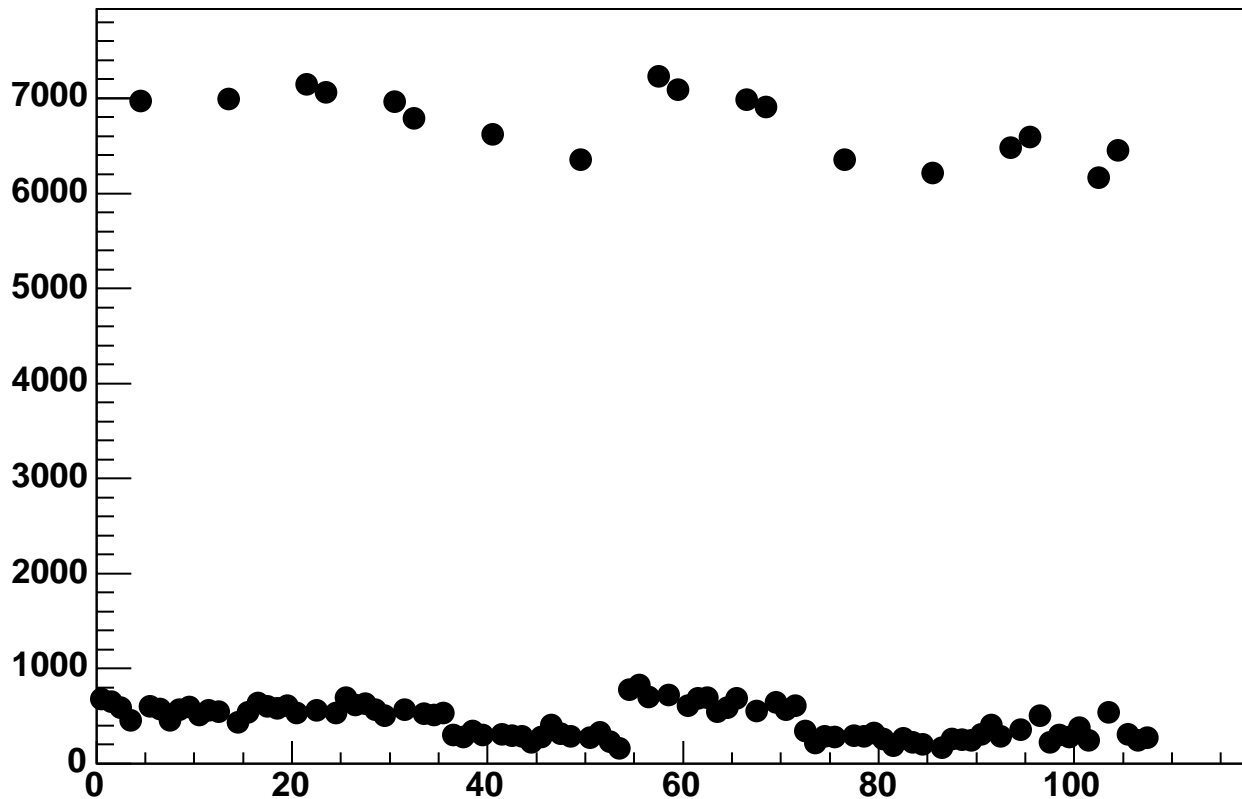
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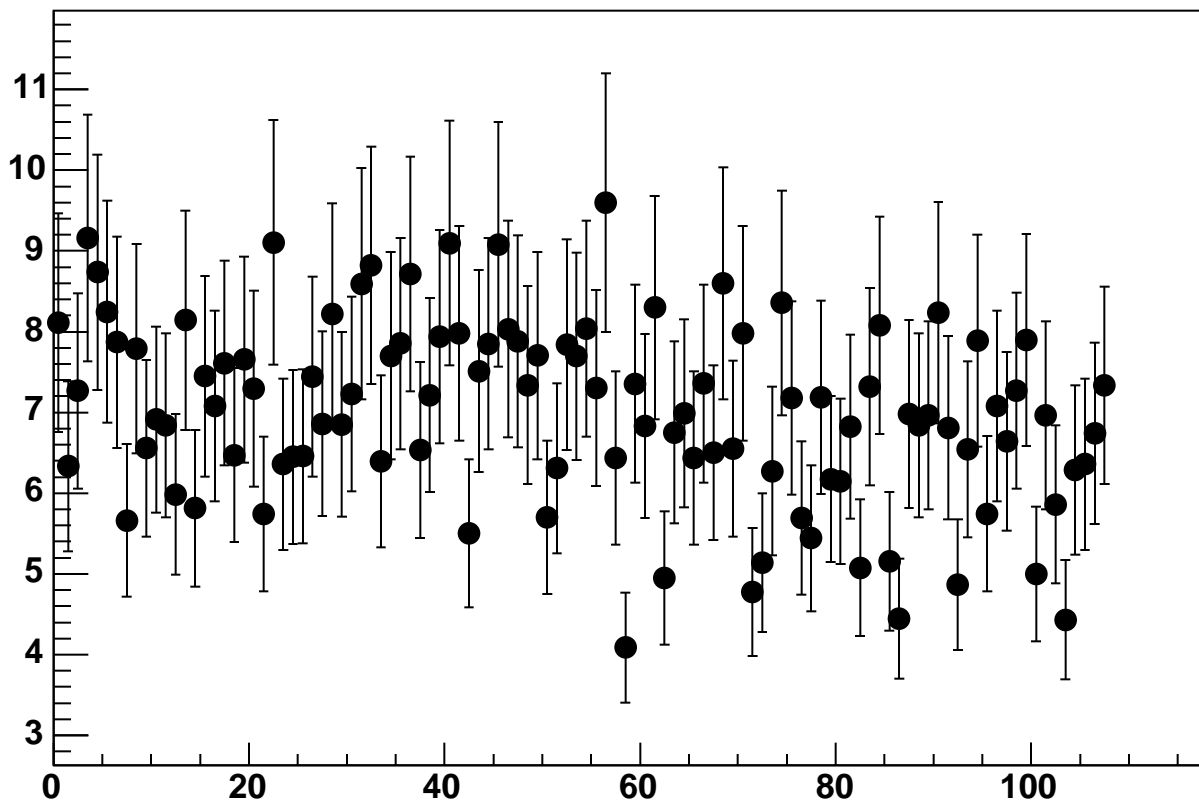
Enable 4, DAC=4000, Hold=70, ADC Noise vs 18\*Chip+Chan



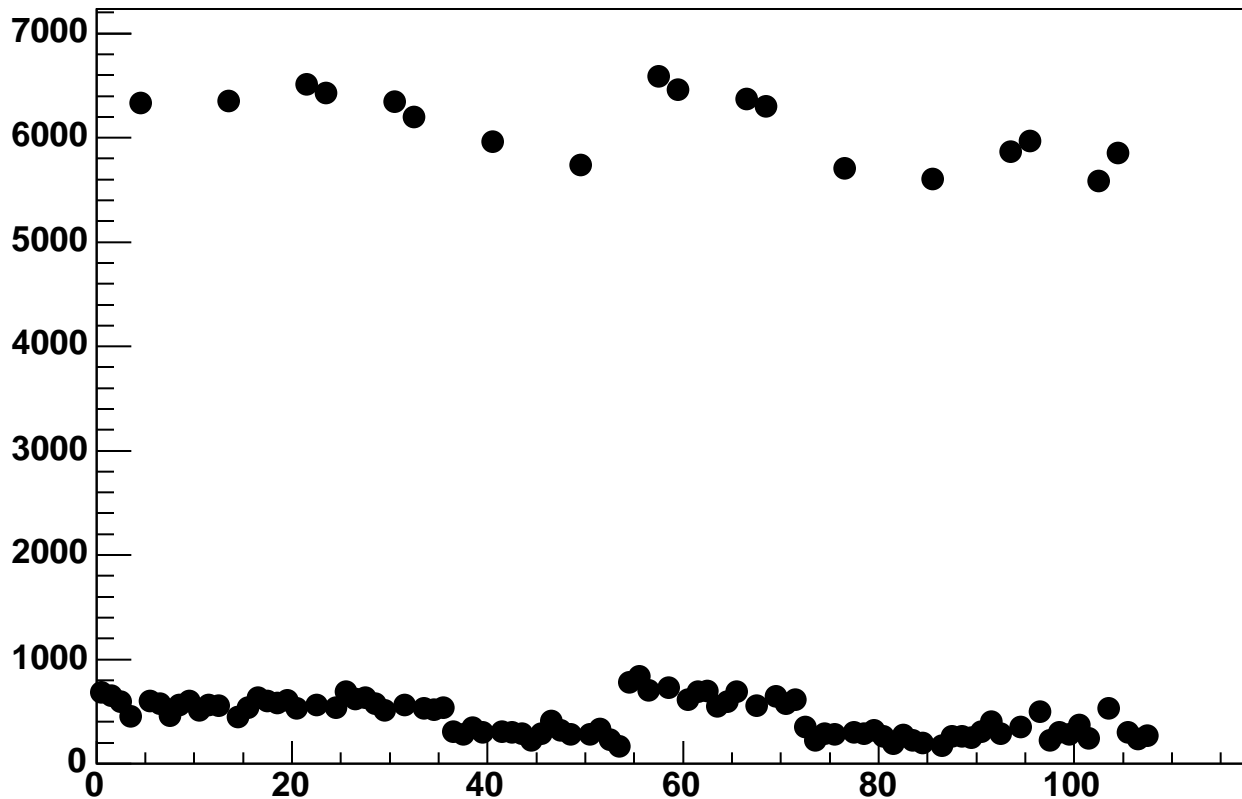
Enable 4, DAC=4000, Hold=75, ADC Mean vs 18\*Chip+Chan



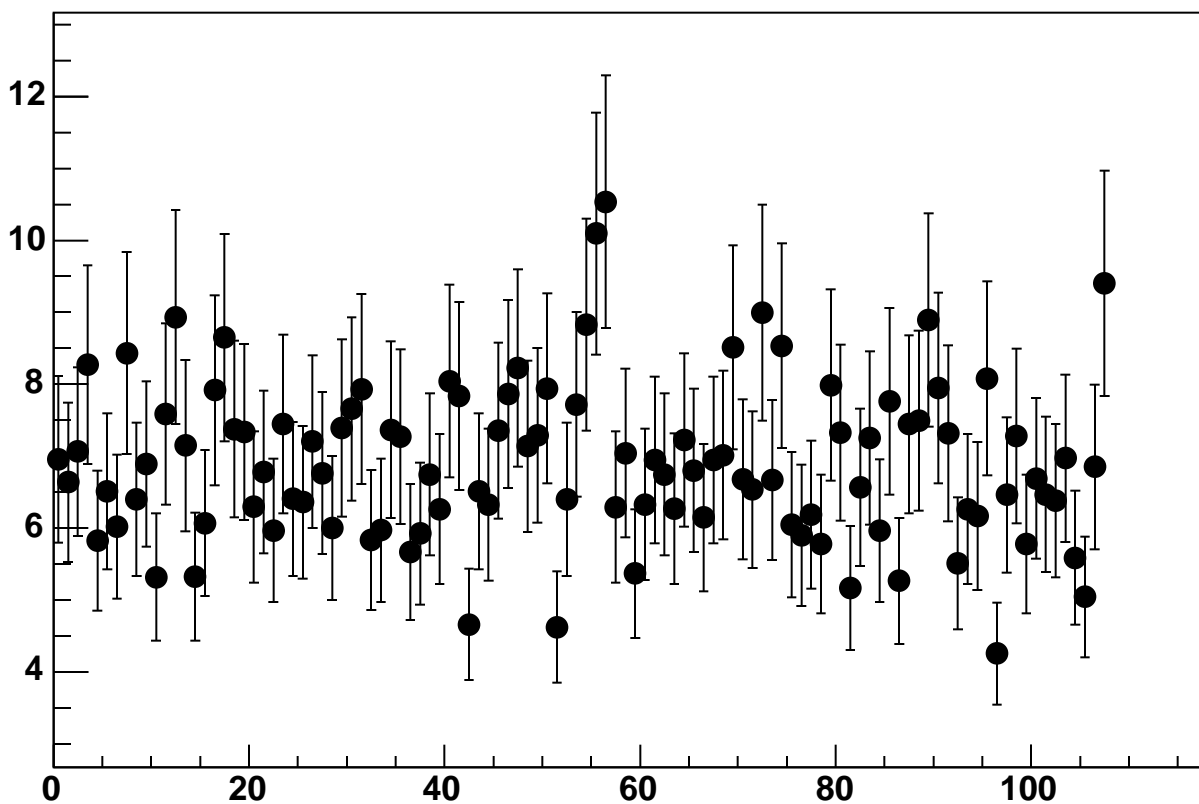
Enable 4, DAC=4000, Hold=75, ADC Noise vs 18\*Chip+Chan



Enable 4, DAC=4000, Hold=80, ADC Mean vs 18\*Chip+Chan

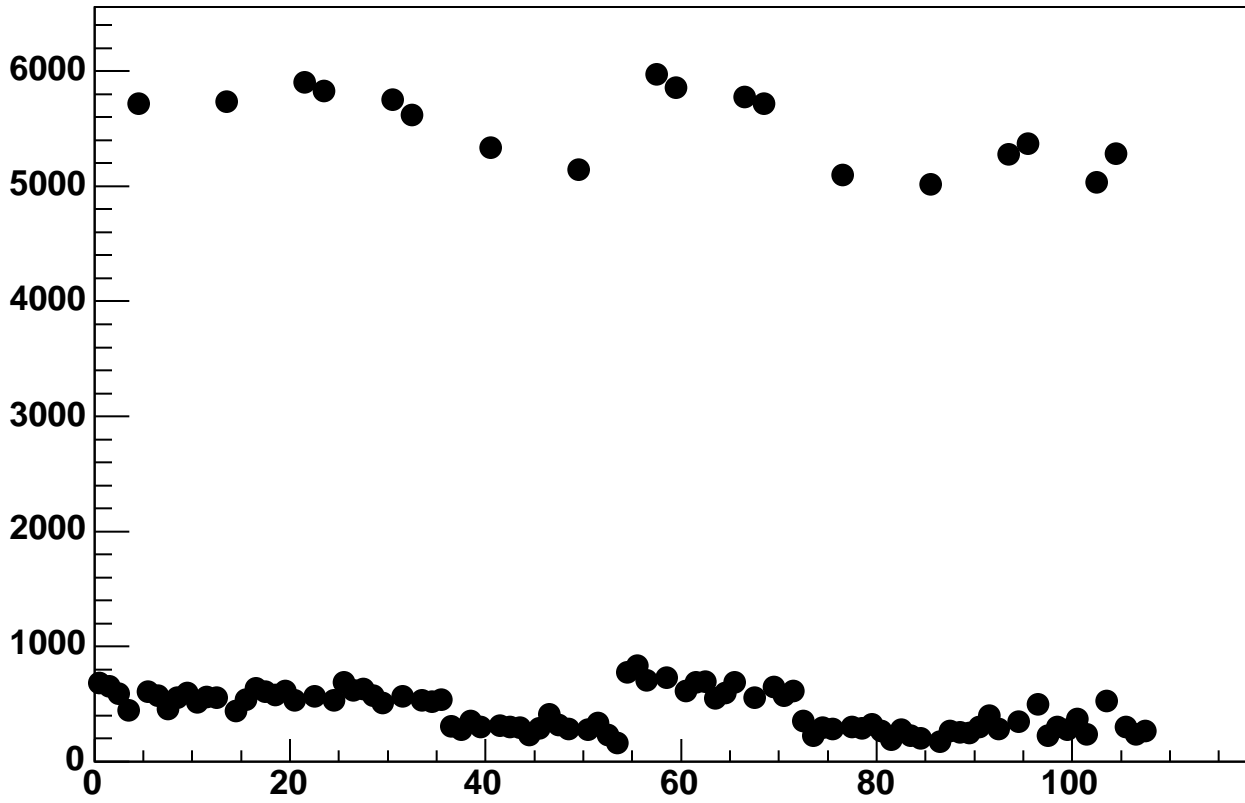


Enable 4, DAC=4000, Hold=80, ADC Noise vs 18\*Chip+Chan

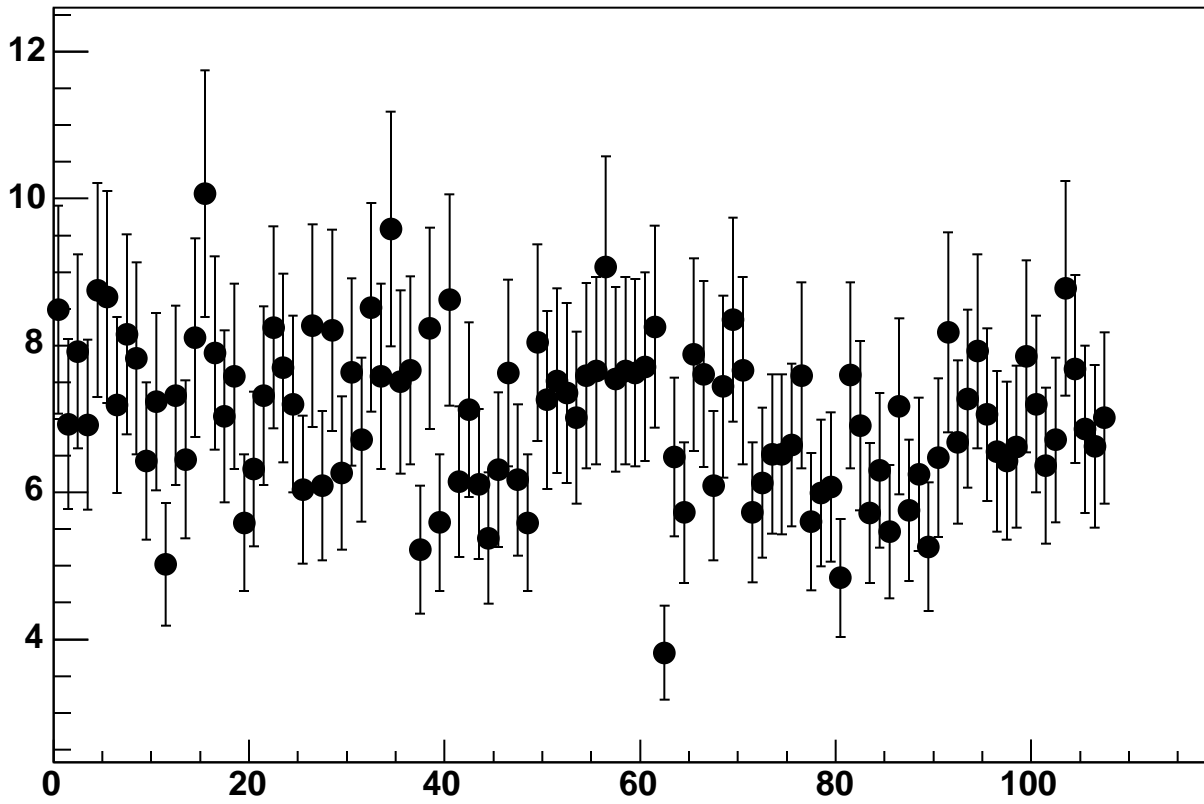




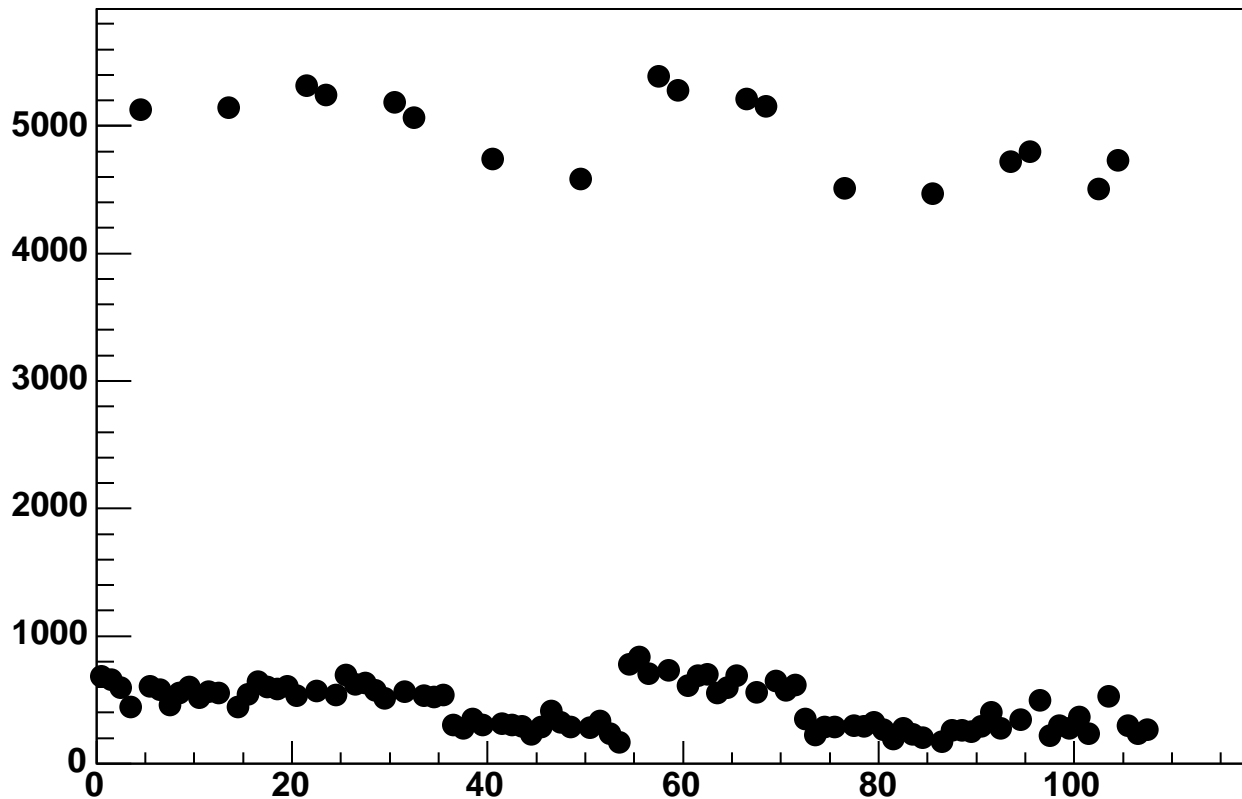
Enable 4, DAC=4000, Hold=85, ADC Mean vs 18\*Chip+Chan



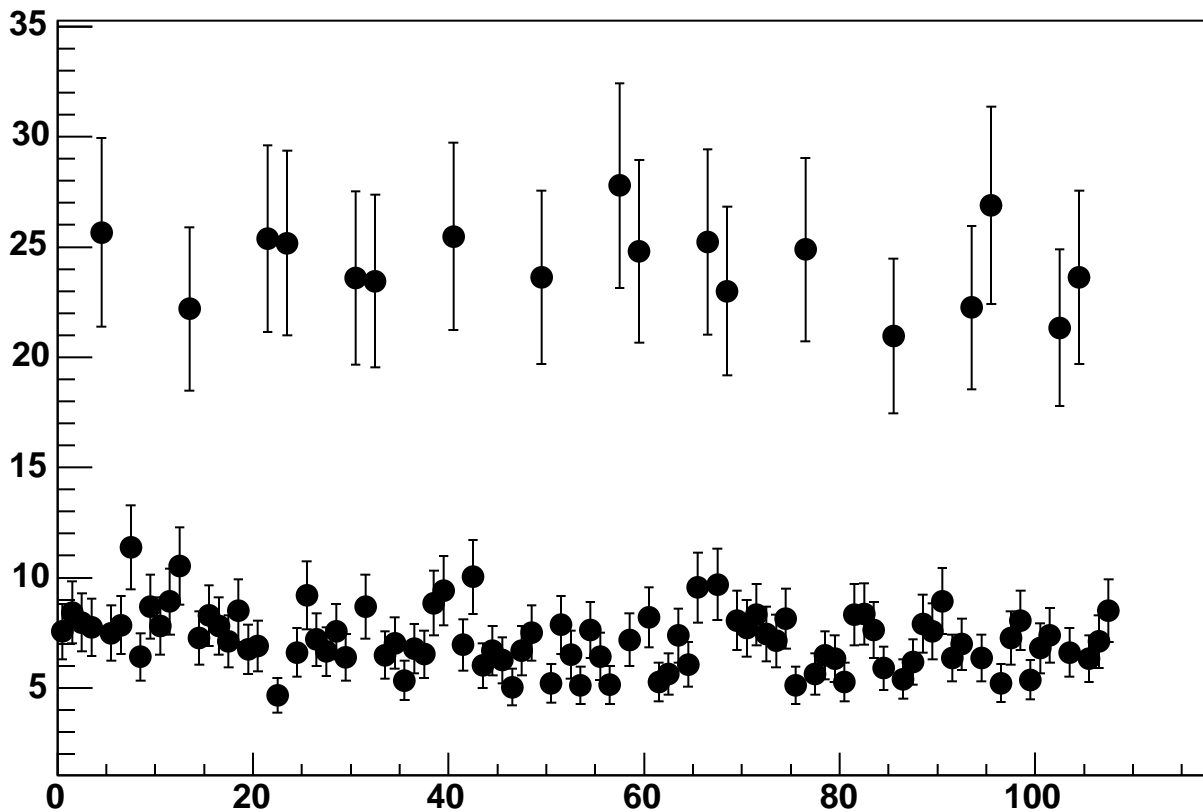
Enable 4, DAC=4000, Hold=85, ADC Noise vs 18\*Chip+Chan



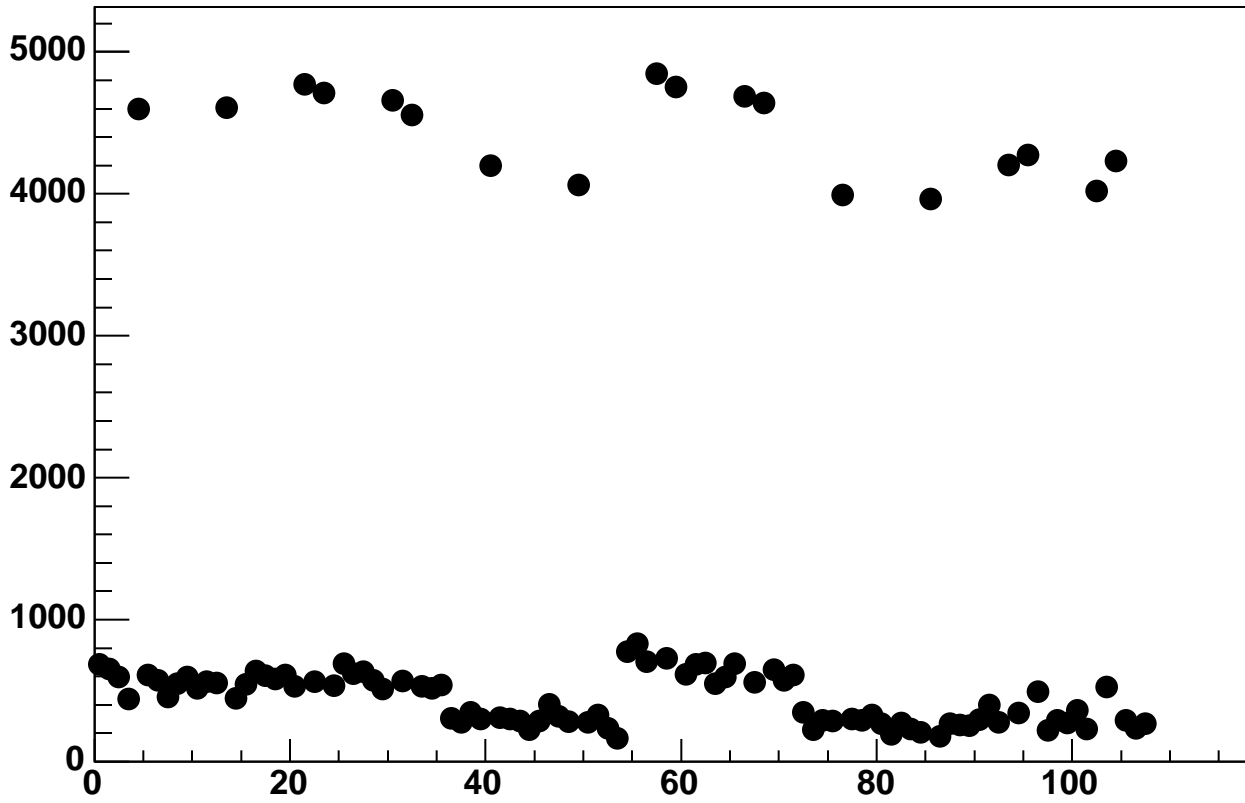
Enable 4, DAC=4000, Hold=90, ADC Mean vs 18\*Chip+Chan



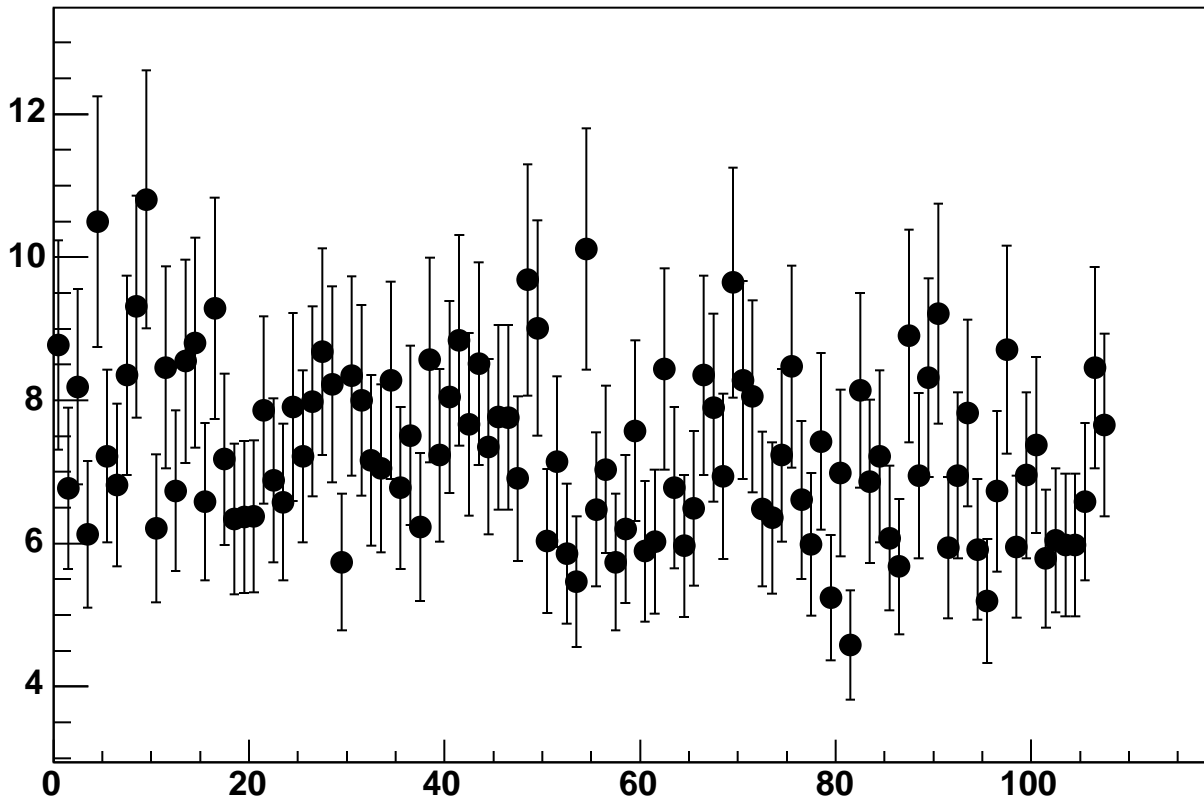
Enable 4, DAC=4000, Hold=90, ADC Noise vs 18\*Chip+Chan



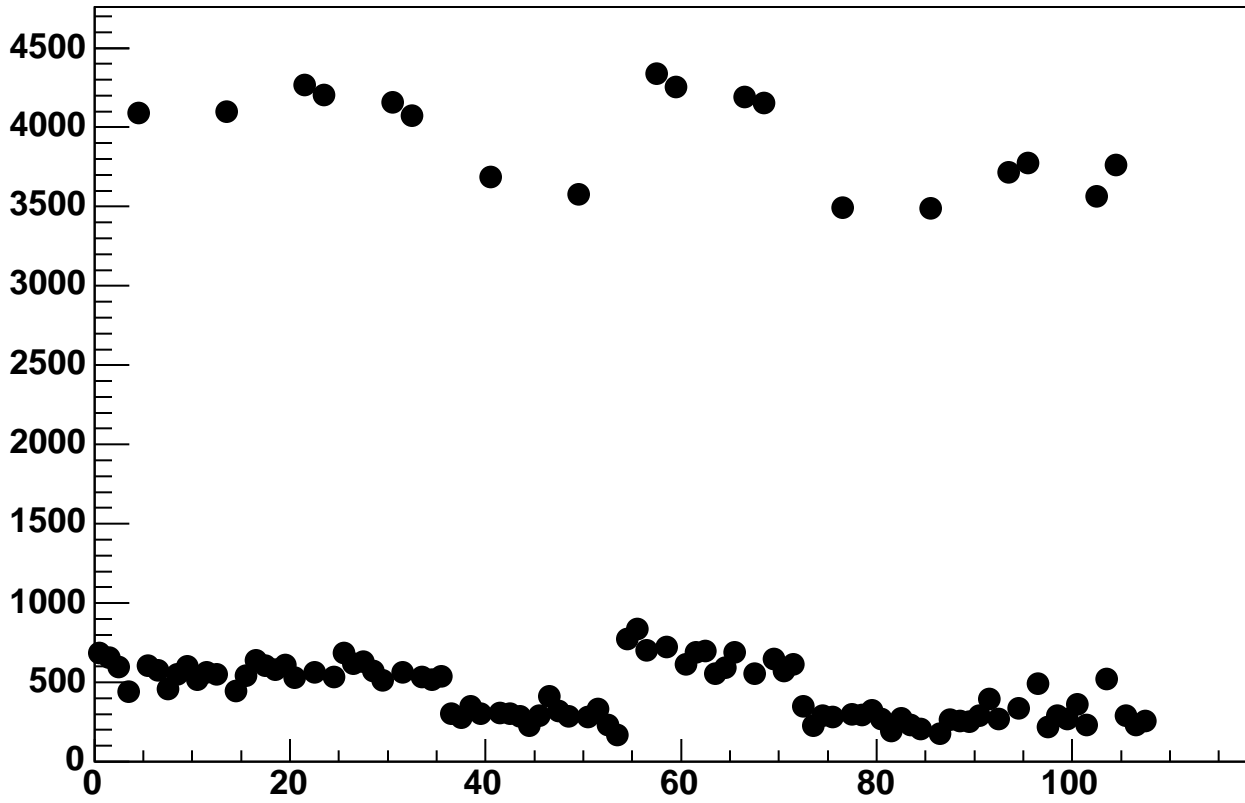
Enable 4, DAC=4000, Hold=95, ADC Mean vs 18\*Chip+Chan



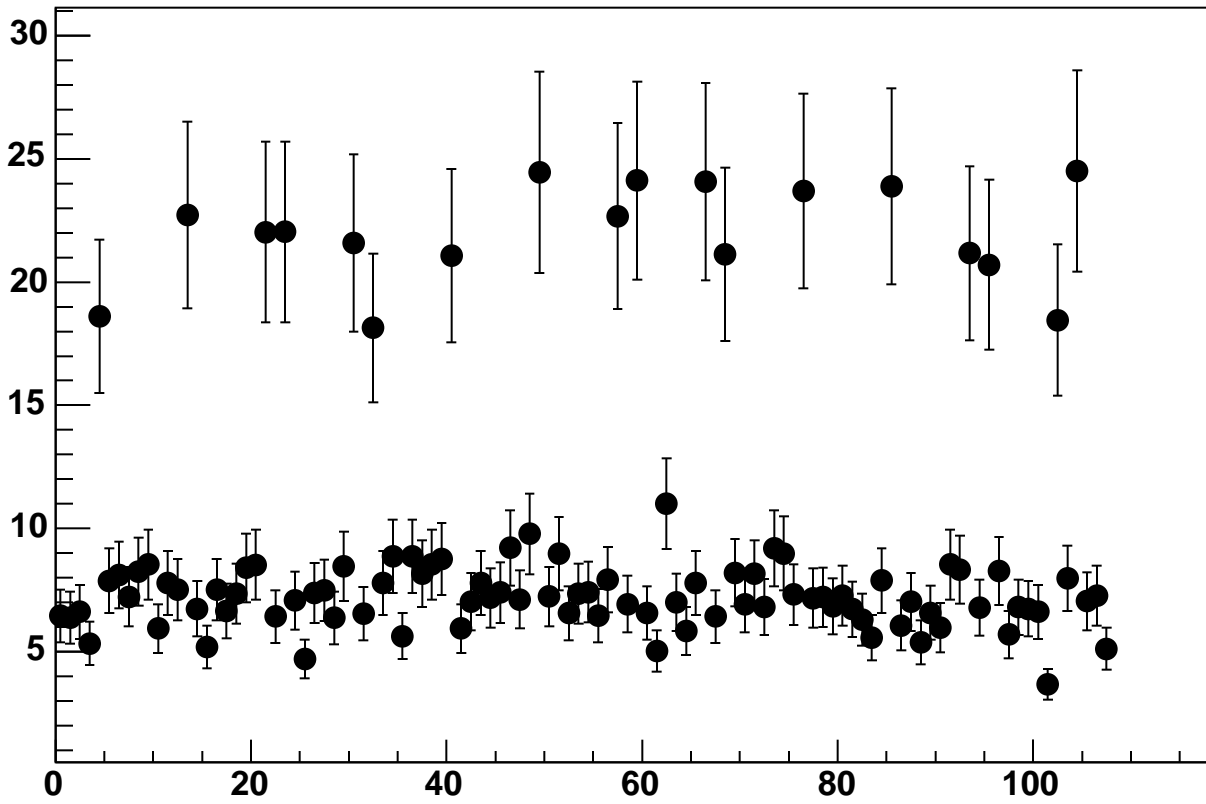
Enable 4, DAC=4000, Hold=95, ADC Noise vs 18\*Chip+Chan



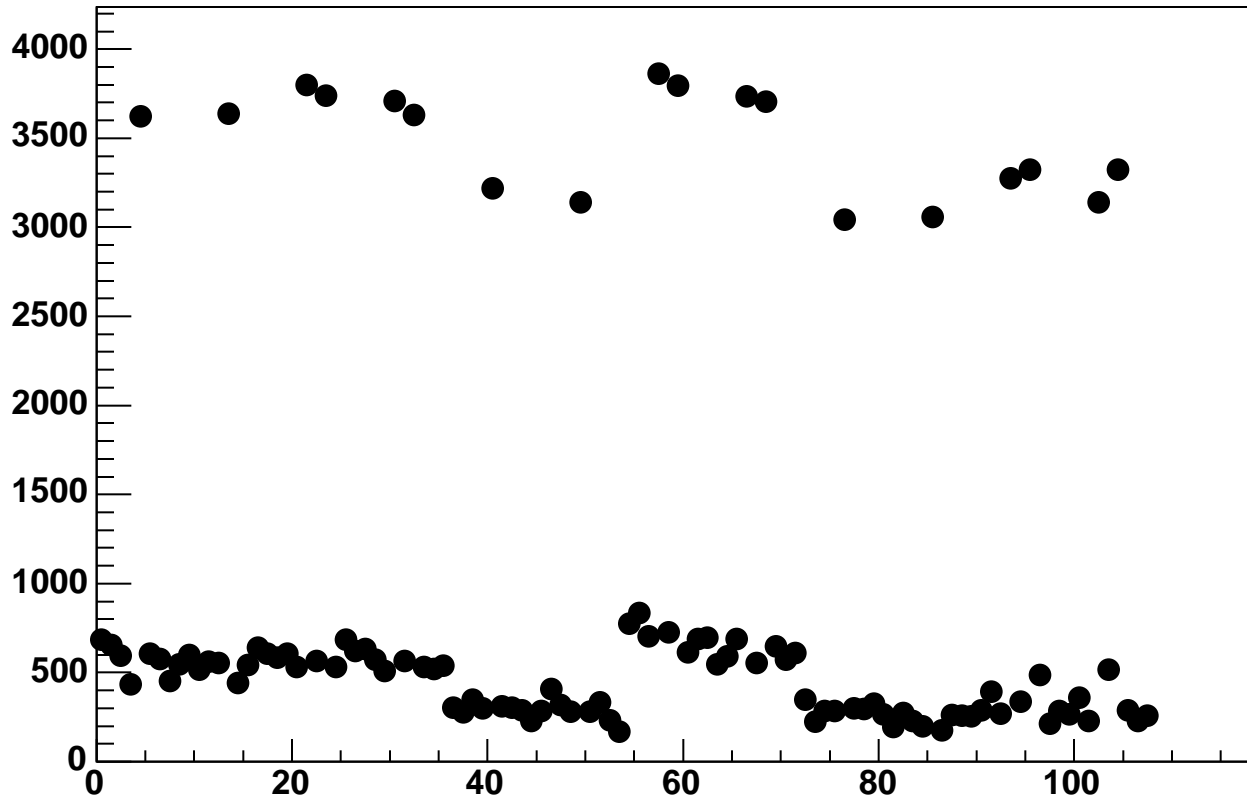
Enable 4, DAC=4000, Hold=100, ADC Mean vs 18\*Chip+Chan



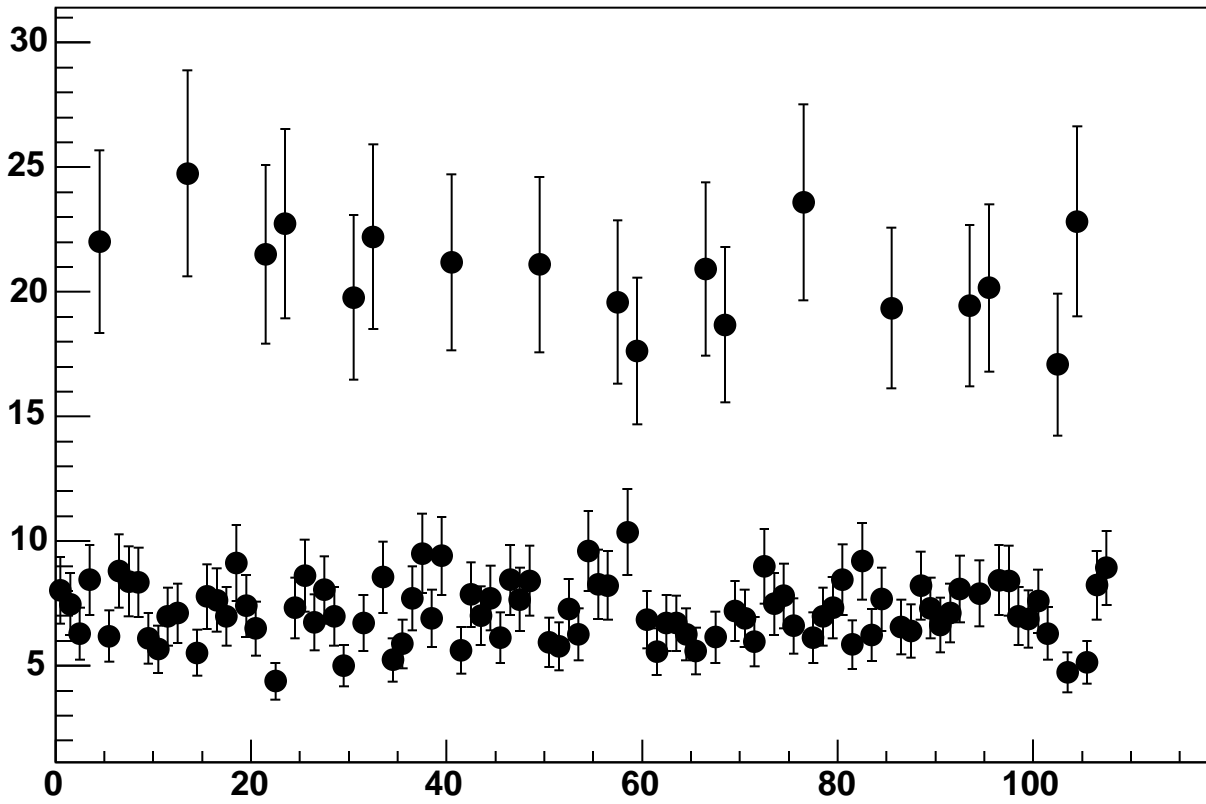
Enable 4, DAC=4000, Hold=100, ADC Noise vs 18\*Chip+Chan



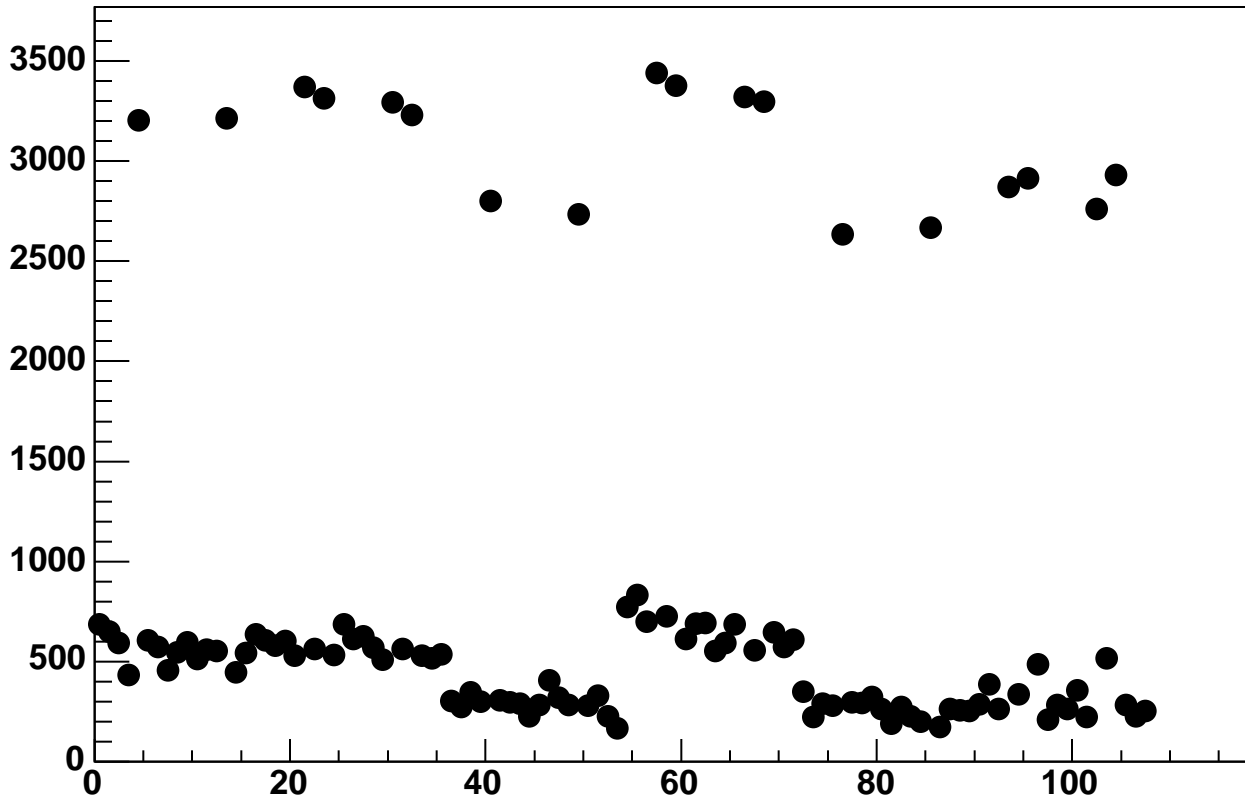
Enable 4, DAC=4000, Hold=105, ADC Mean vs 18\*Chip+Chan



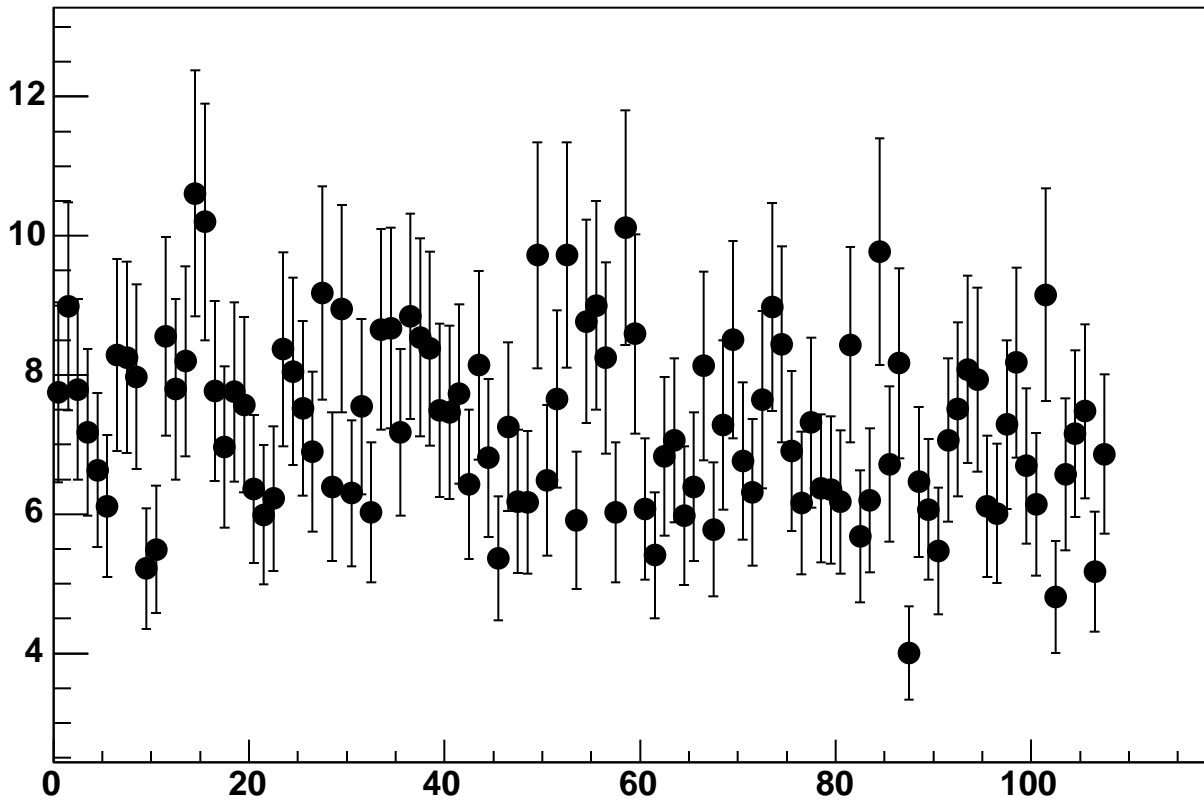
Enable 4, DAC=4000, Hold=105, ADC Noise vs 18\*Chip+Chan



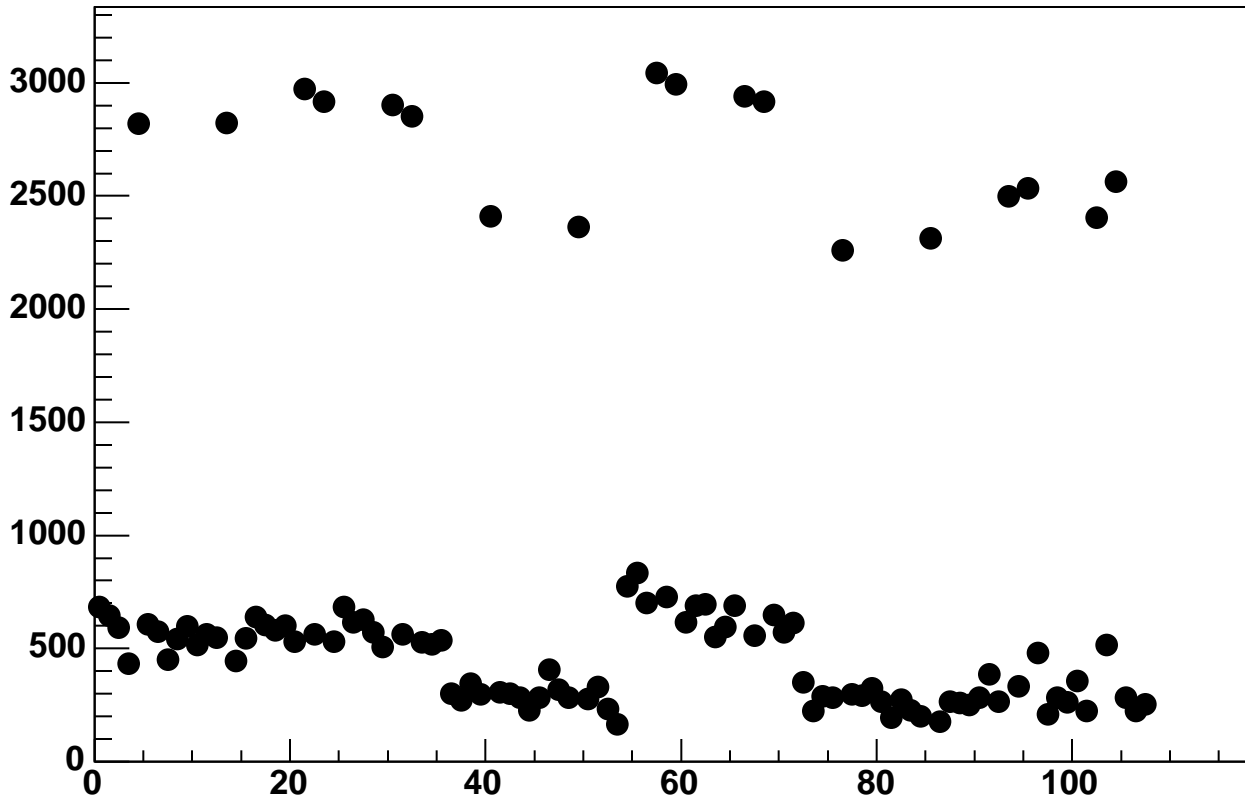
Enable 4, DAC=4000, Hold=110, ADC Mean vs 18\*Chip+Chan



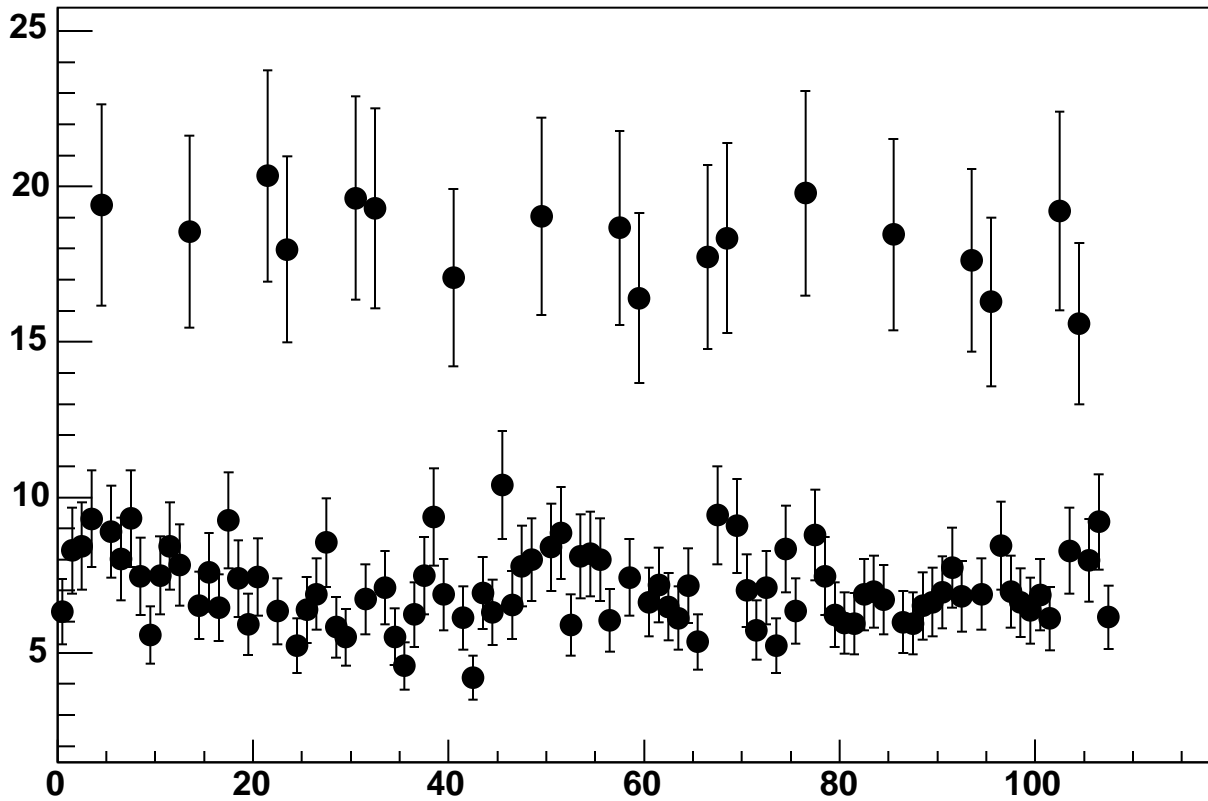
Enable 4, DAC=4000, Hold=110, ADC Noise vs 18\*Chip+Chan



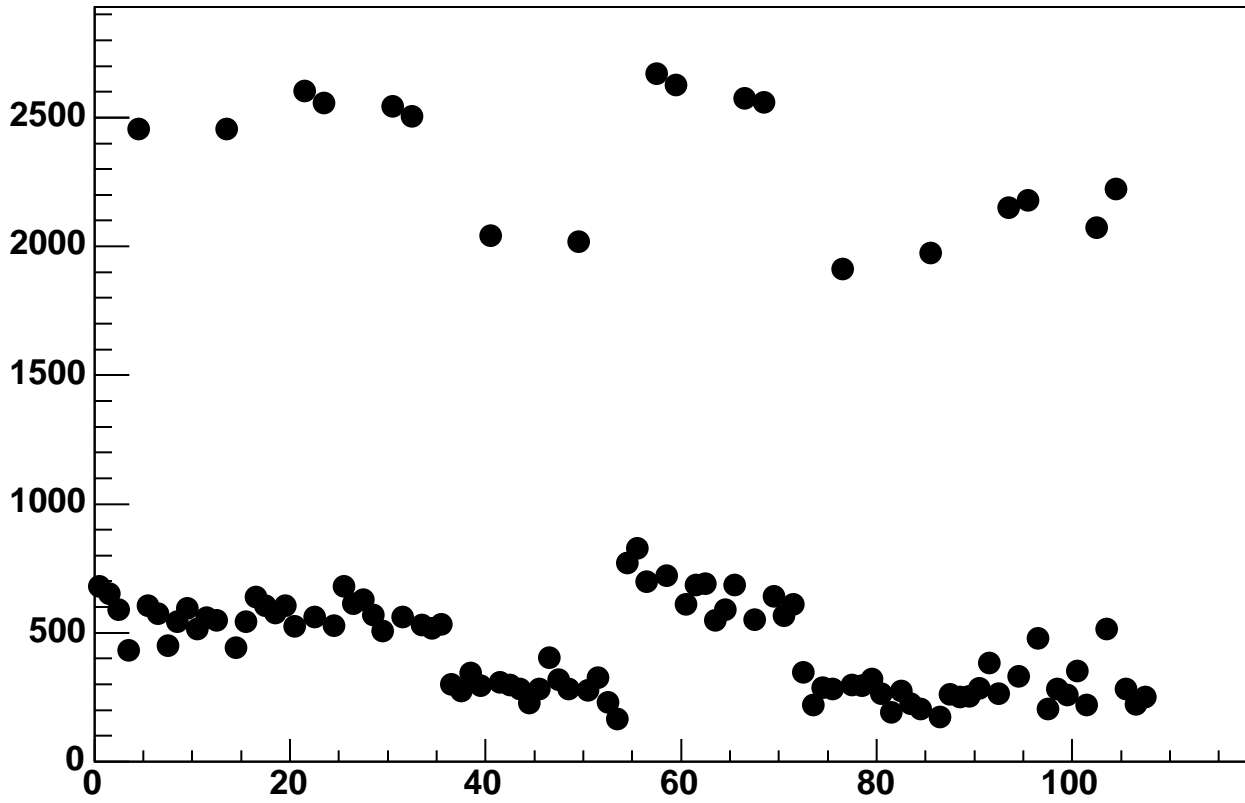
Enable 4, DAC=4000, Hold=115, ADC Mean vs 18\*Chip+Chan



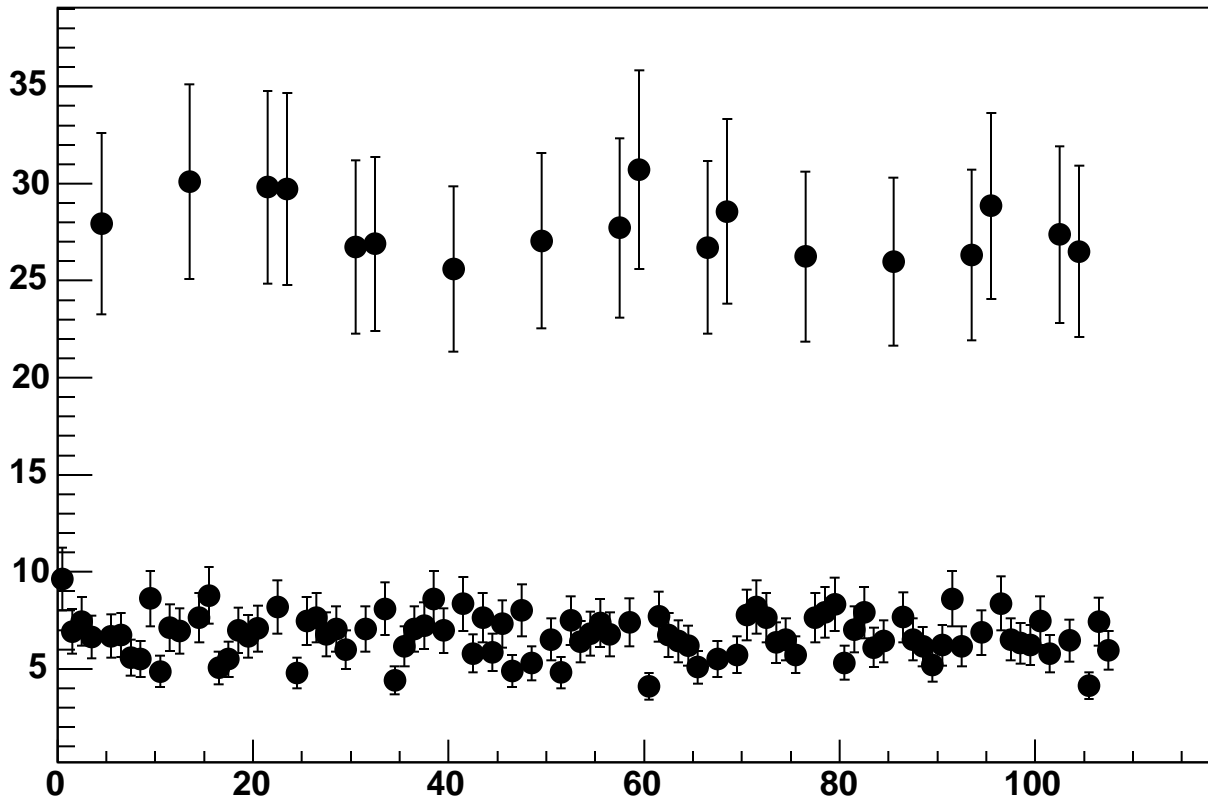
Enable 4, DAC=4000, Hold=115, ADC Noise vs 18\*Chip+Chan



Enable 4, DAC=4000, Hold=120, ADC Mean vs 18\*Chip+Chan

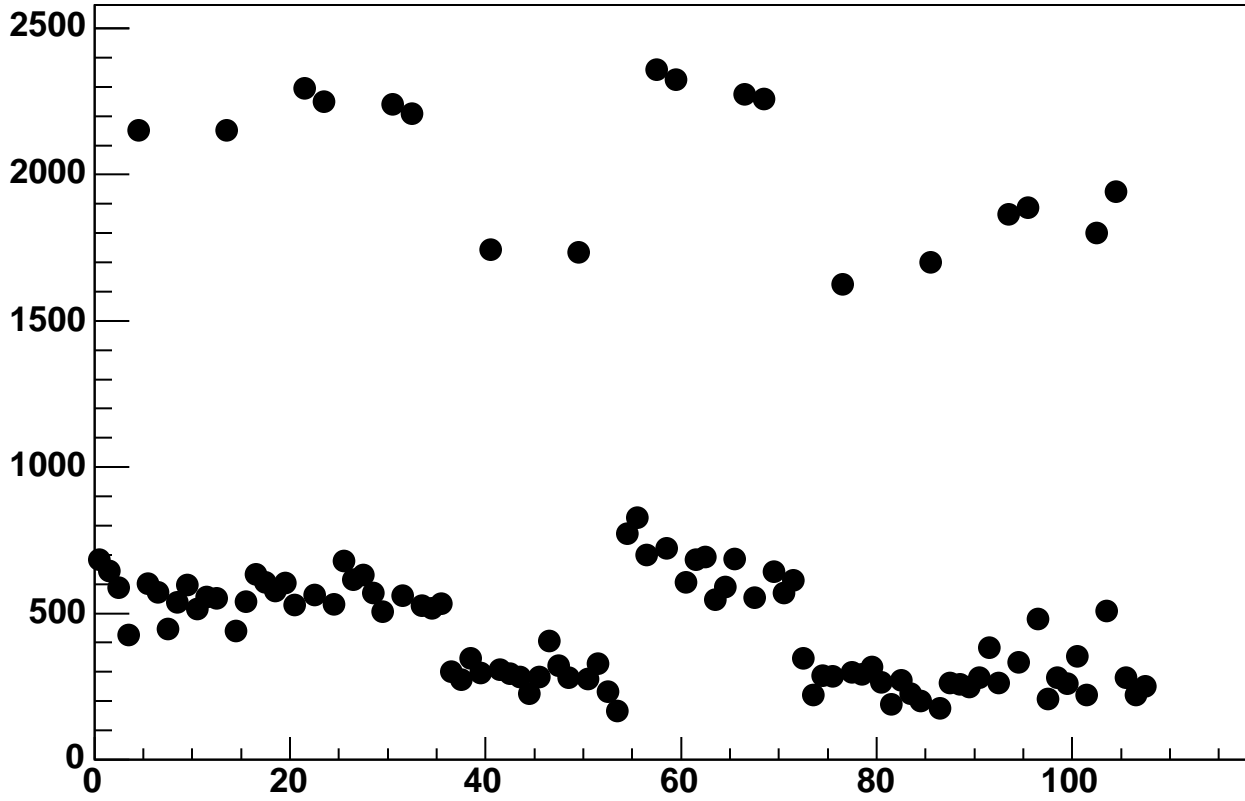


Enable 4, DAC=4000, Hold=120, ADC Noise vs 18\*Chip+Chan

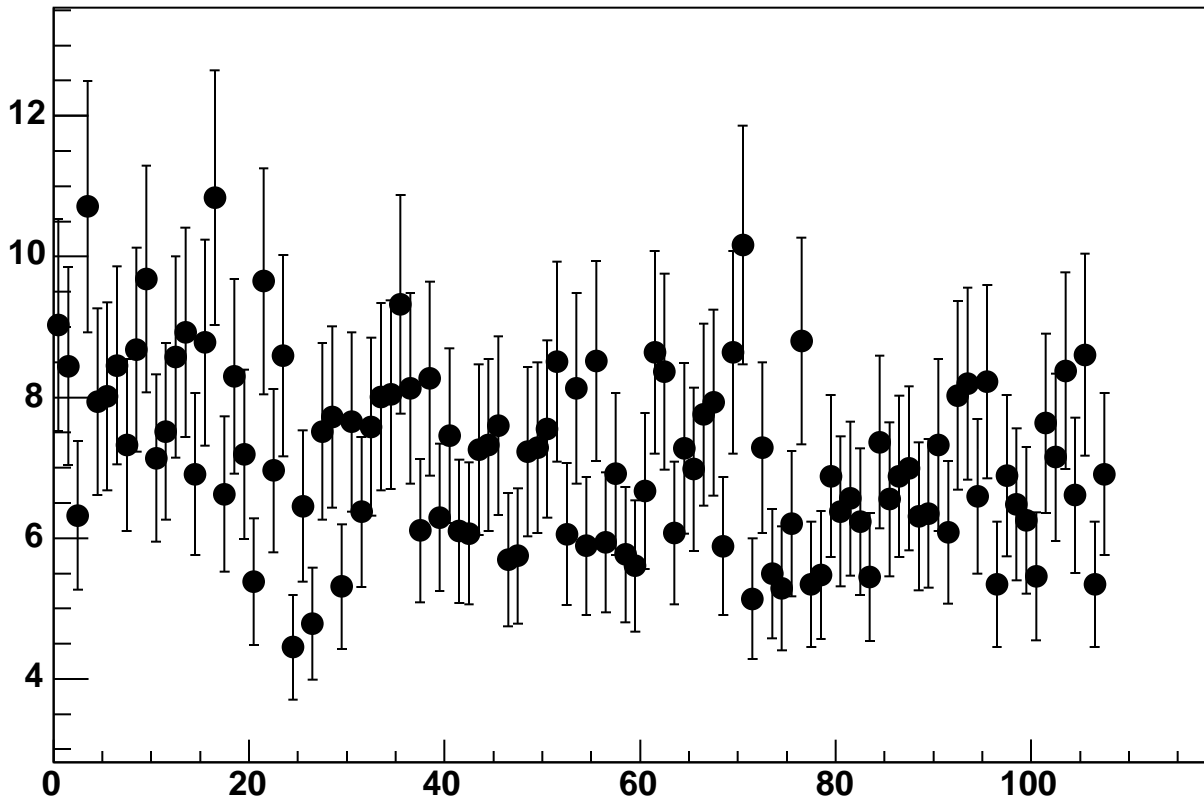




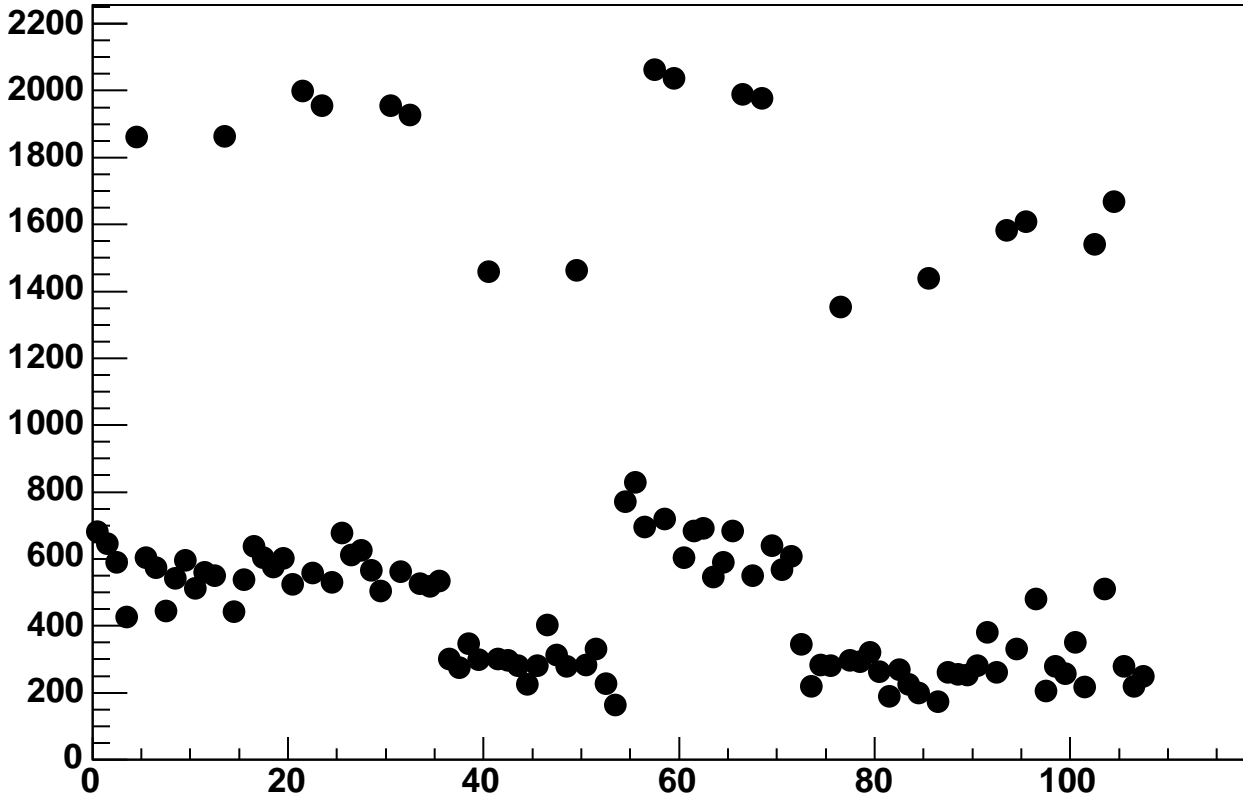
Enable 4, DAC=4000, Hold=125, ADC Mean vs 18\*Chip+Chan



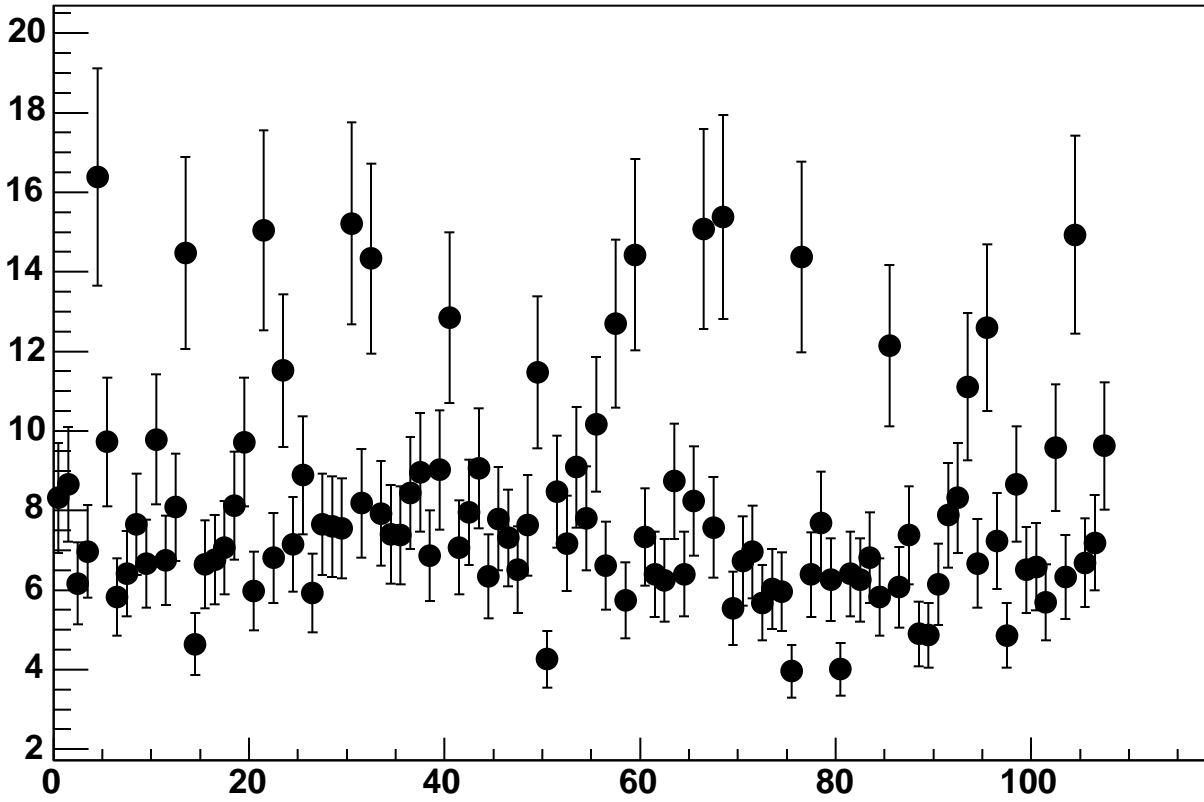
Enable 4, DAC=4000, Hold=125, ADC Noise vs 18\*Chip+Chan



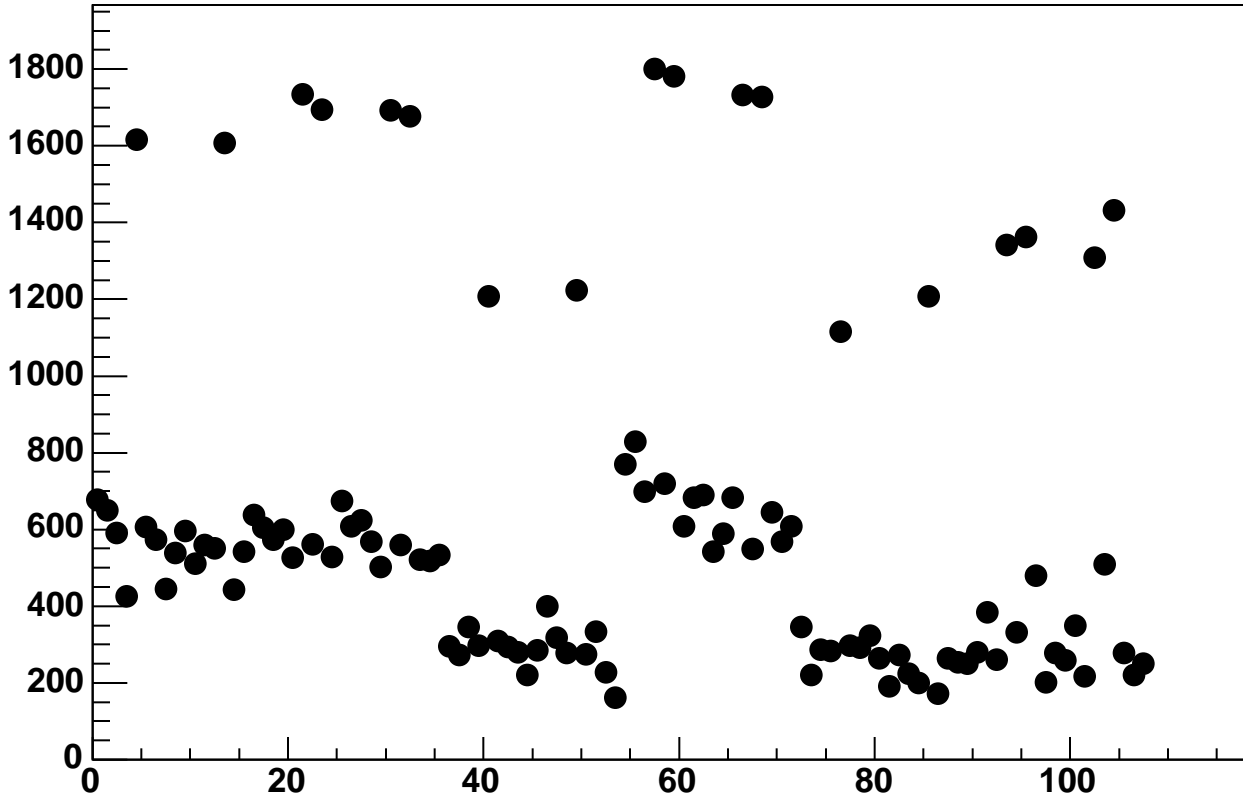
Enable 4, DAC=4000, Hold=130, ADC Mean vs 18\*Chip+Chan



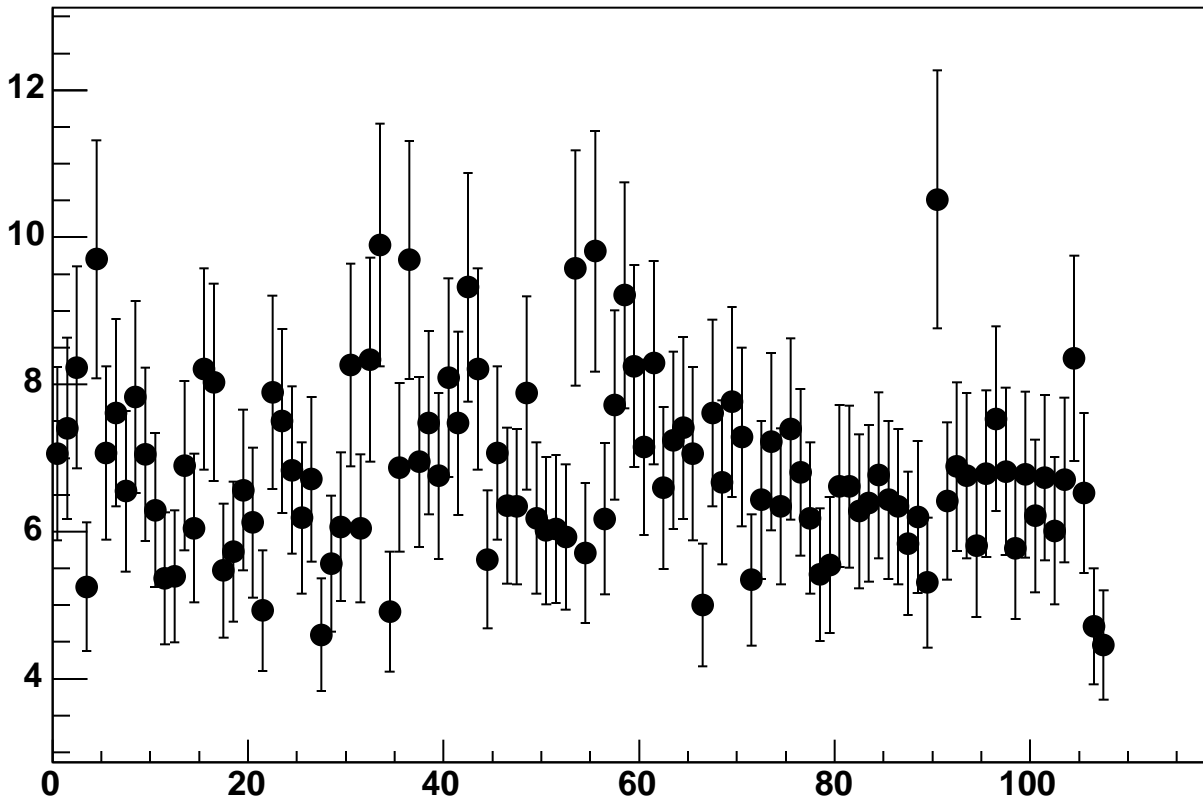
Enable 4, DAC=4000, Hold=130, ADC Noise vs 18\*Chip+Chan



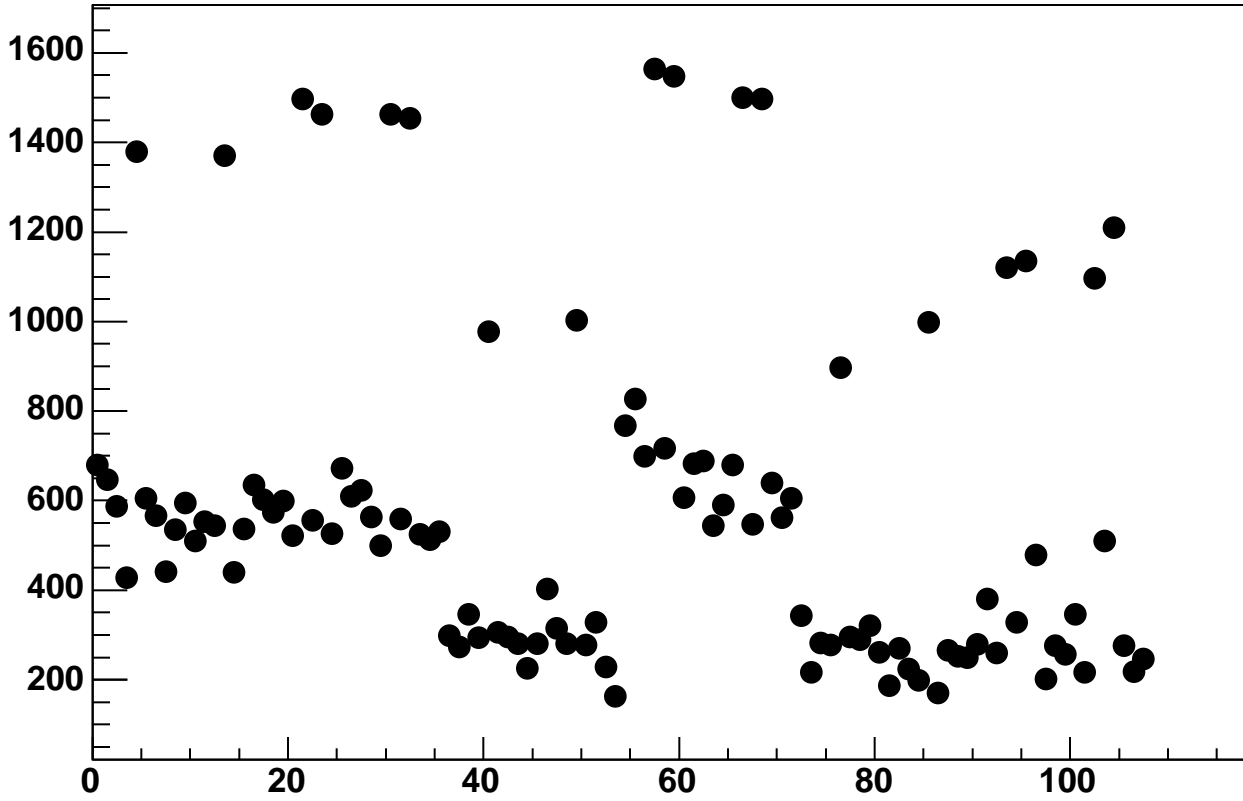
Enable 4, DAC=4000, Hold=135, ADC Mean vs 18\*Chip+Chan



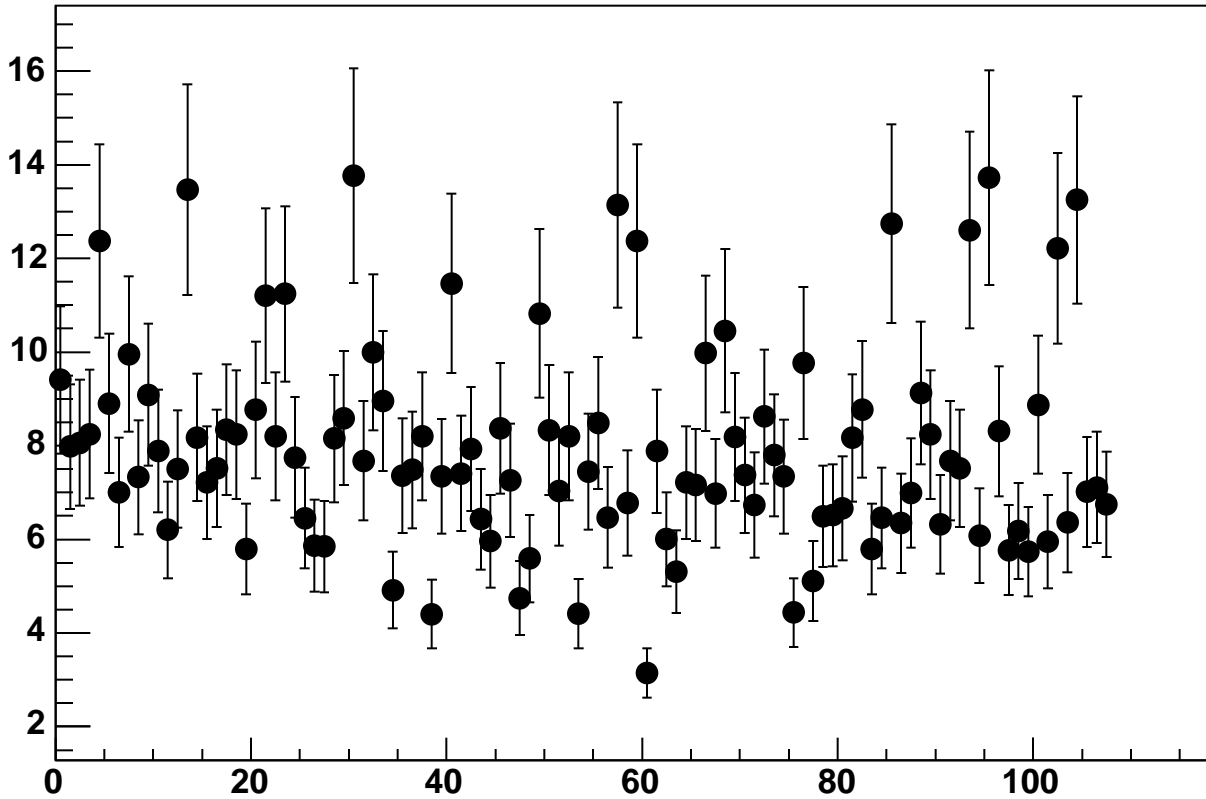
Enable 4, DAC=4000, Hold=135, ADC Noise vs 18\*Chip+Chan



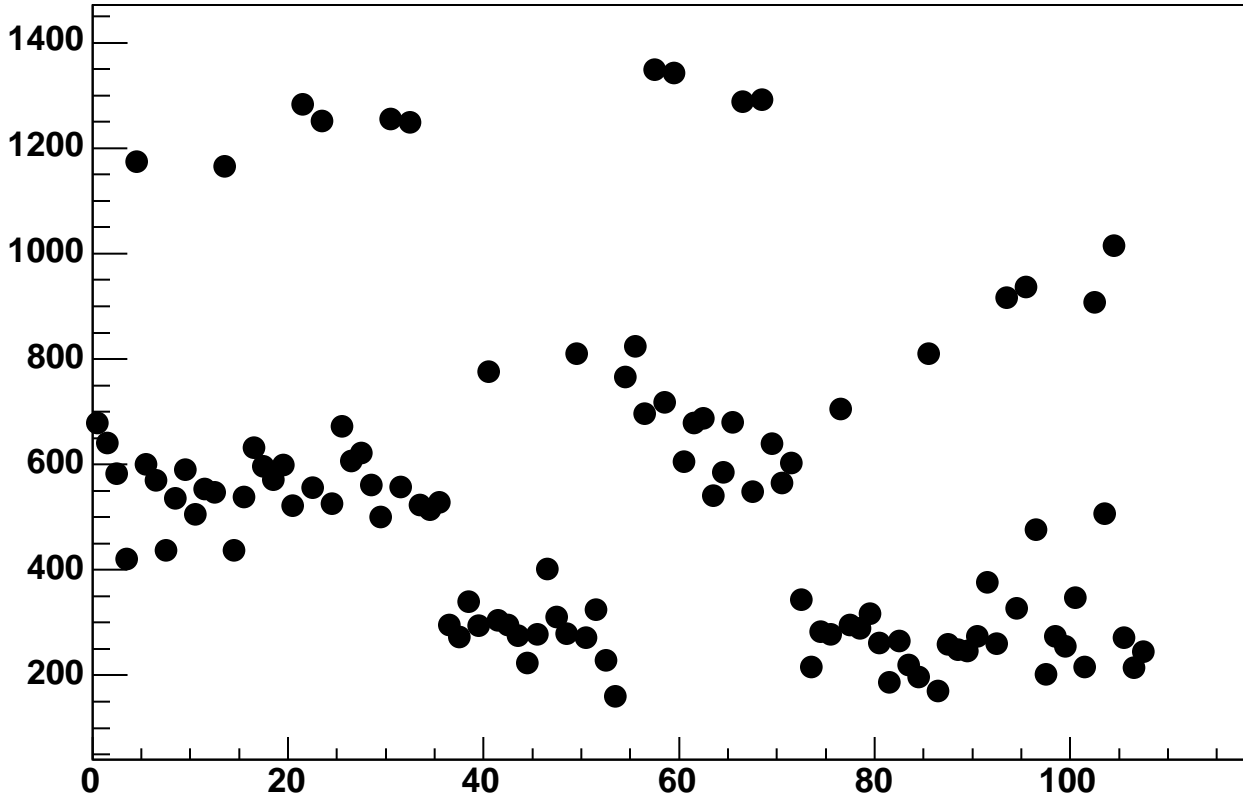
Enable 4, DAC=4000, Hold=140, ADC Mean vs 18\*Chip+Chan



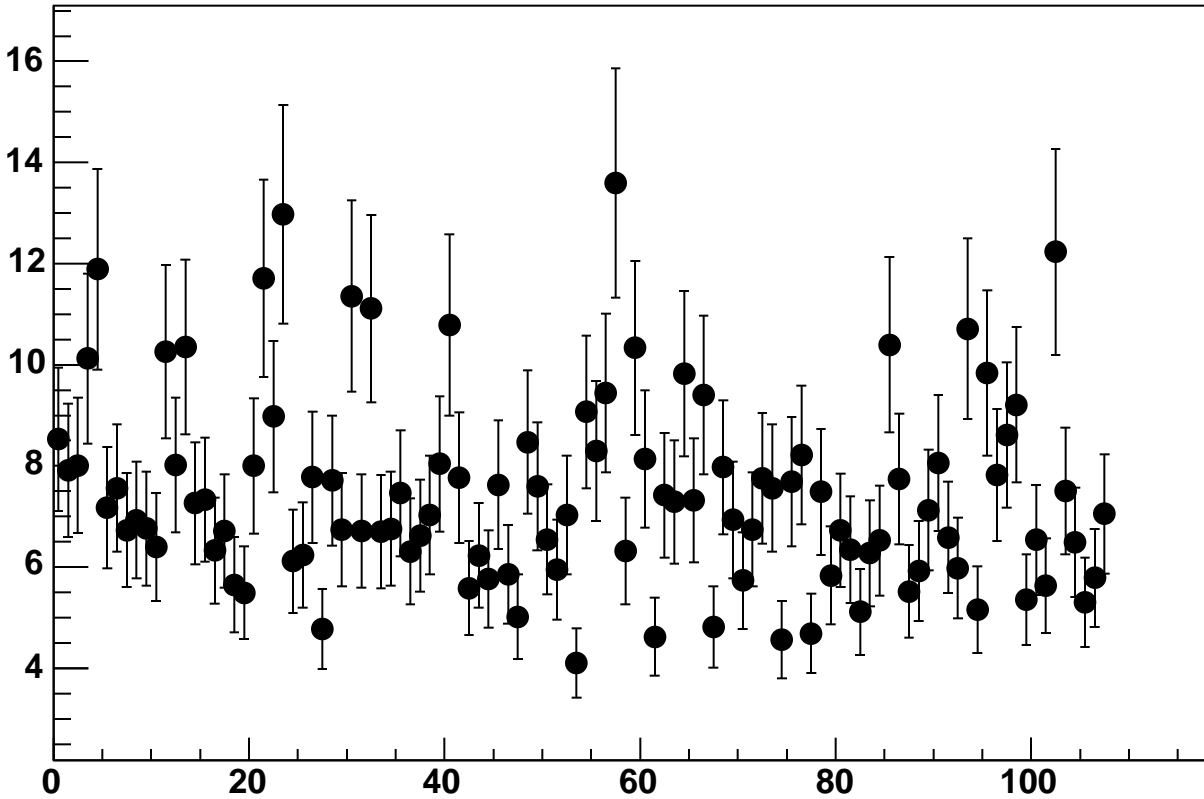
Enable 4, DAC=4000, Hold=140, ADC Noise vs 18\*Chip+Chan



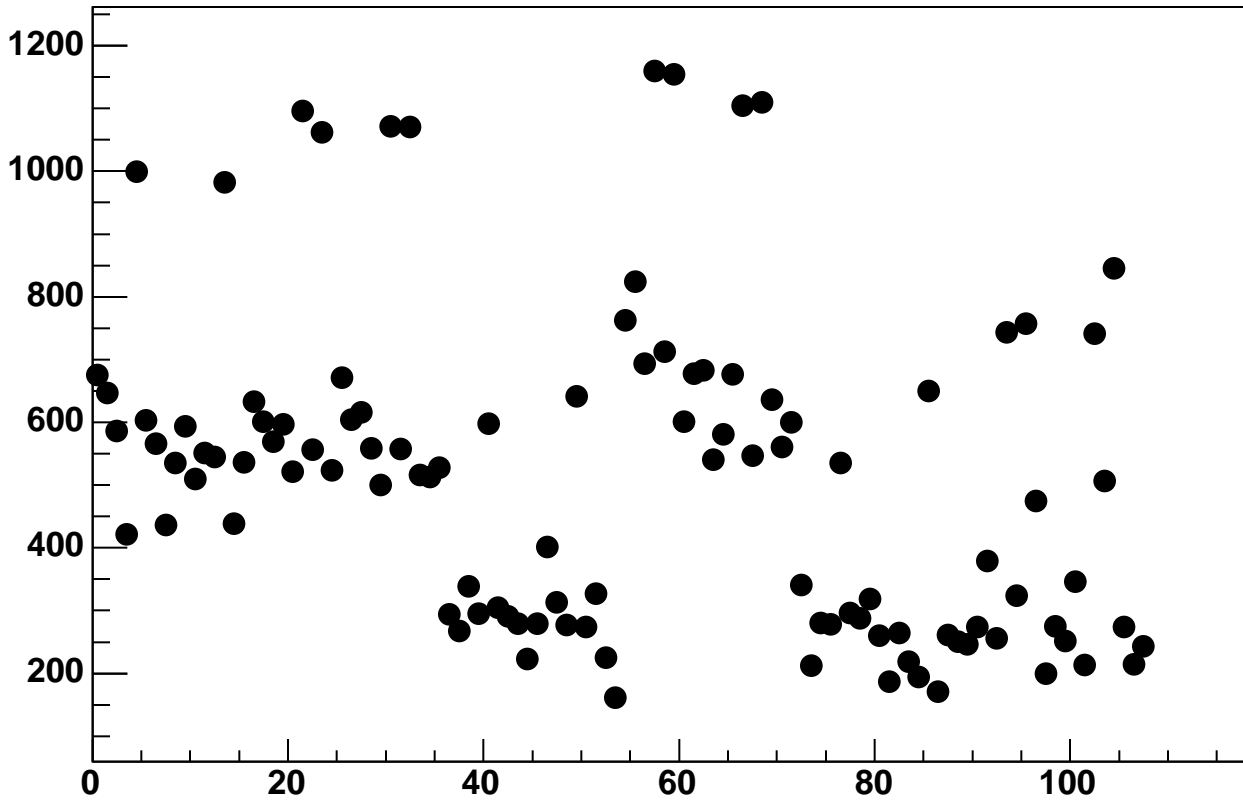
Enable 4, DAC=4000, Hold=145, ADC Mean vs 18\*Chip+Chan



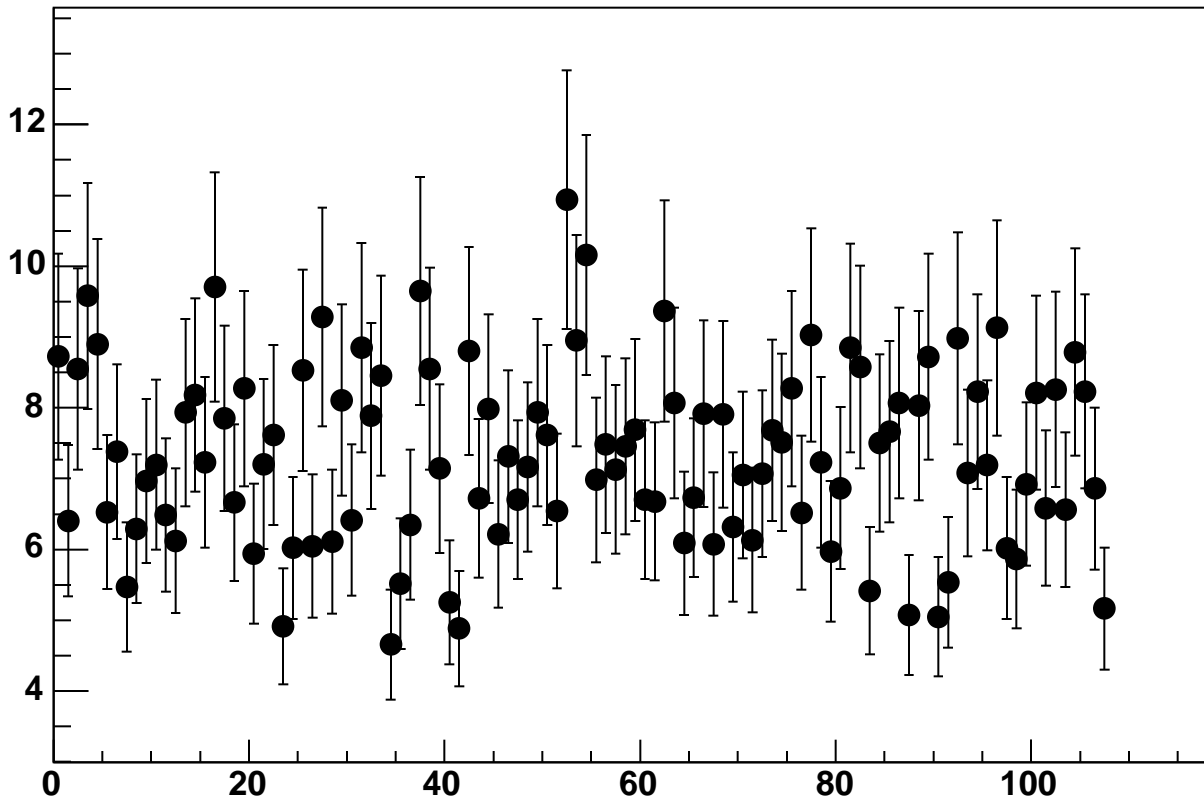
Enable 4, DAC=4000, Hold=145, ADC Noise vs 18\*Chip+Chan



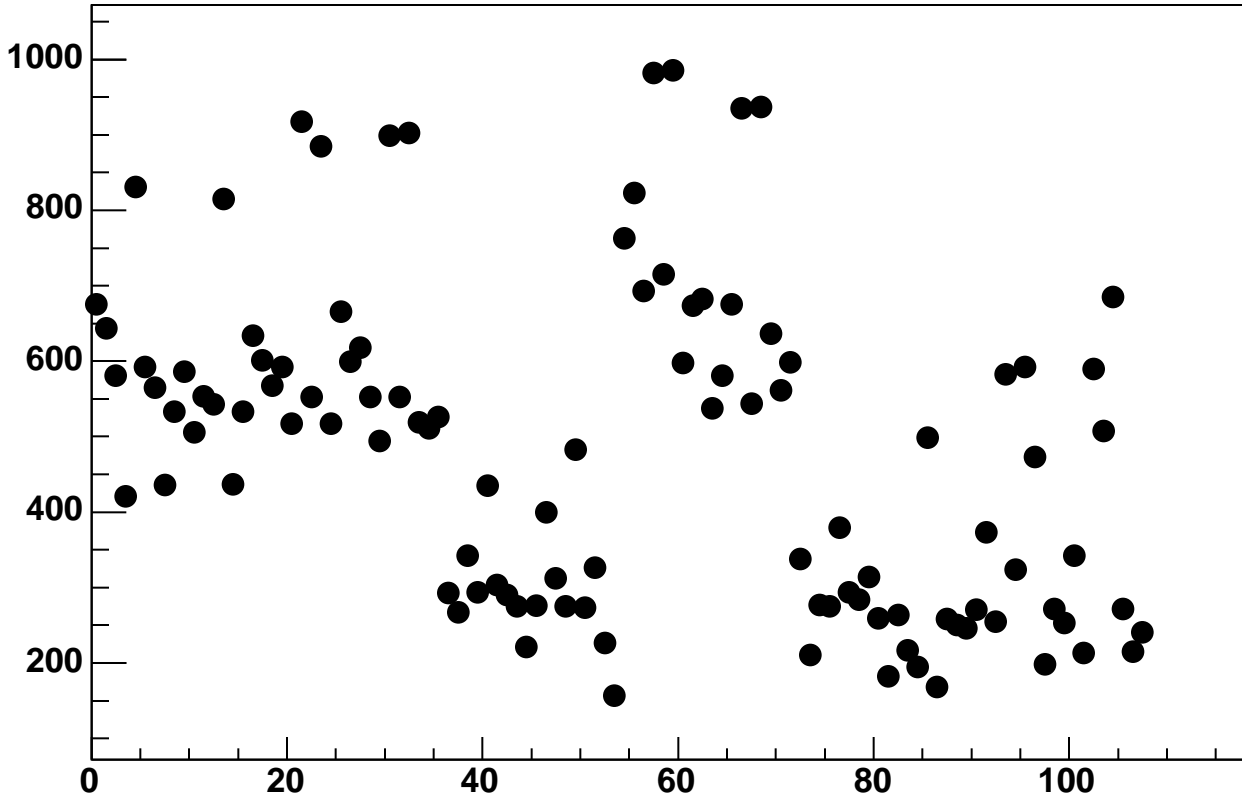
Enable 4, DAC=4000, Hold=150, ADC Mean vs 18\*Chip+Chan



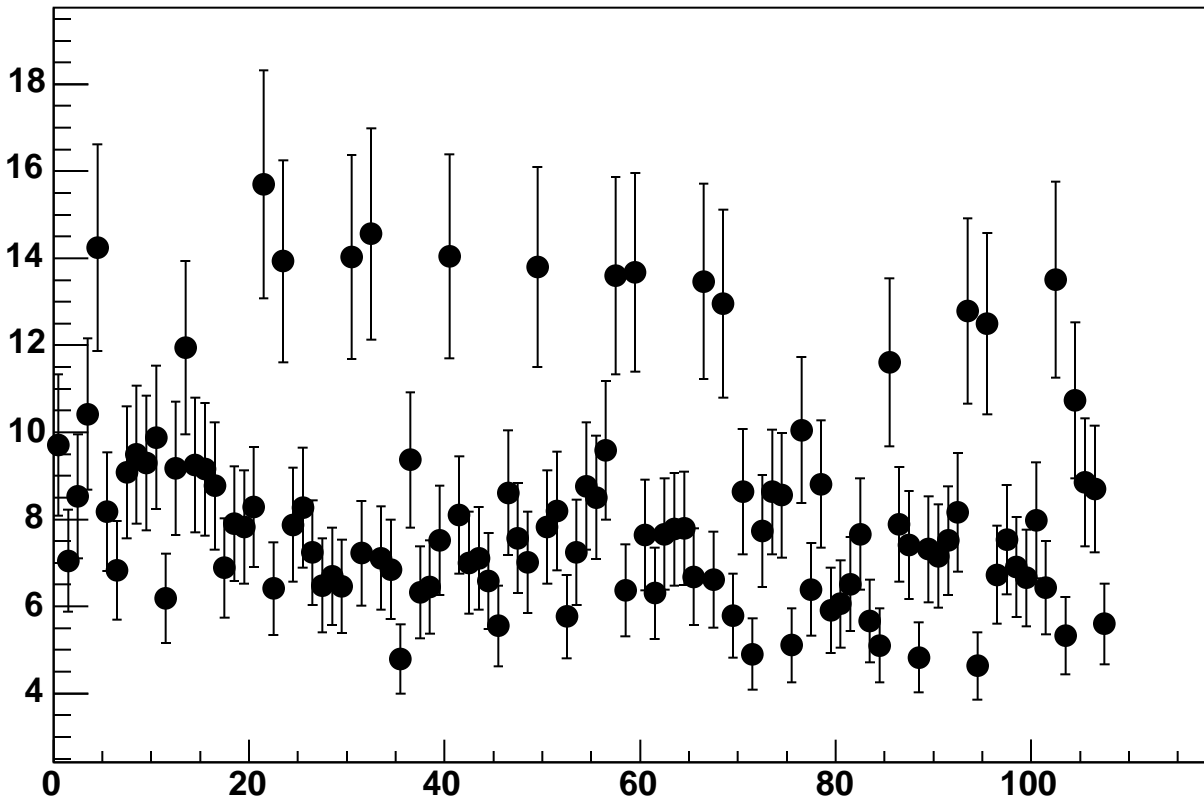
Enable 4, DAC=4000, Hold=150, ADC Noise vs 18\*Chip+Chan



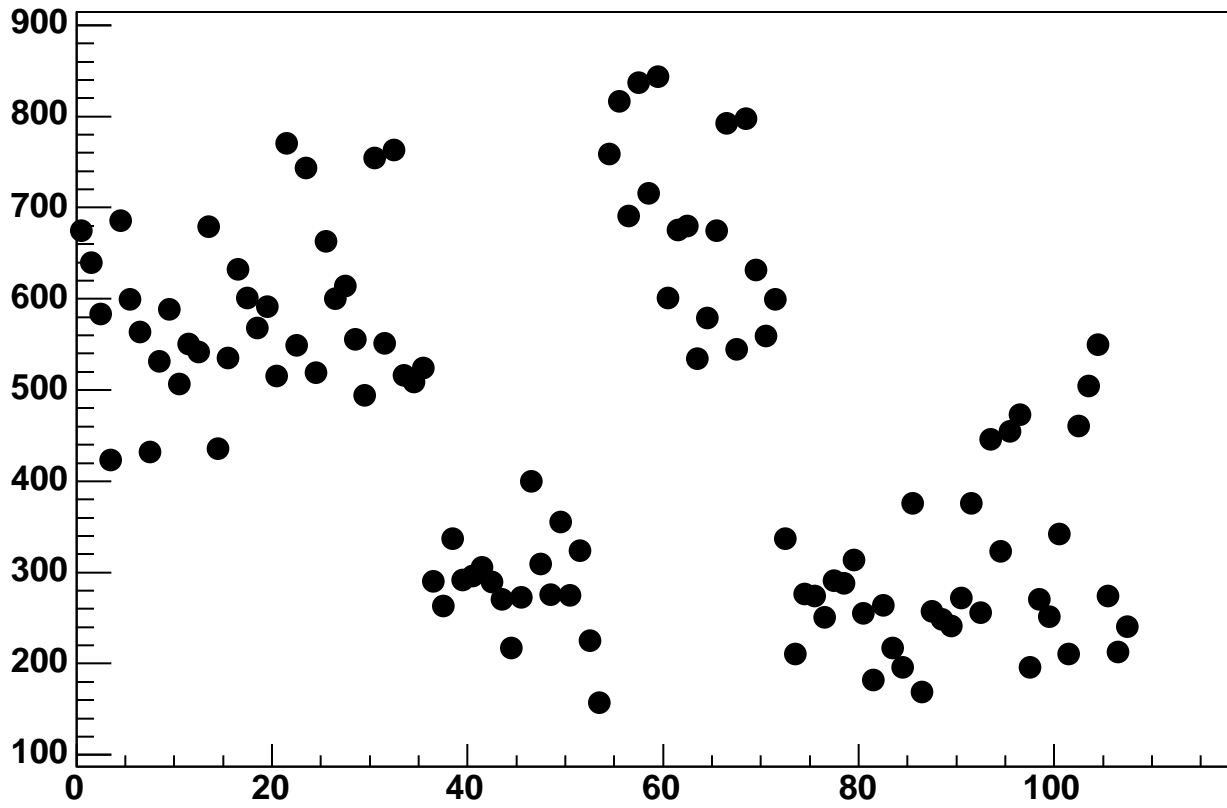
Enable 4, DAC=4000, Hold=155, ADC Mean vs 18\*Chip+Chan



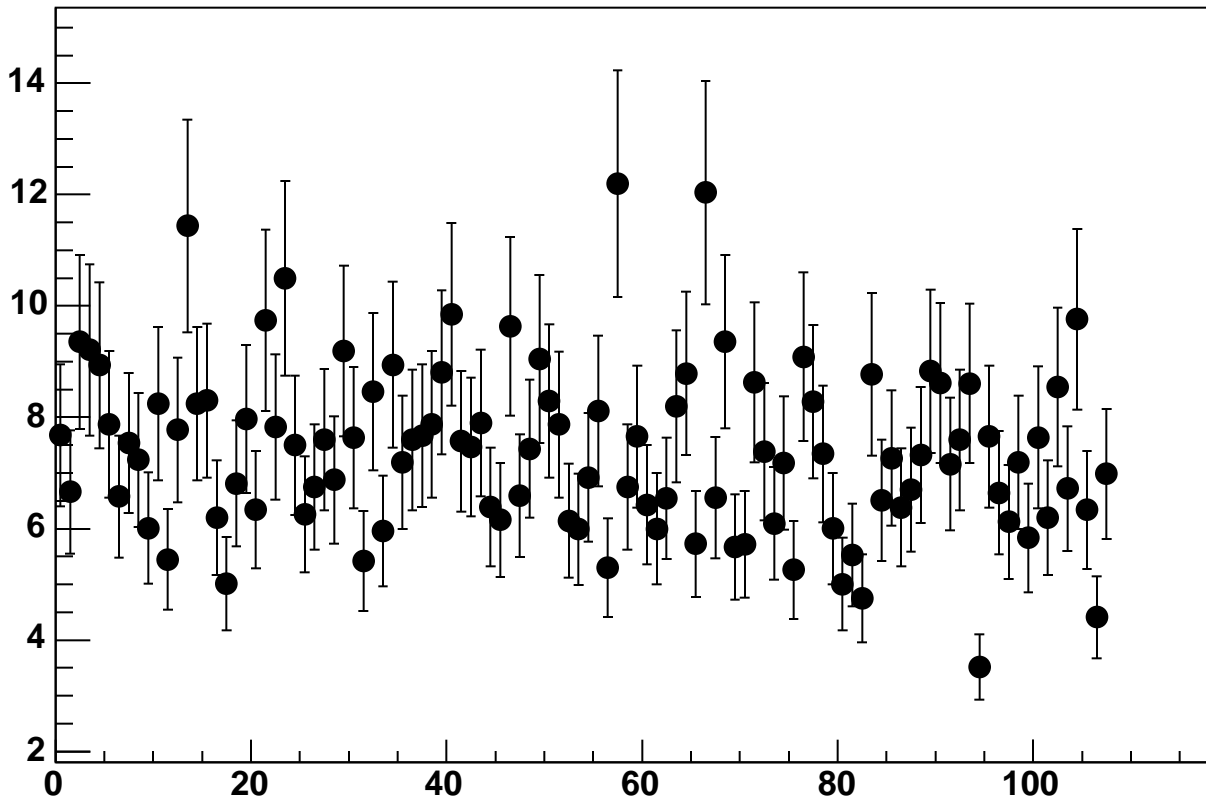
Enable 4, DAC=4000, Hold=155, ADC Noise vs 18\*Chip+Chan



Enable 4, DAC=4000, Hold=160, ADC Mean vs 18\*Chip+Chan

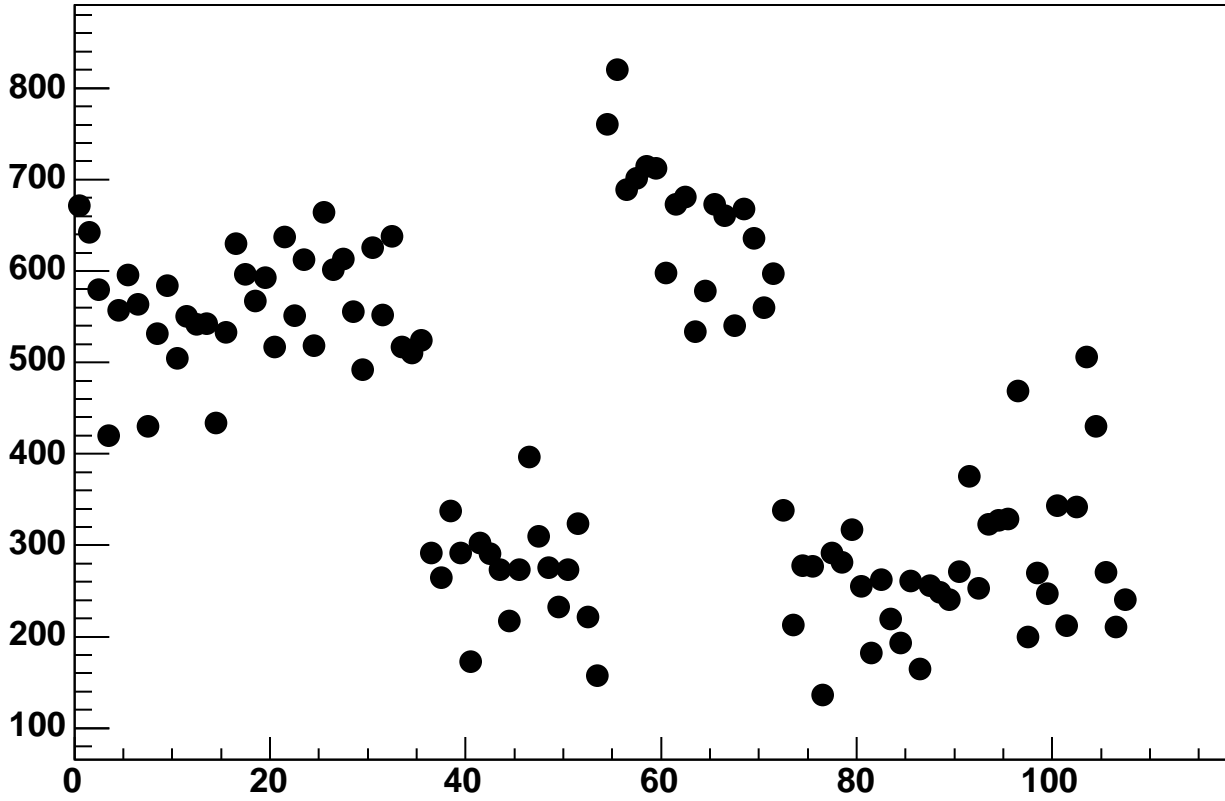


Enable 4, DAC=4000, Hold=160, ADC Noise vs 18\*Chip+Chan

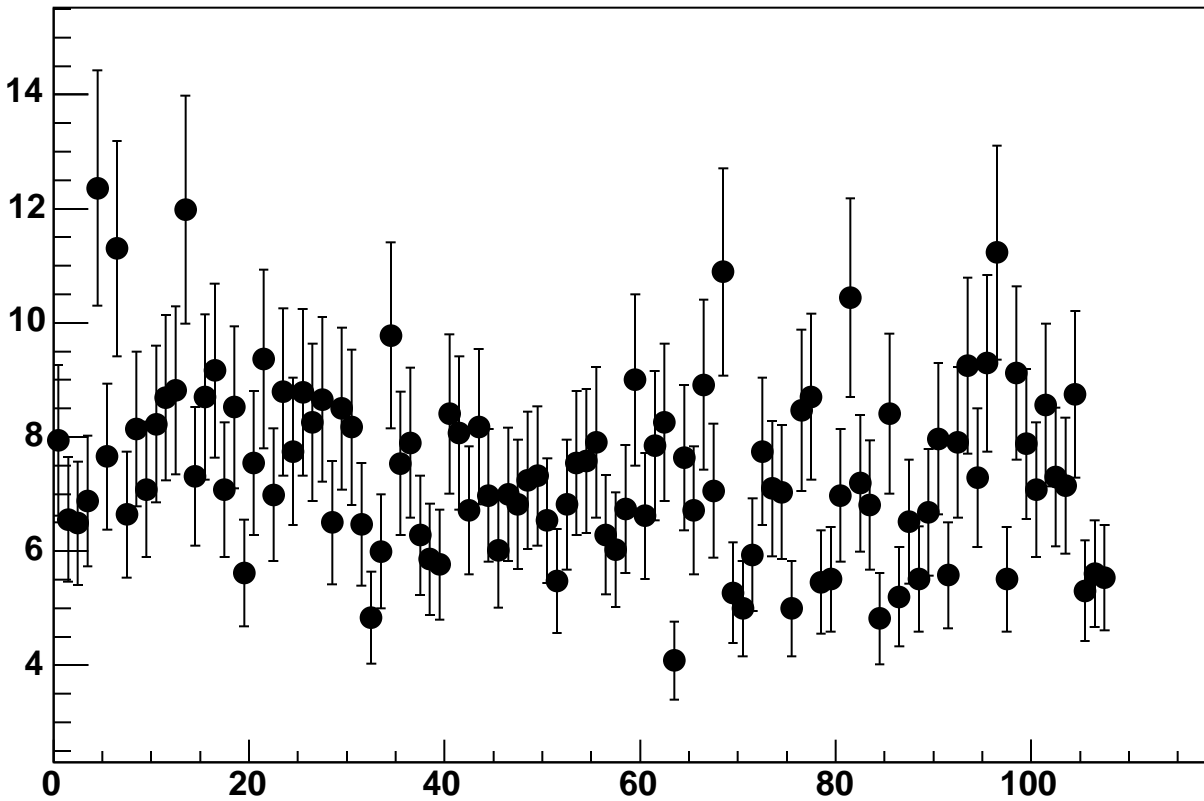




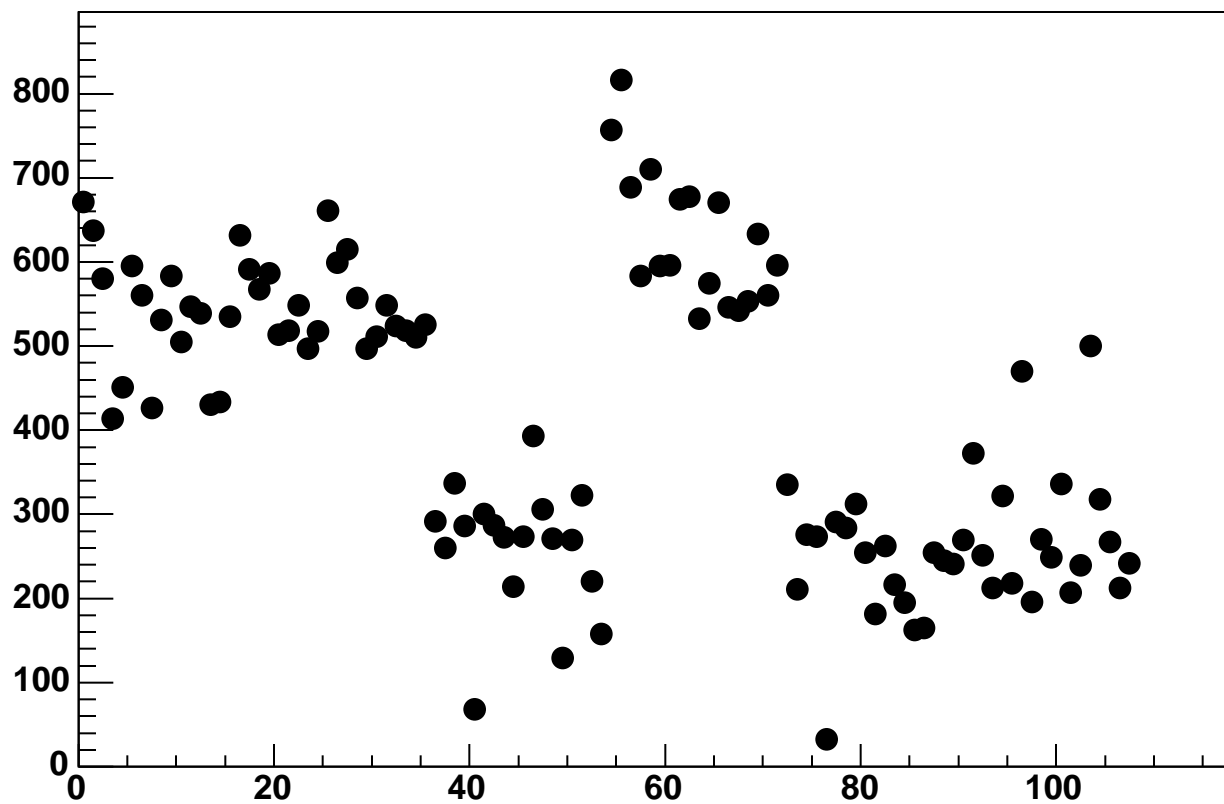
Enable 4, DAC=4000, Hold=165, ADC Mean vs 18\*Chip+Chan



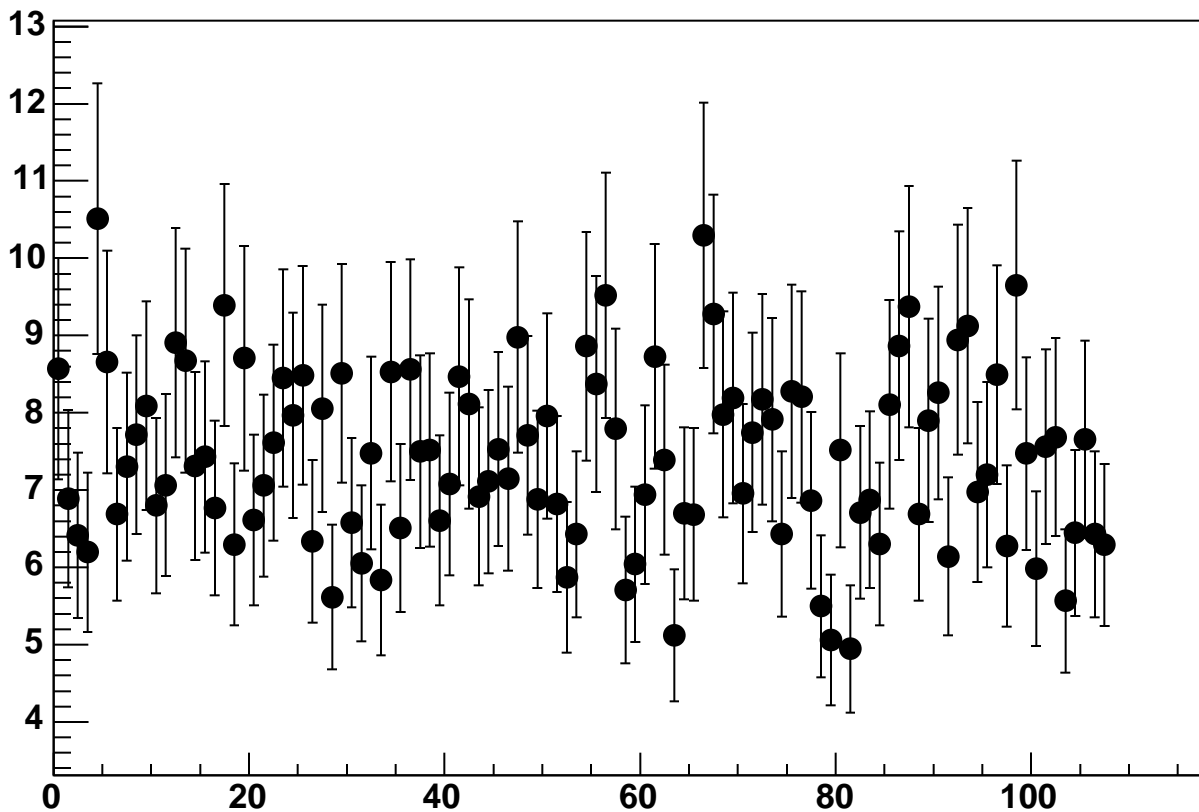
Enable 4, DAC=4000, Hold=165, ADC Noise vs 18\*Chip+Chan



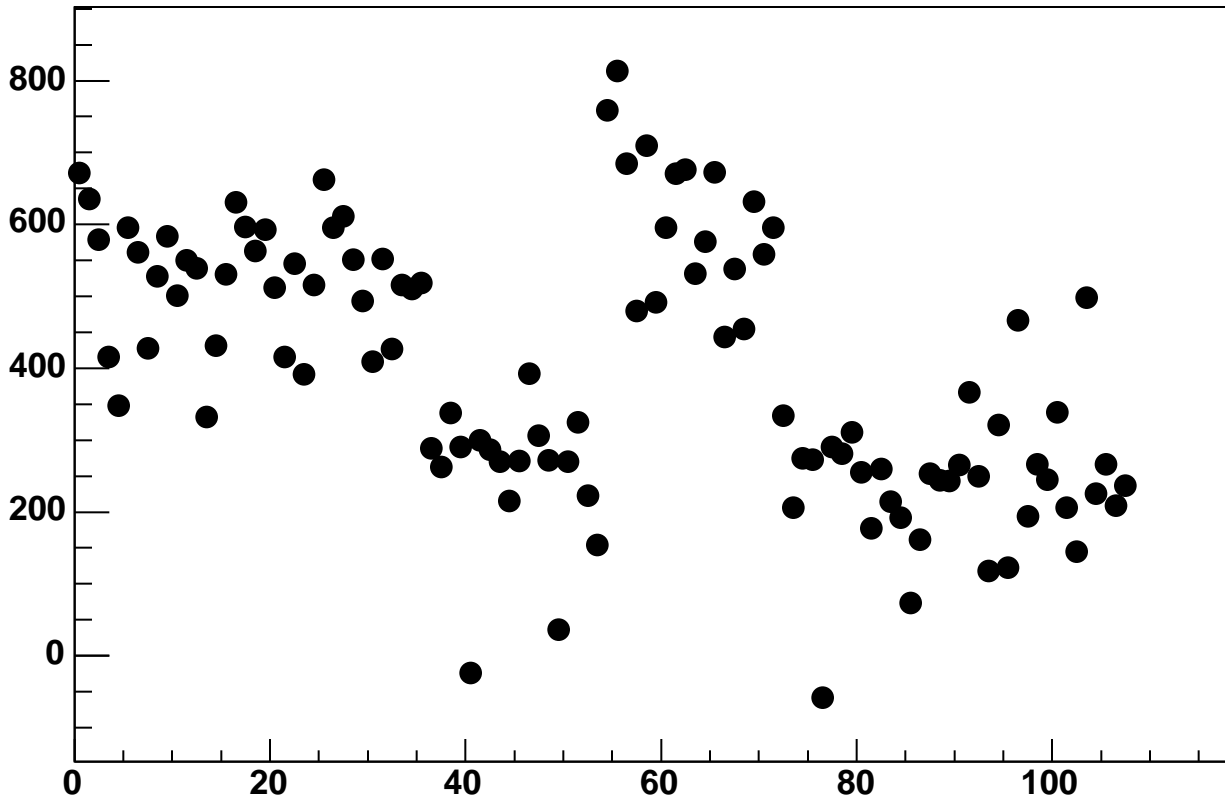
Enable 4, DAC=4000, Hold=170, ADC Mean vs 18\*Chip+Chan



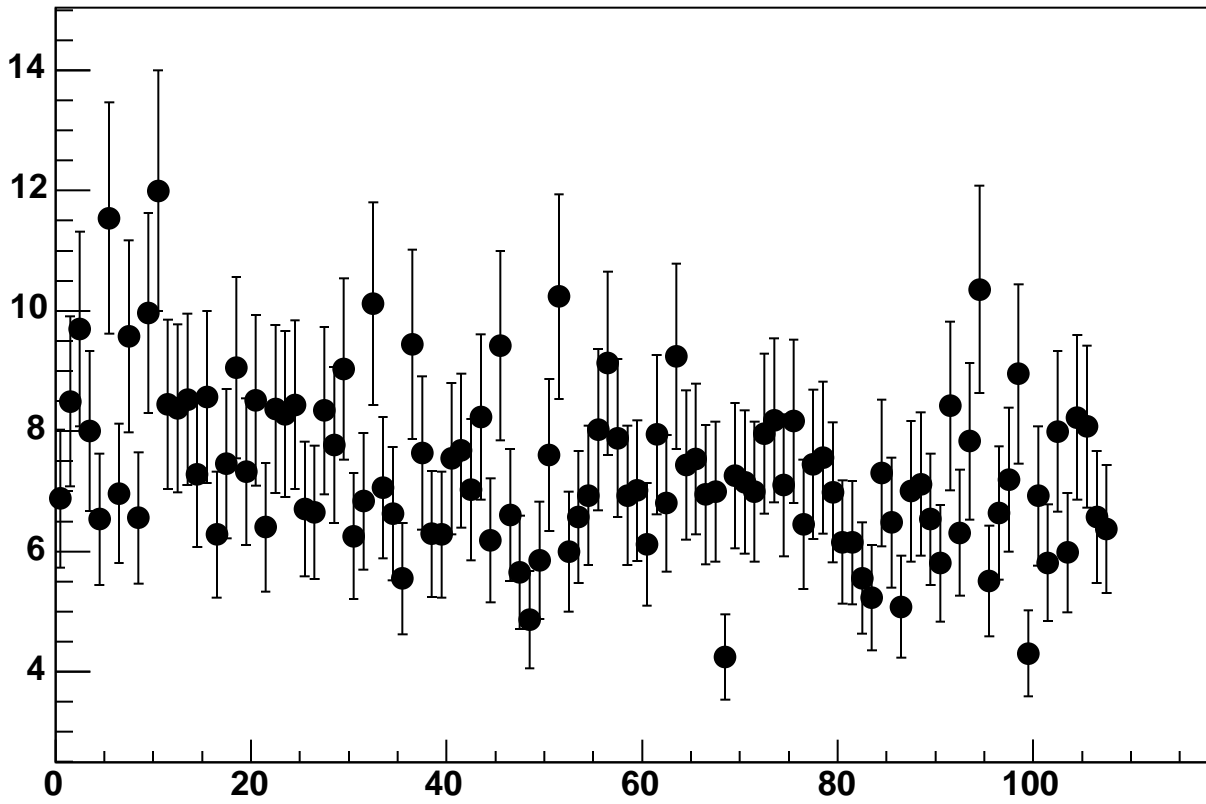
Enable 4, DAC=4000, Hold=170, ADC Noise vs 18\*Chip+Chan



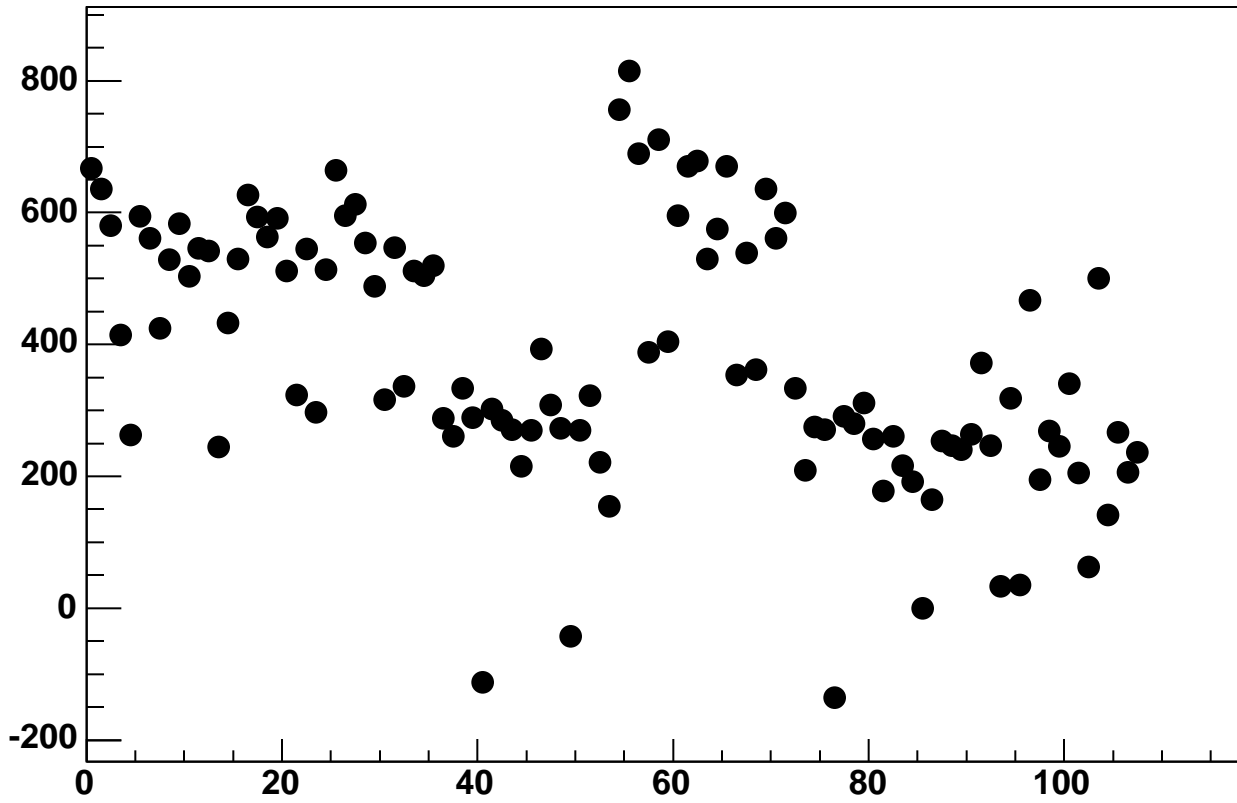
Enable 4, DAC=4000, Hold=175, ADC Mean vs 18\*Chip+Chan



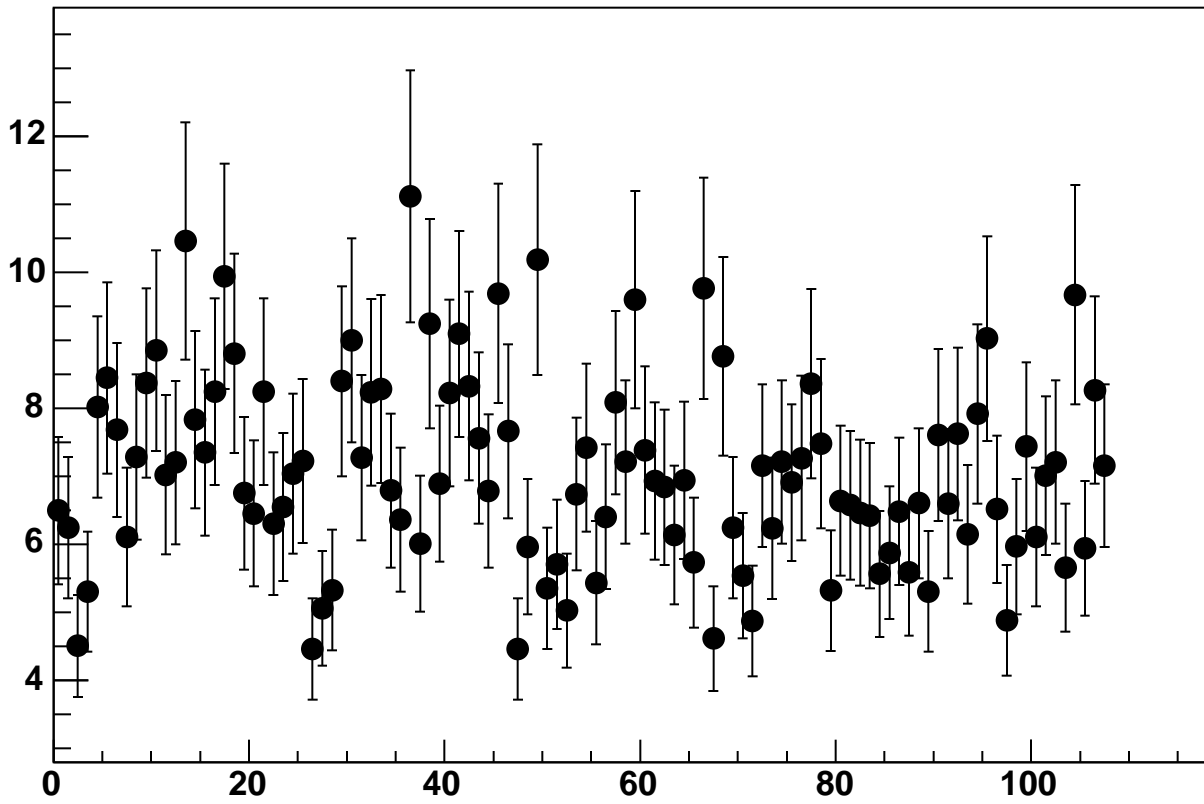
Enable 4, DAC=4000, Hold=175, ADC Noise vs 18\*Chip+Chan



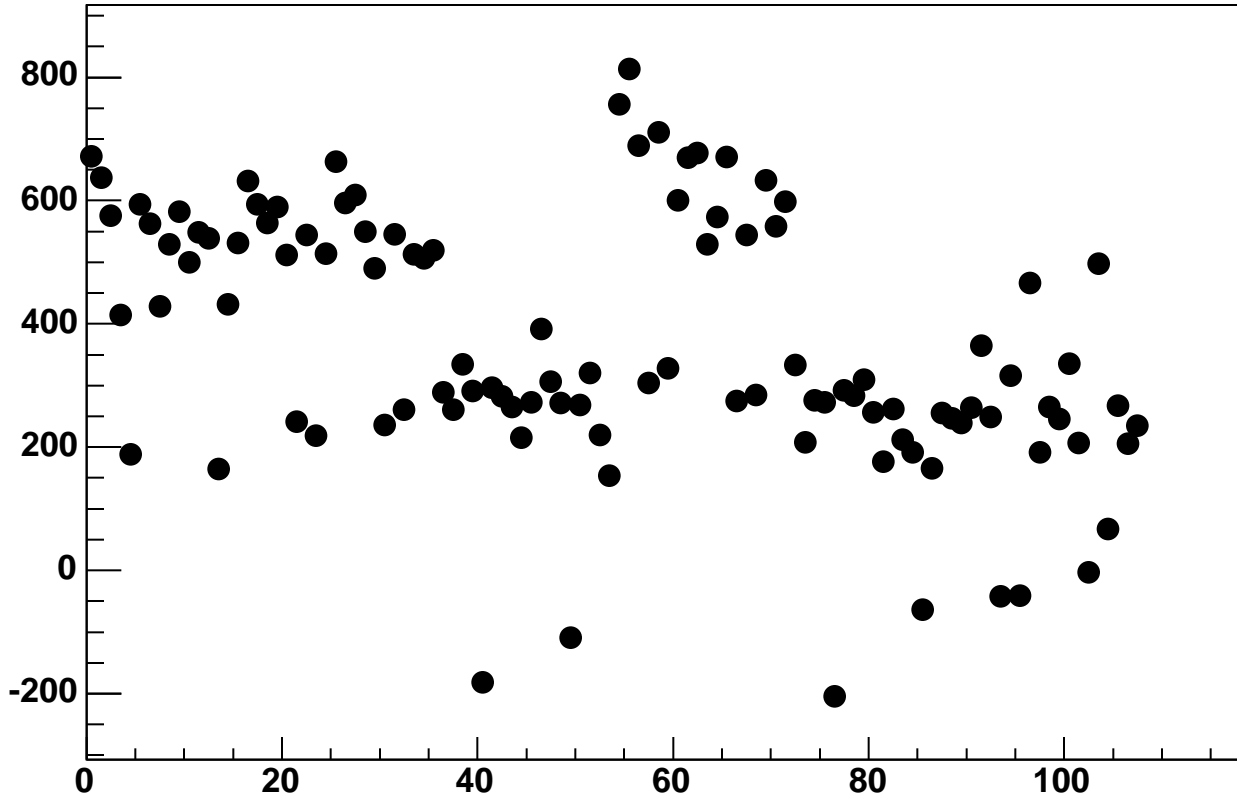
Enable 4, DAC=4000, Hold=180, ADC Mean vs 18\*Chip+Chan



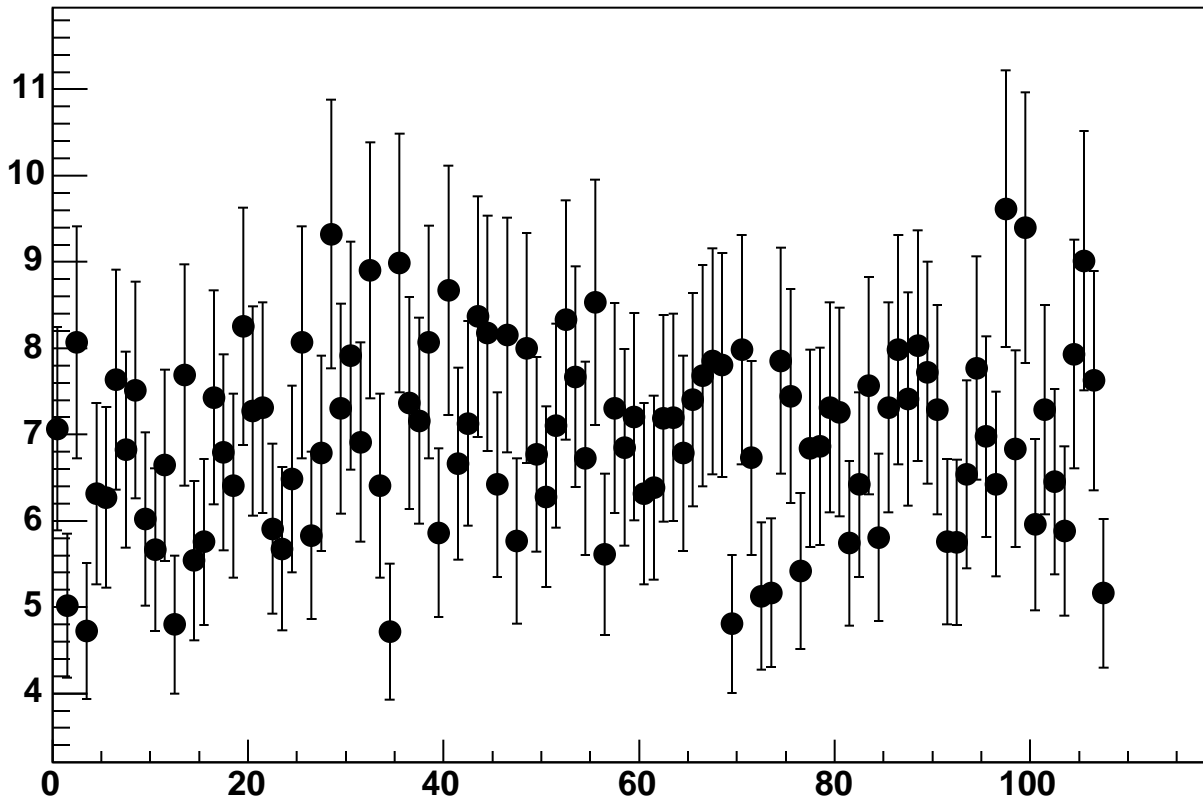
Enable 4, DAC=4000, Hold=180, ADC Noise vs 18\*Chip+Chan



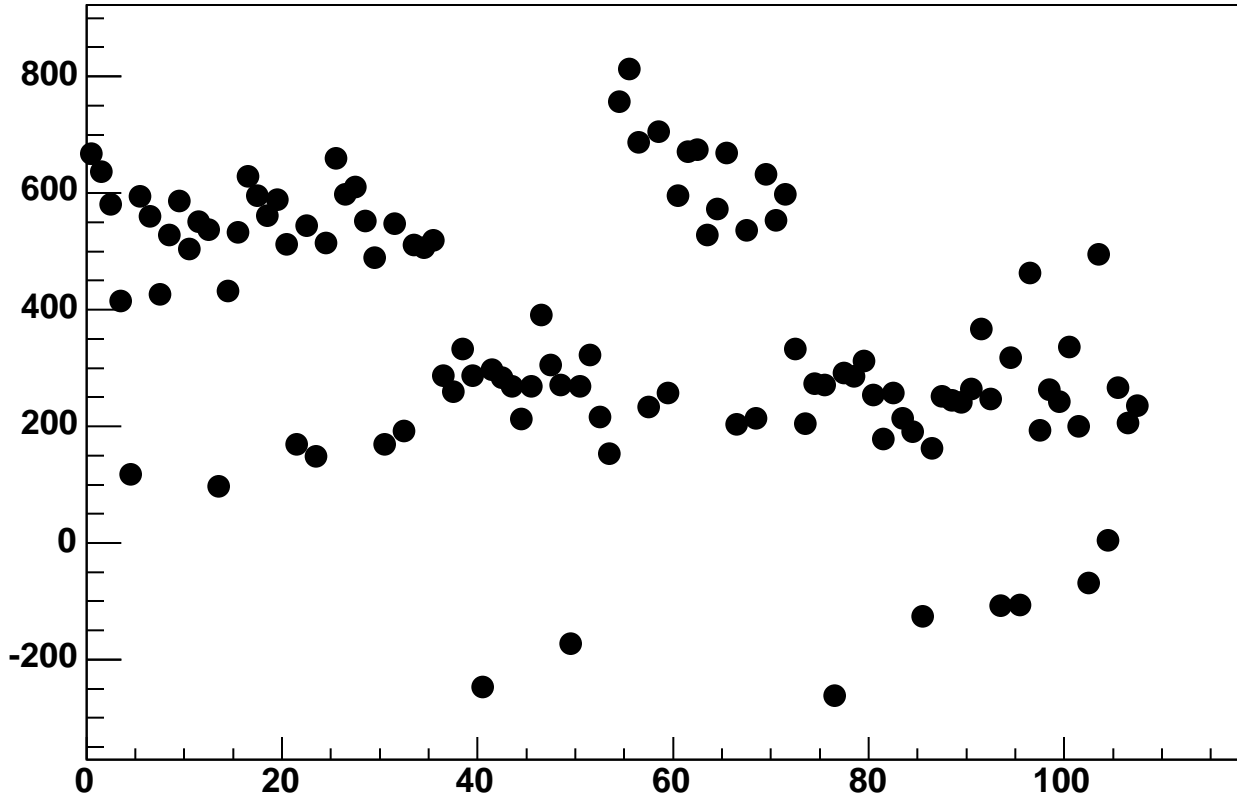
Enable 4, DAC=4000, Hold=185, ADC Mean vs 18\*Chip+Chan



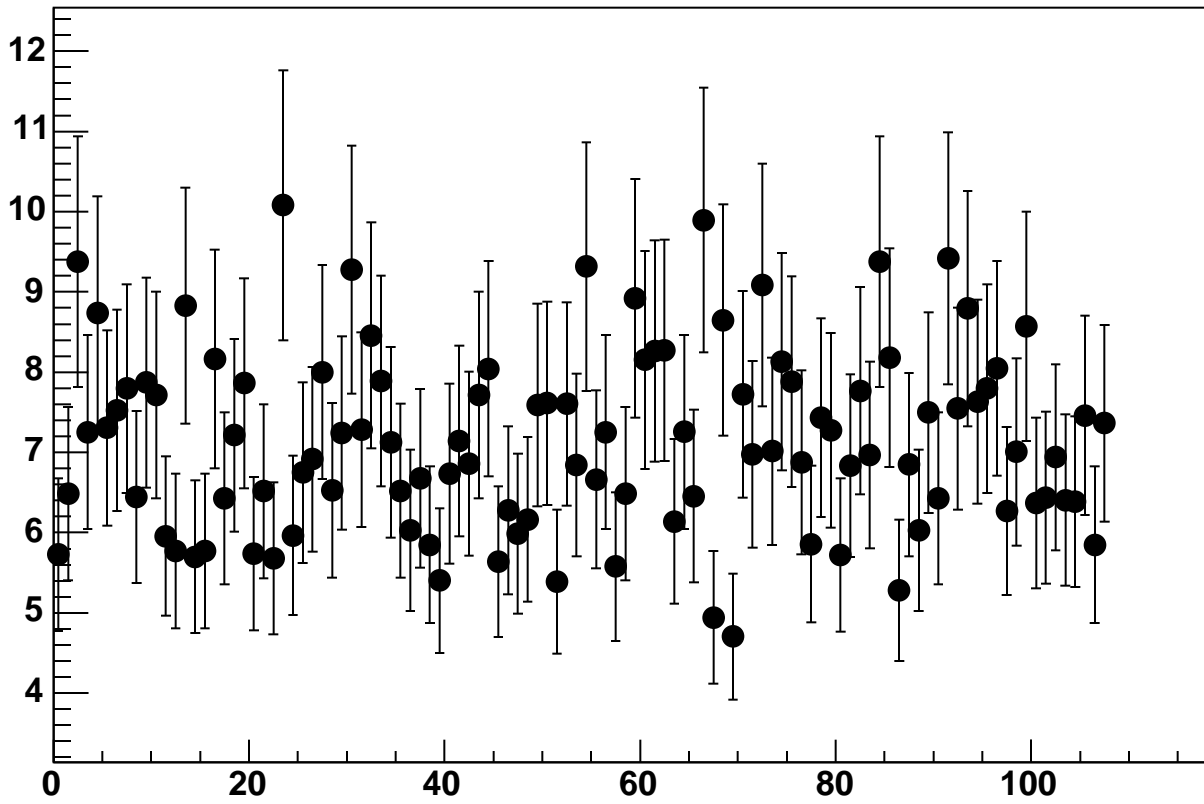
Enable 4, DAC=4000, Hold=185, ADC Noise vs 18\*Chip+Chan



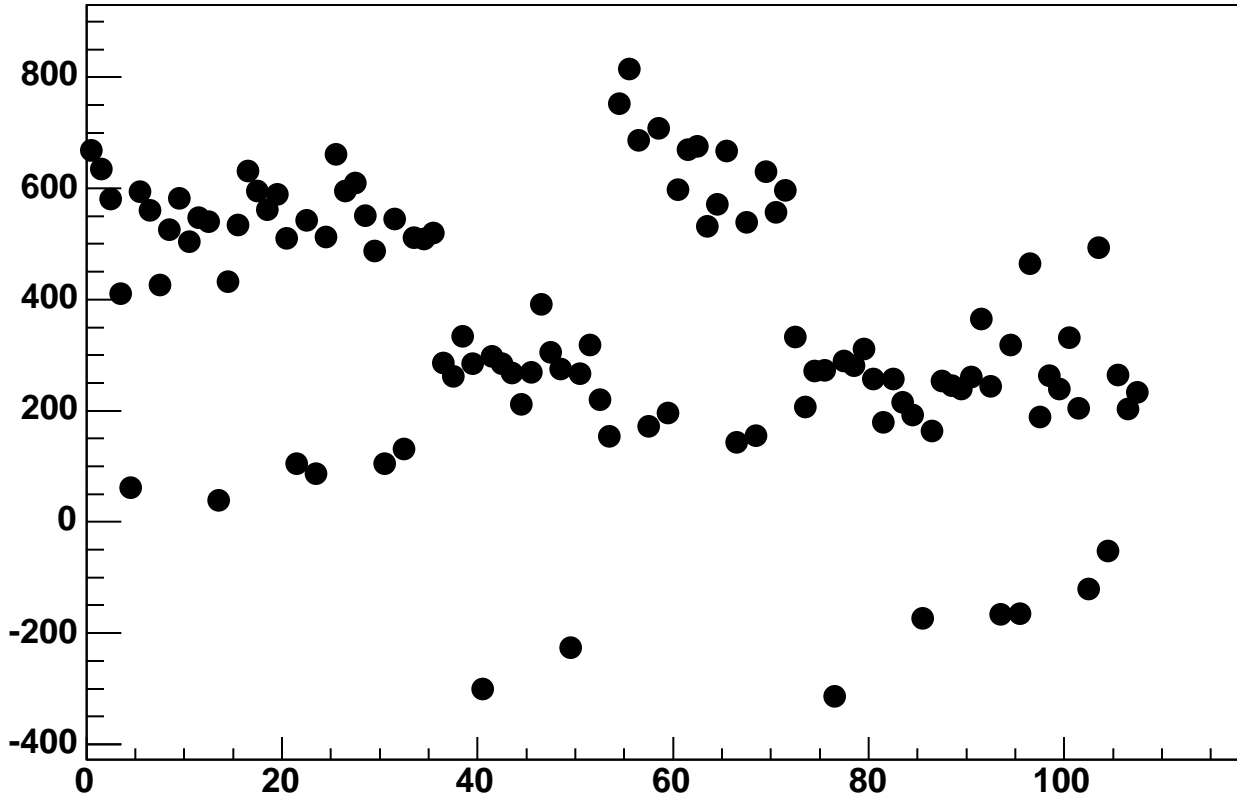
Enable 4, DAC=4000, Hold=190, ADC Mean vs 18\*Chip+Chan



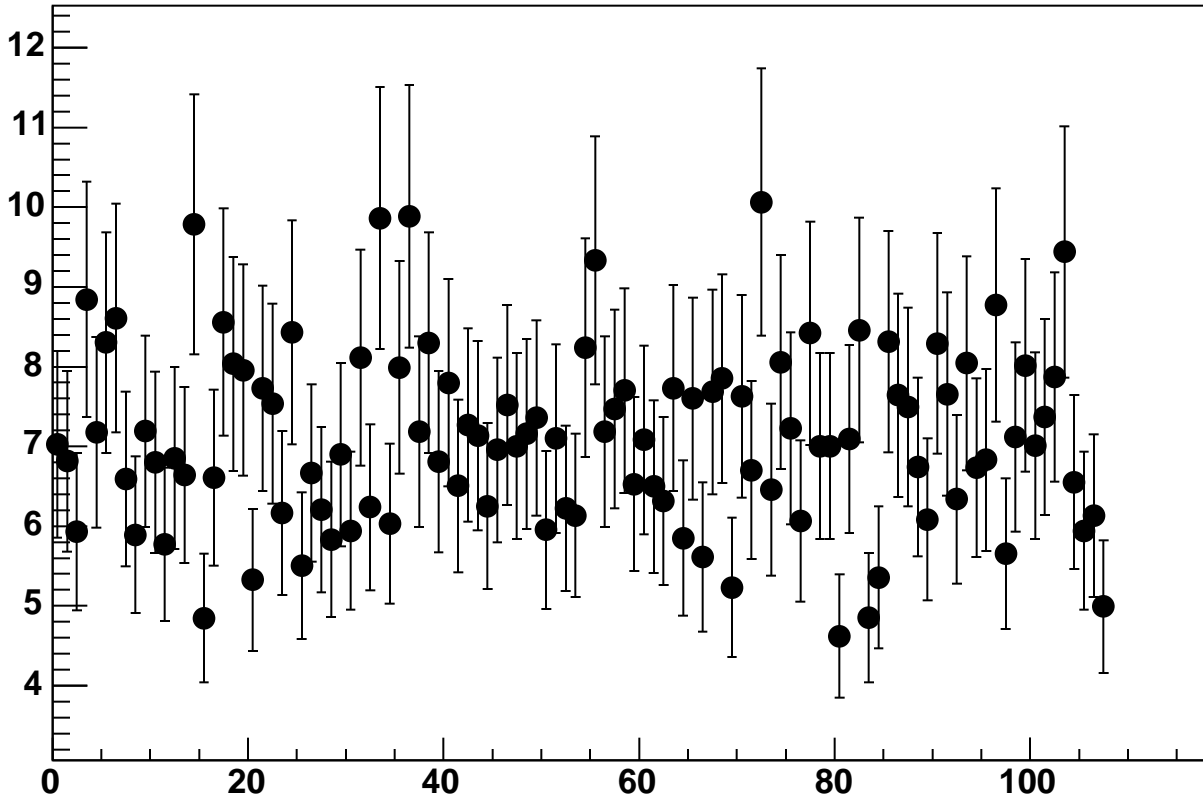
Enable 4, DAC=4000, Hold=190, ADC Noise vs 18\*Chip+Chan



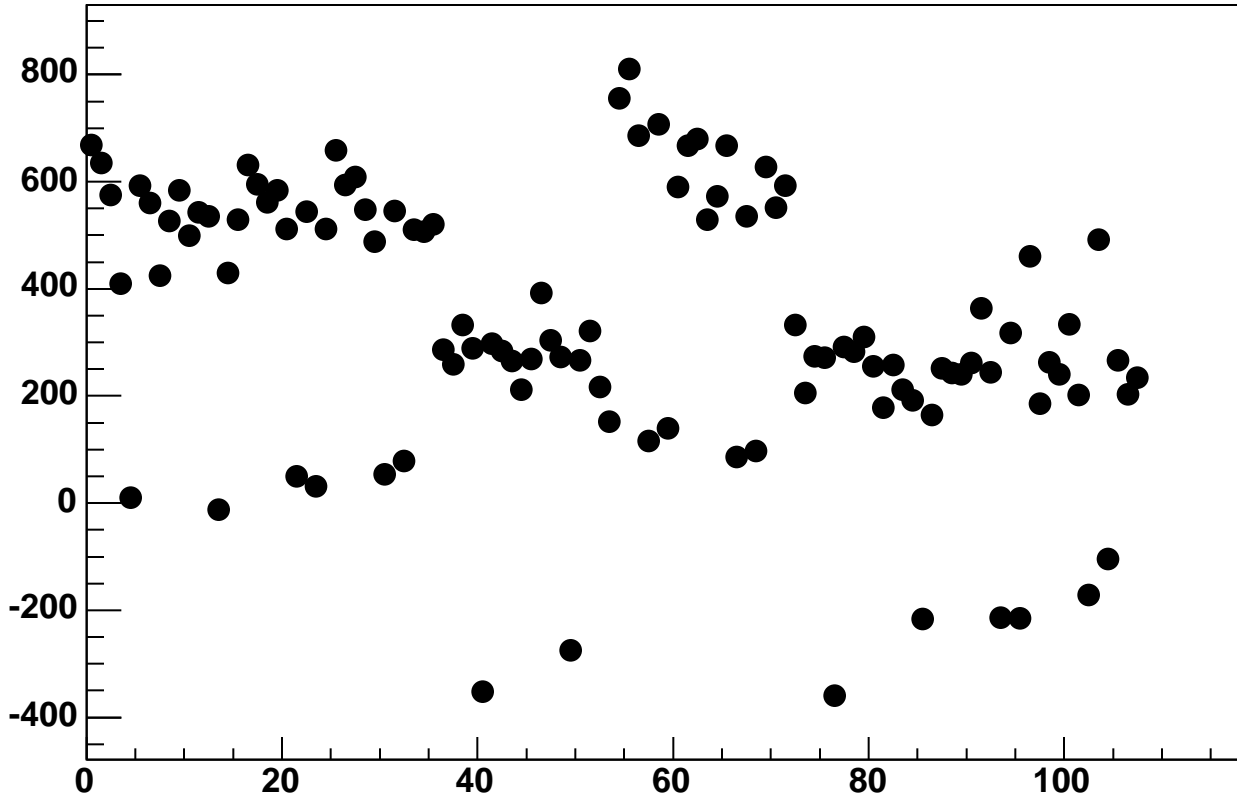
Enable 4, DAC=4000, Hold=195, ADC Mean vs 18\*Chip+Chan



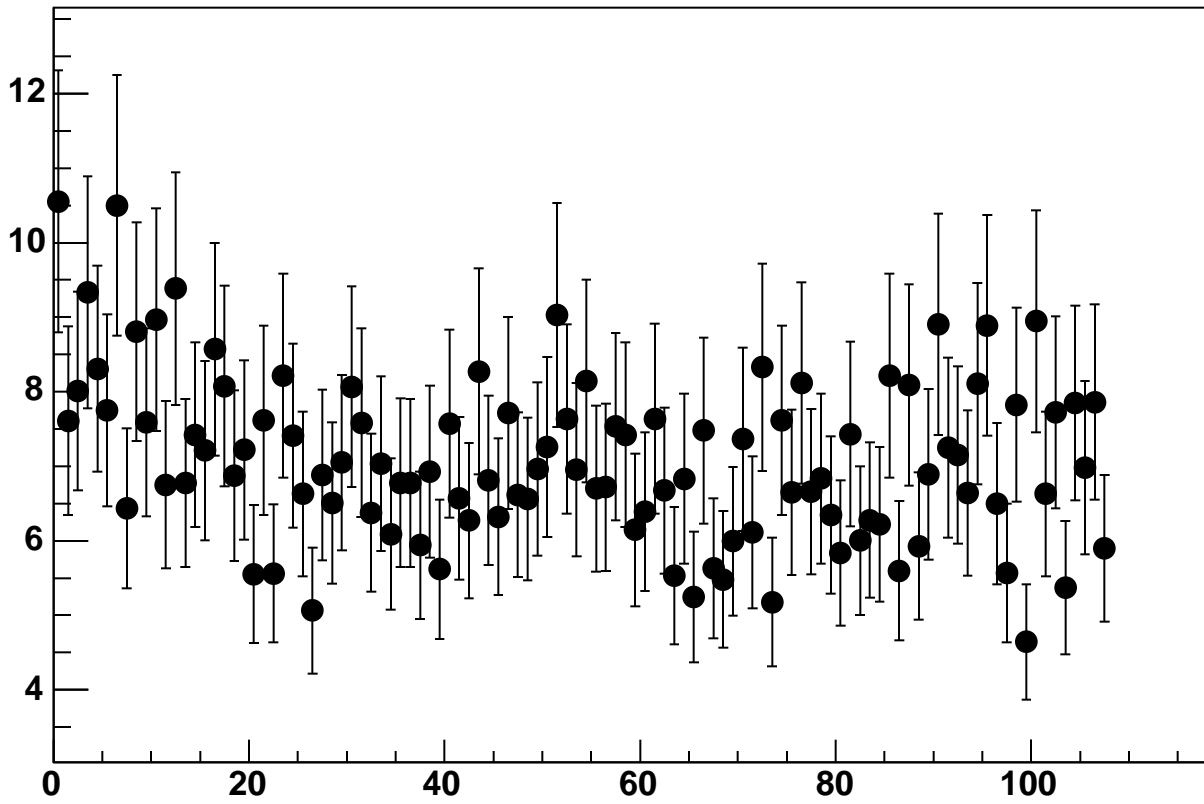
Enable 4, DAC=4000, Hold=195, ADC Noise vs 18\*Chip+Chan



Enable 4, DAC=4000, Hold=200, ADC Mean vs 18\*Chip+Chan

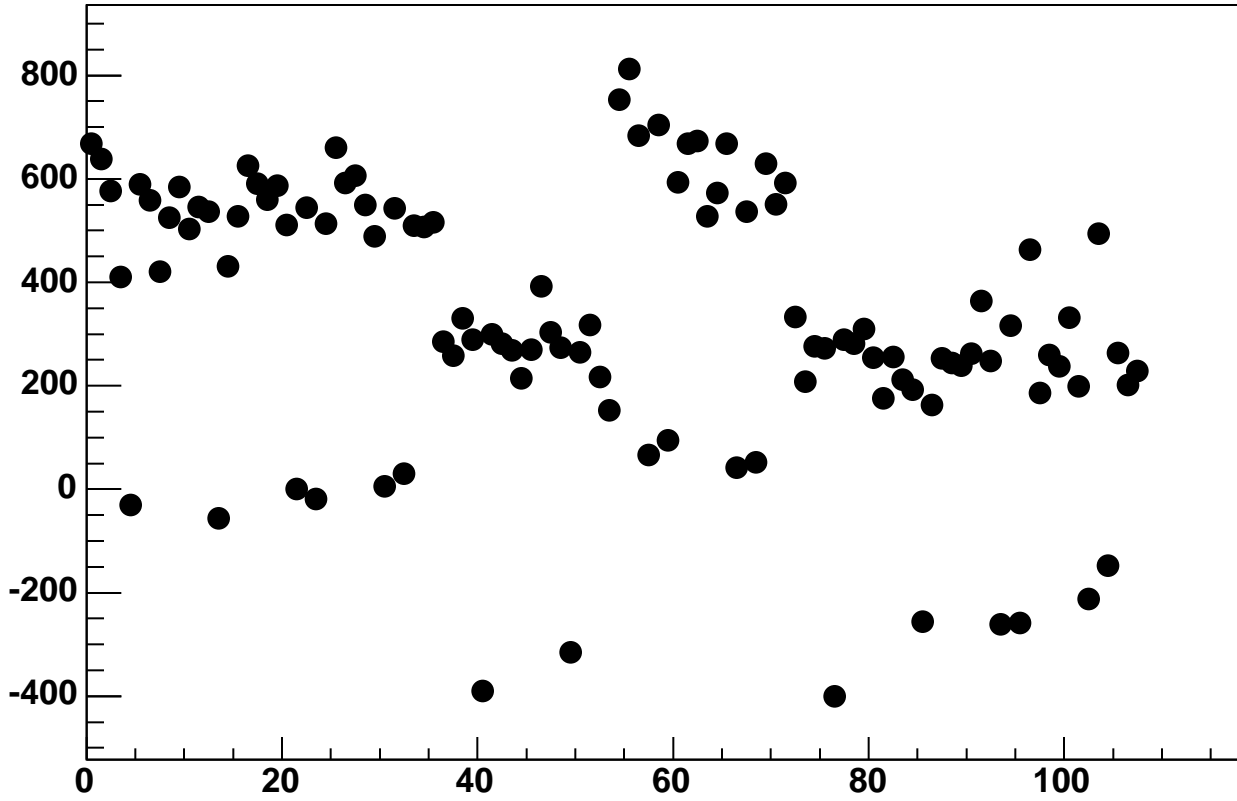


Enable 4, DAC=4000, Hold=200, ADC Noise vs 18\*Chip+Chan

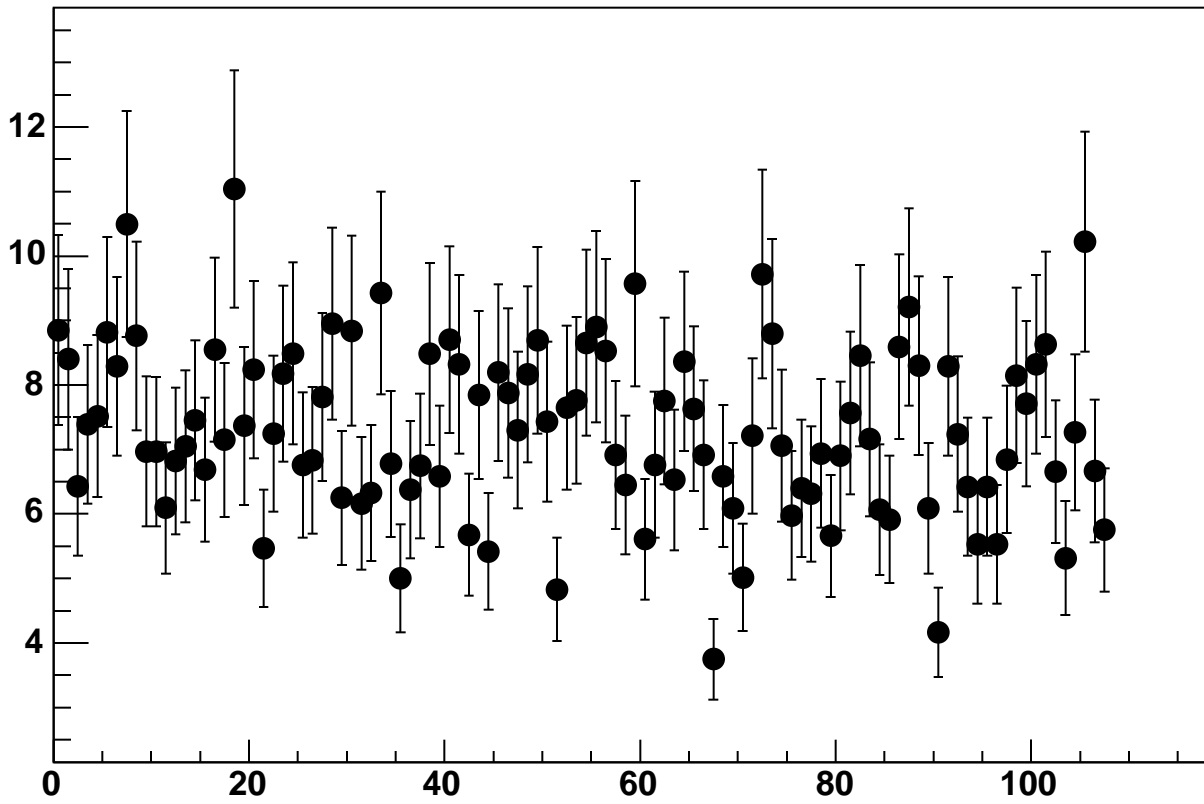




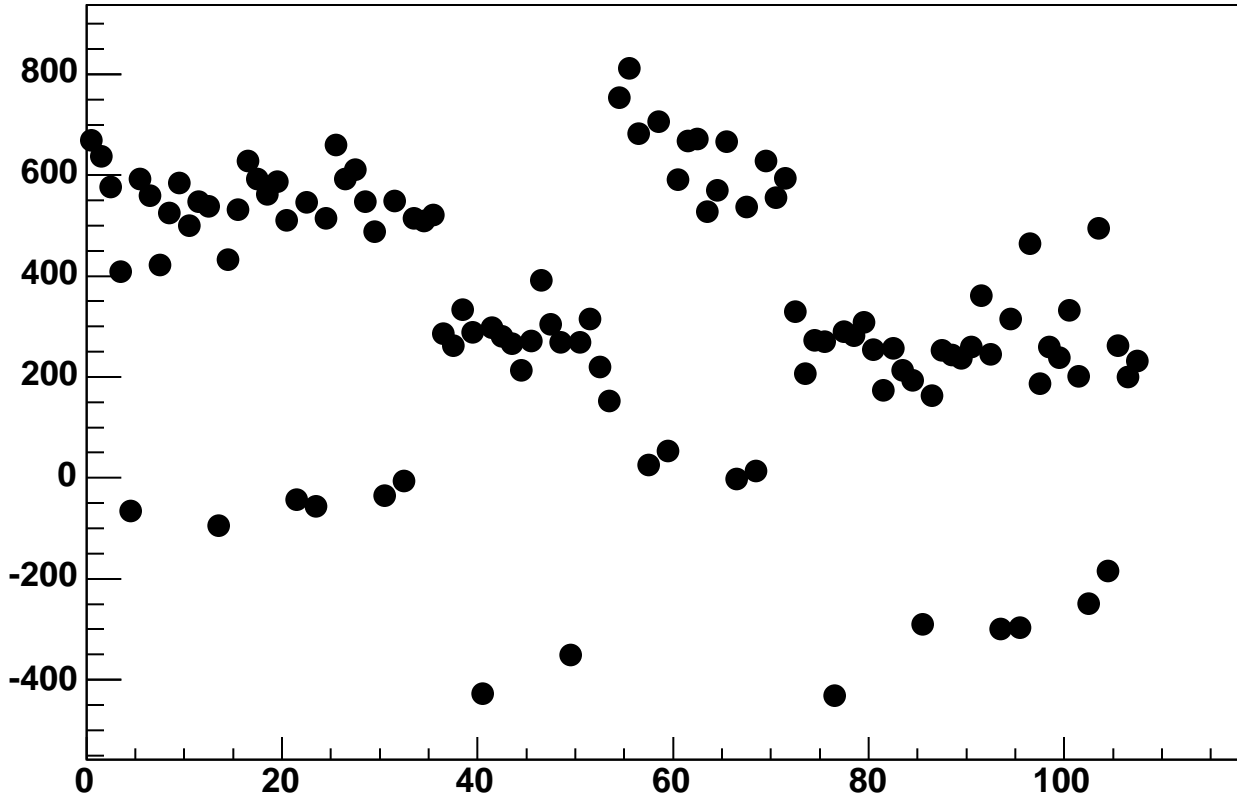
Enable 4, DAC=4000, Hold=205, ADC Mean vs 18\*Chip+Chan



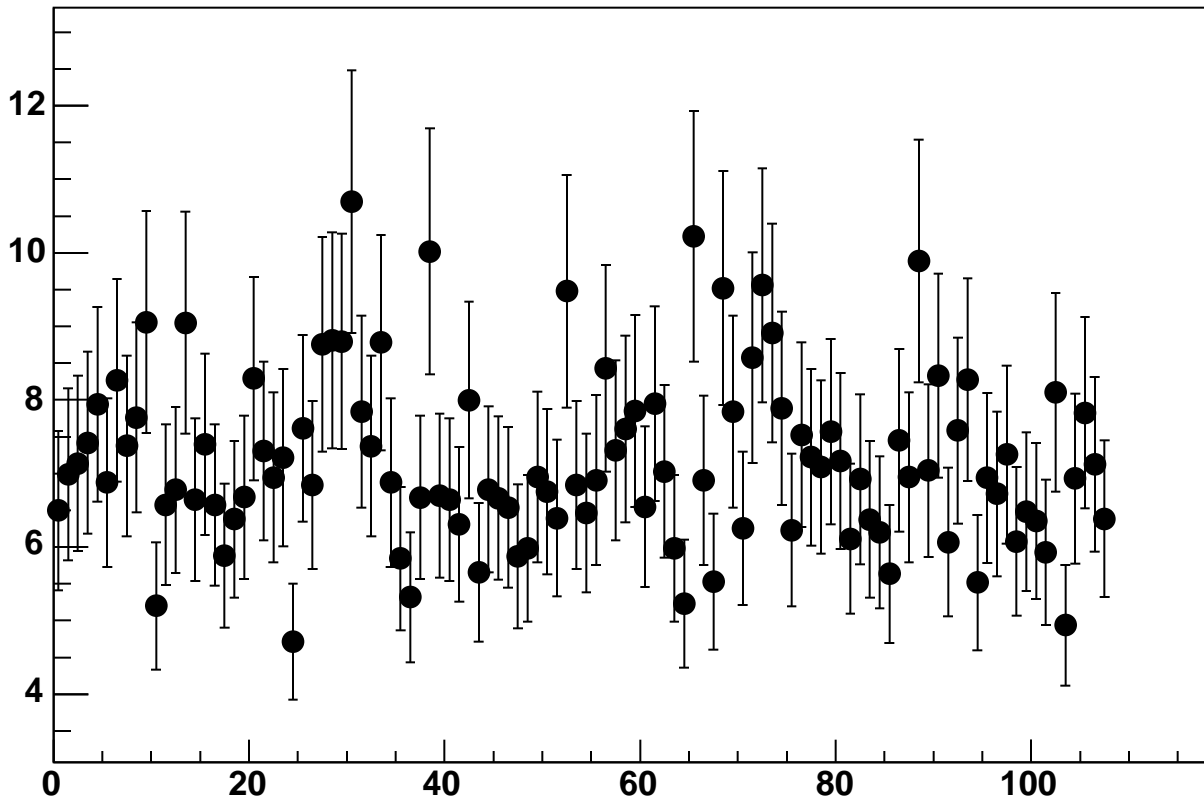
Enable 4, DAC=4000, Hold=205, ADC Noise vs 18\*Chip+Chan



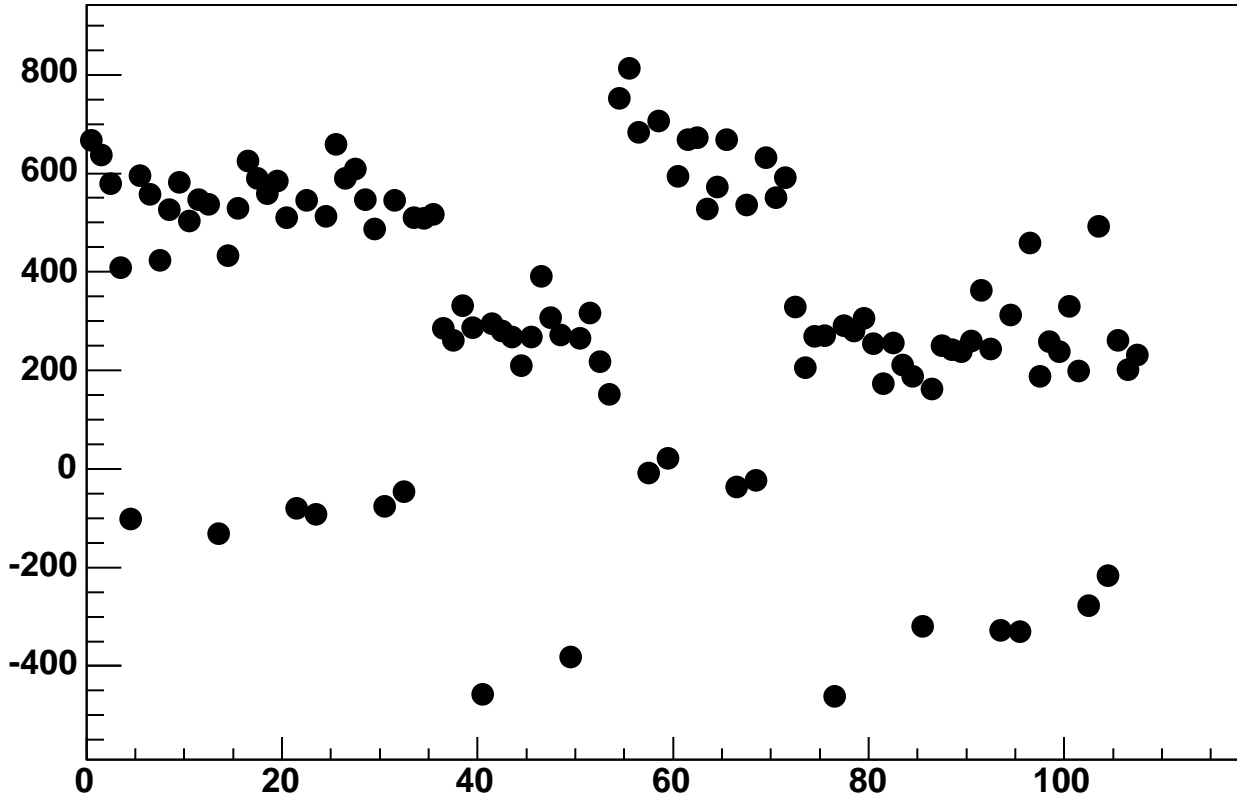
Enable 4, DAC=4000, Hold=210, ADC Mean vs 18\*Chip+Chan



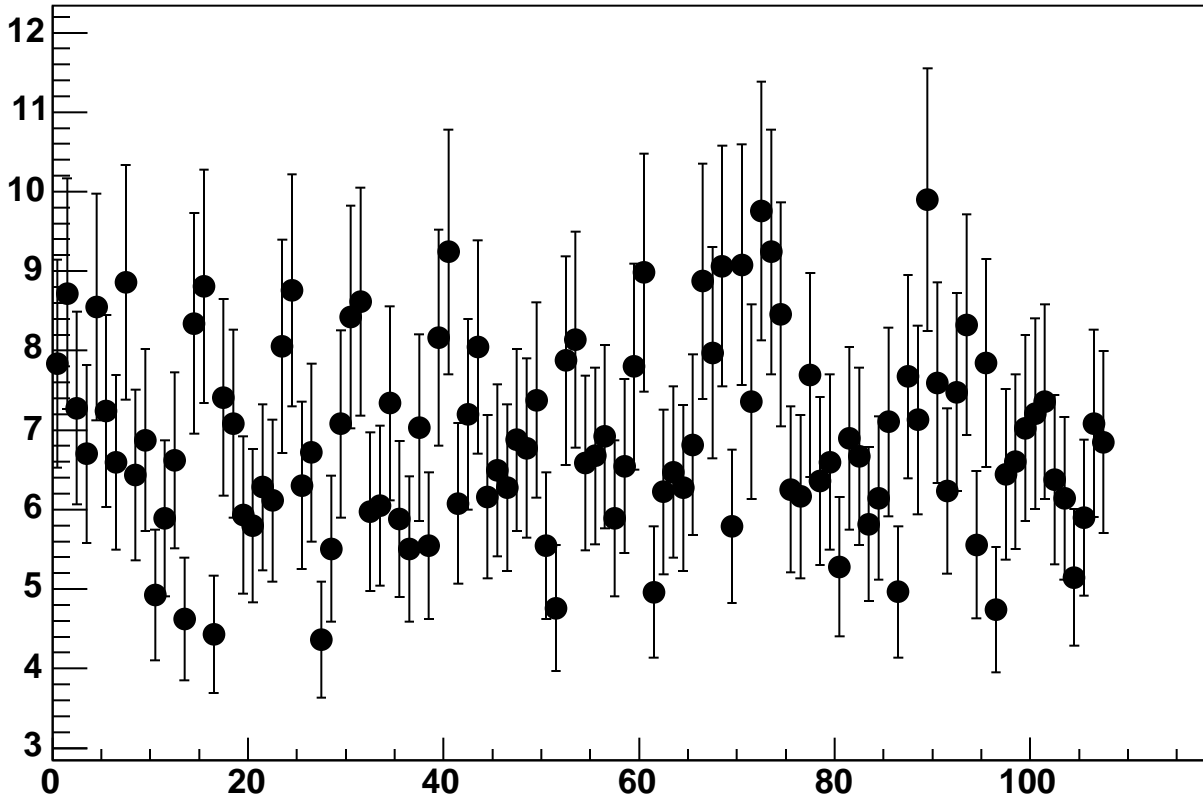
Enable 4, DAC=4000, Hold=210, ADC Noise vs 18\*Chip+Chan



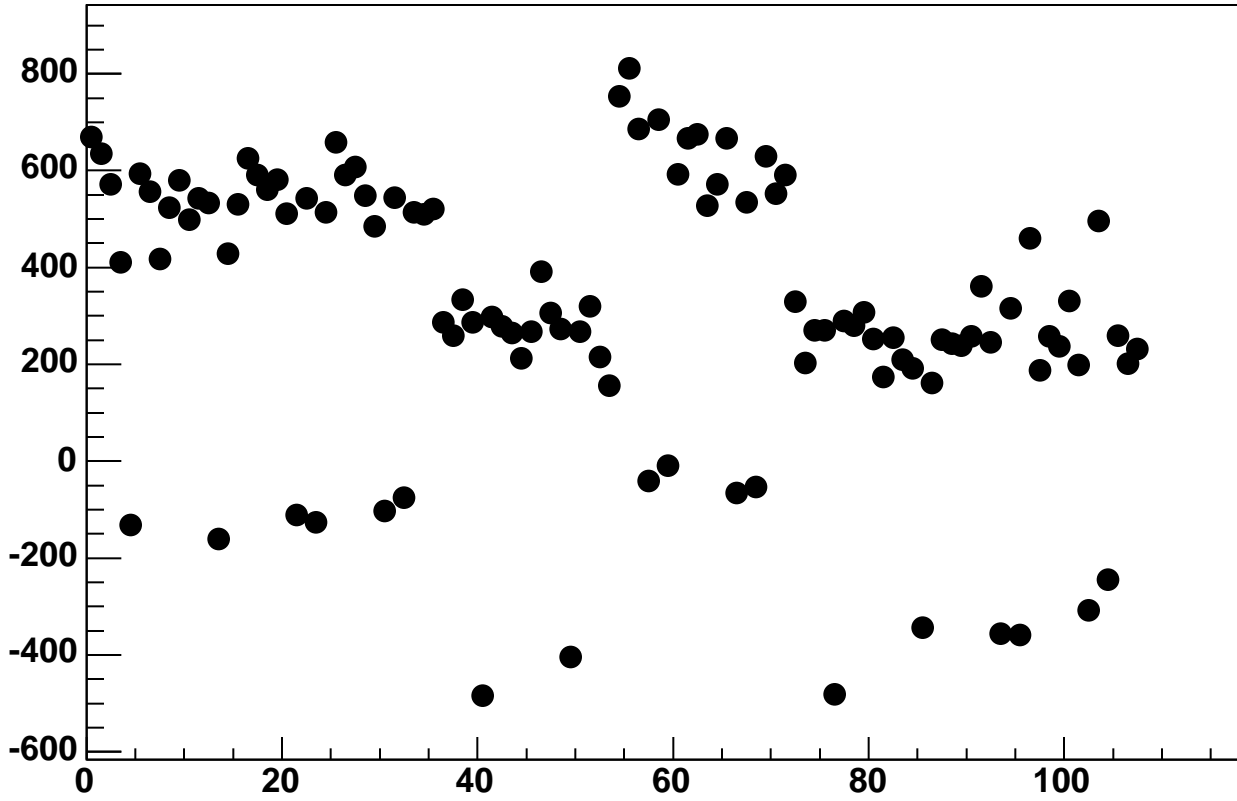
Enable 4, DAC=4000, Hold=215, ADC Mean vs 18\*Chip+Chan



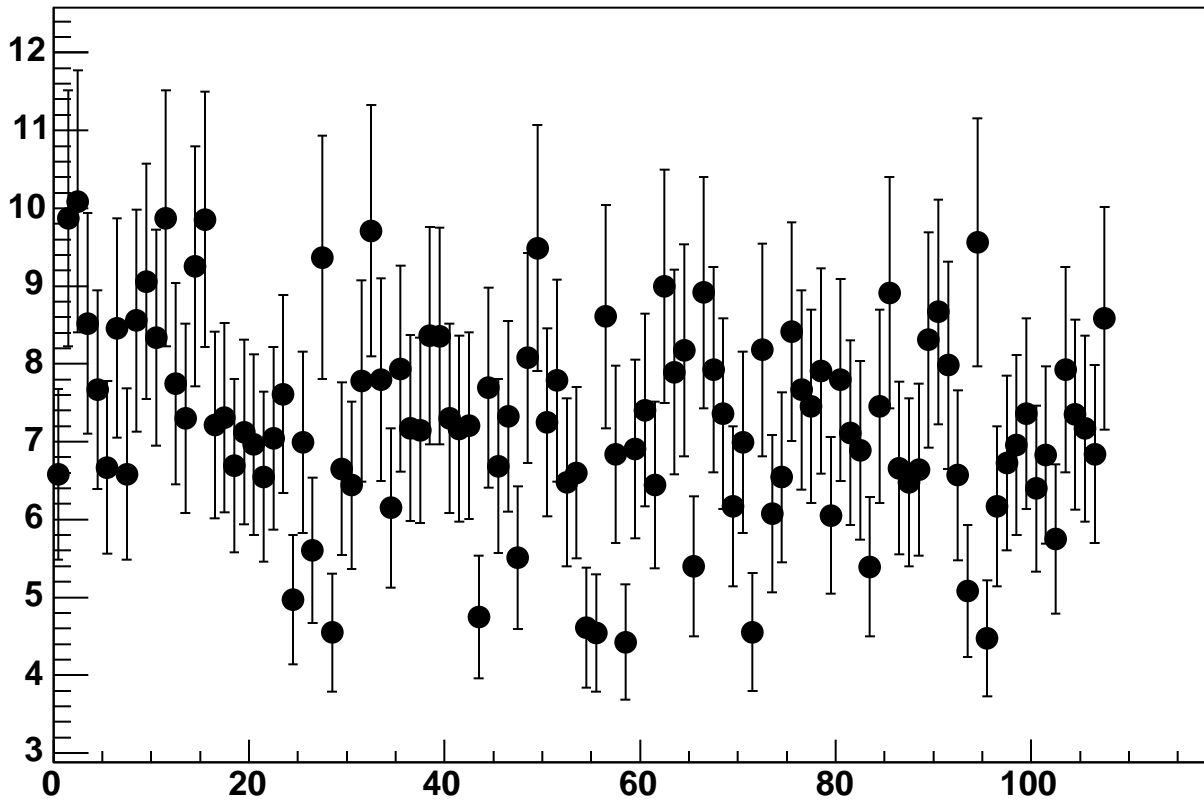
Enable 4, DAC=4000, Hold=215, ADC Noise vs 18\*Chip+Chan



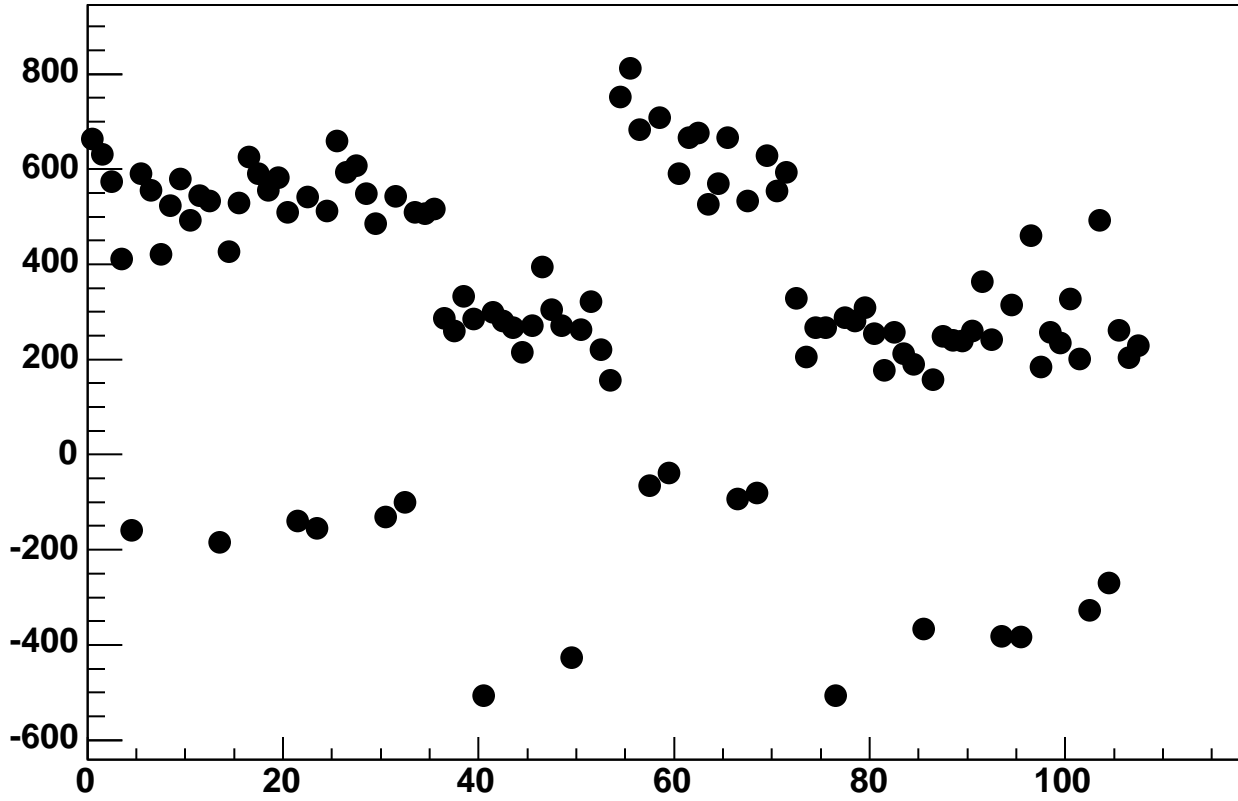
Enable 4, DAC=4000, Hold=220, ADC Mean vs 18\*Chip+Chan



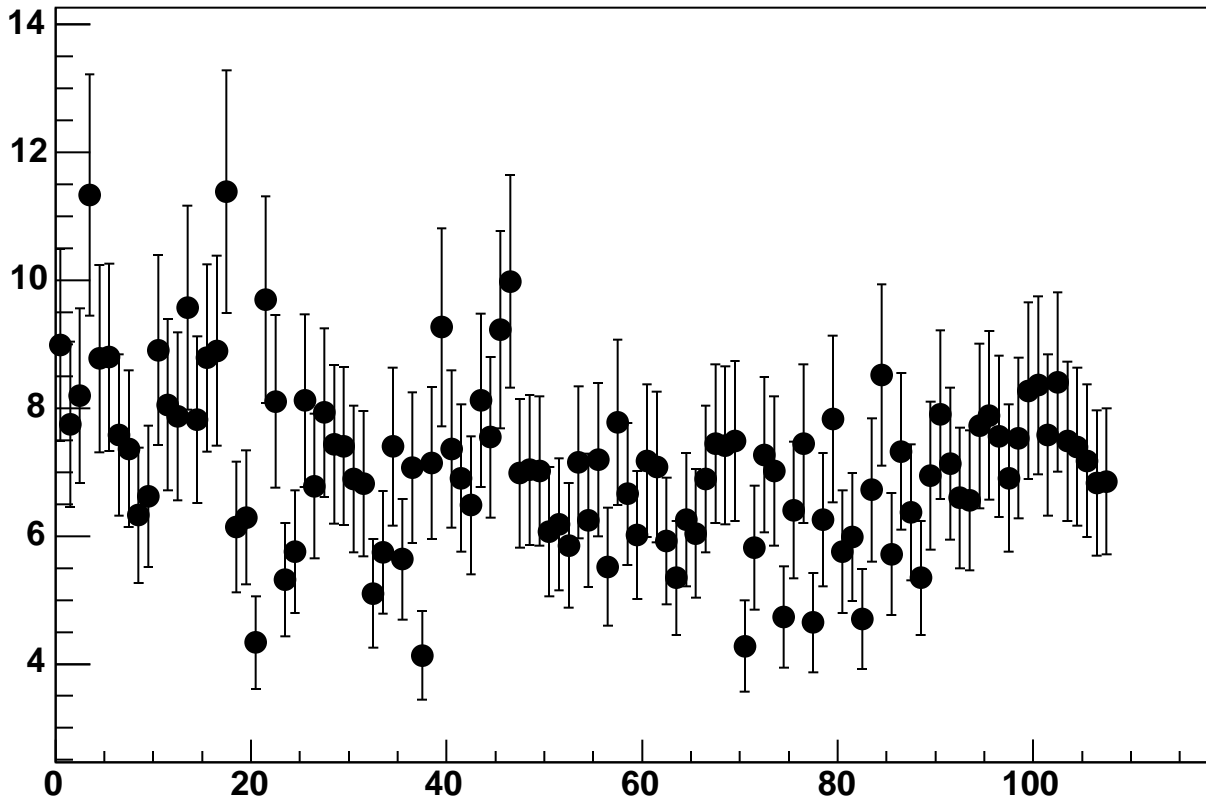
Enable 4, DAC=4000, Hold=220, ADC Noise vs 18\*Chip+Chan



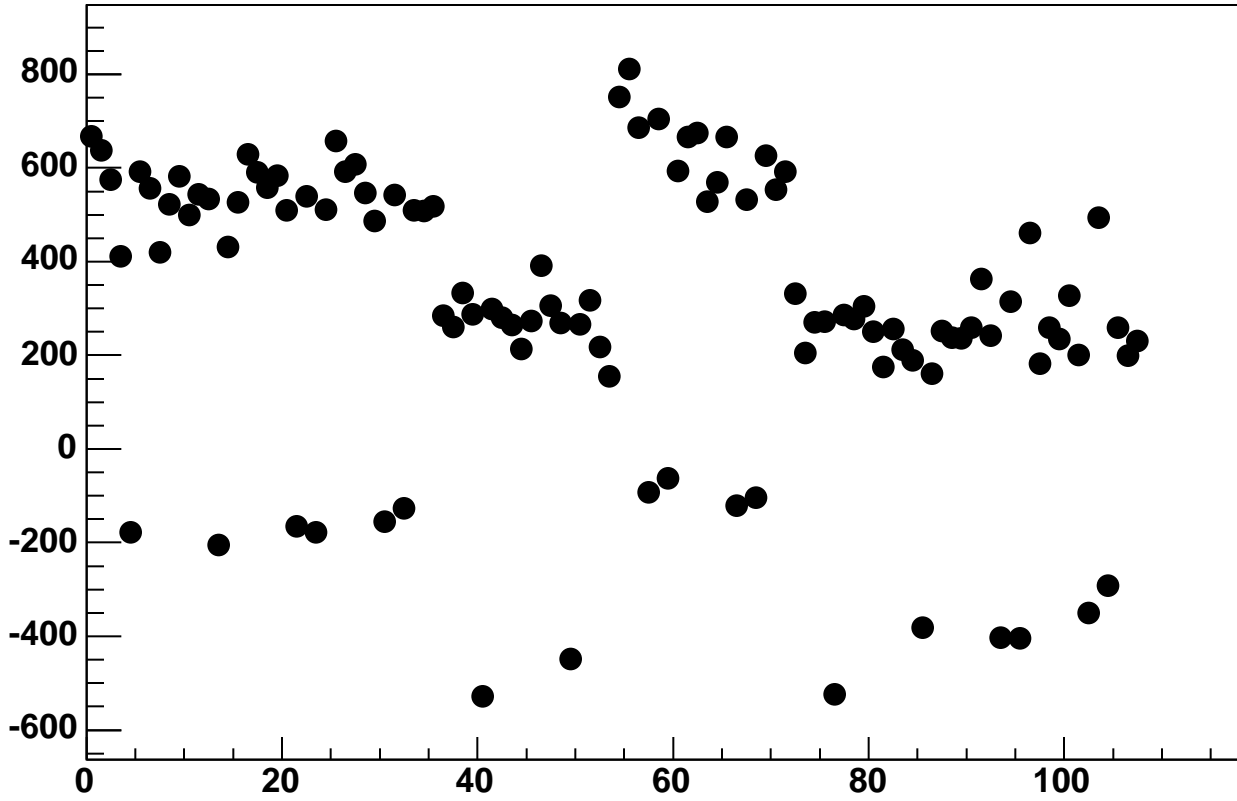
Enable 4, DAC=4000, Hold=225, ADC Mean vs 18\*Chip+Chan



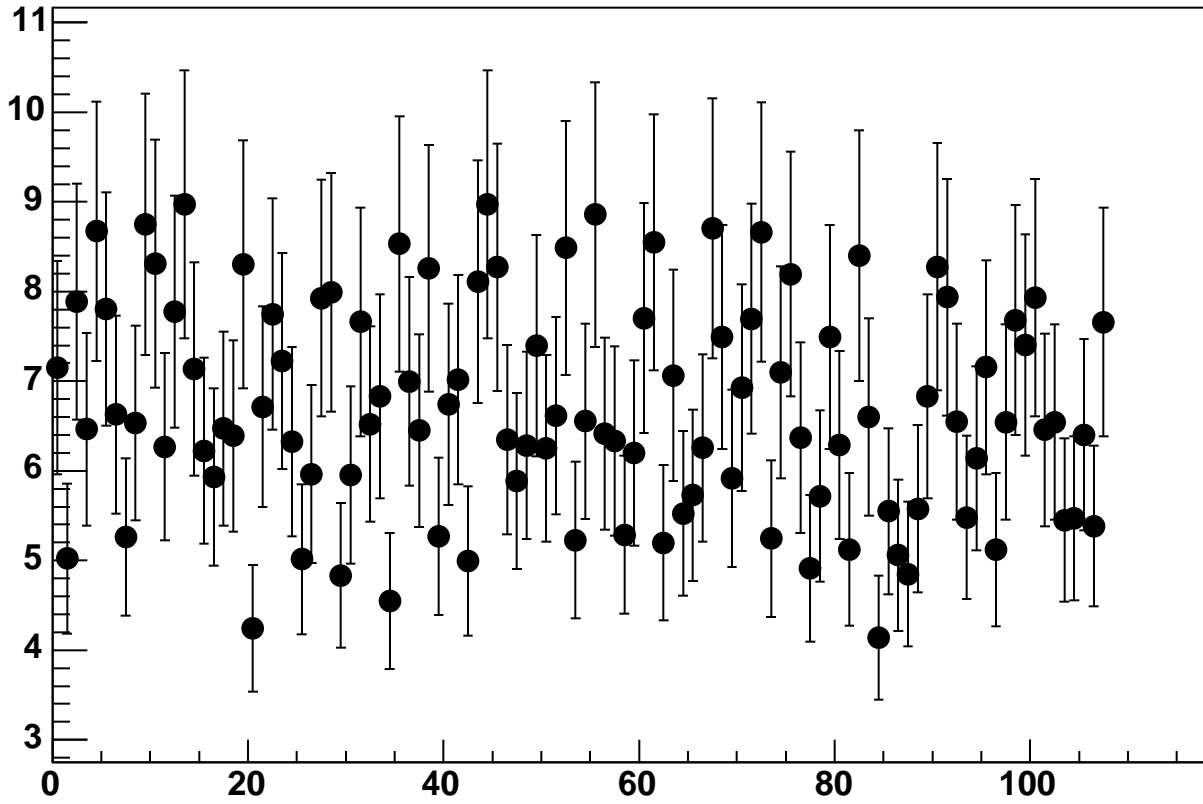
Enable 4, DAC=4000, Hold=225, ADC Noise vs 18\*Chip+Chan



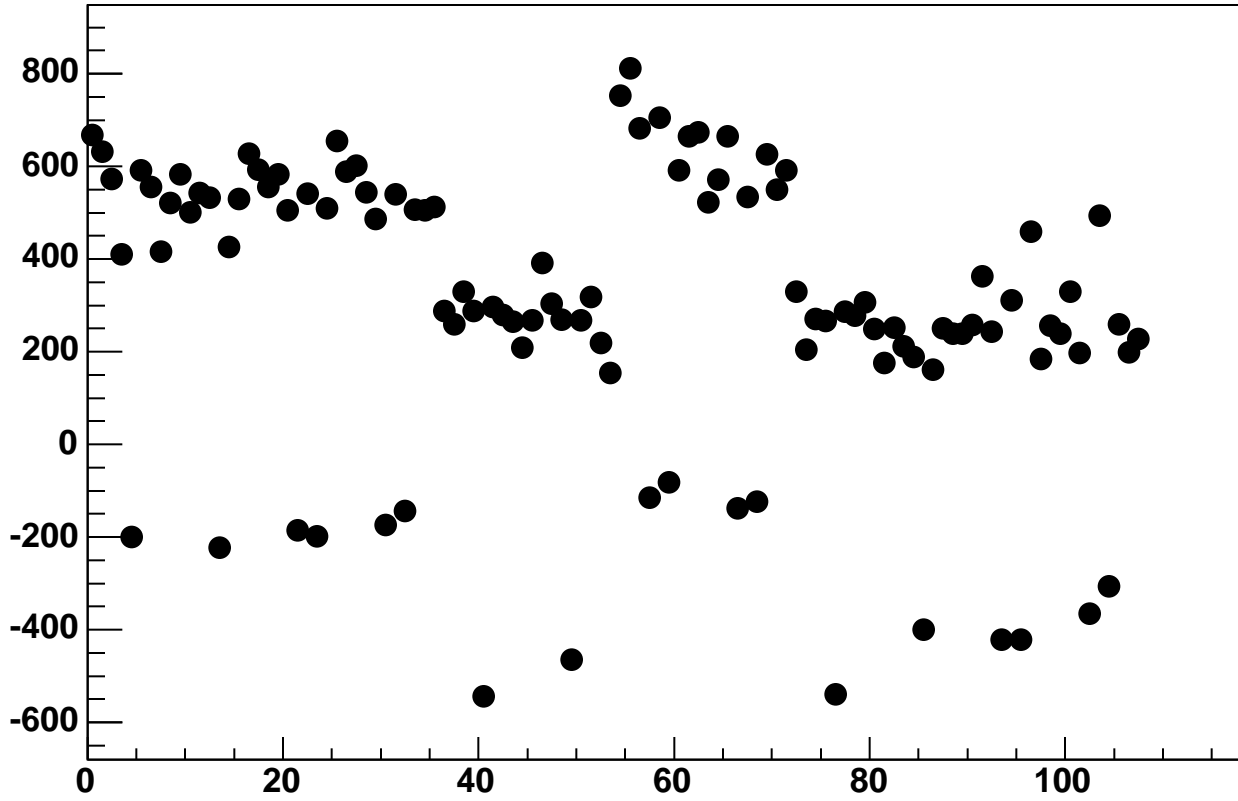
Enable 4, DAC=4000, Hold=230, ADC Mean vs 18\*Chip+Chan



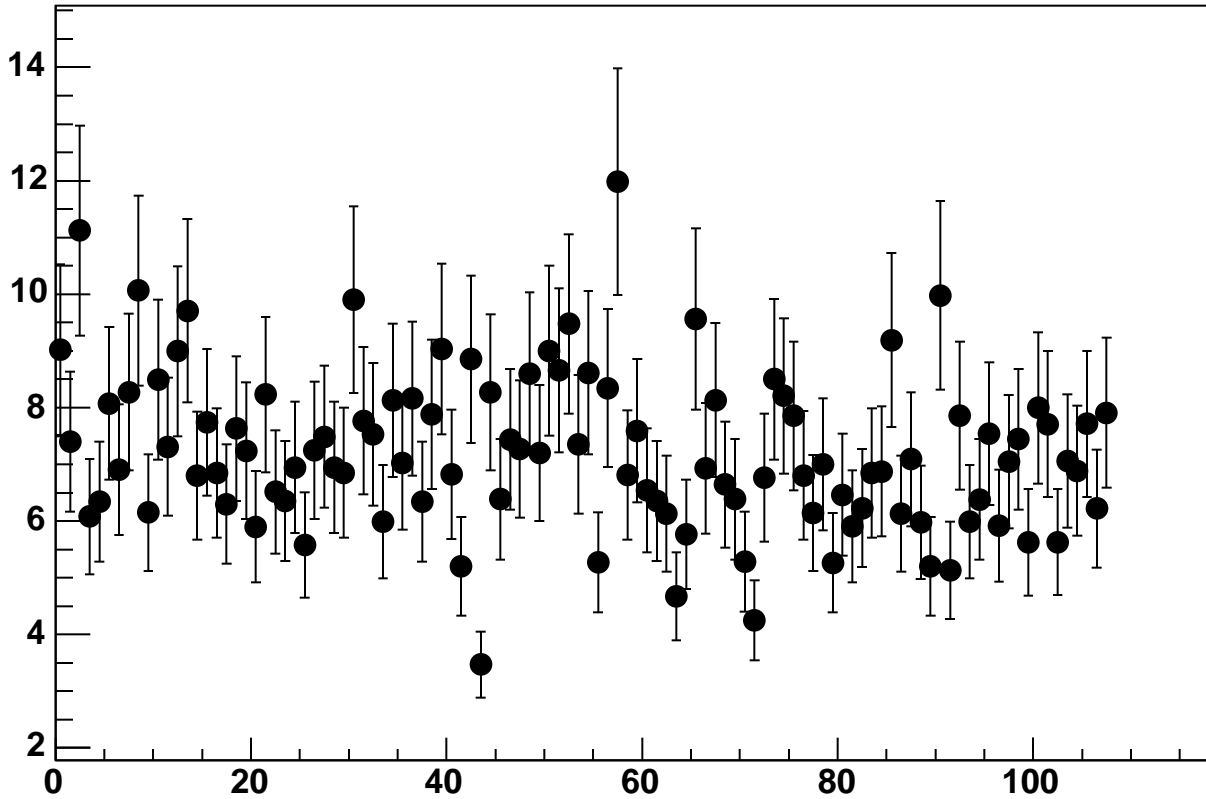
Enable 4, DAC=4000, Hold=230, ADC Noise vs 18\*Chip+Chan



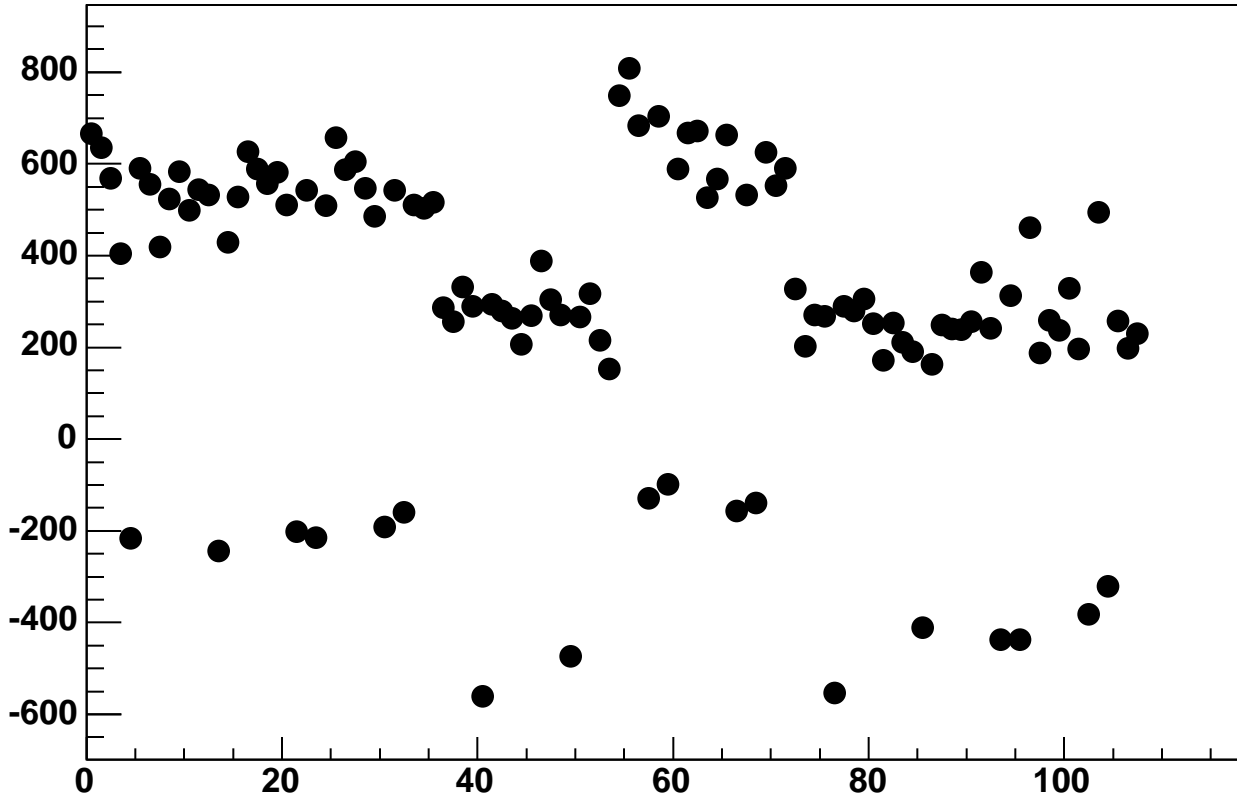
Enable 4, DAC=4000, Hold=235, ADC Mean vs 18\*Chip+Chan



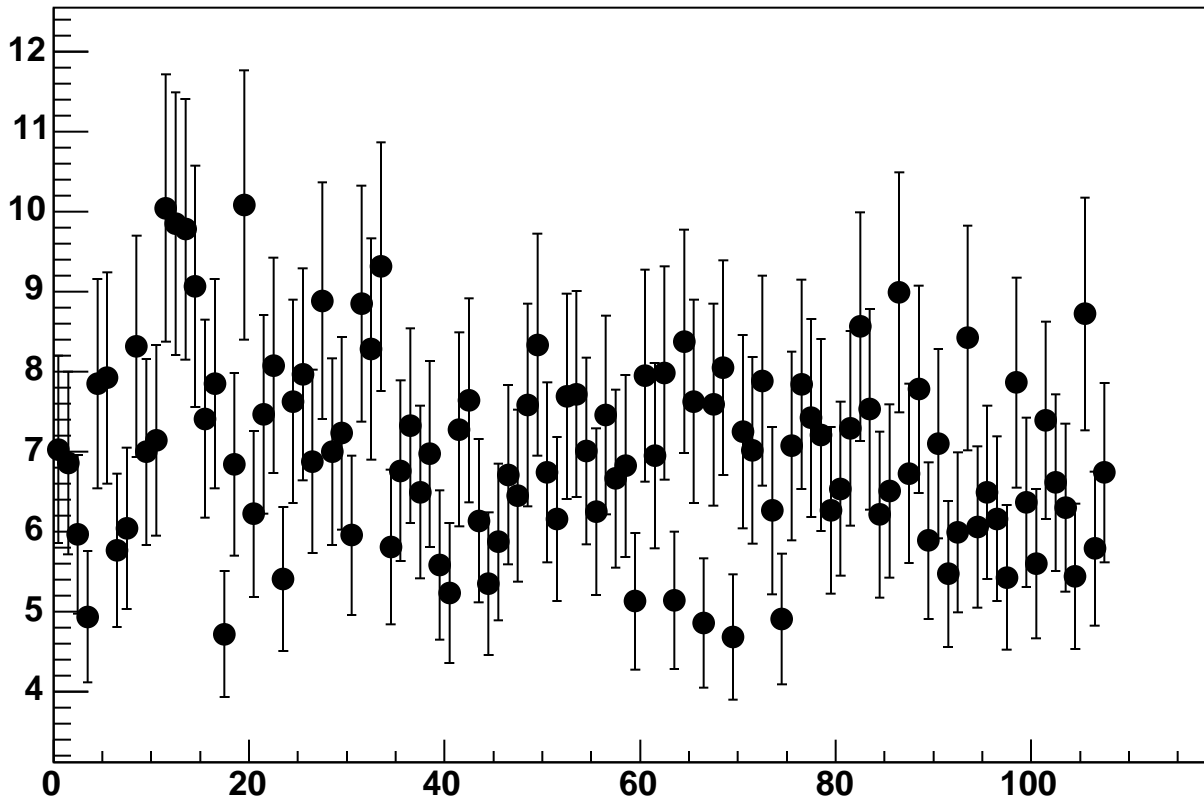
Enable 4, DAC=4000, Hold=235, ADC Noise vs 18\*Chip+Chan



Enable 4, DAC=4000, Hold=240, ADC Mean vs 18\*Chip+Chan

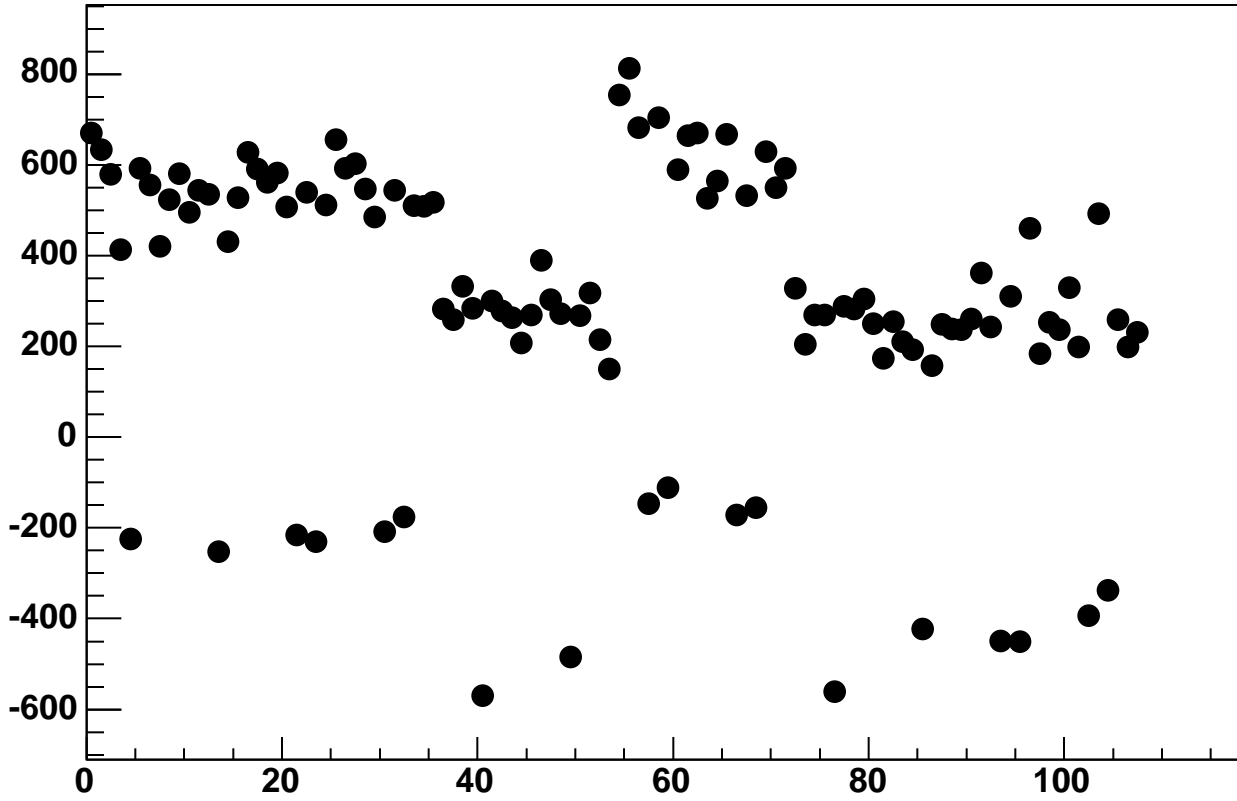


Enable 4, DAC=4000, Hold=240, ADC Noise vs 18\*Chip+Chan

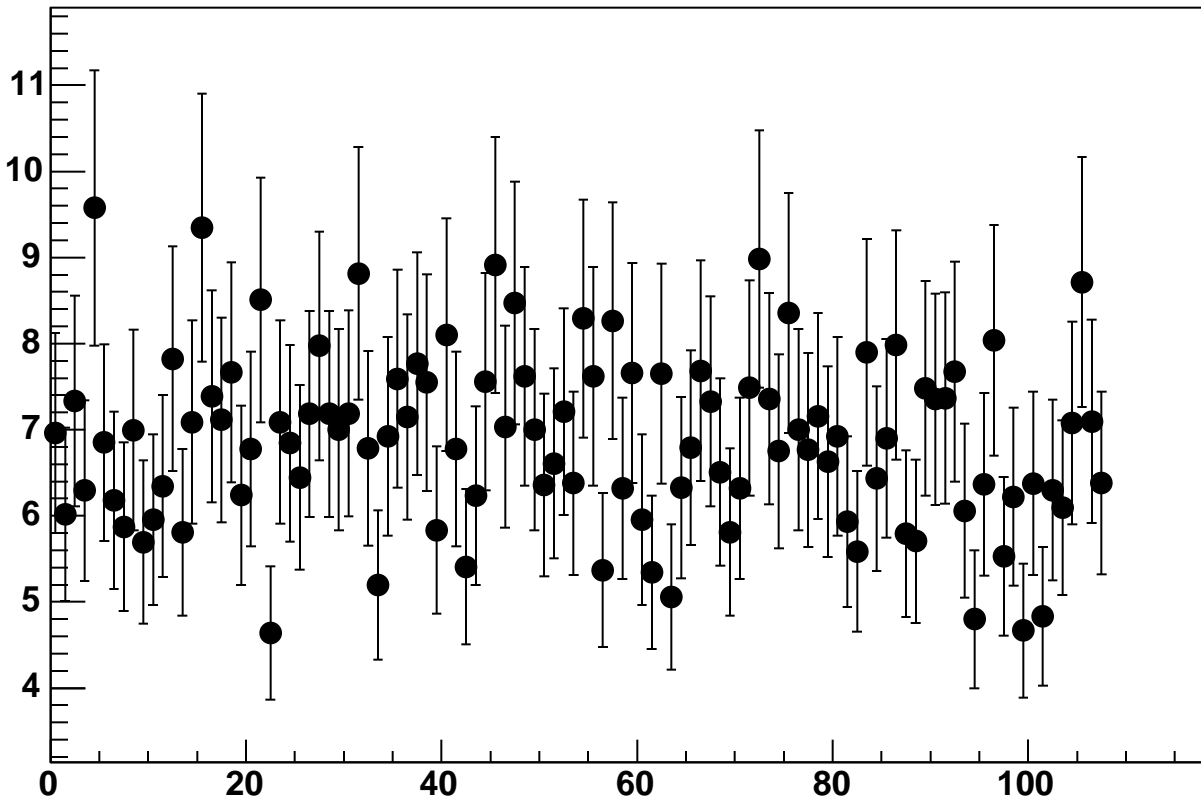




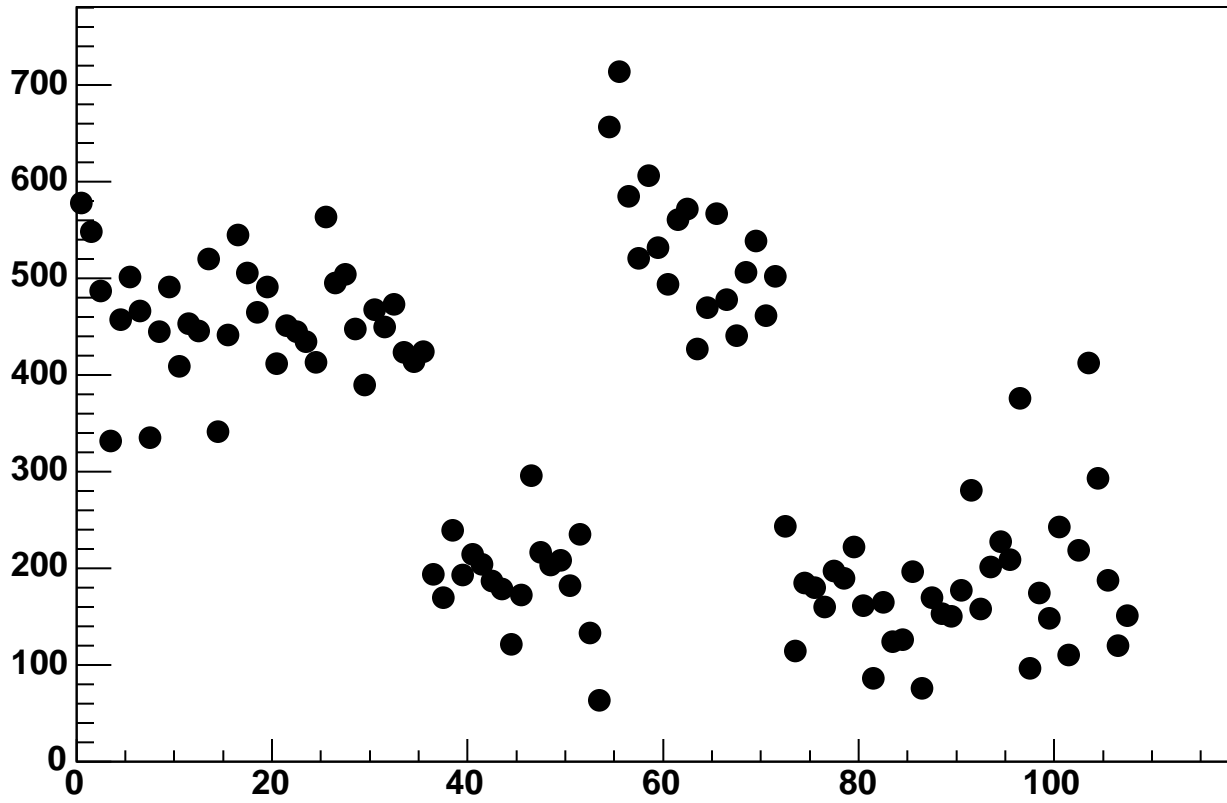
Enable 4, DAC=4000, Hold=245, ADC Mean vs 18\*Chip+Chan



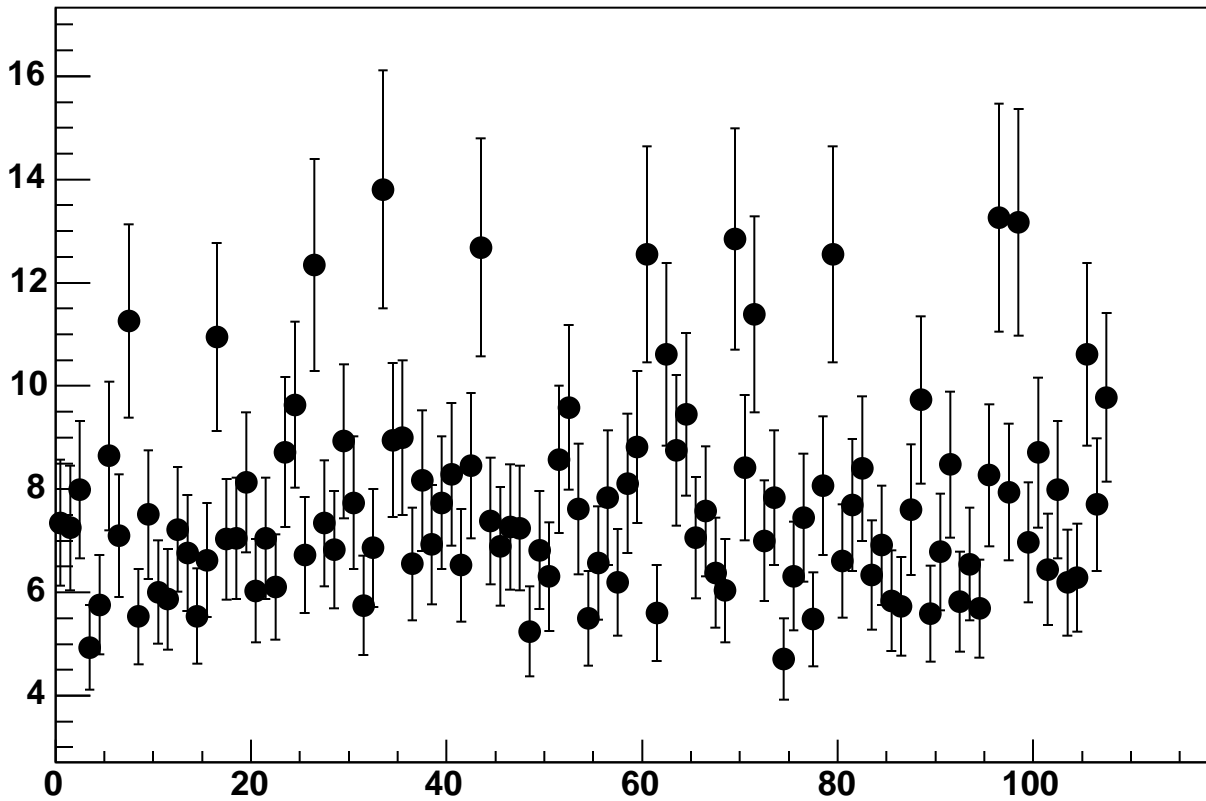
Enable 4, DAC=4000, Hold=245, ADC Noise vs 18\*Chip+Chan



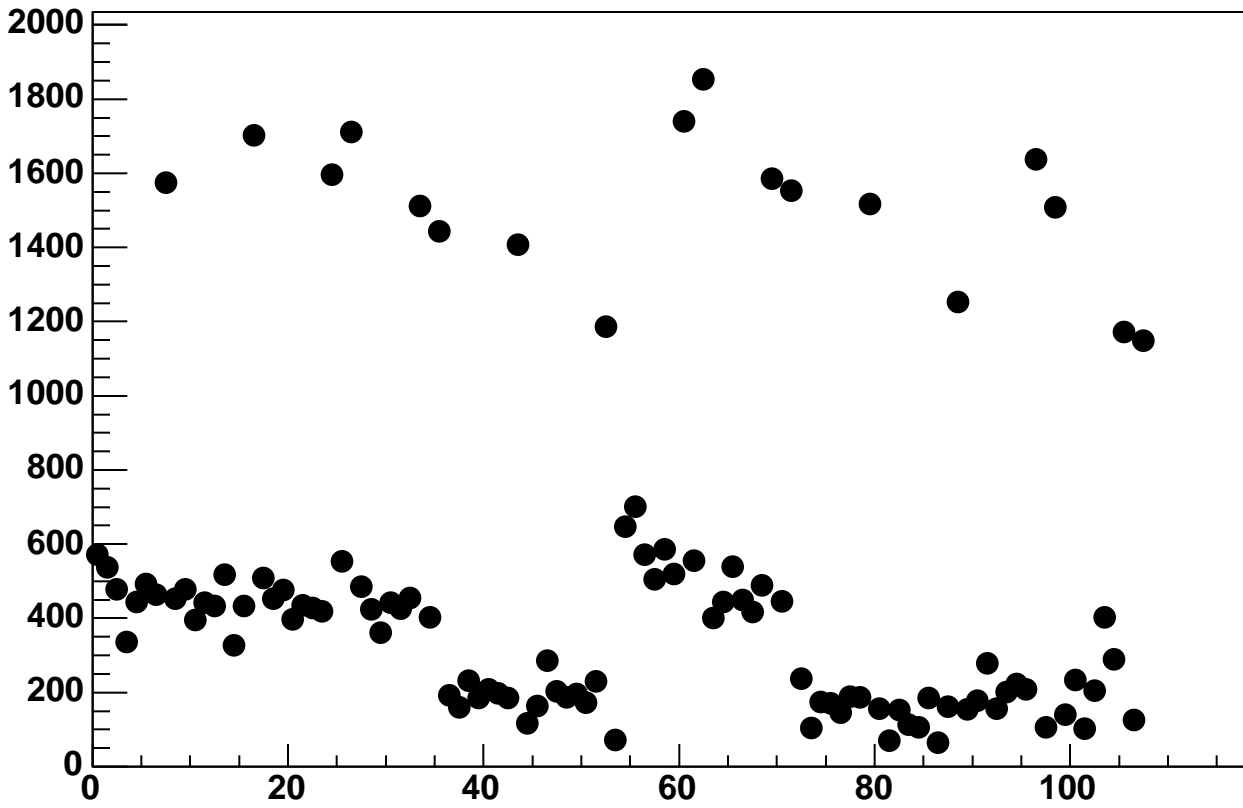
Enable 5, DAC=4000, Hold=0, ADC Mean vs 18\*Chip+Chan



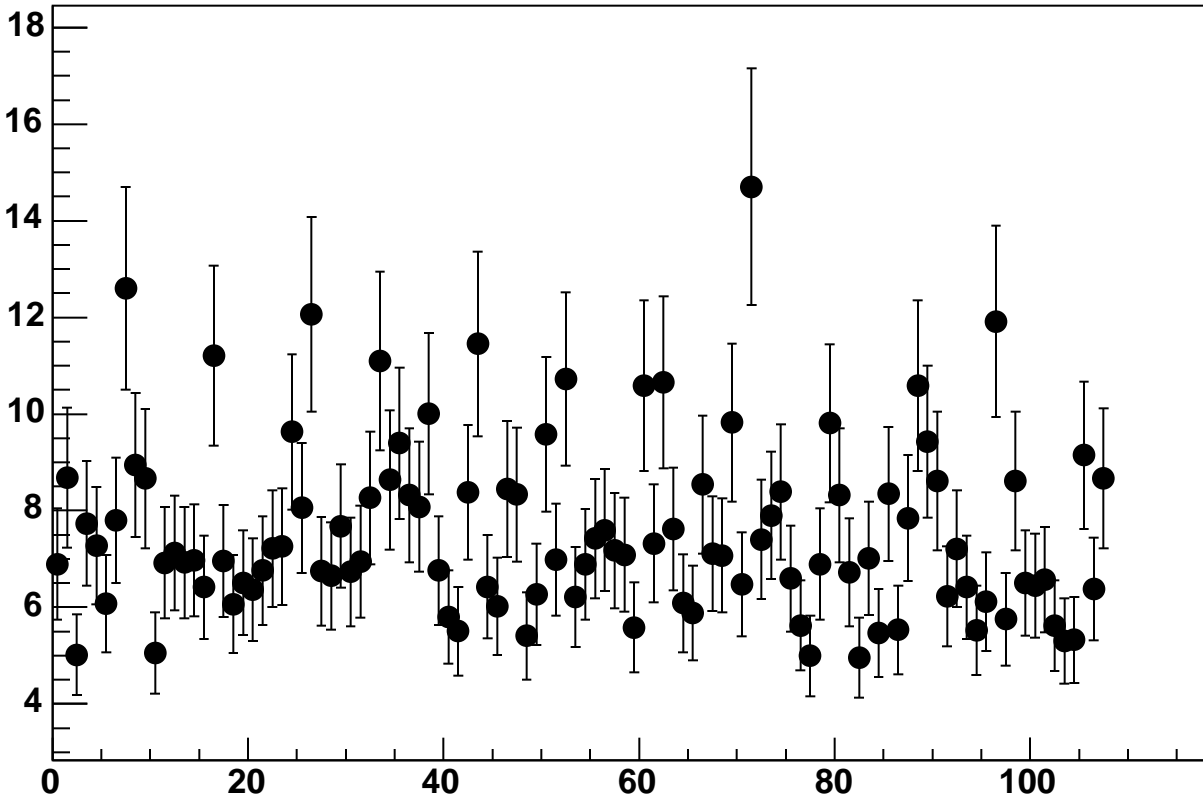
Enable 5, DAC=4000, Hold=0, ADC Noise vs 18\*Chip+Chan



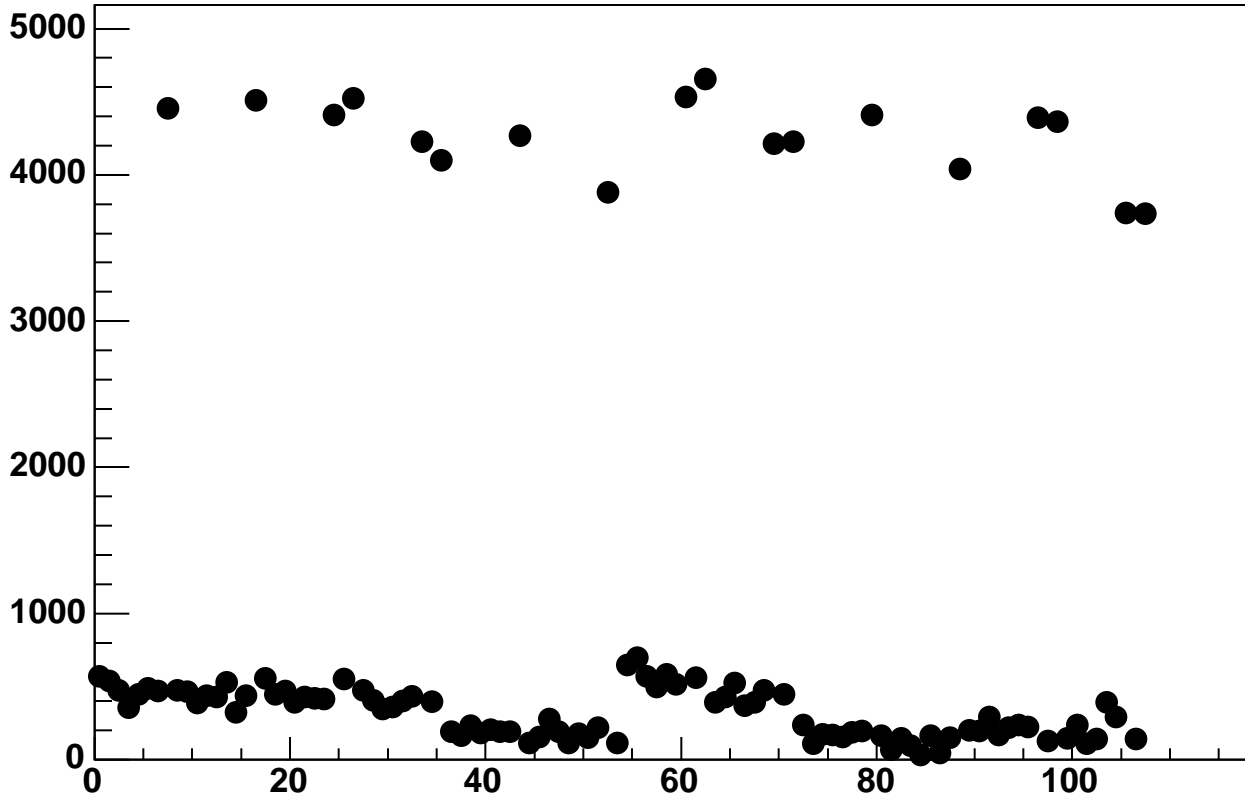
Enable 5, DAC=4000, Hold=5, ADC Mean vs 18\*Chip+Chan



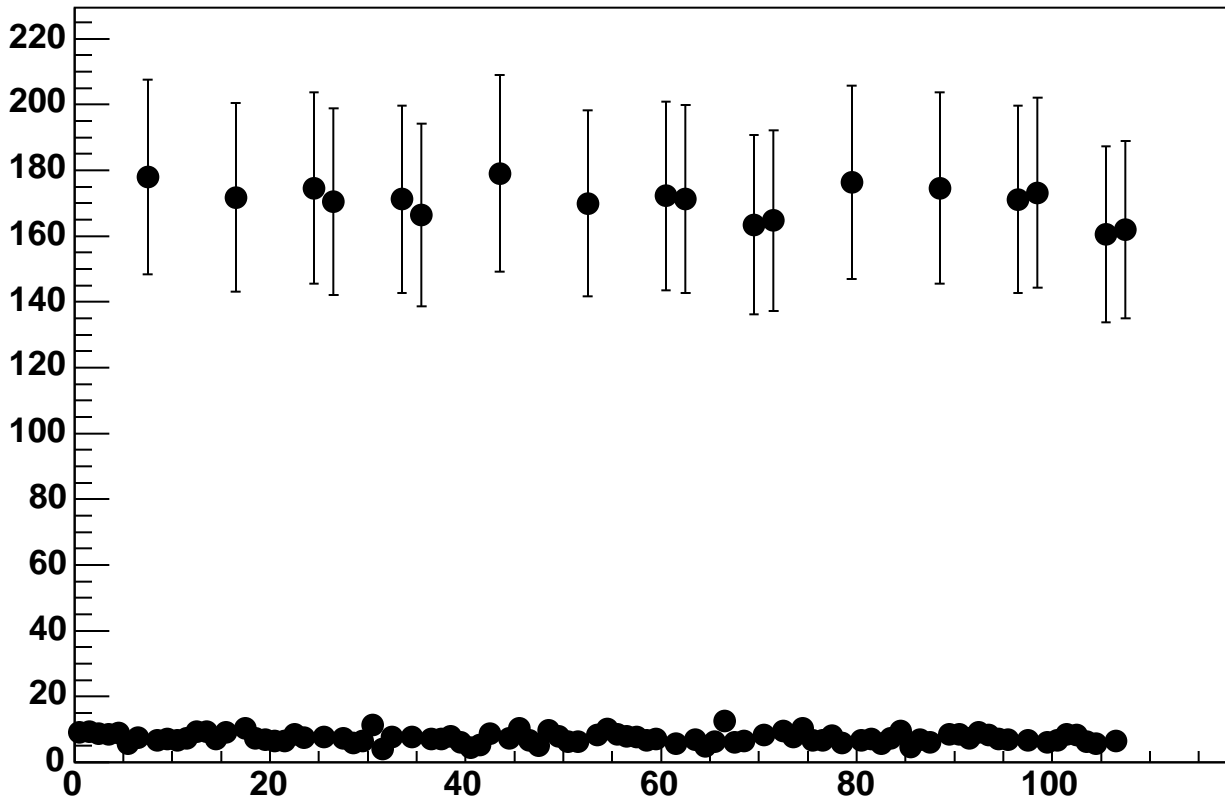
Enable 5, DAC=4000, Hold=5, ADC Noise vs 18\*Chip+Chan



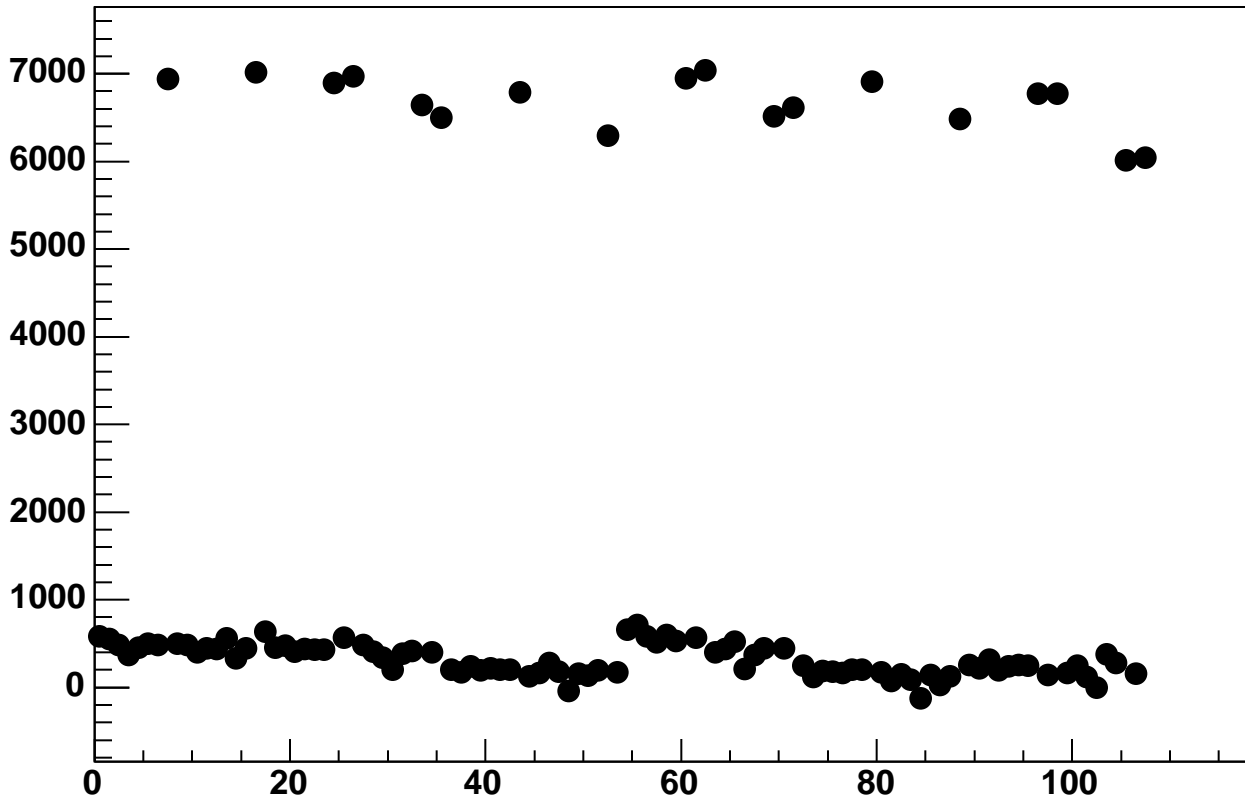
Enable 5, DAC=4000, Hold=10, ADC Mean vs 18\*Chip+Chan



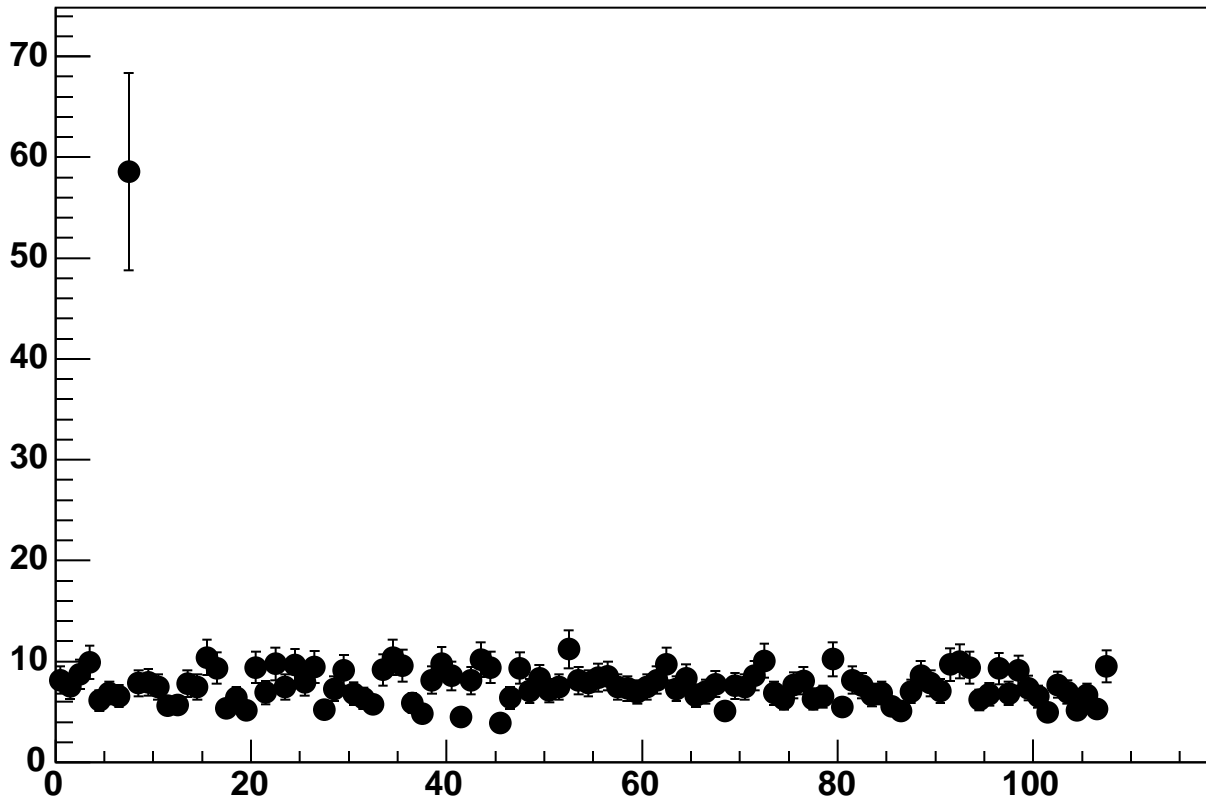
Enable 5, DAC=4000, Hold=10, ADC Noise vs 18\*Chip+Chan



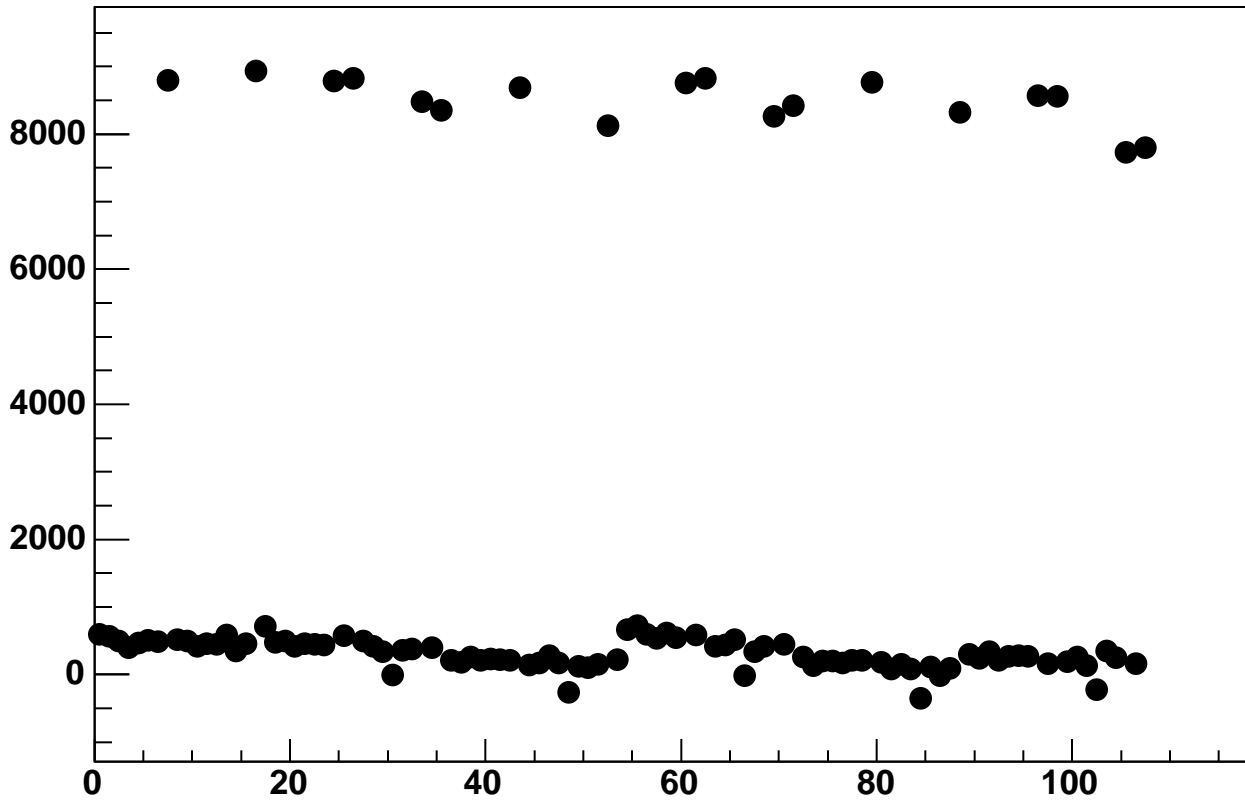
Enable 5, DAC=4000, Hold=15, ADC Mean vs 18\*Chip+Chan



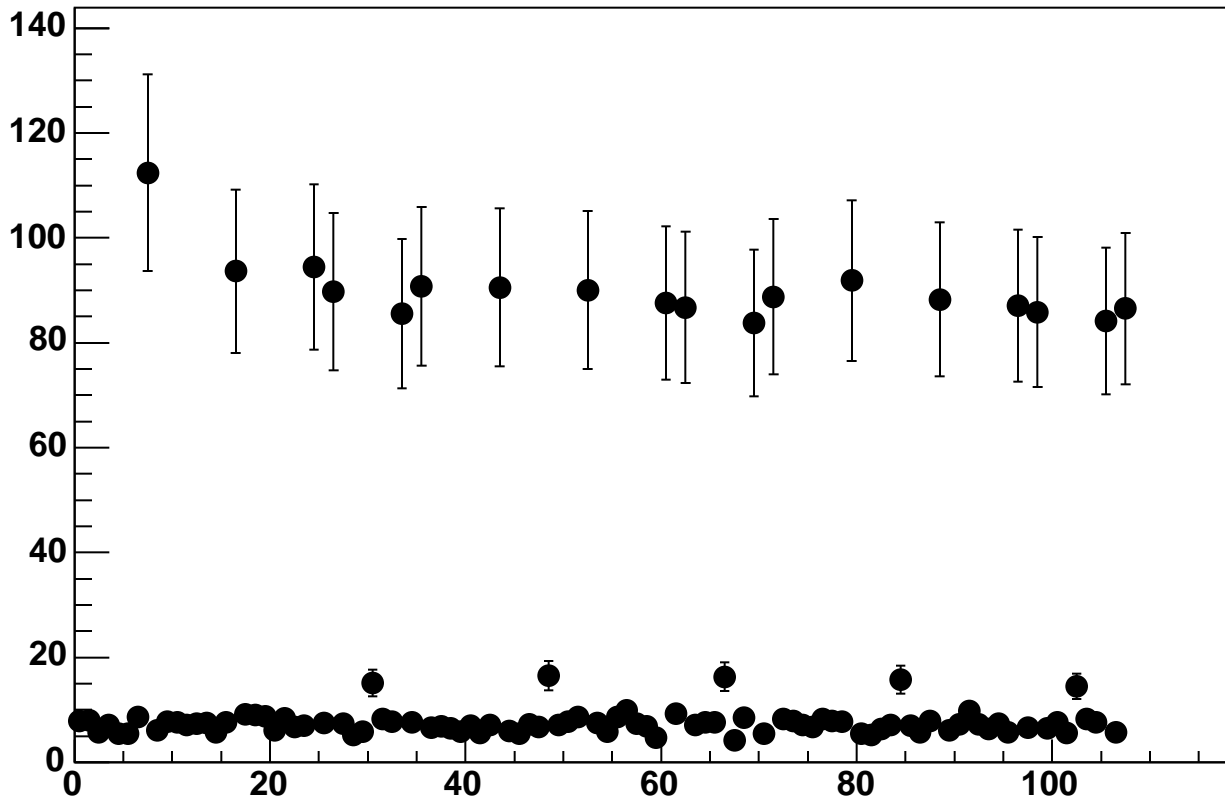
Enable 5, DAC=4000, Hold=15, ADC Noise vs 18\*Chip+Chan



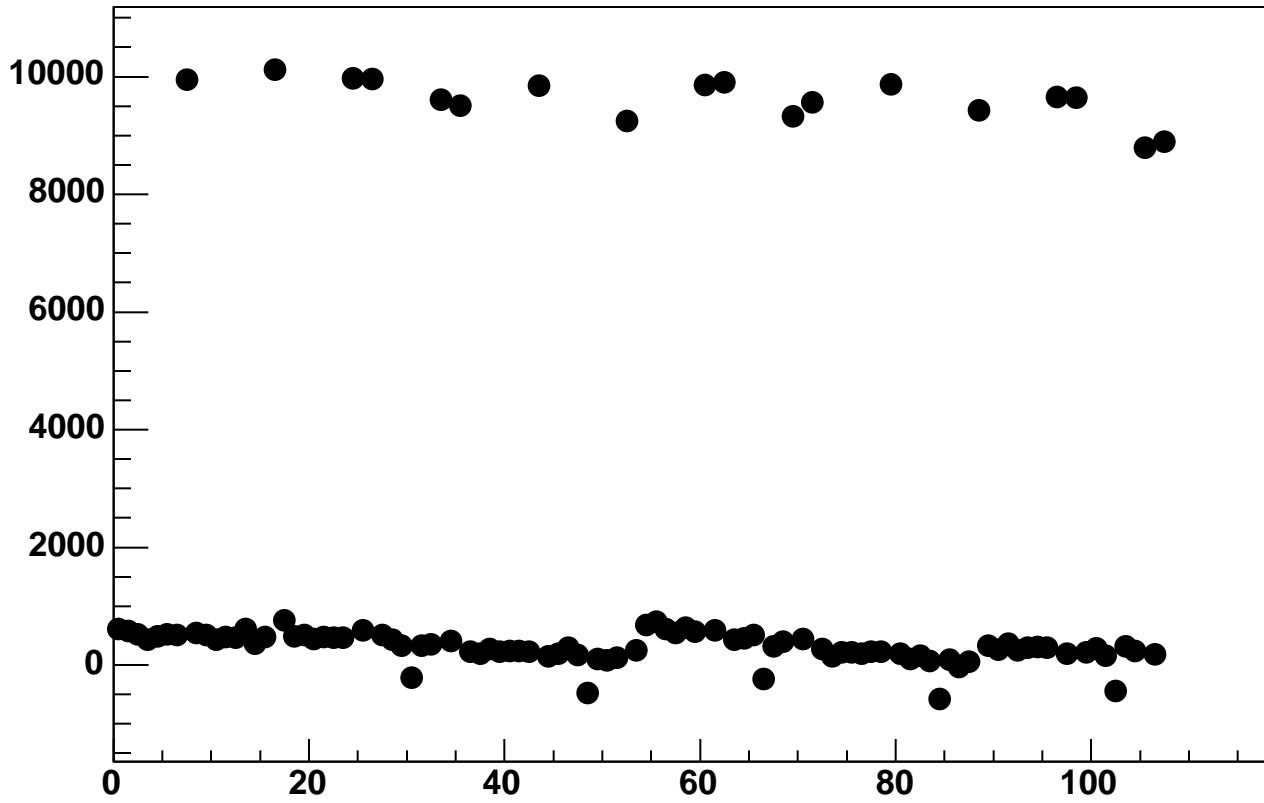
Enable 5, DAC=4000, Hold=20, ADC Mean vs 18\*Chip+Chan



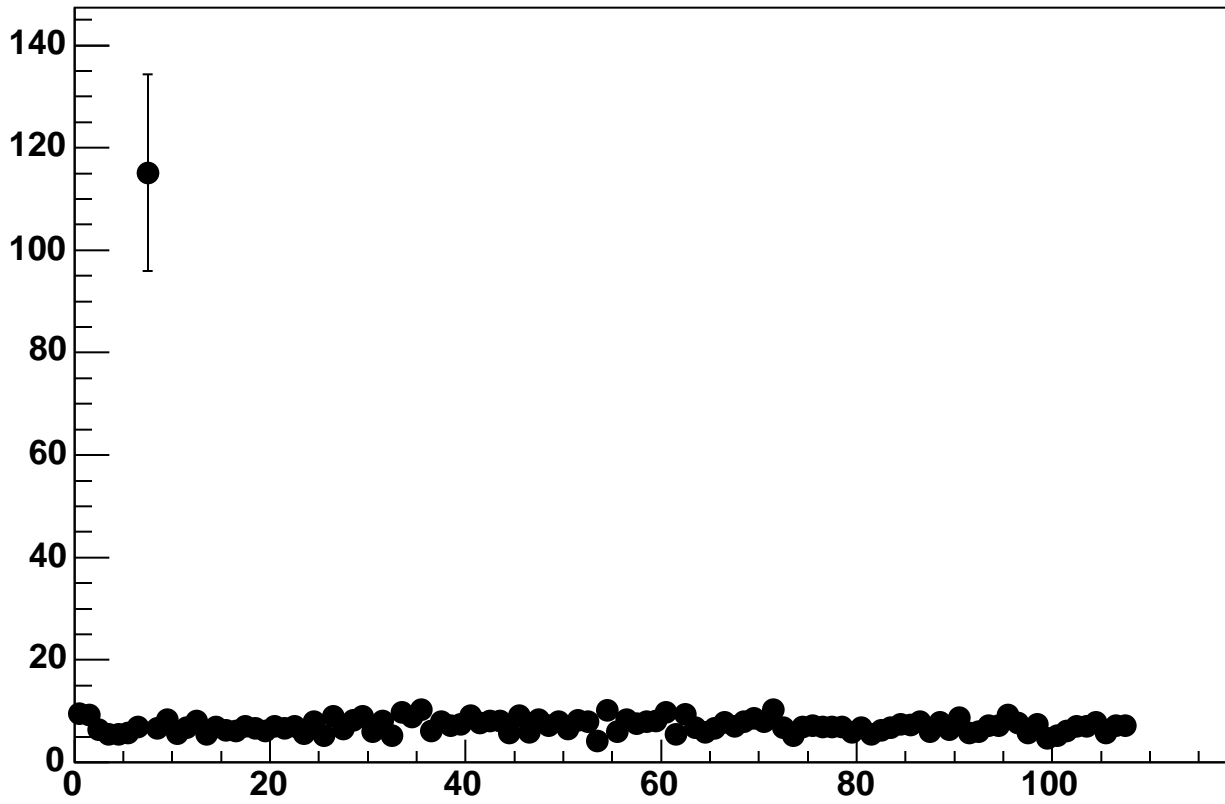
Enable 5, DAC=4000, Hold=20, ADC Noise vs 18\*Chip+Chan



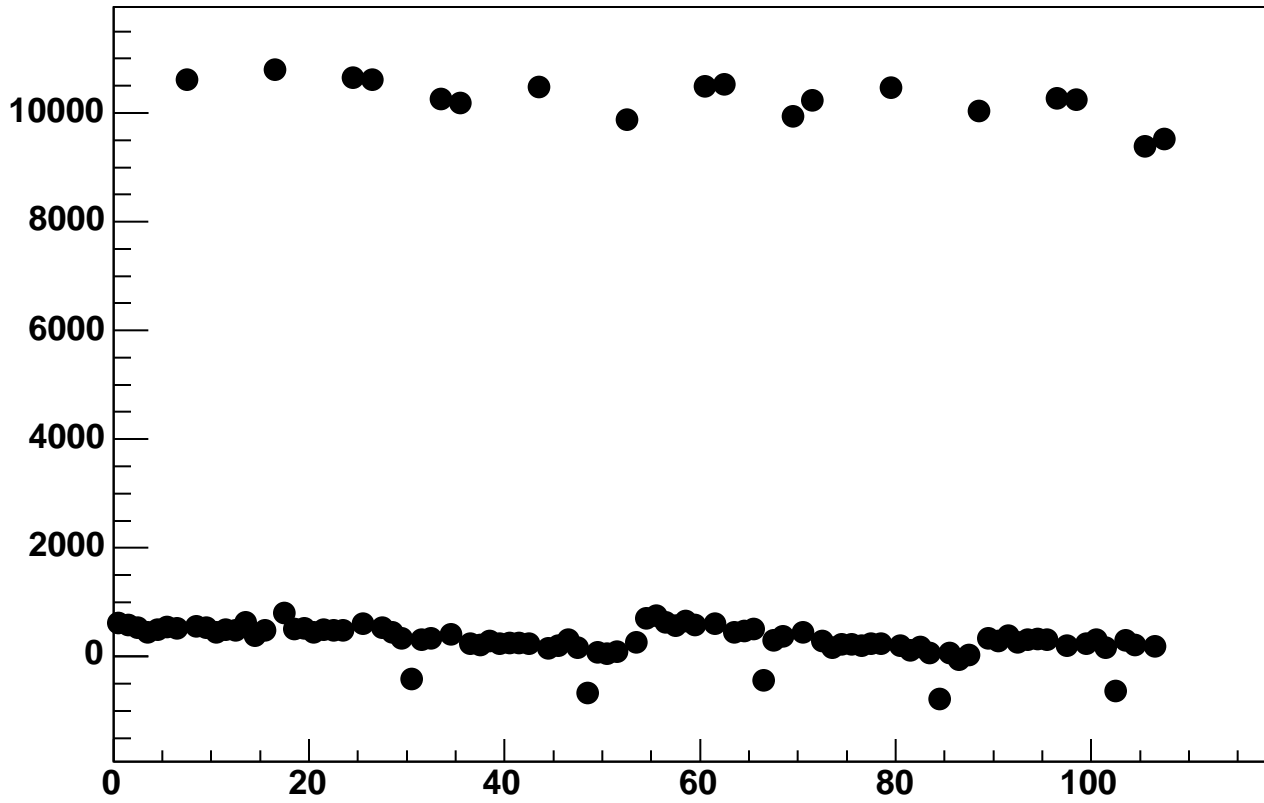
Enable 5, DAC=4000, Hold=25, ADC Mean vs 18\*Chip+Chan



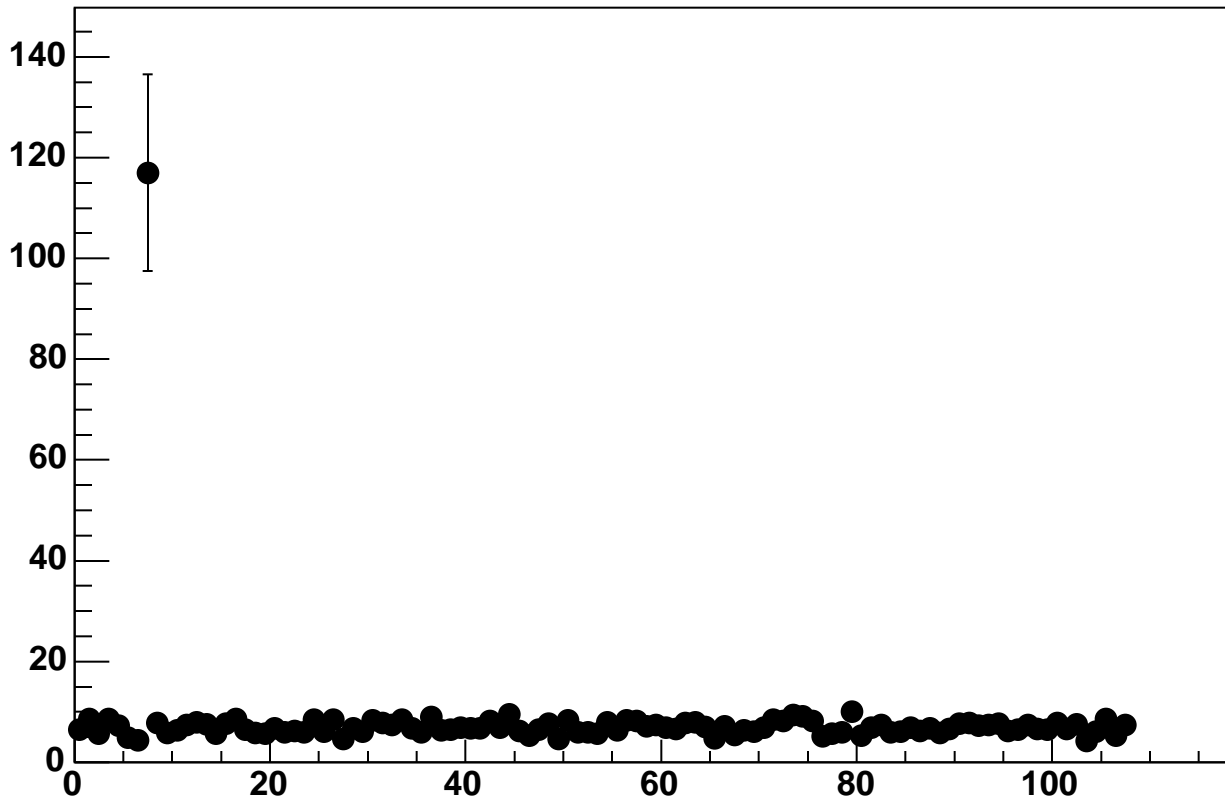
Enable 5, DAC=4000, Hold=25, ADC Noise vs 18\*Chip+Chan



Enable 5, DAC=4000, Hold=30, ADC Mean vs 18\*Chip+Chan

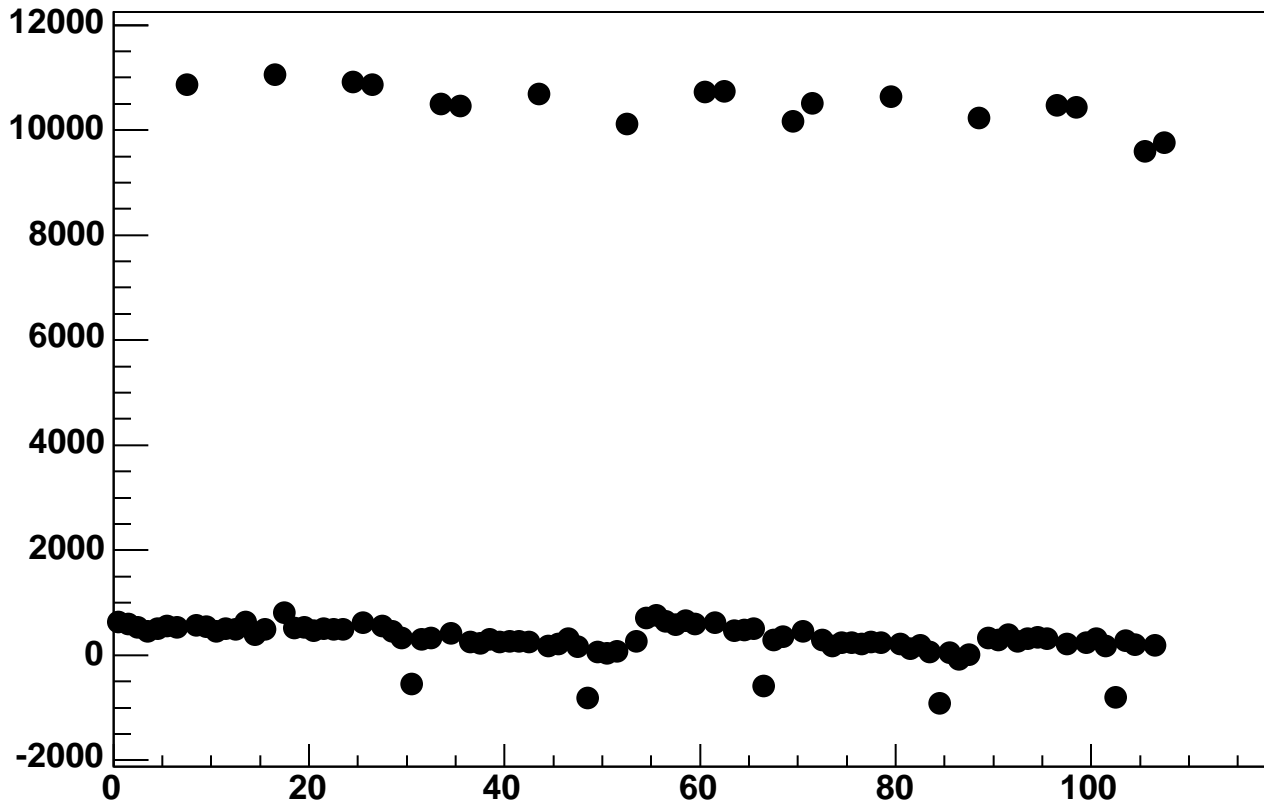


Enable 5, DAC=4000, Hold=30, ADC Noise vs 18\*Chip+Chan

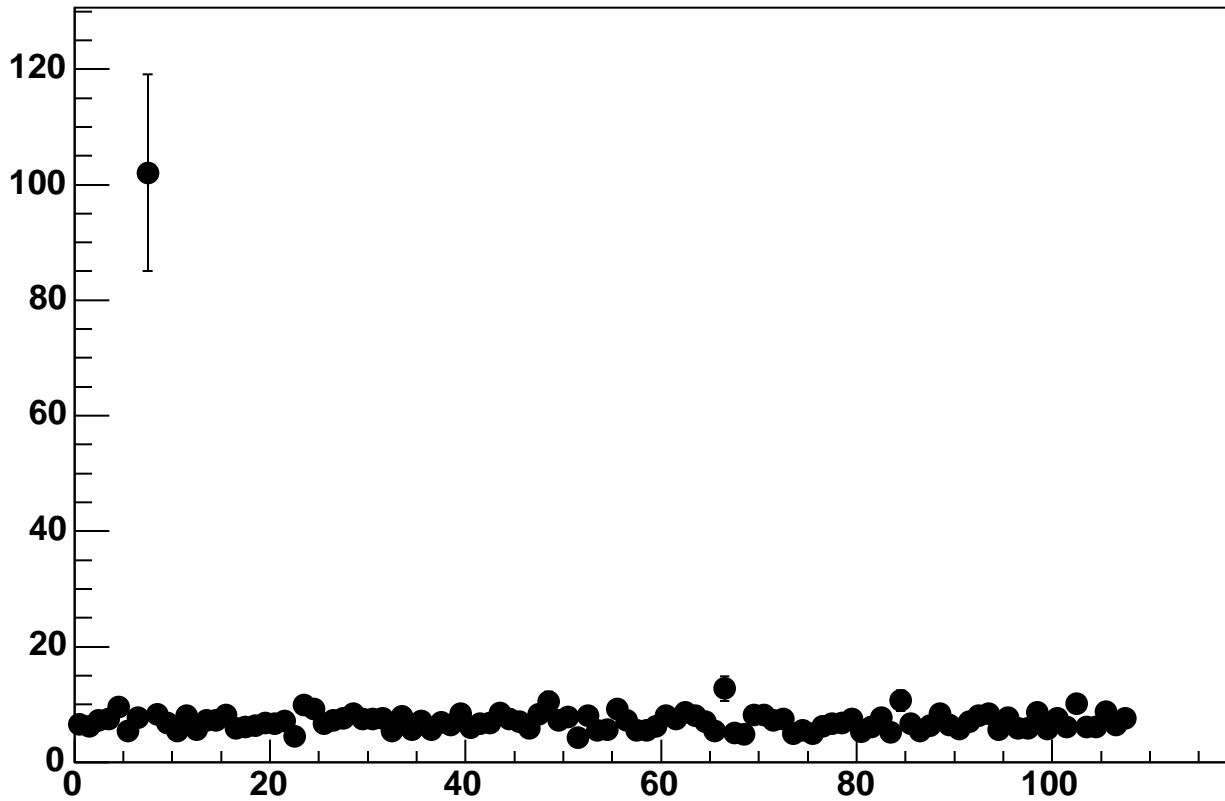




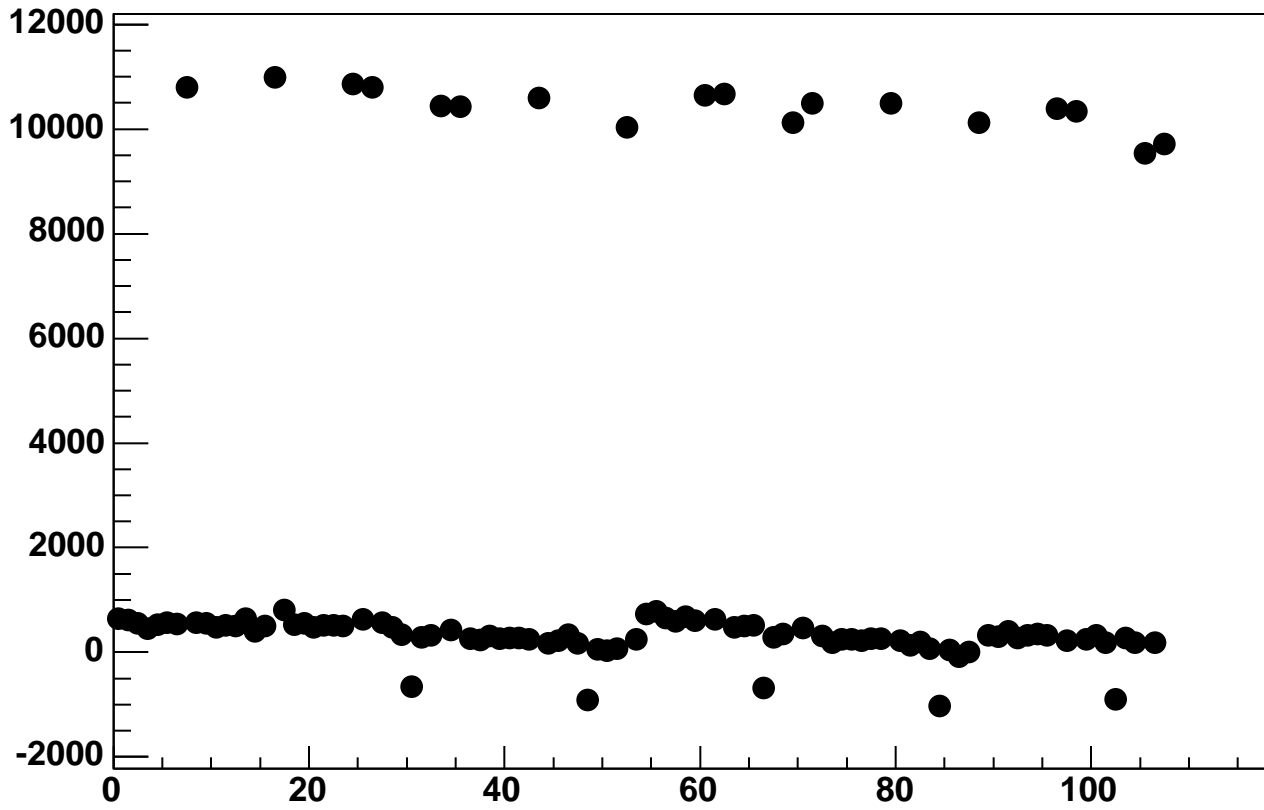
Enable 5, DAC=4000, Hold=35, ADC Mean vs 18\*Chip+Chan



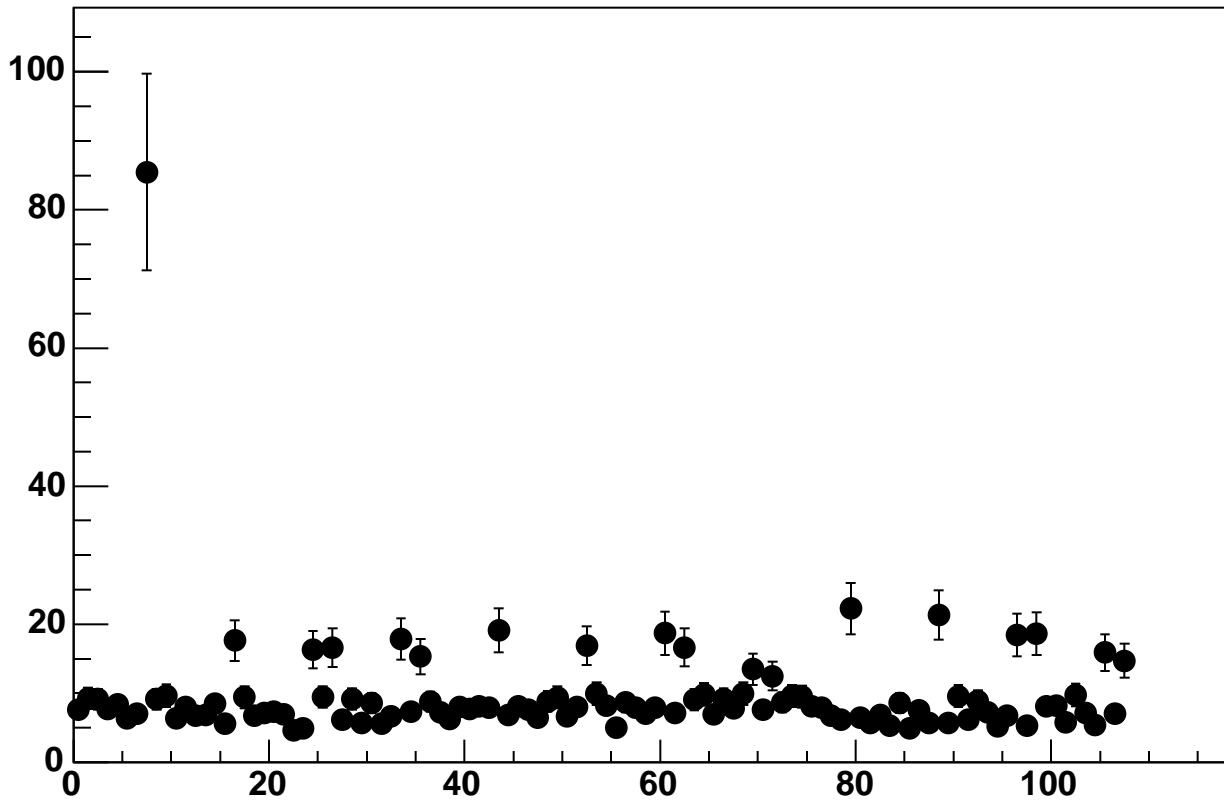
Enable 5, DAC=4000, Hold=35, ADC Noise vs 18\*Chip+Chan



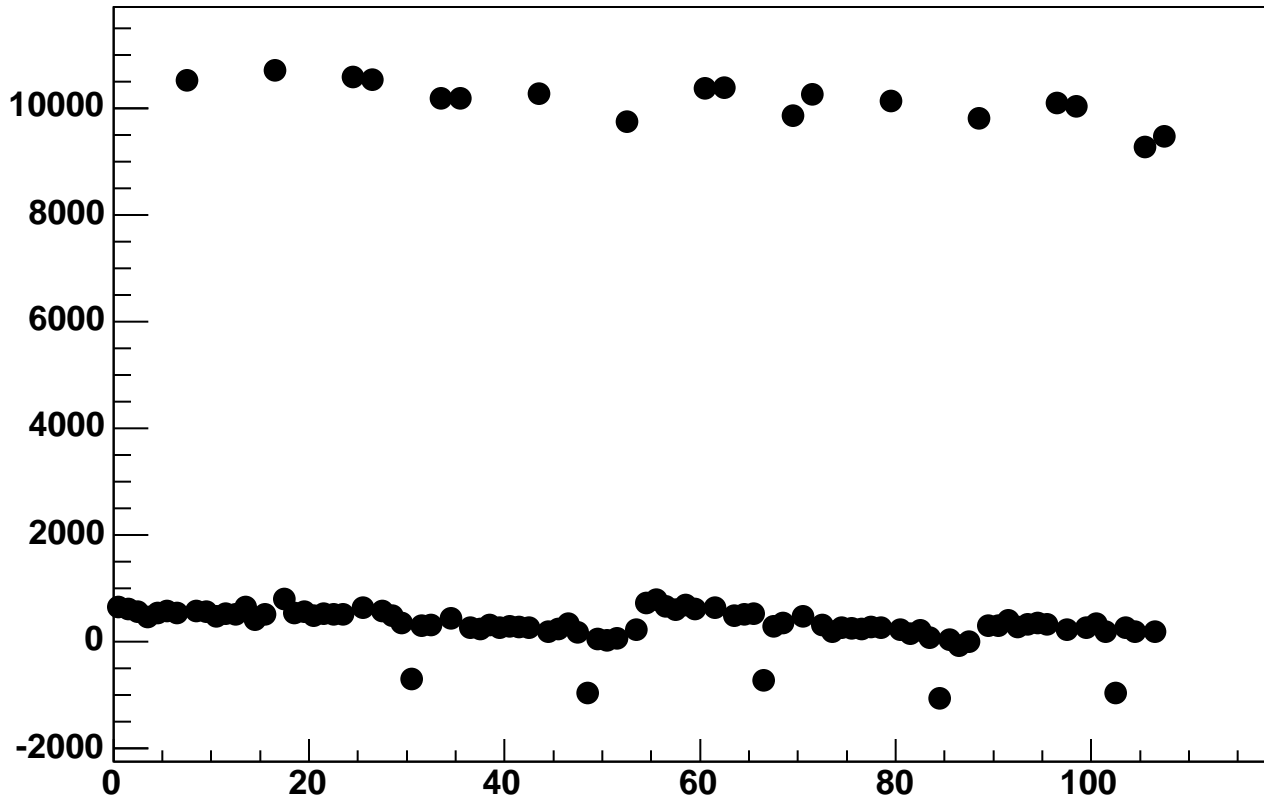
Enable 5, DAC=4000, Hold=40, ADC Mean vs 18\*Chip+Chan



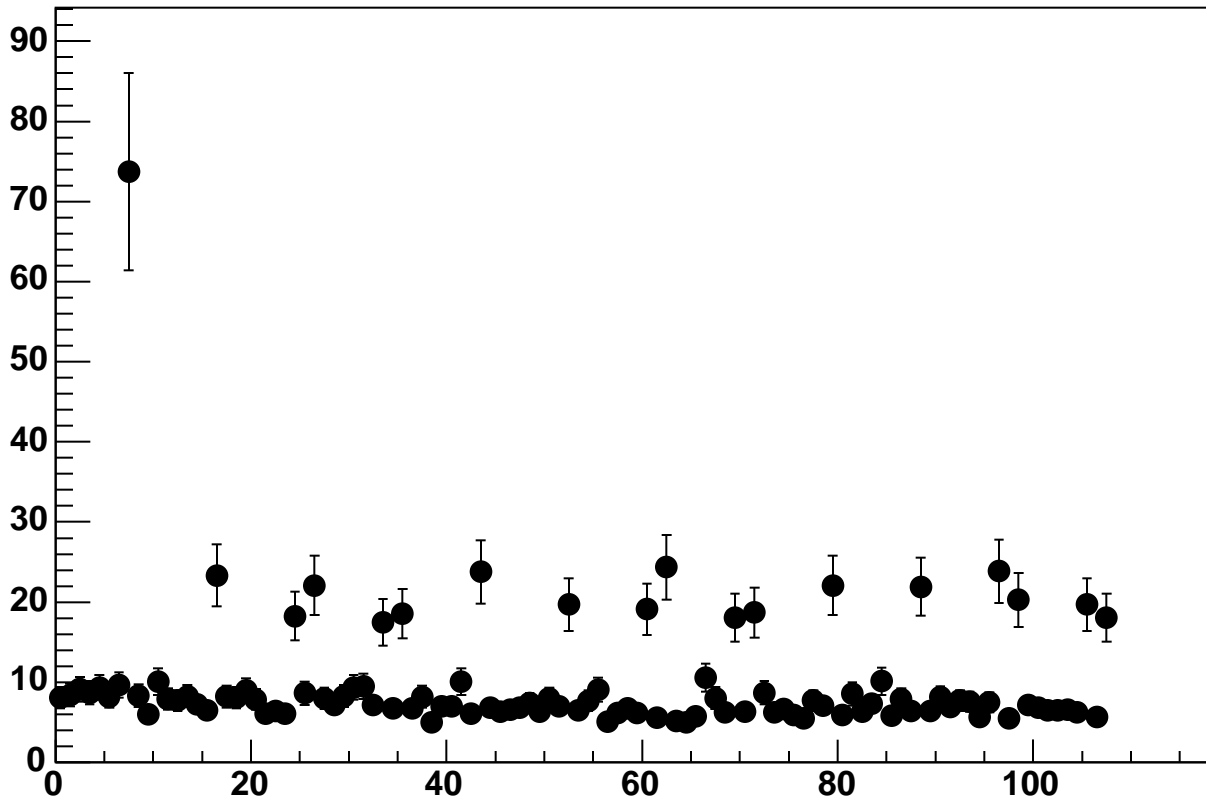
Enable 5, DAC=4000, Hold=40, ADC Noise vs 18\*Chip+Chan



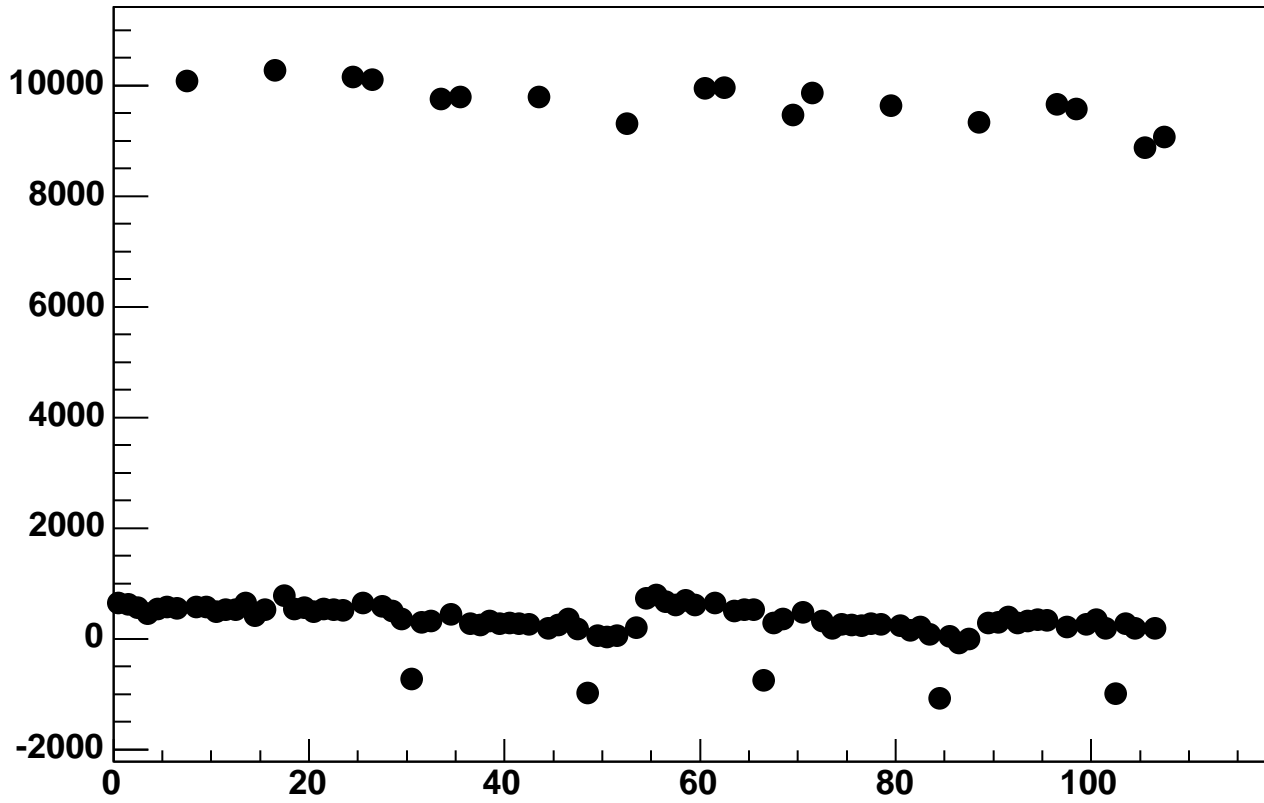
Enable 5, DAC=4000, Hold=45, ADC Mean vs 18\*Chip+Chan



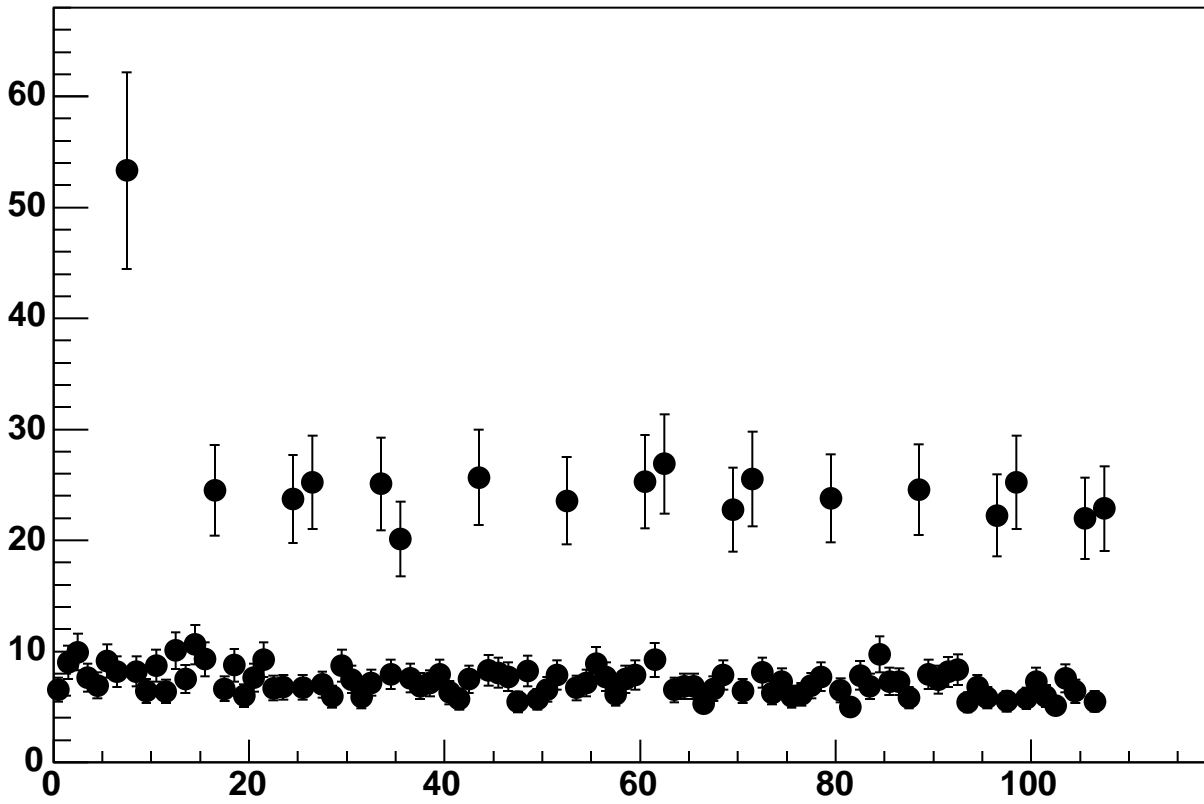
Enable 5, DAC=4000, Hold=45, ADC Noise vs 18\*Chip+Chan



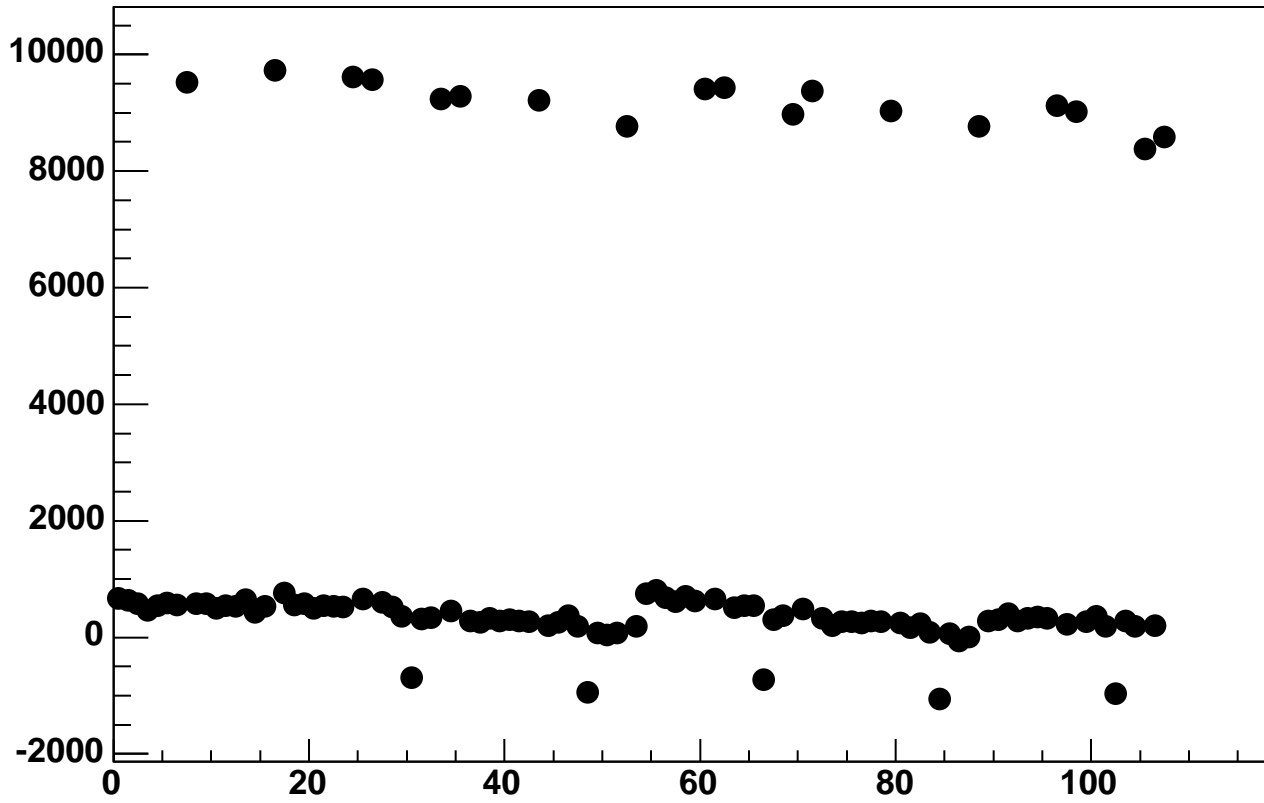
Enable 5, DAC=4000, Hold=50, ADC Mean vs 18\*Chip+Chan



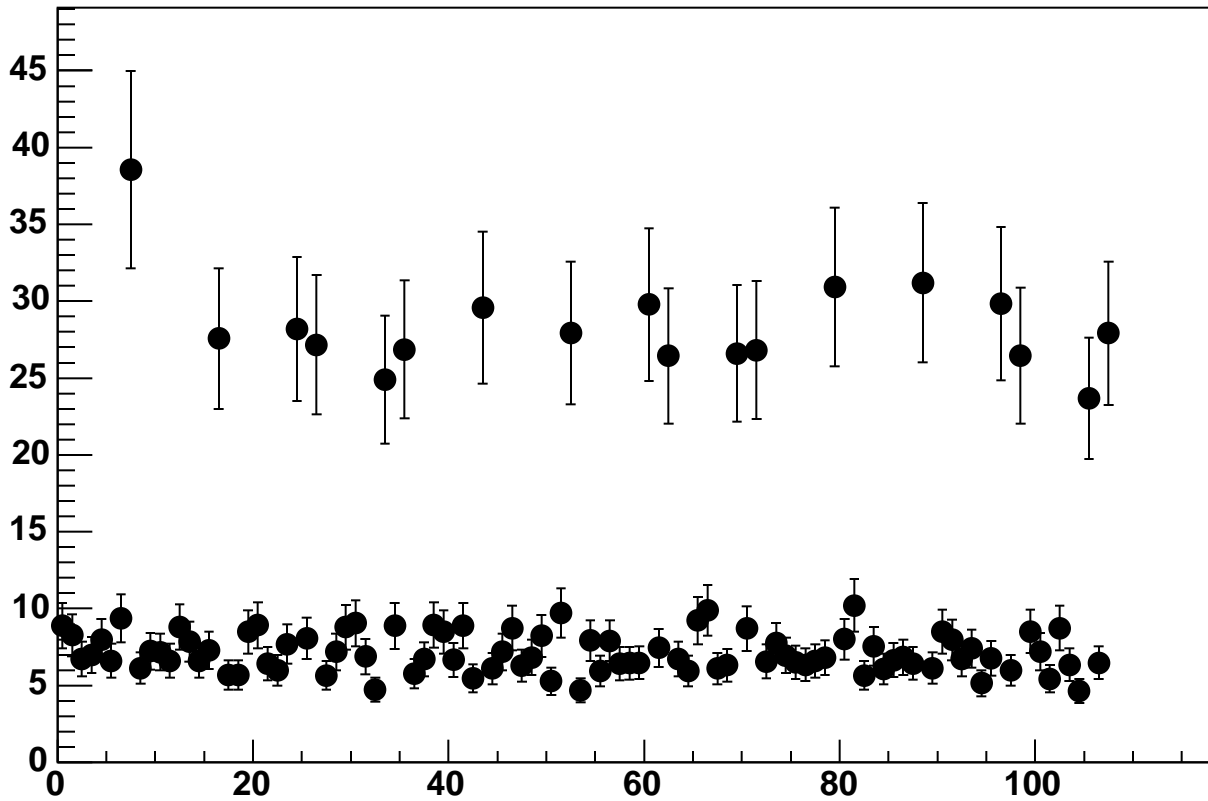
Enable 5, DAC=4000, Hold=50, ADC Noise vs 18\*Chip+Chan



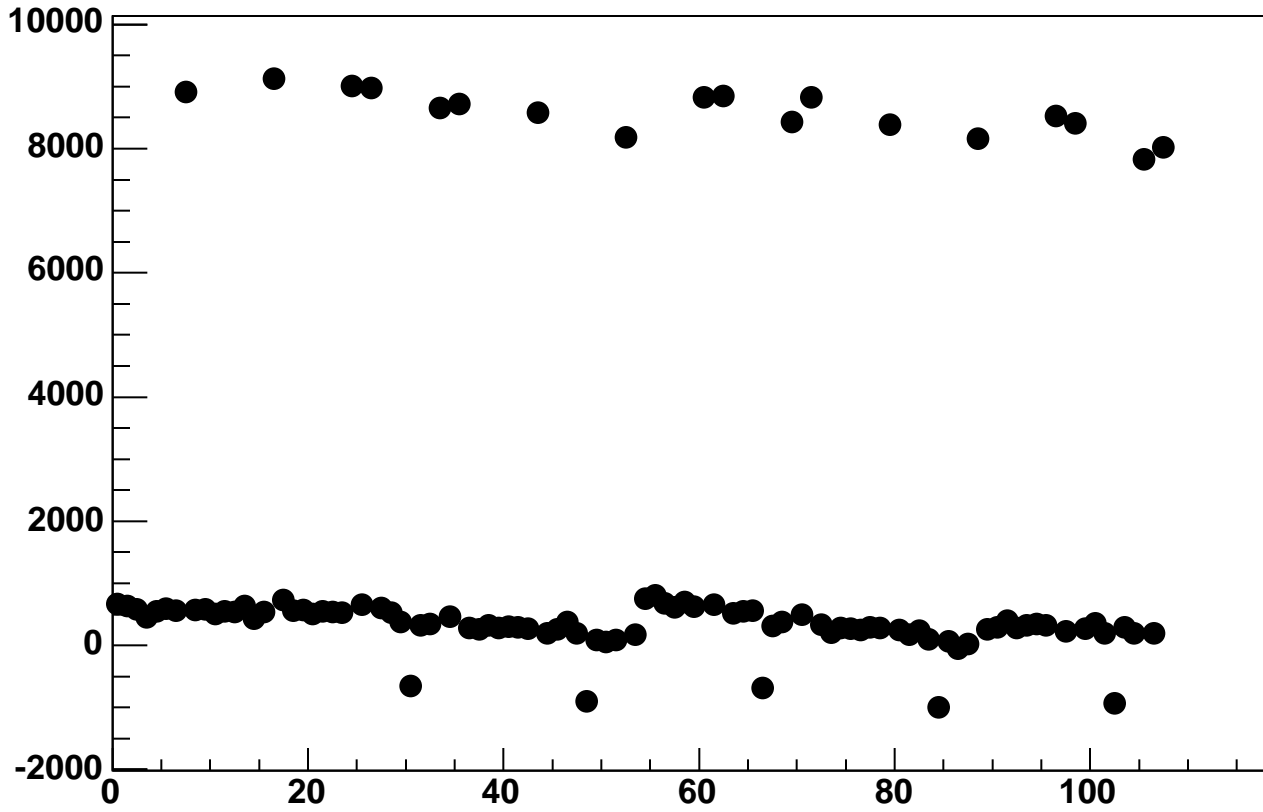
Enable 5, DAC=4000, Hold=55, ADC Mean vs 18\*Chip+Chan



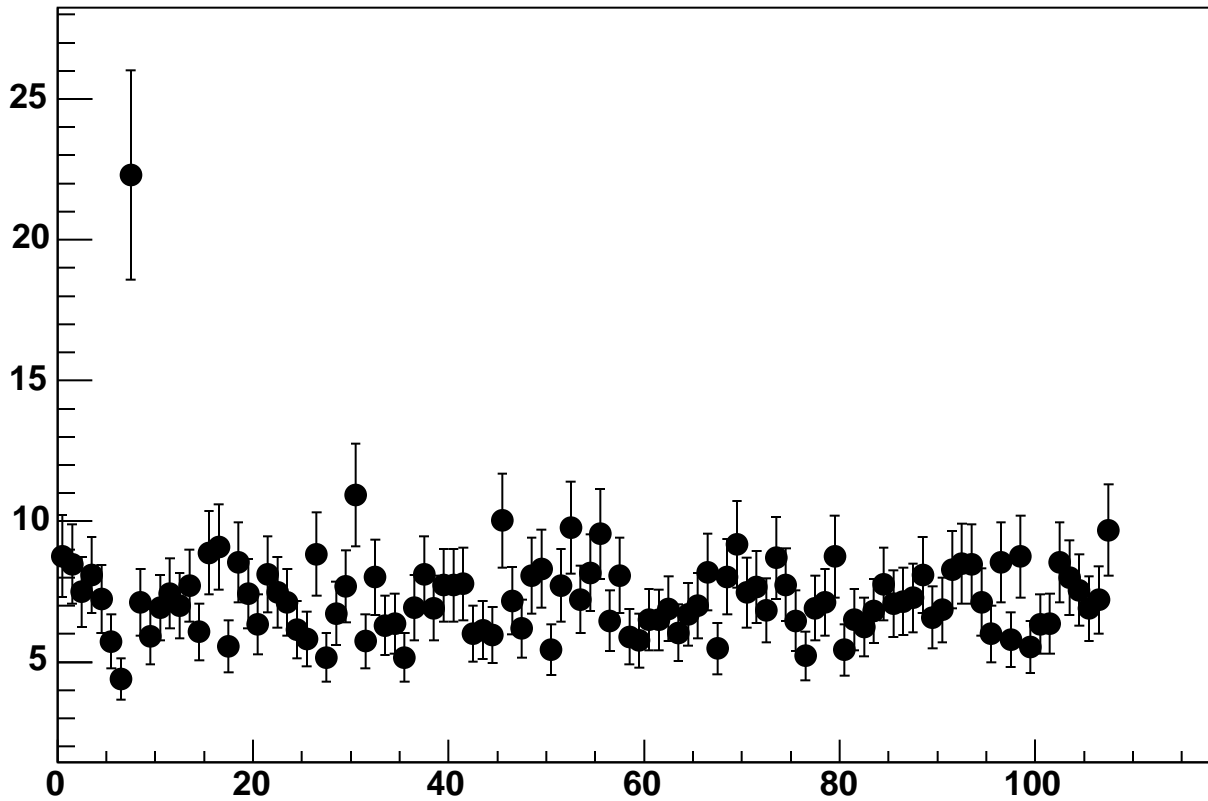
Enable 5, DAC=4000, Hold=55, ADC Noise vs 18\*Chip+Chan



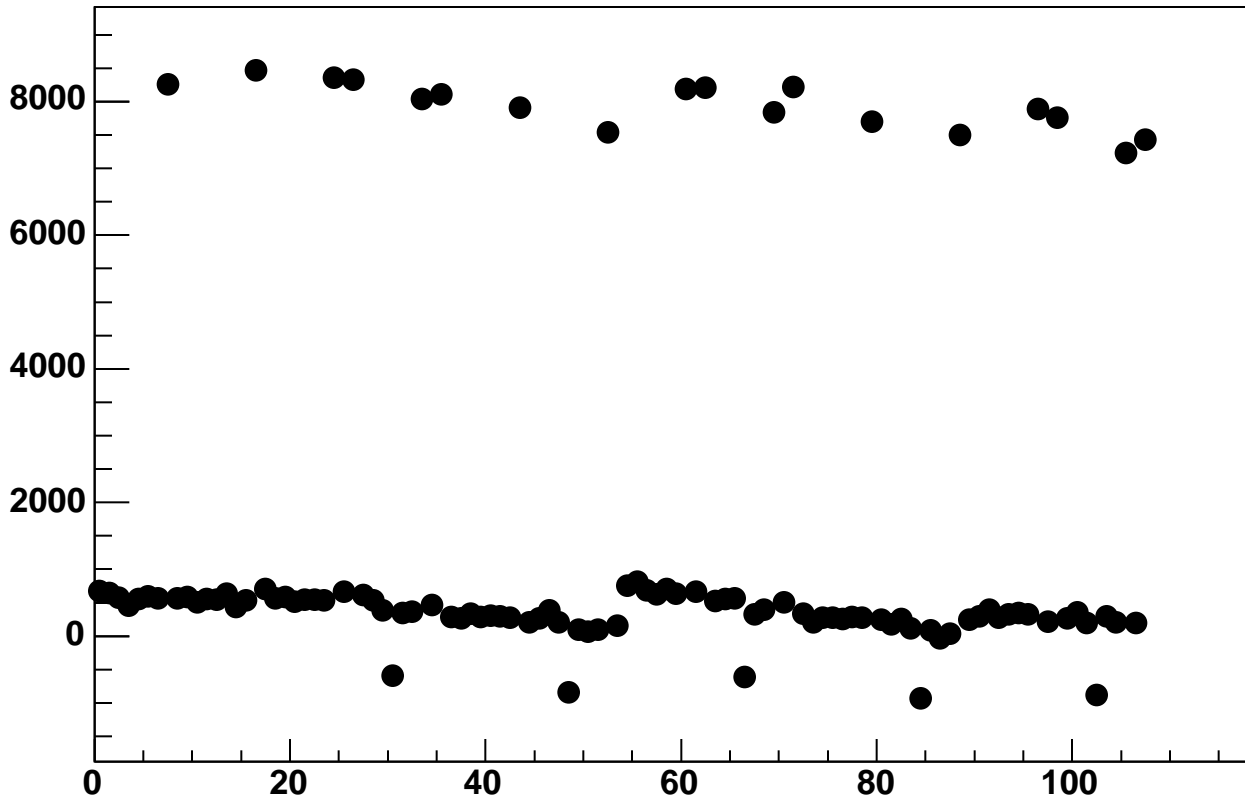
Enable 5, DAC=4000, Hold=60, ADC Mean vs 18\*Chip+Chan



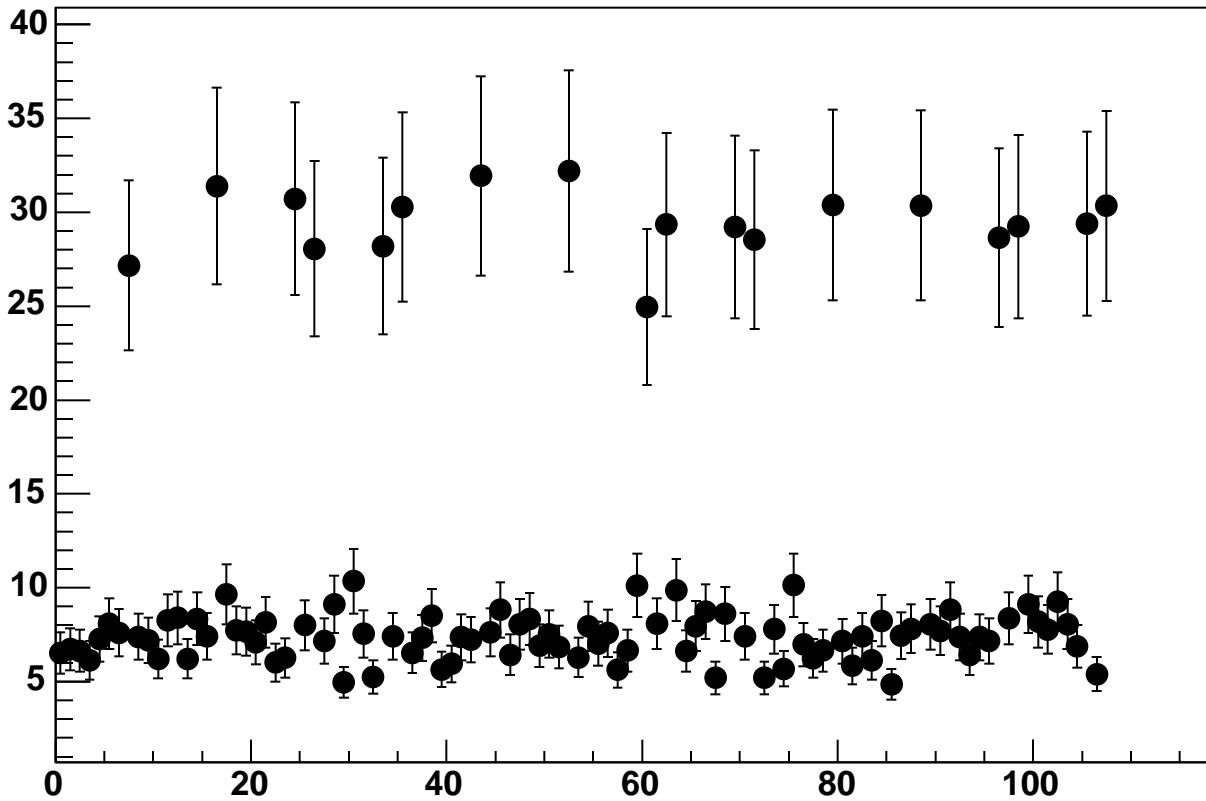
Enable 5, DAC=4000, Hold=60, ADC Noise vs 18\*Chip+Chan



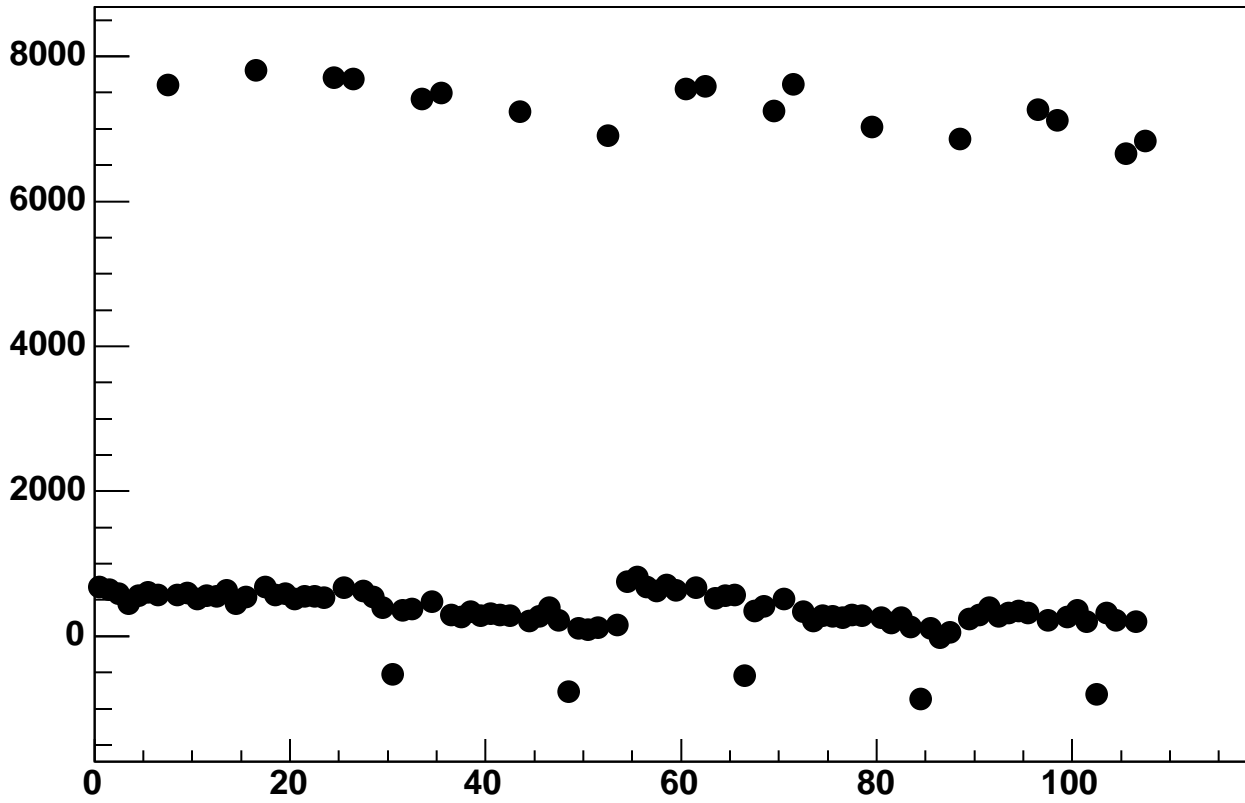
Enable 5, DAC=4000, Hold=65, ADC Mean vs 18\*Chip+Chan



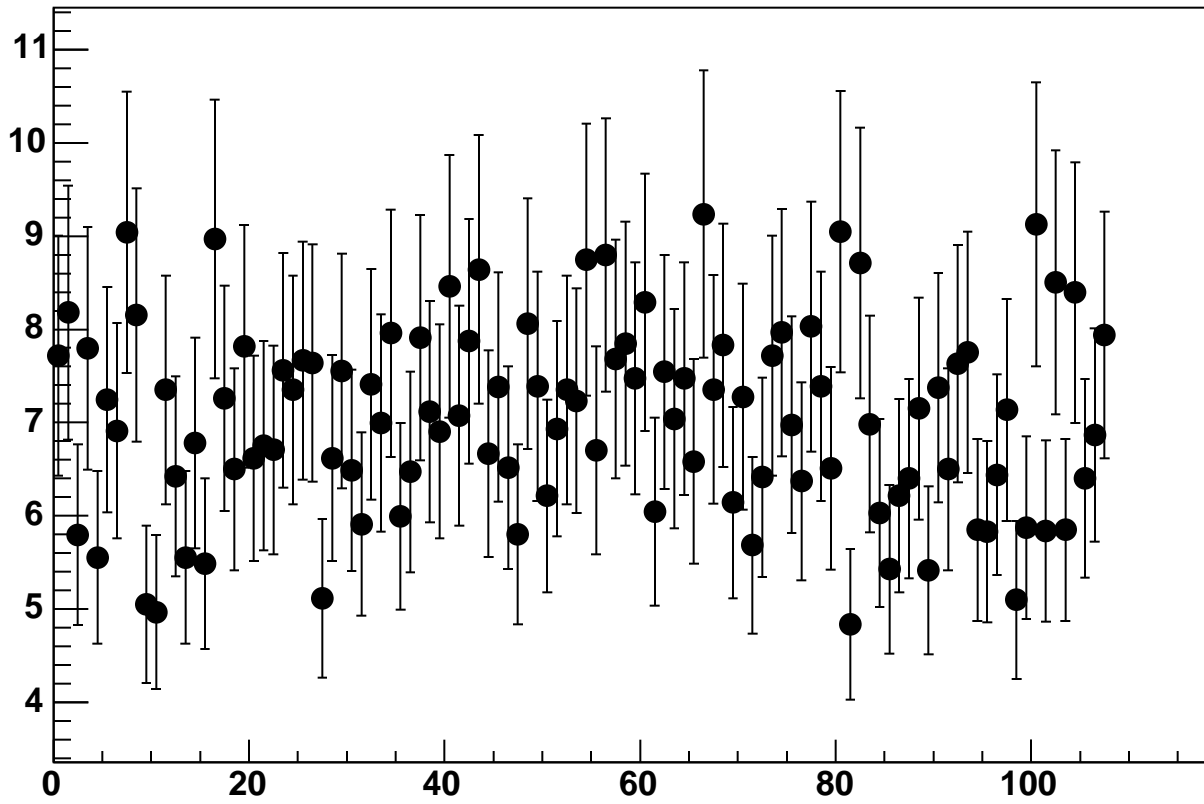
Enable 5, DAC=4000, Hold=65, ADC Noise vs 18\*Chip+Chan



Enable 5, DAC=4000, Hold=70, ADC Mean vs 18\*Chip+Chan

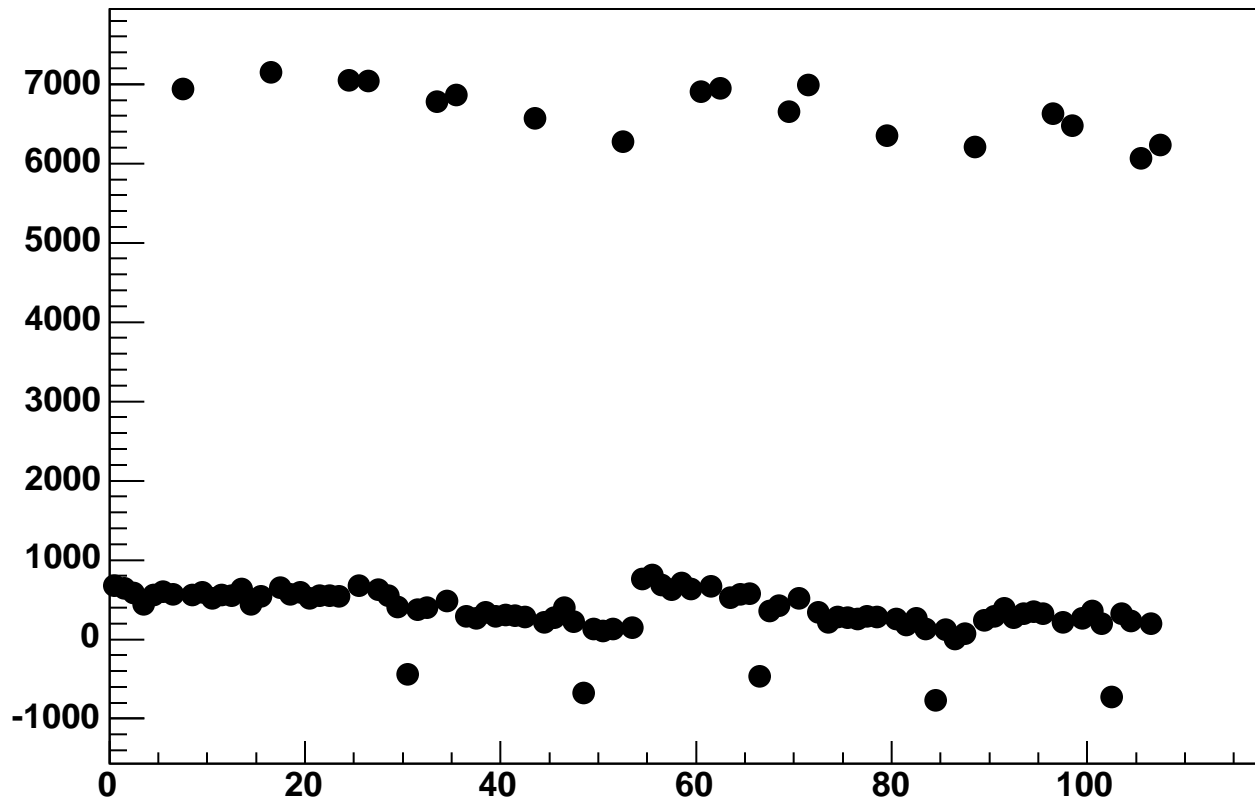


Enable 5, DAC=4000, Hold=70, ADC Noise vs 18\*Chip+Chan

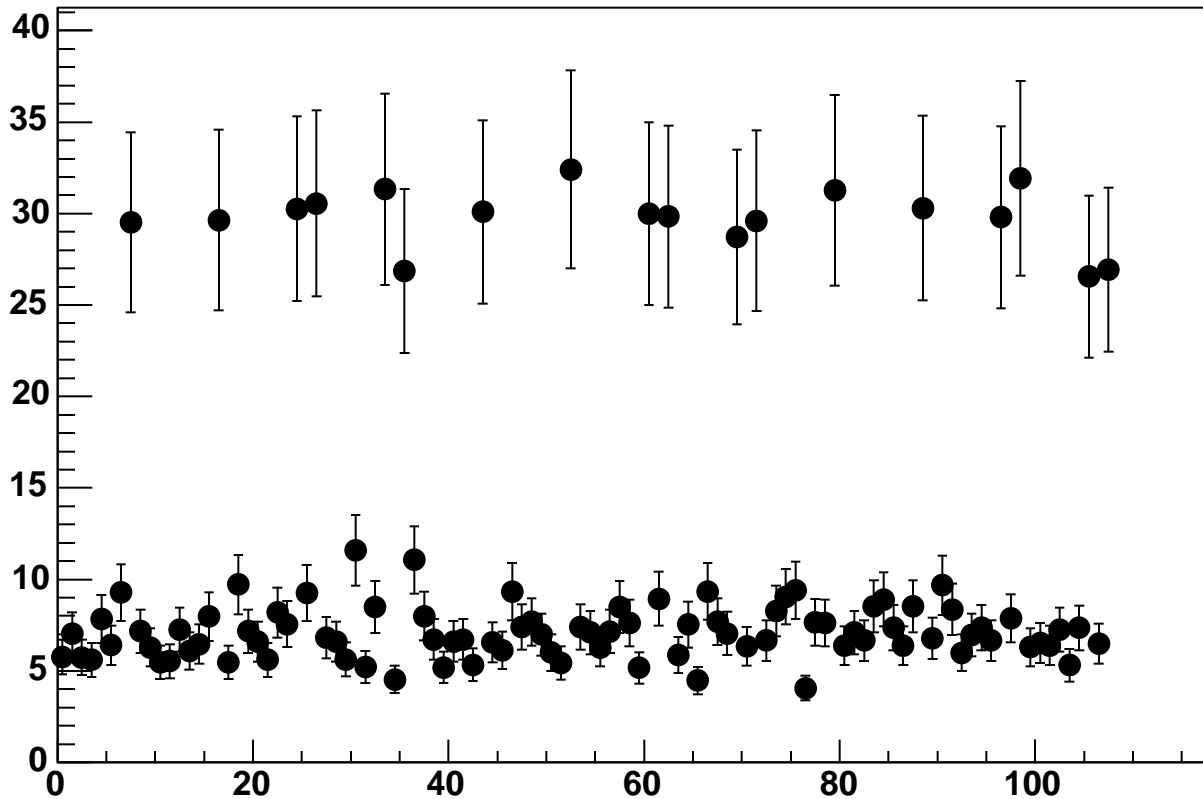




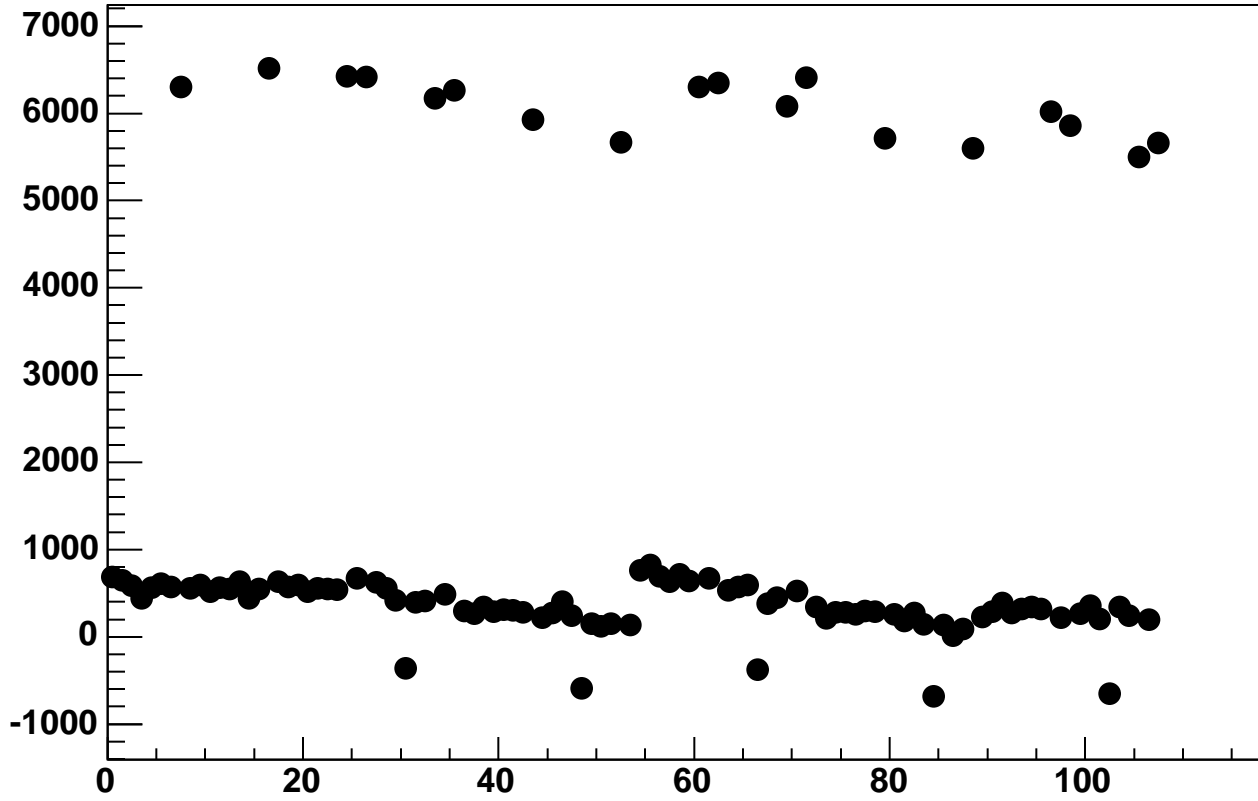
Enable 5, DAC=4000, Hold=75, ADC Mean vs 18\*Chip+Chan



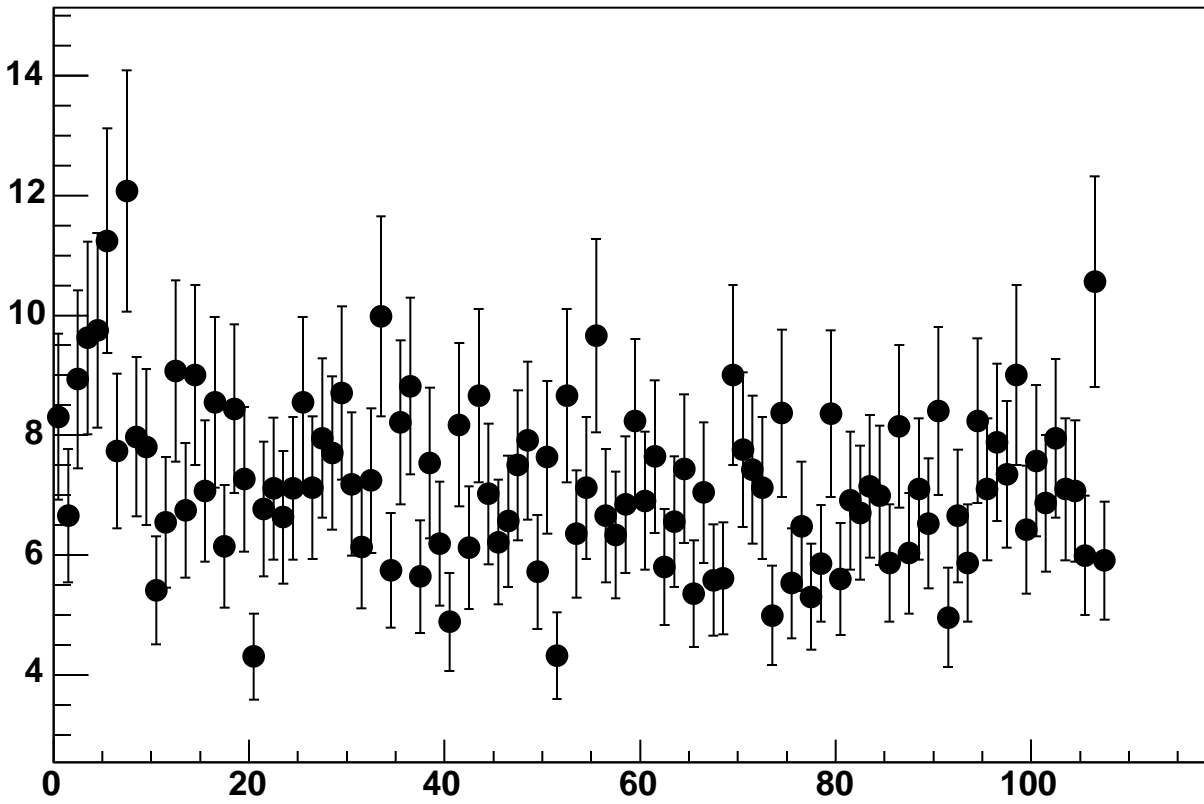
Enable 5, DAC=4000, Hold=75, ADC Noise vs 18\*Chip+Chan



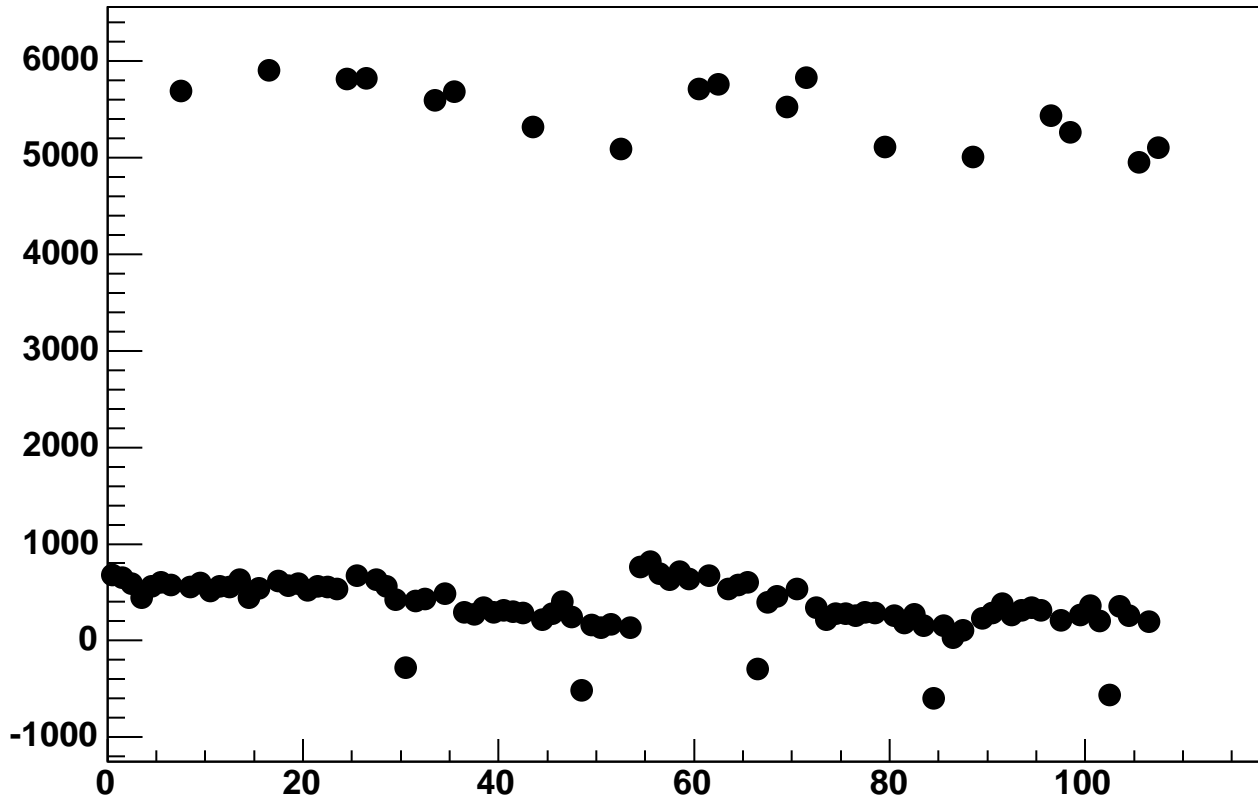
Enable 5, DAC=4000, Hold=80, ADC Mean vs 18\*Chip+Chan



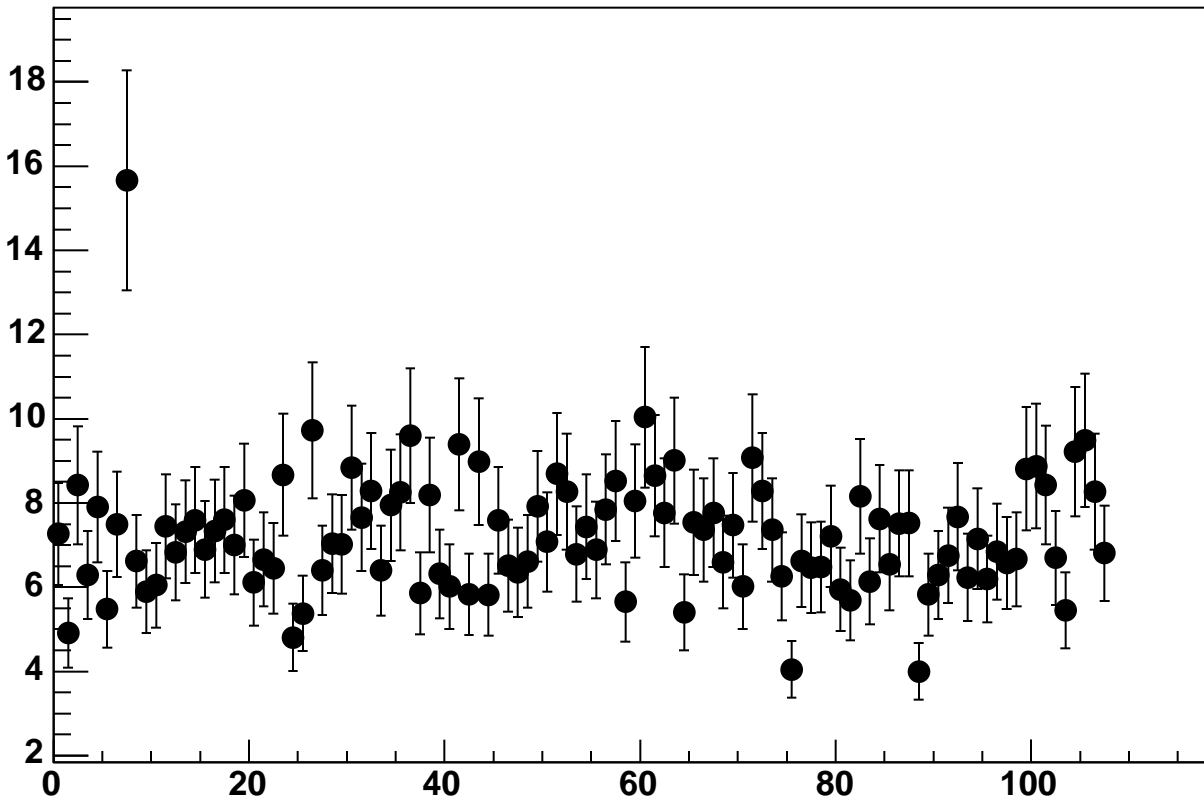
Enable 5, DAC=4000, Hold=80, ADC Noise vs 18\*Chip+Chan



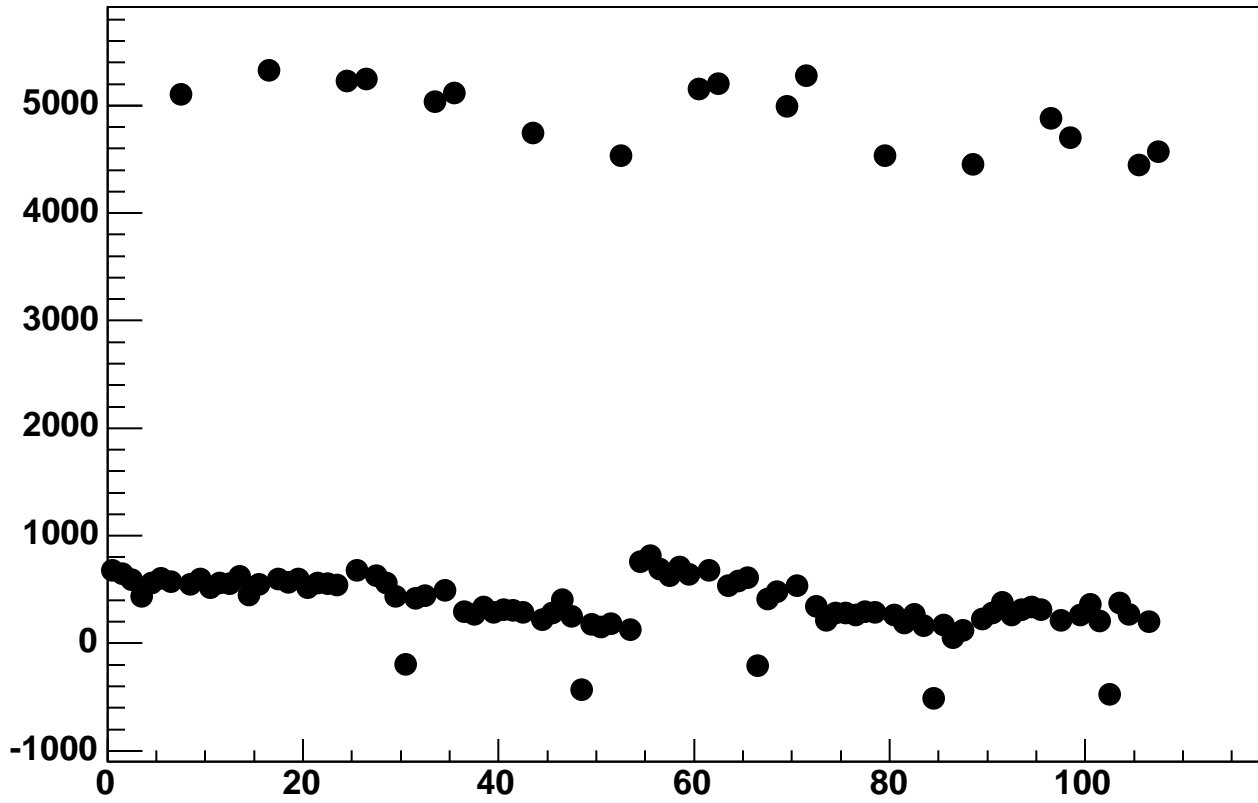
Enable 5, DAC=4000, Hold=85, ADC Mean vs 18\*Chip+Chan



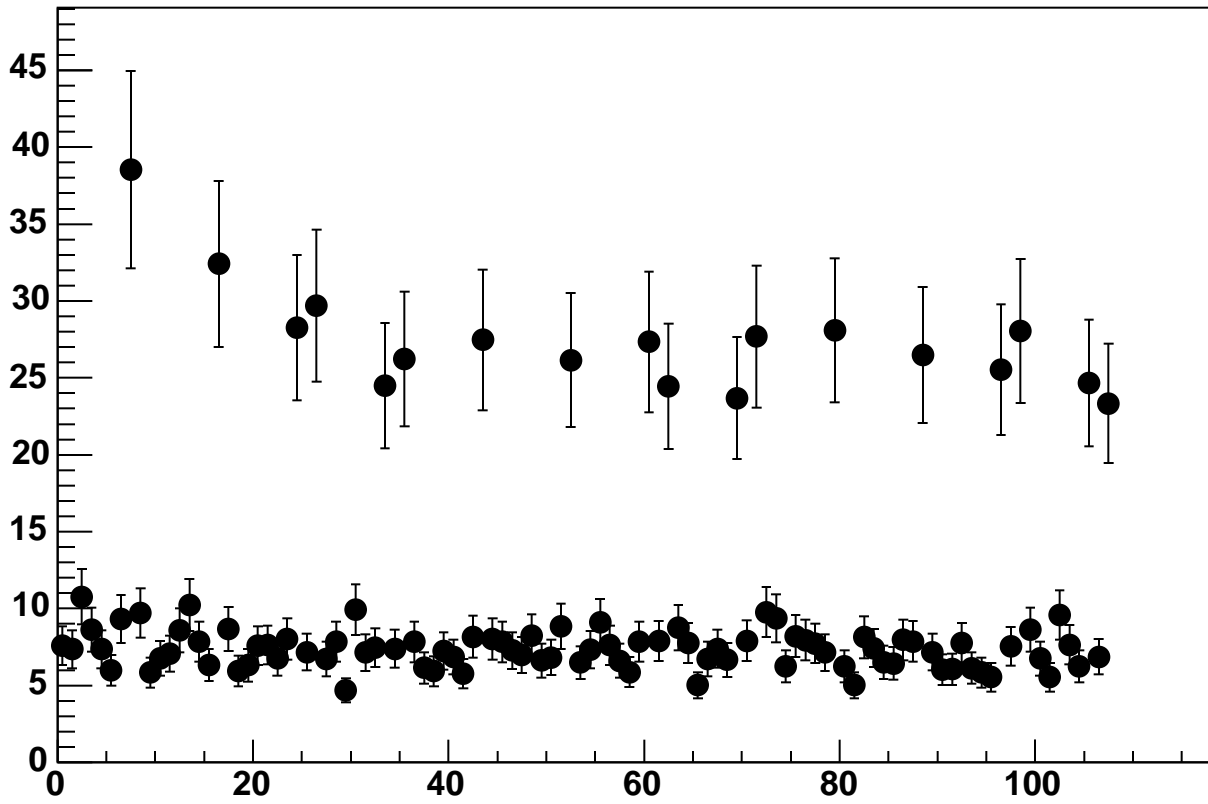
Enable 5, DAC=4000, Hold=85, ADC Noise vs 18\*Chip+Chan



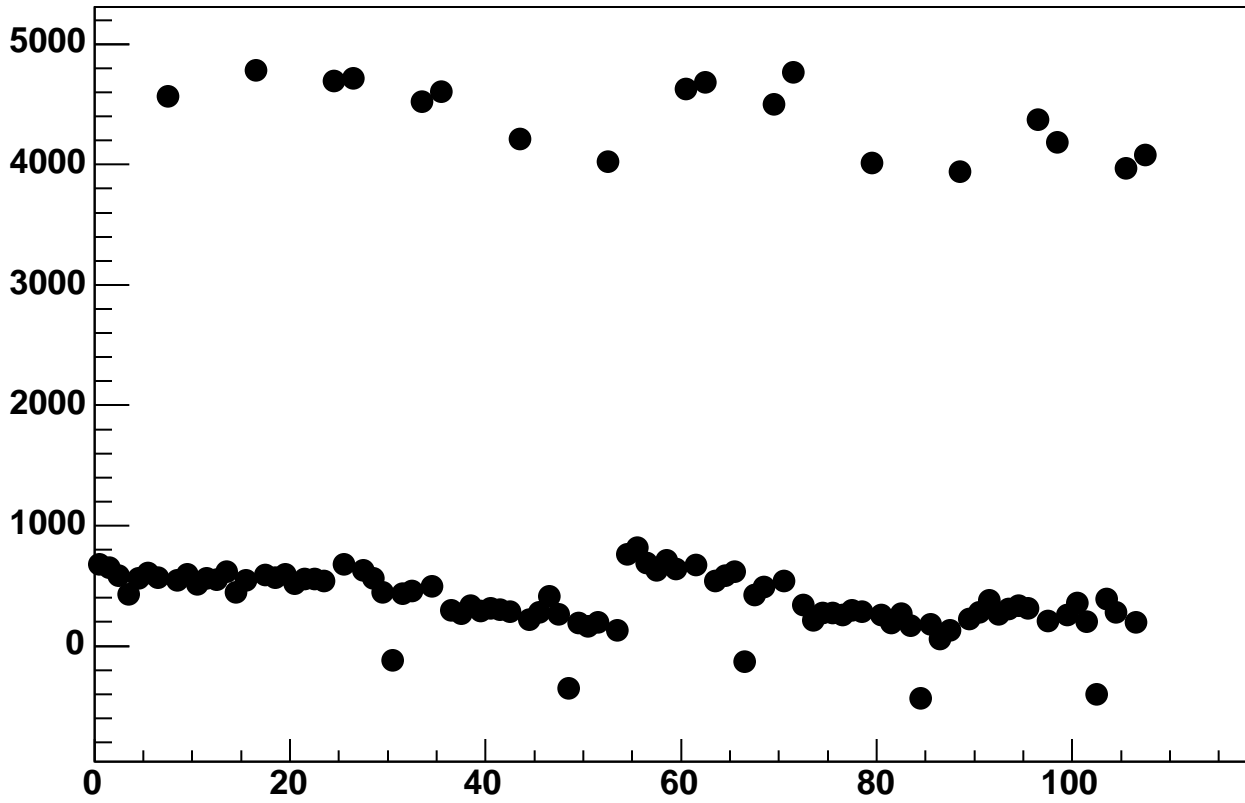
Enable 5, DAC=4000, Hold=90, ADC Mean vs 18\*Chip+Chan



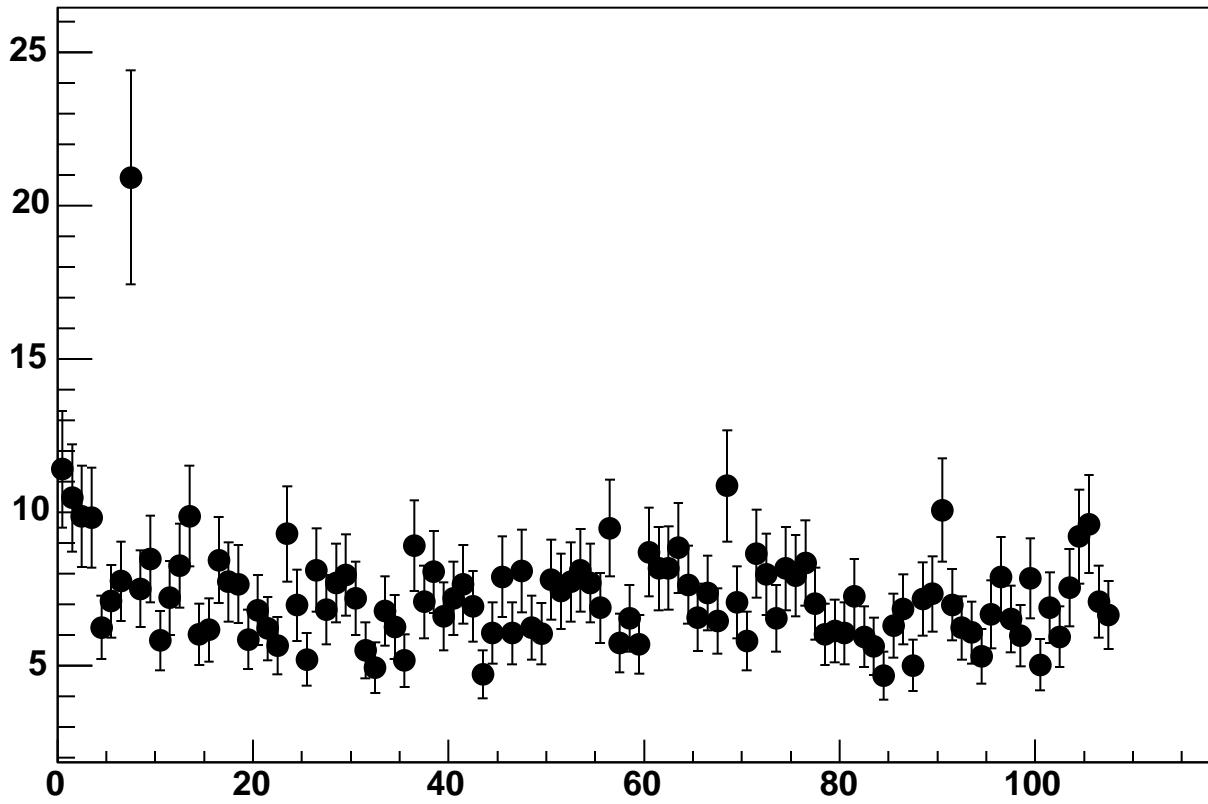
Enable 5, DAC=4000, Hold=90, ADC Noise vs 18\*Chip+Chan



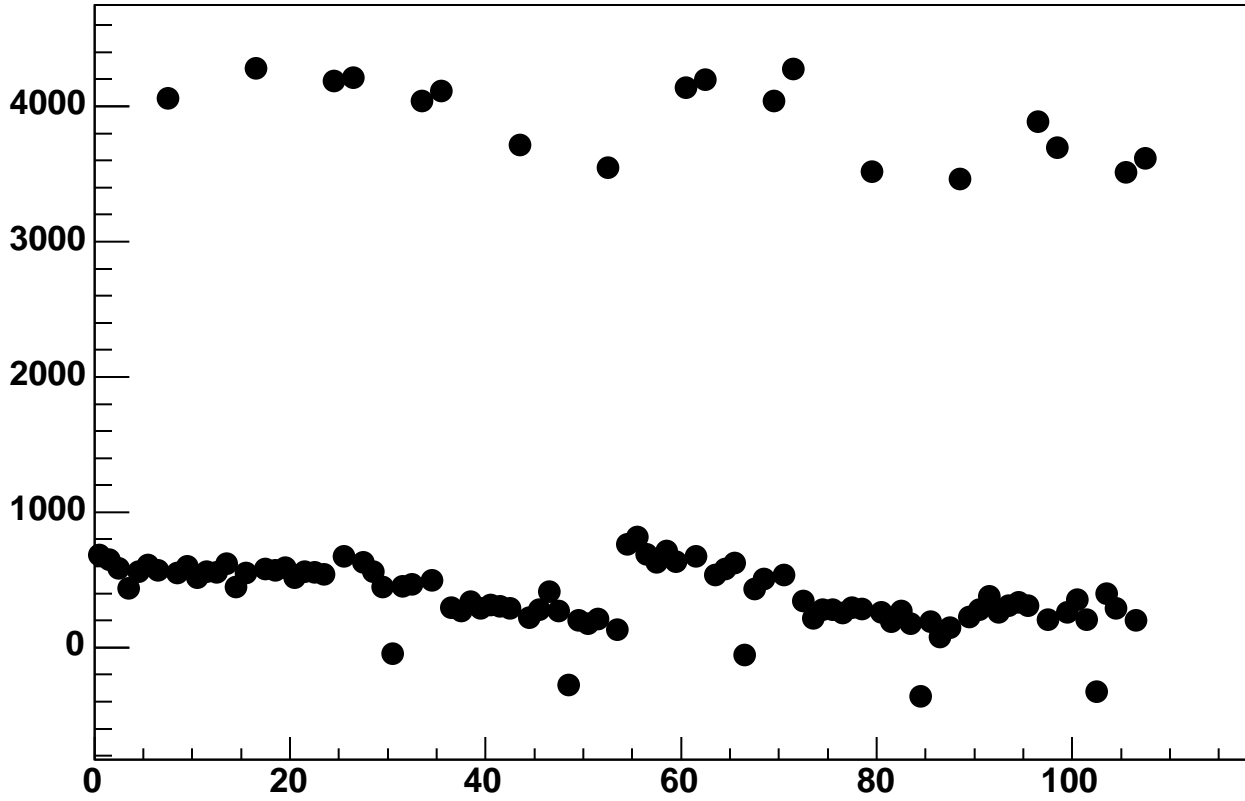
Enable 5, DAC=4000, Hold=95, ADC Mean vs 18\*Chip+Chan



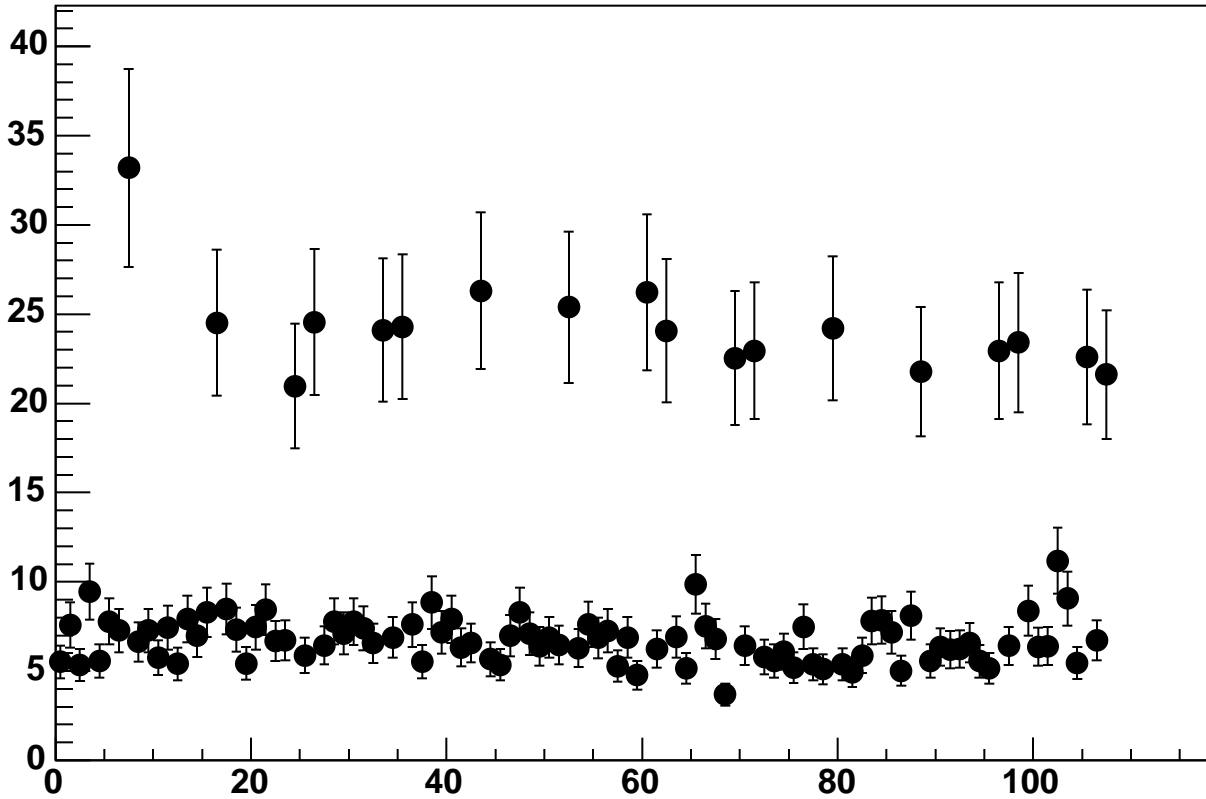
Enable 5, DAC=4000, Hold=95, ADC Noise vs 18\*Chip+Chan



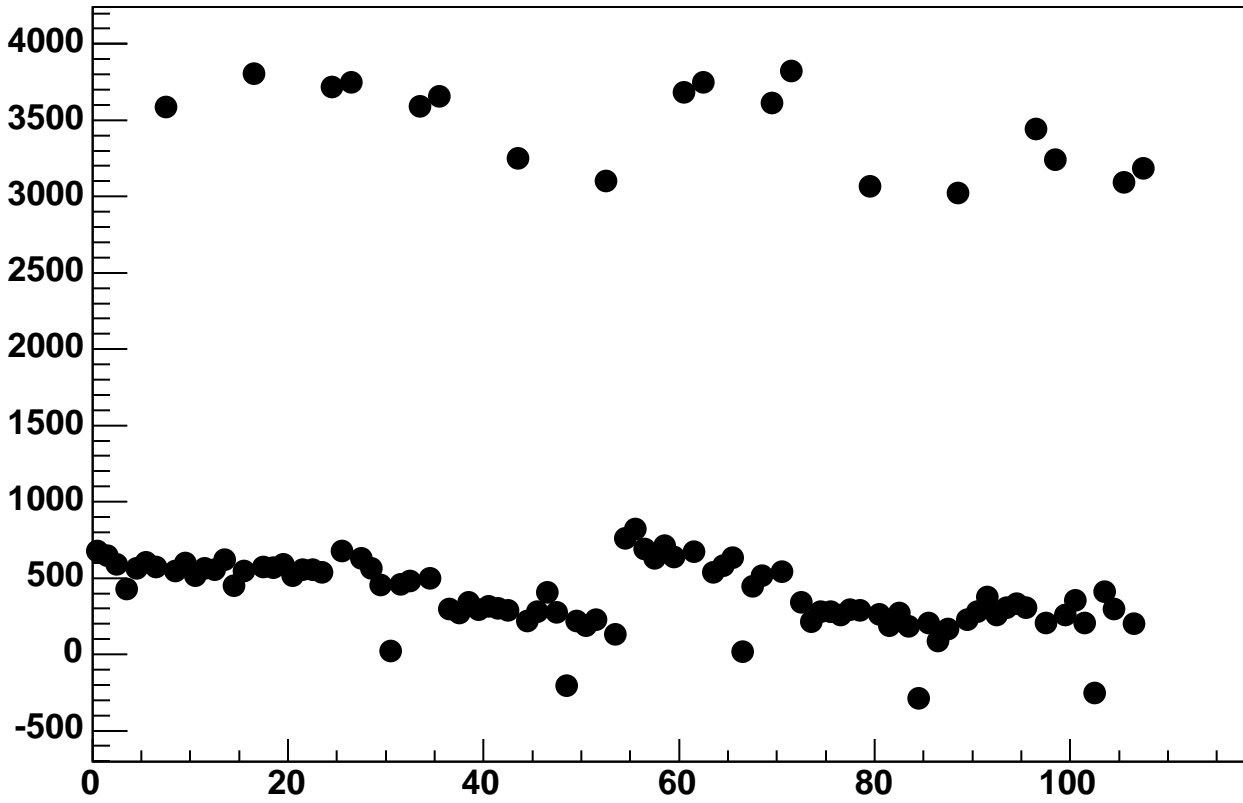
Enable 5, DAC=4000, Hold=100, ADC Mean vs 18\*Chip+Chan



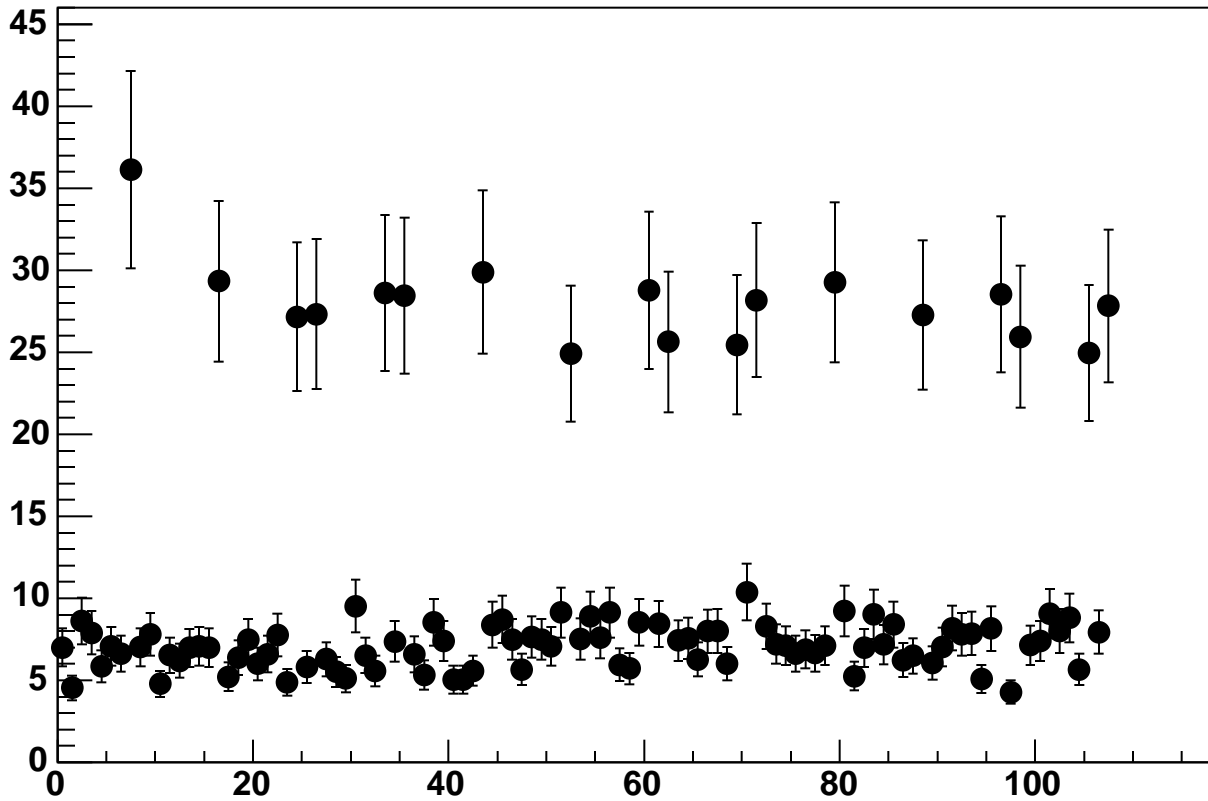
Enable 5, DAC=4000, Hold=100, ADC Noise vs 18\*Chip+Chan



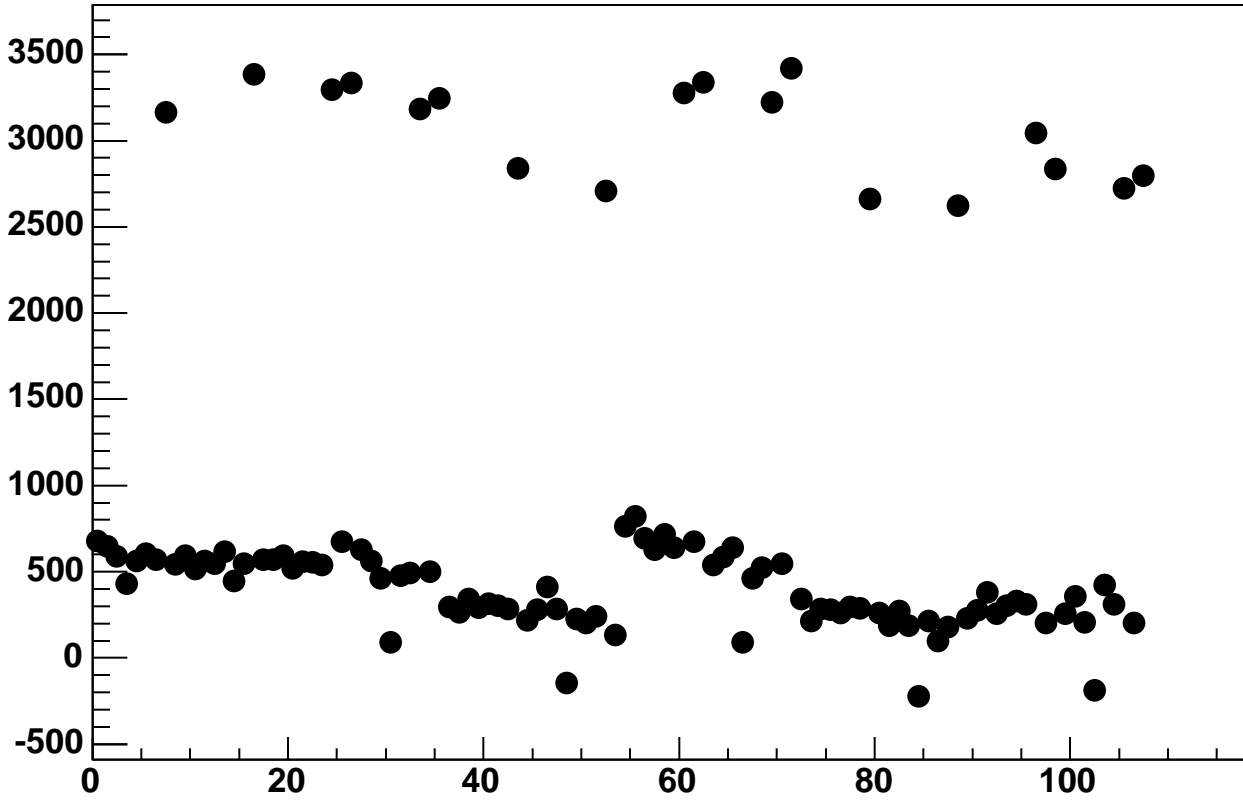
Enable 5, DAC=4000, Hold=105, ADC Mean vs 18\*Chip+Chan



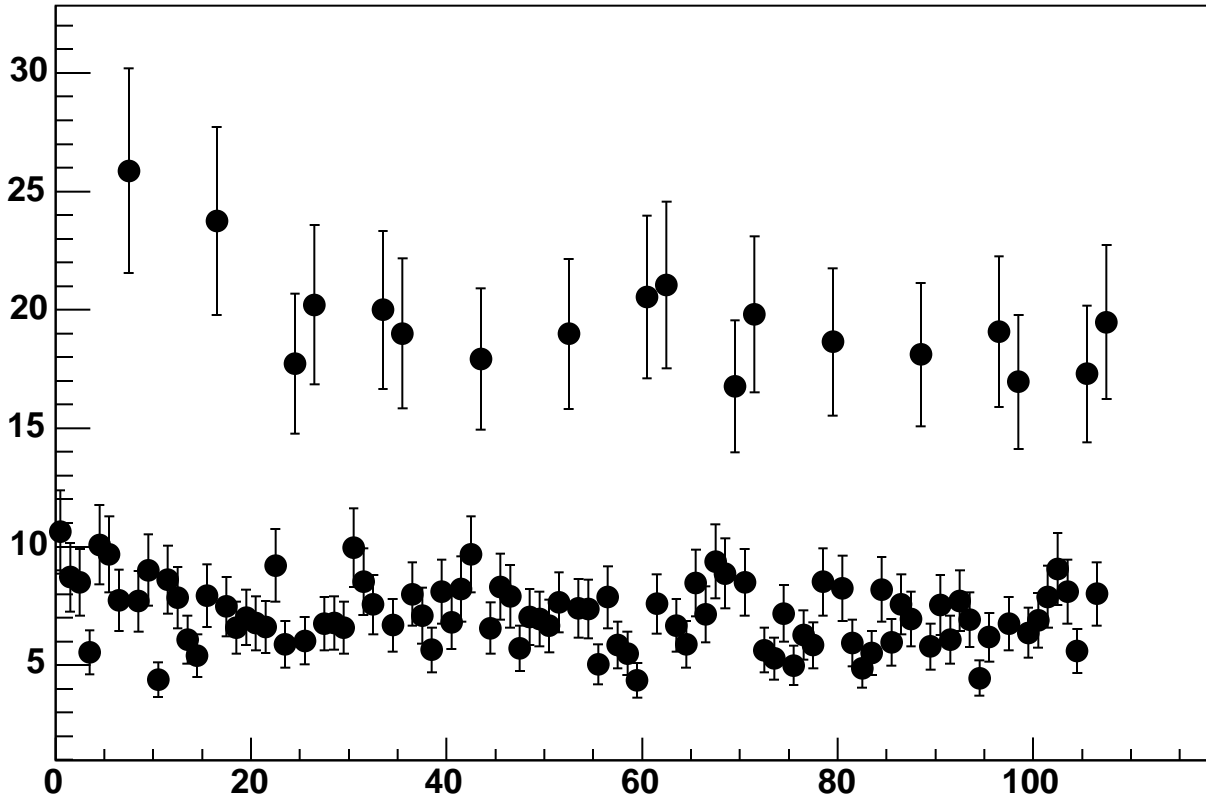
Enable 5, DAC=4000, Hold=105, ADC Noise vs 18\*Chip+Chan



Enable 5, DAC=4000, Hold=110, ADC Mean vs 18\*Chip+Chan

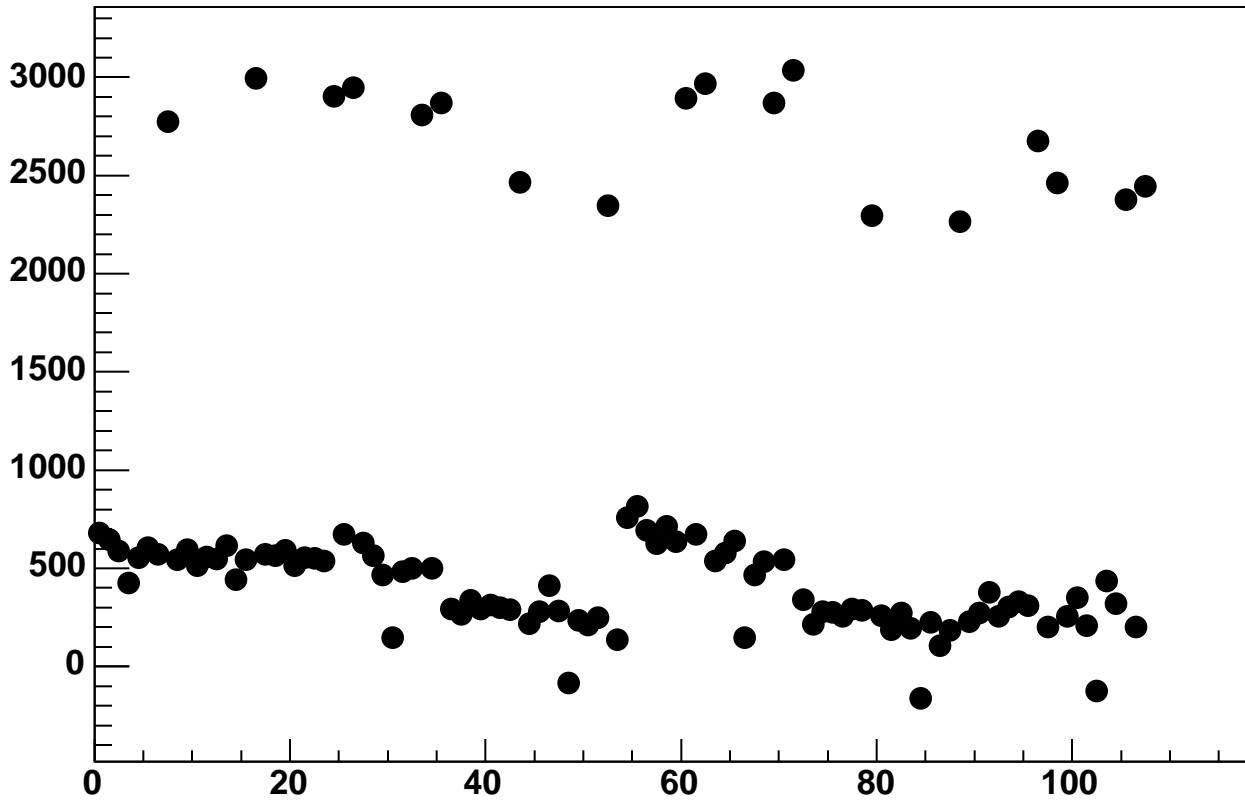


Enable 5, DAC=4000, Hold=110, ADC Noise vs 18\*Chip+Chan

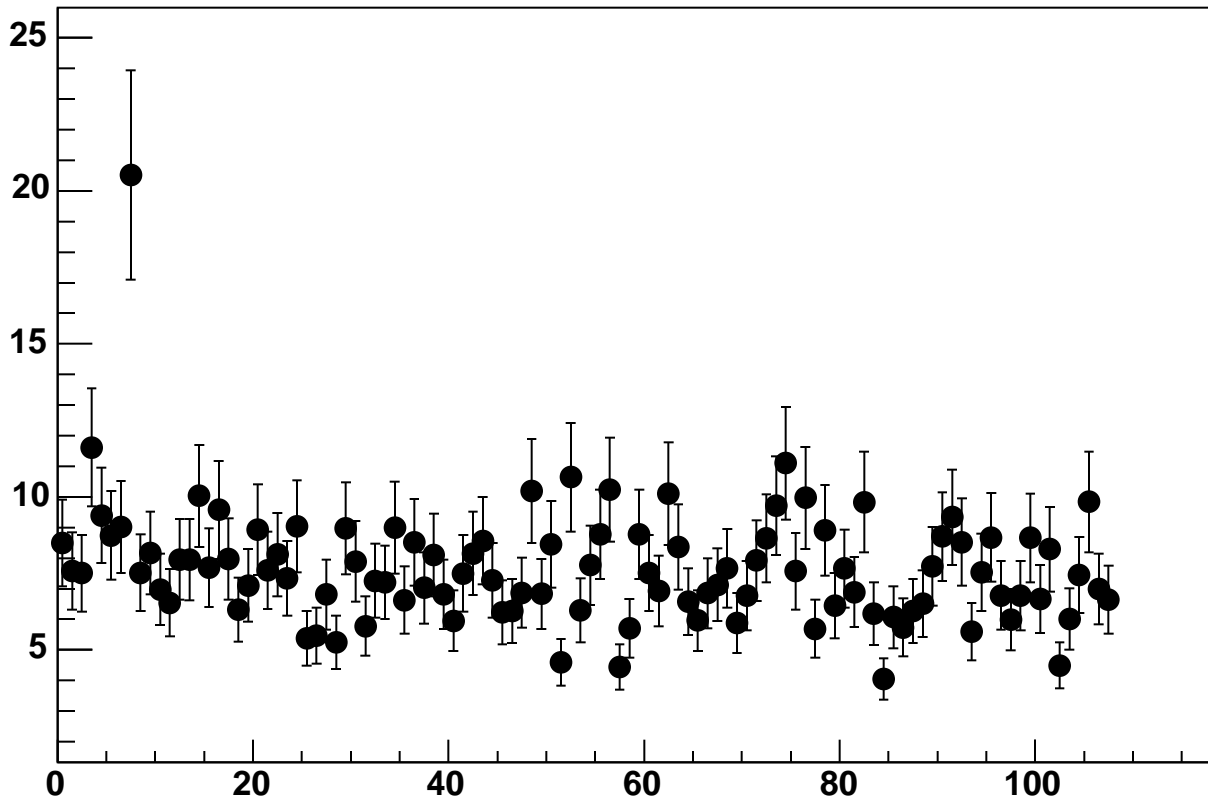




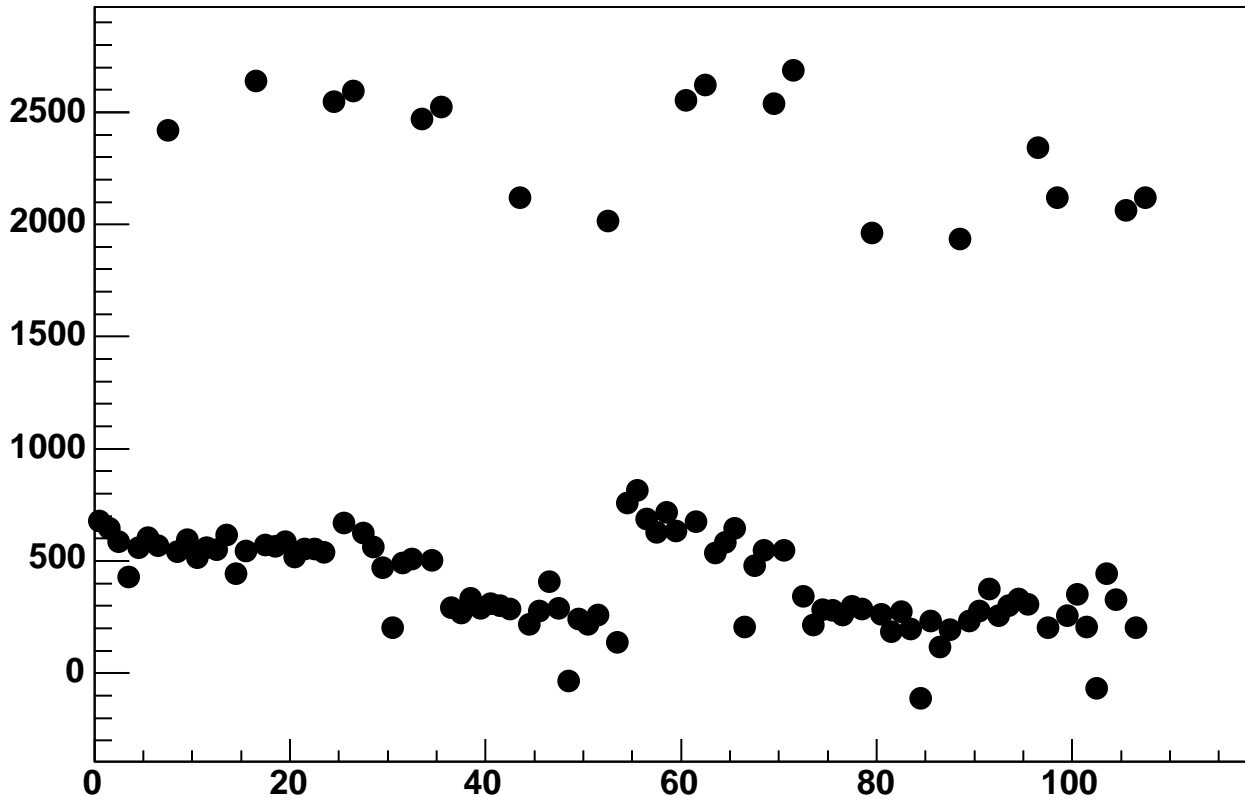
Enable 5, DAC=4000, Hold=115, ADC Mean vs 18\*Chip+Chan



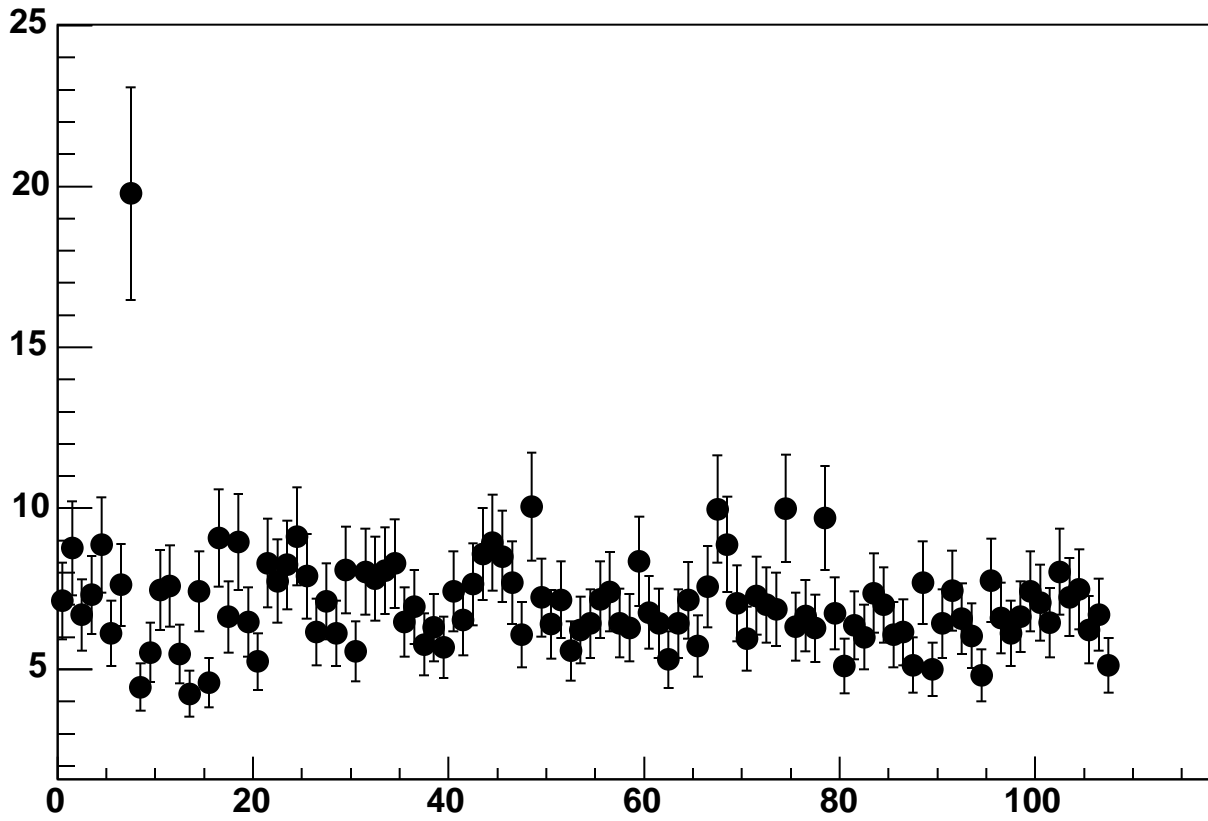
Enable 5, DAC=4000, Hold=115, ADC Noise vs 18\*Chip+Chan



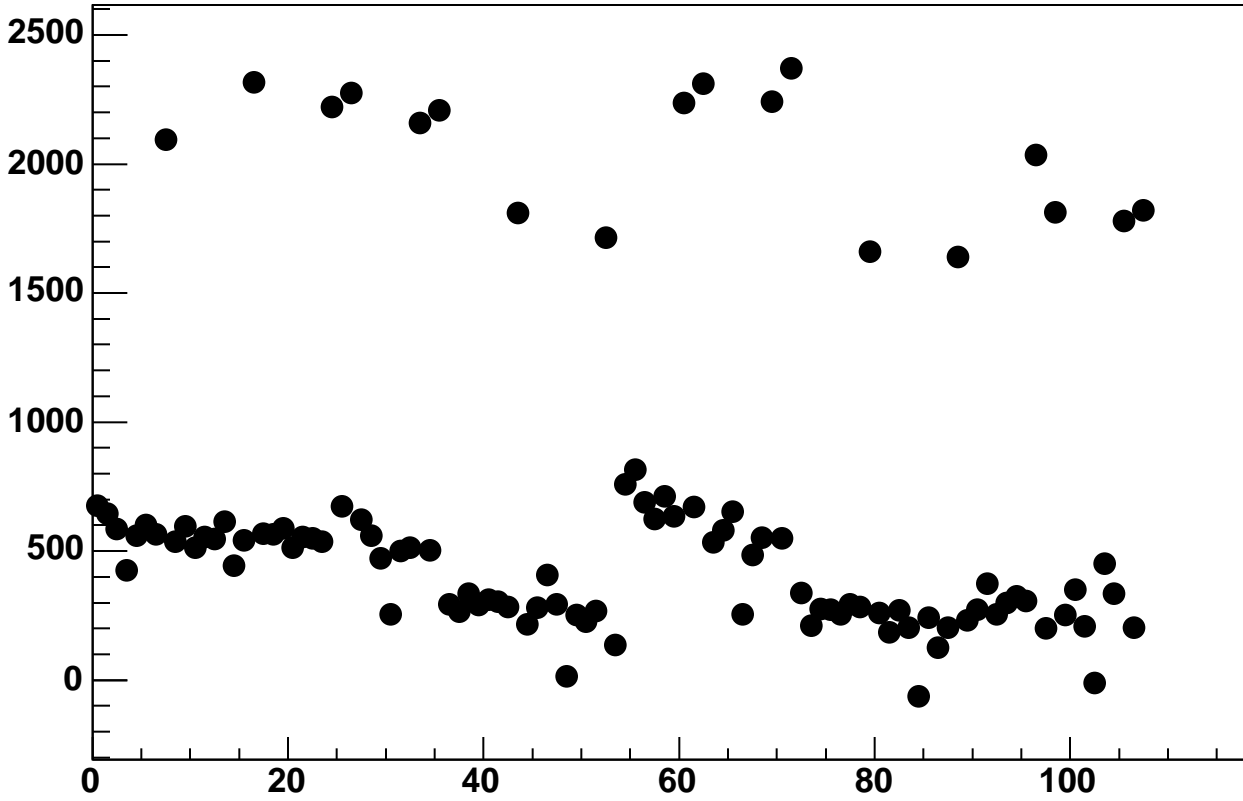
Enable 5, DAC=4000, Hold=120, ADC Mean vs 18\*Chip+Chan



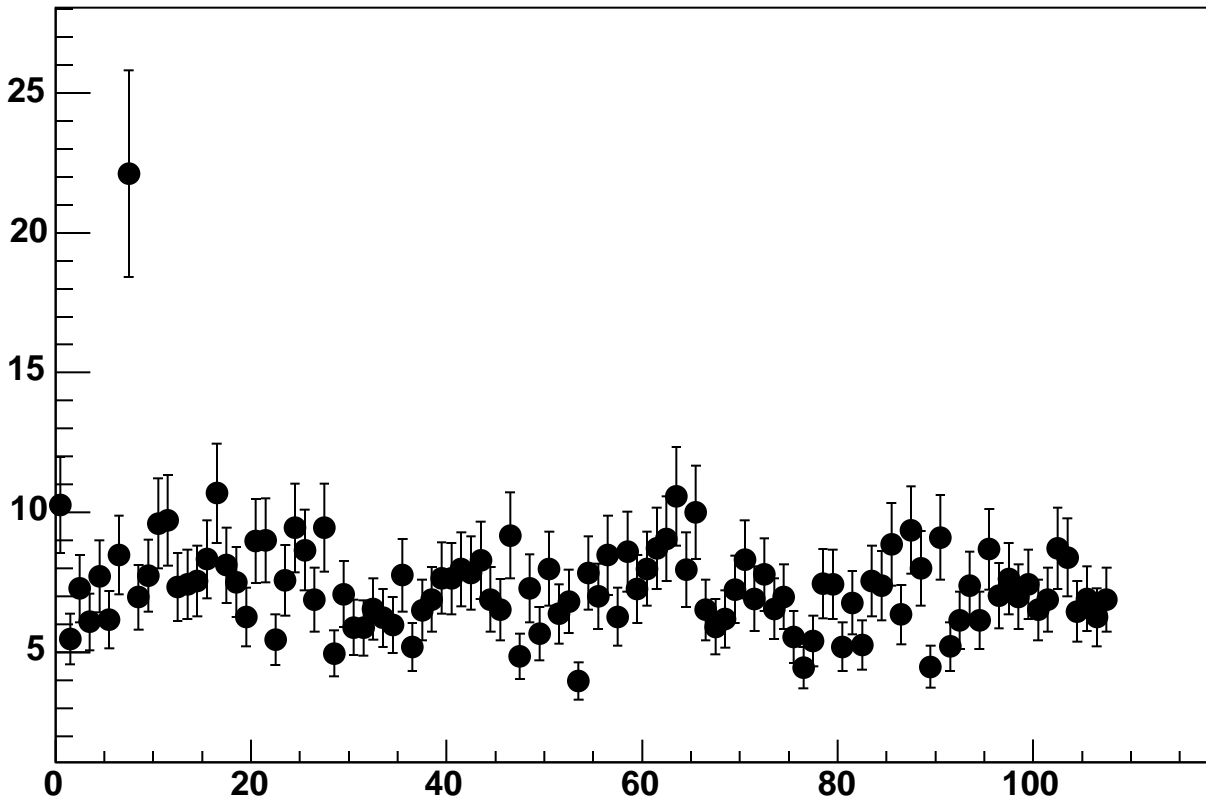
Enable 5, DAC=4000, Hold=120, ADC Noise vs 18\*Chip+Chan



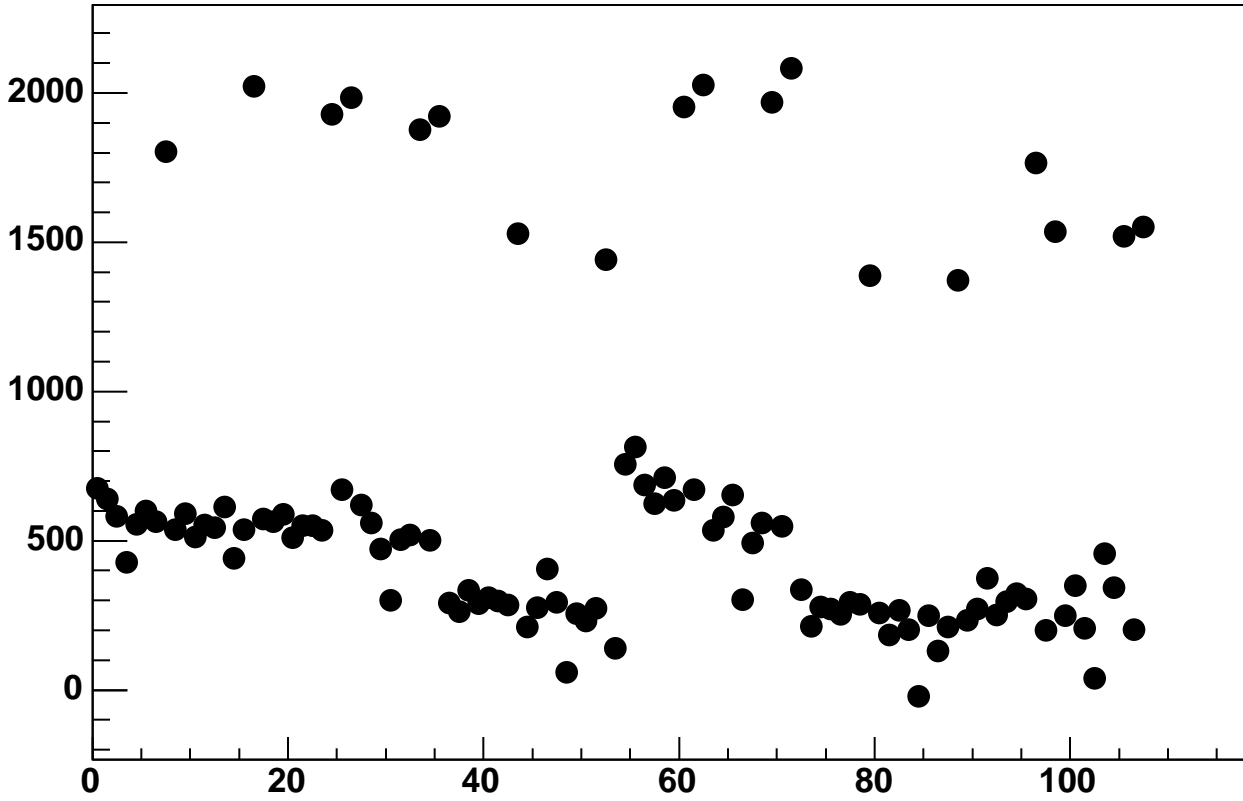
Enable 5, DAC=4000, Hold=125, ADC Mean vs 18\*Chip+Chan



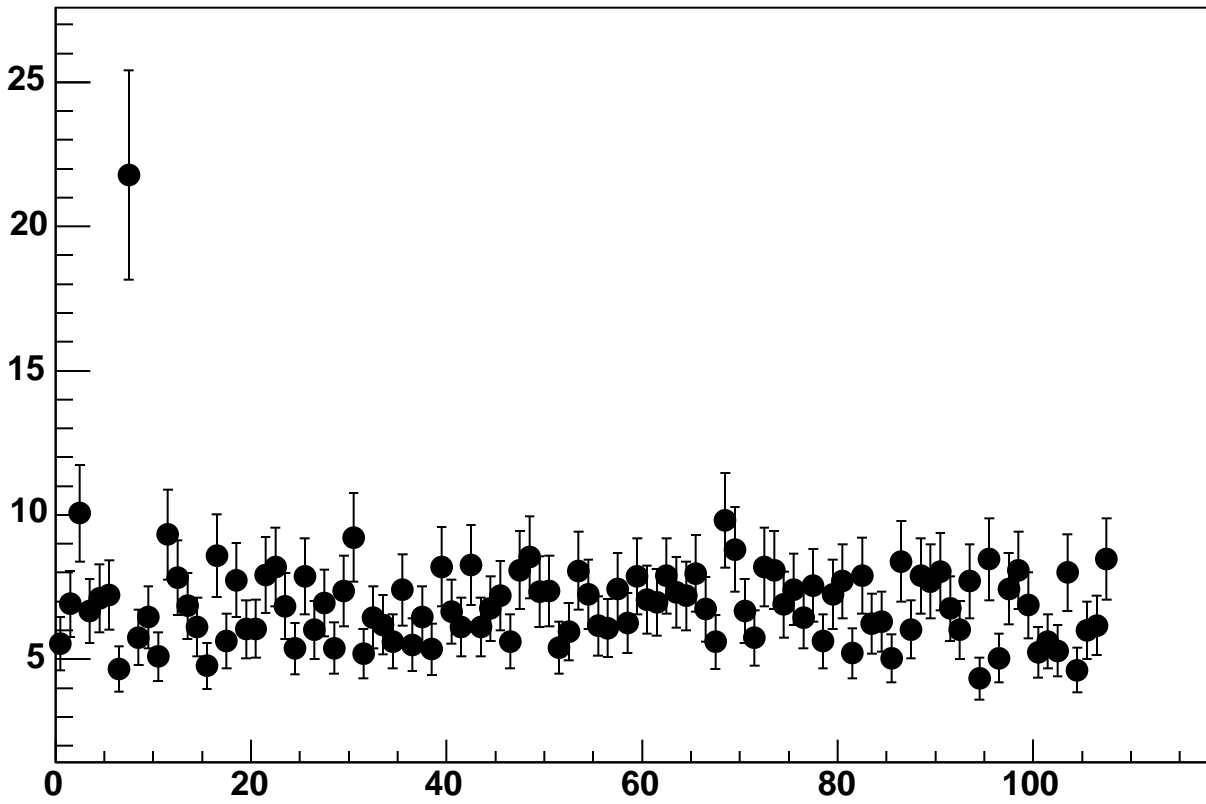
Enable 5, DAC=4000, Hold=125, ADC Noise vs 18\*Chip+Chan



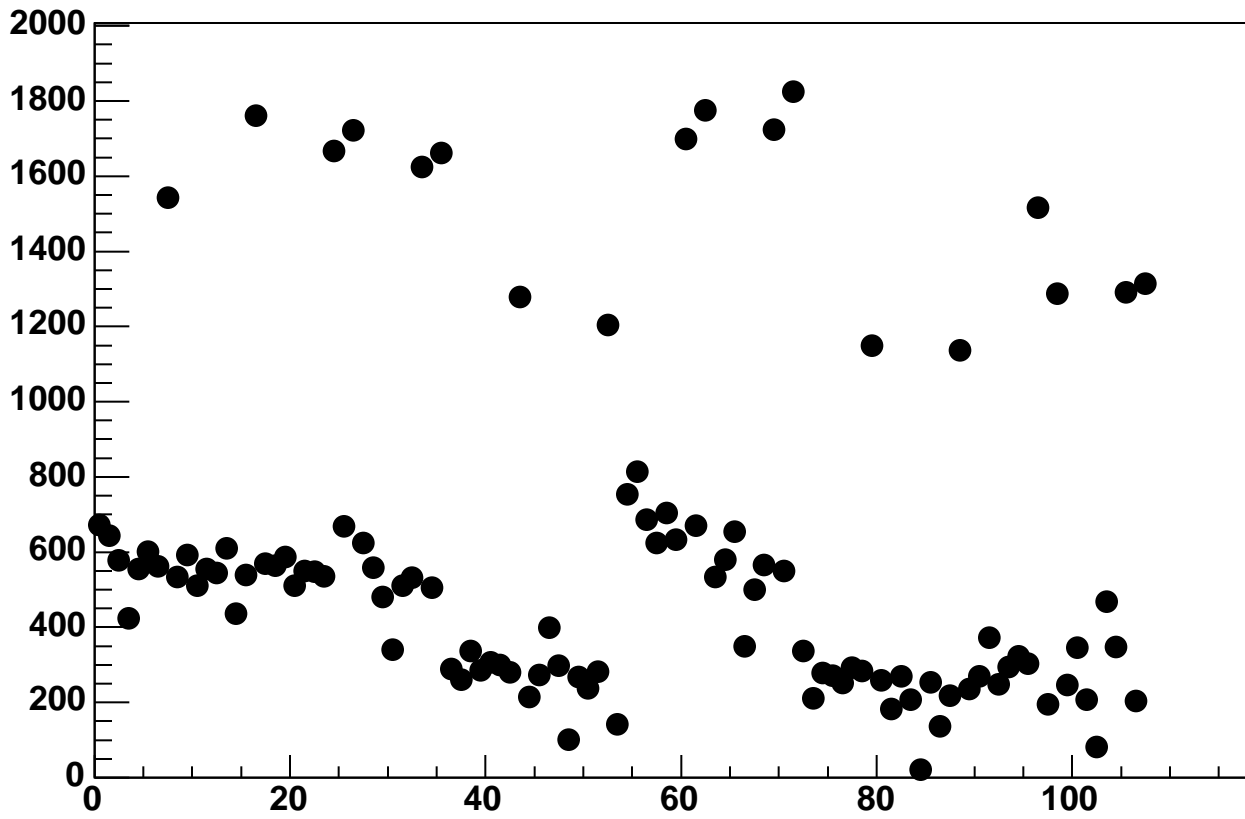
Enable 5, DAC=4000, Hold=130, ADC Mean vs 18\*Chip+Chan



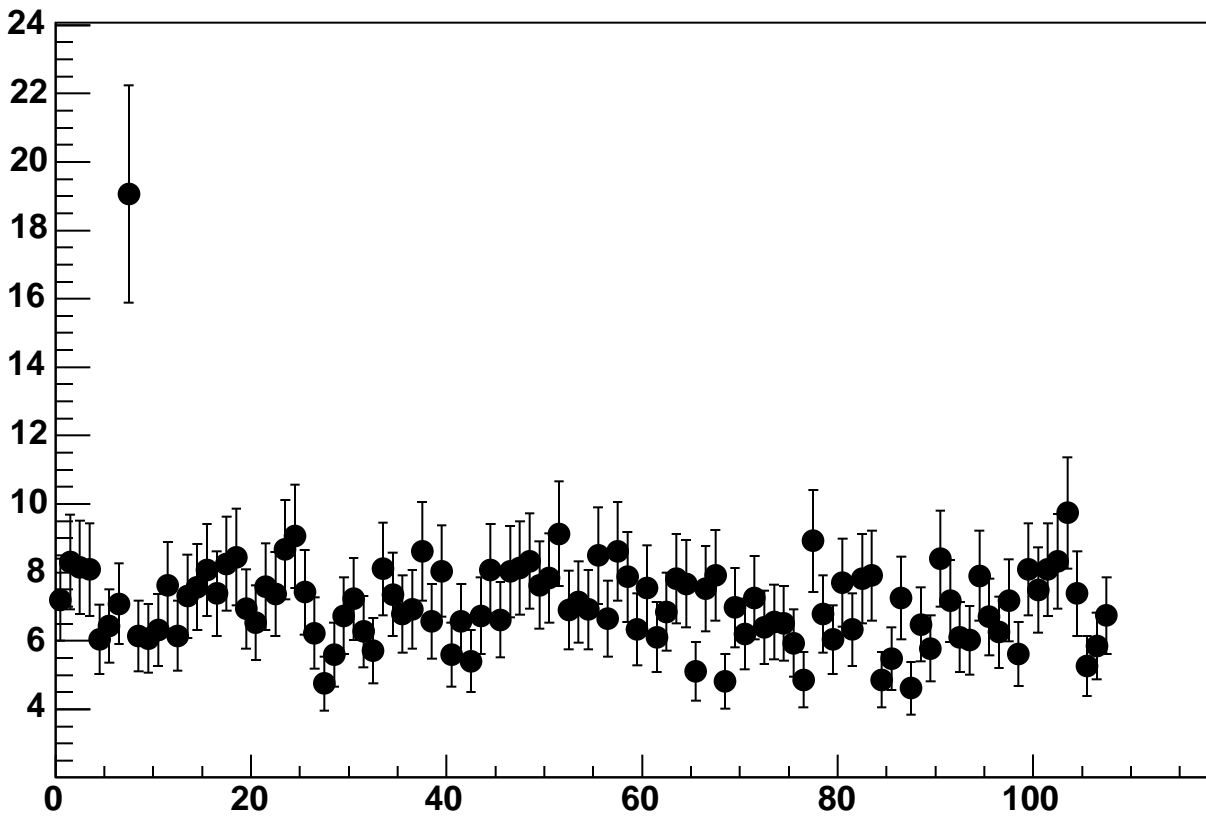
Enable 5, DAC=4000, Hold=130, ADC Noise vs 18\*Chip+Chan



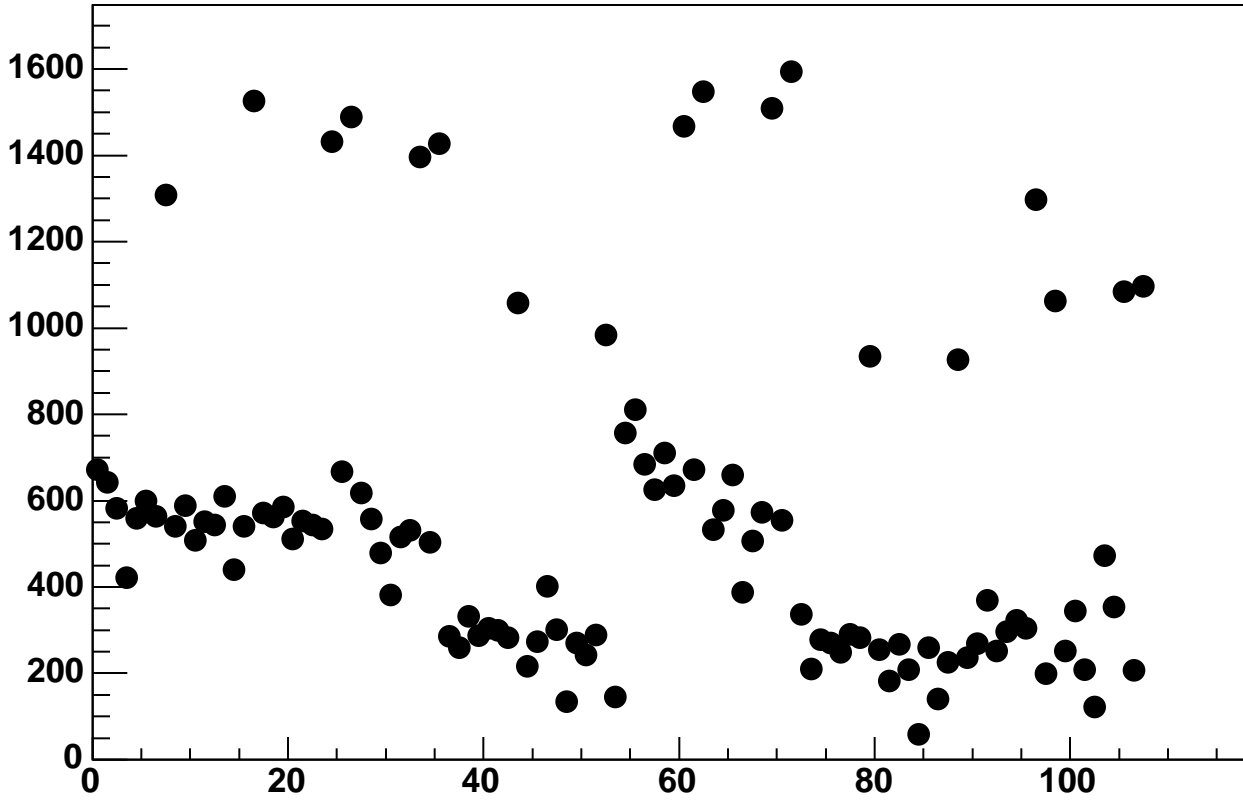
Enable 5, DAC=4000, Hold=135, ADC Mean vs 18\*Chip+Chan



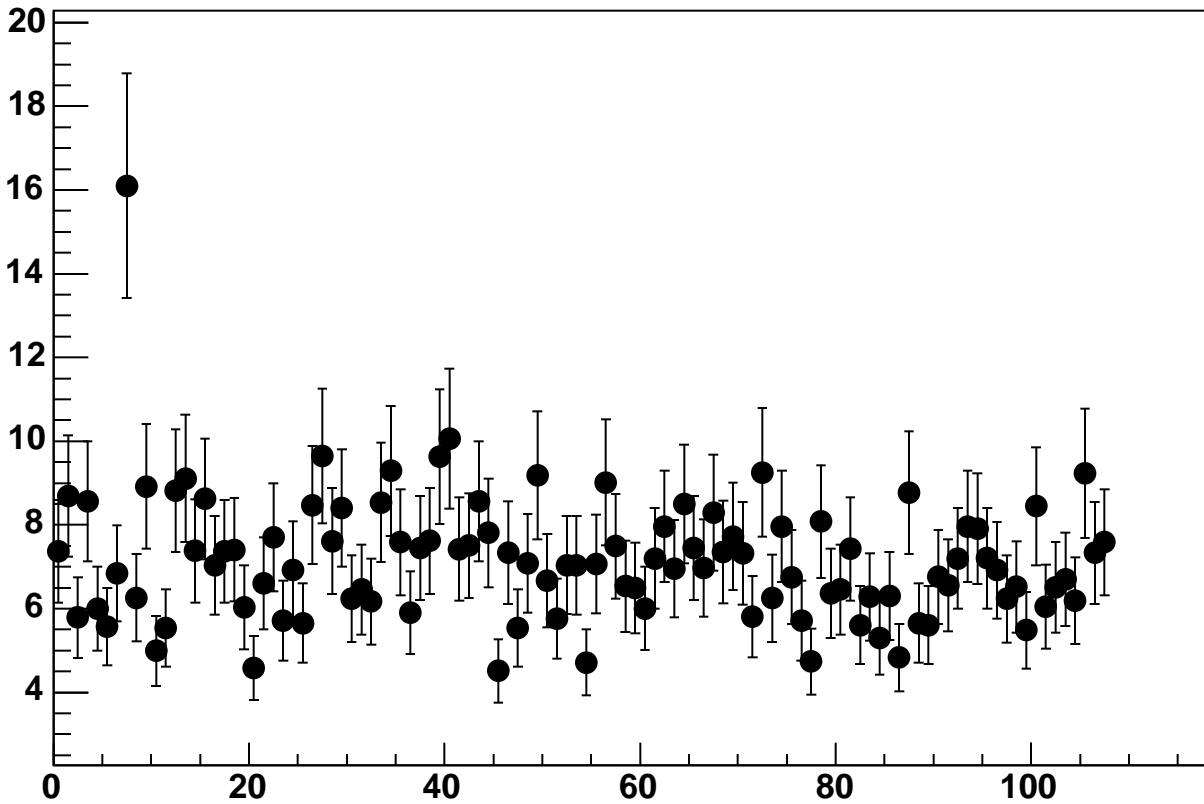
Enable 5, DAC=4000, Hold=135, ADC Noise vs 18\*Chip+Chan



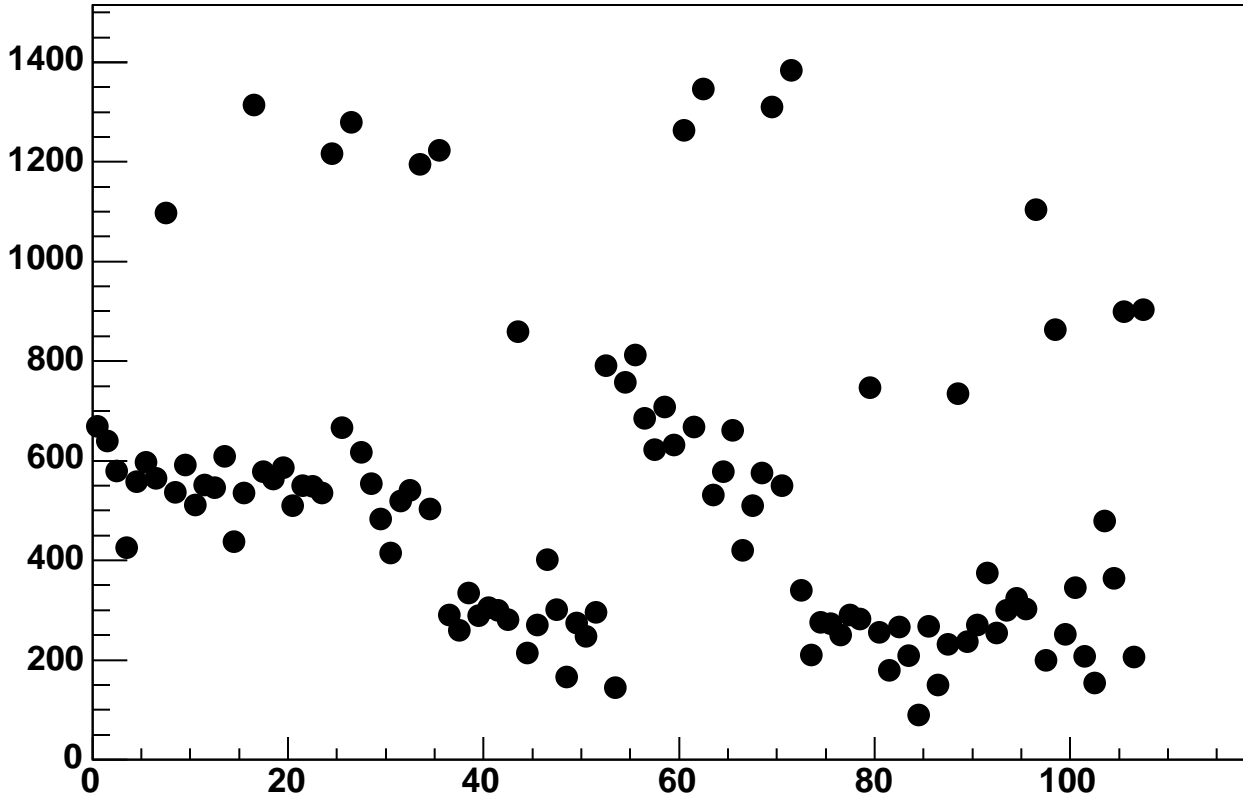
Enable 5, DAC=4000, Hold=140, ADC Mean vs 18\*Chip+Chan



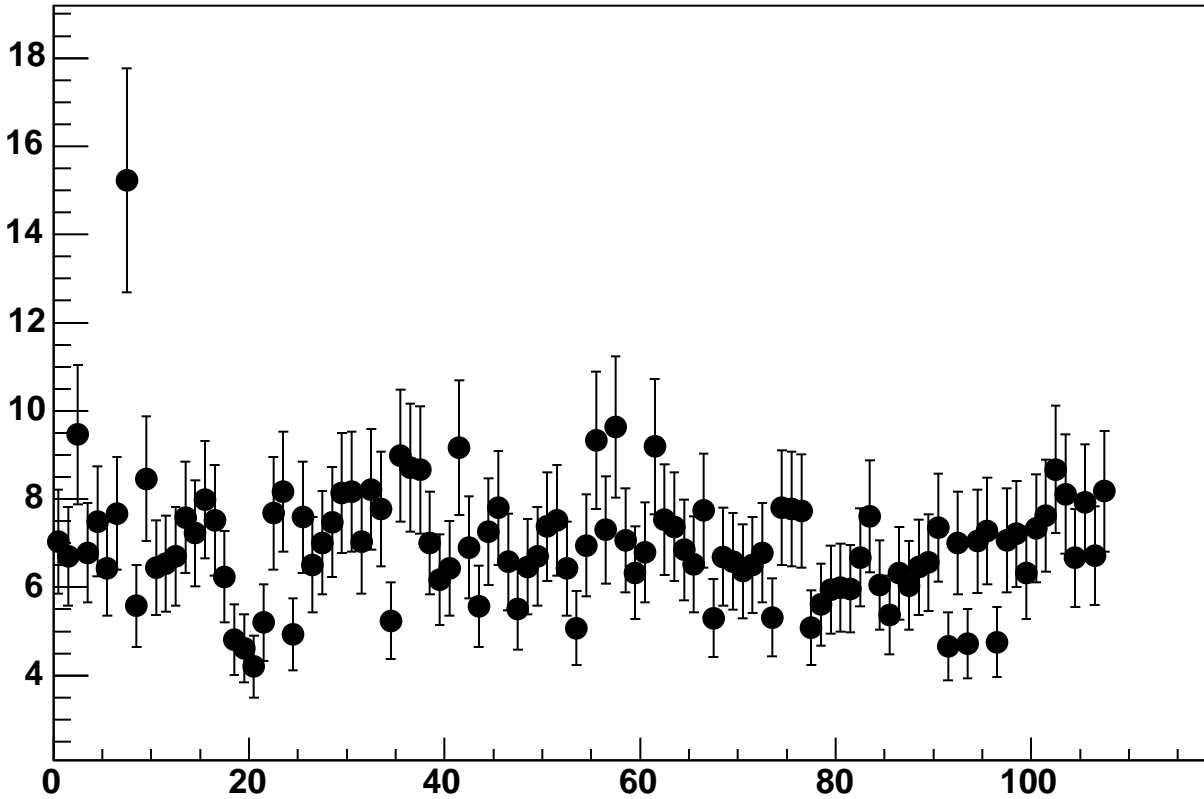
Enable 5, DAC=4000, Hold=140, ADC Noise vs 18\*Chip+Chan



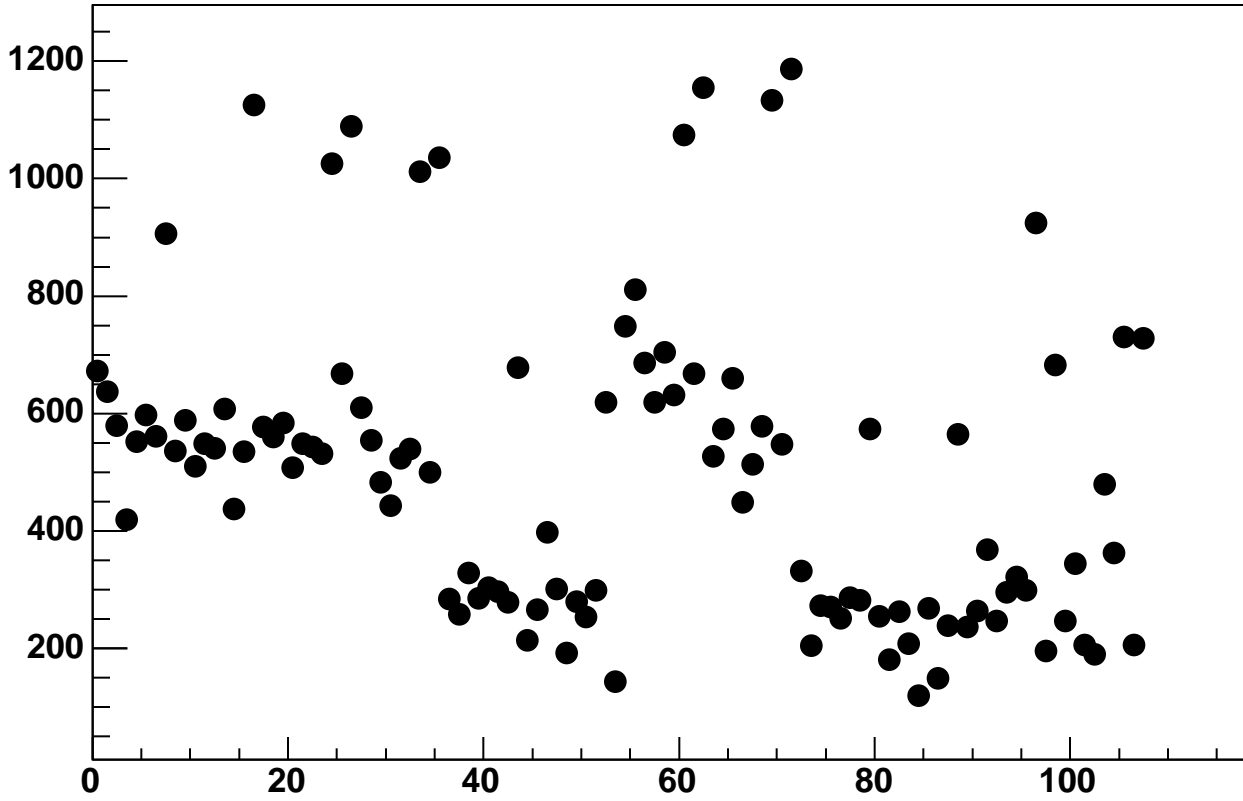
Enable 5, DAC=4000, Hold=145, ADC Mean vs 18\*Chip+Chan



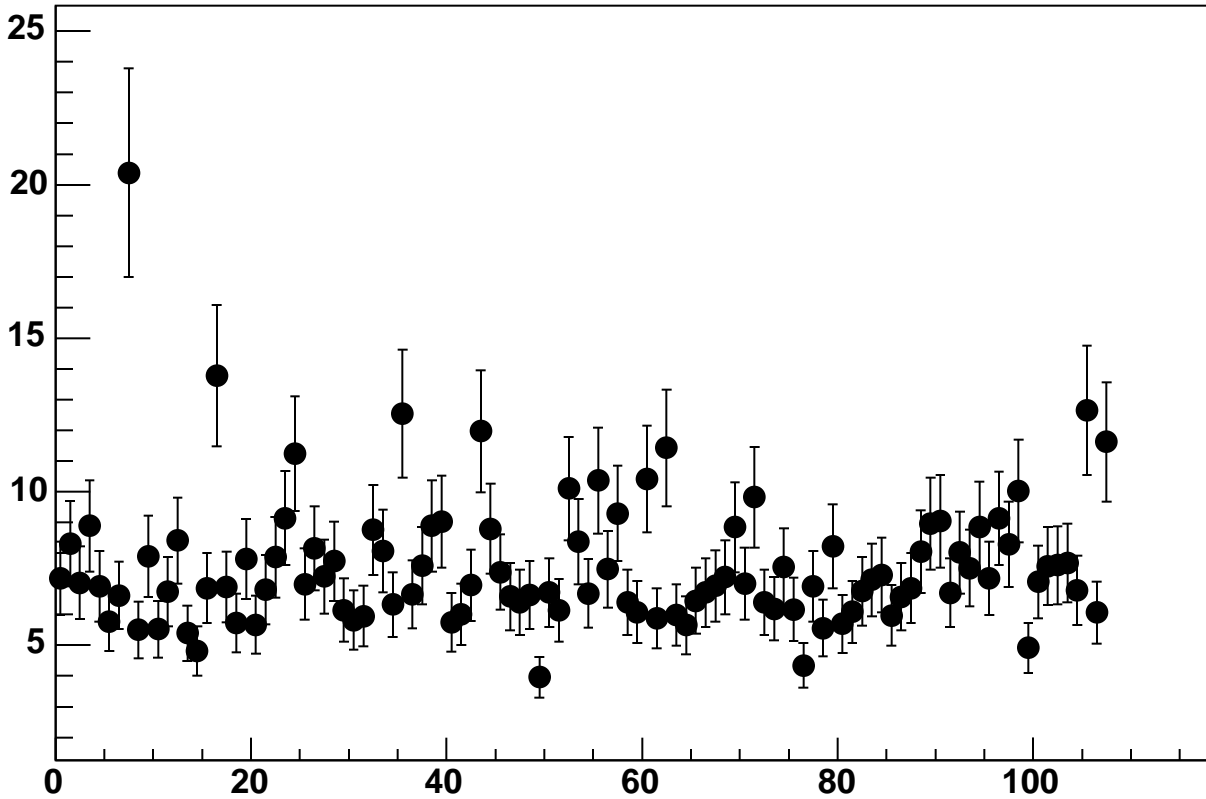
Enable 5, DAC=4000, Hold=145, ADC Noise vs 18\*Chip+Chan



Enable 5, DAC=4000, Hold=150, ADC Mean vs 18\*Chip+Chan

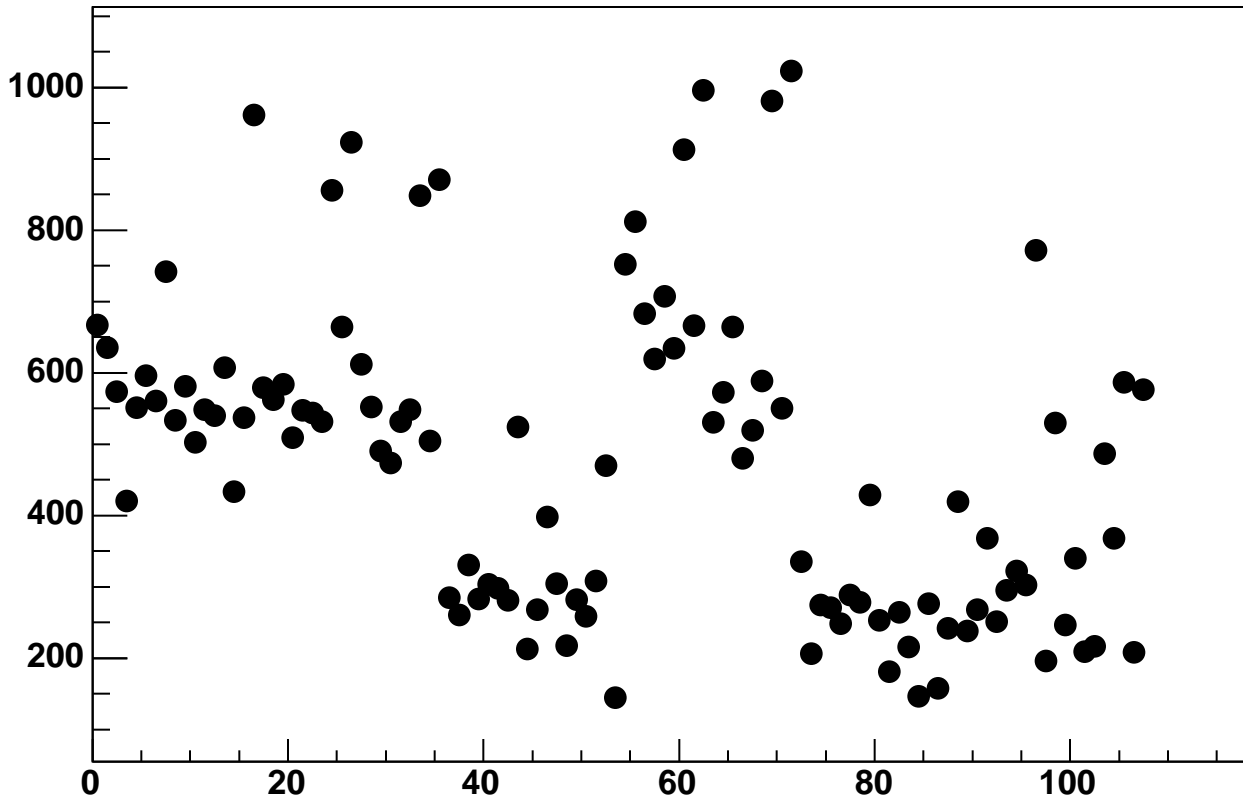


Enable 5, DAC=4000, Hold=150, ADC Noise vs 18\*Chip+Chan

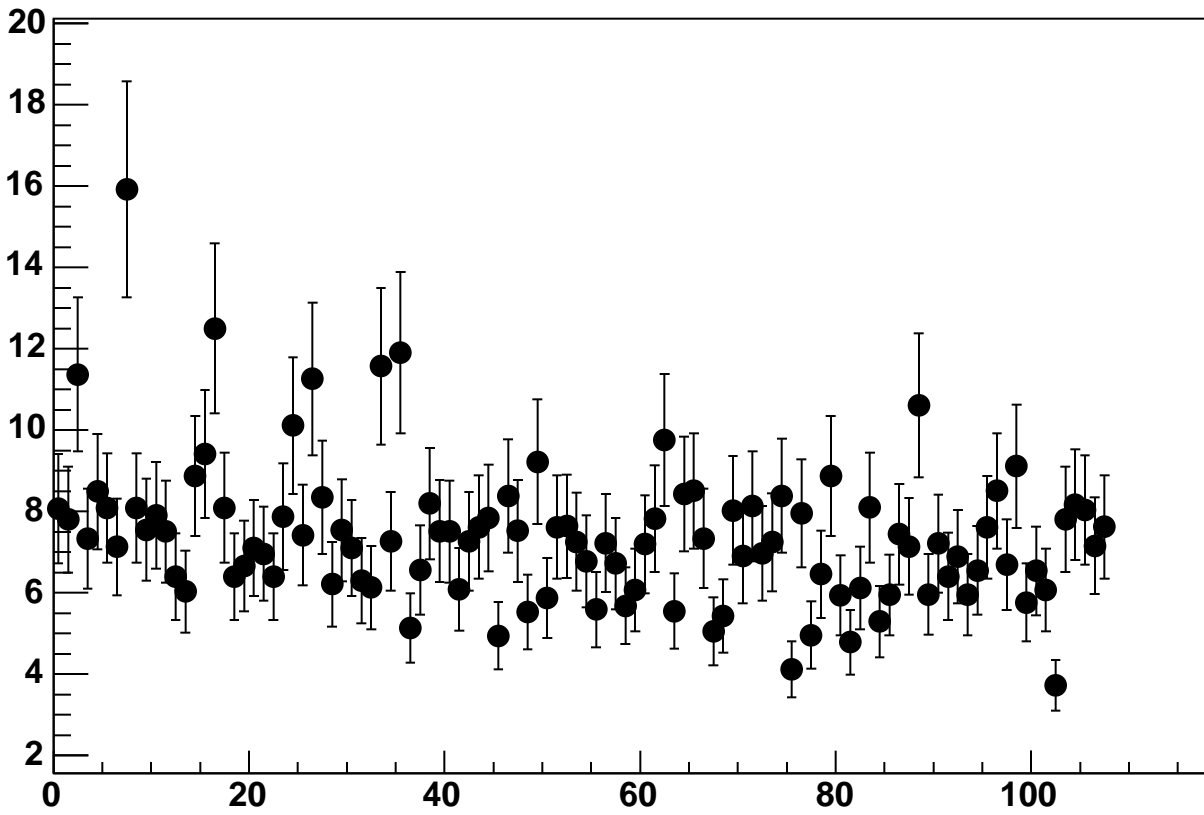




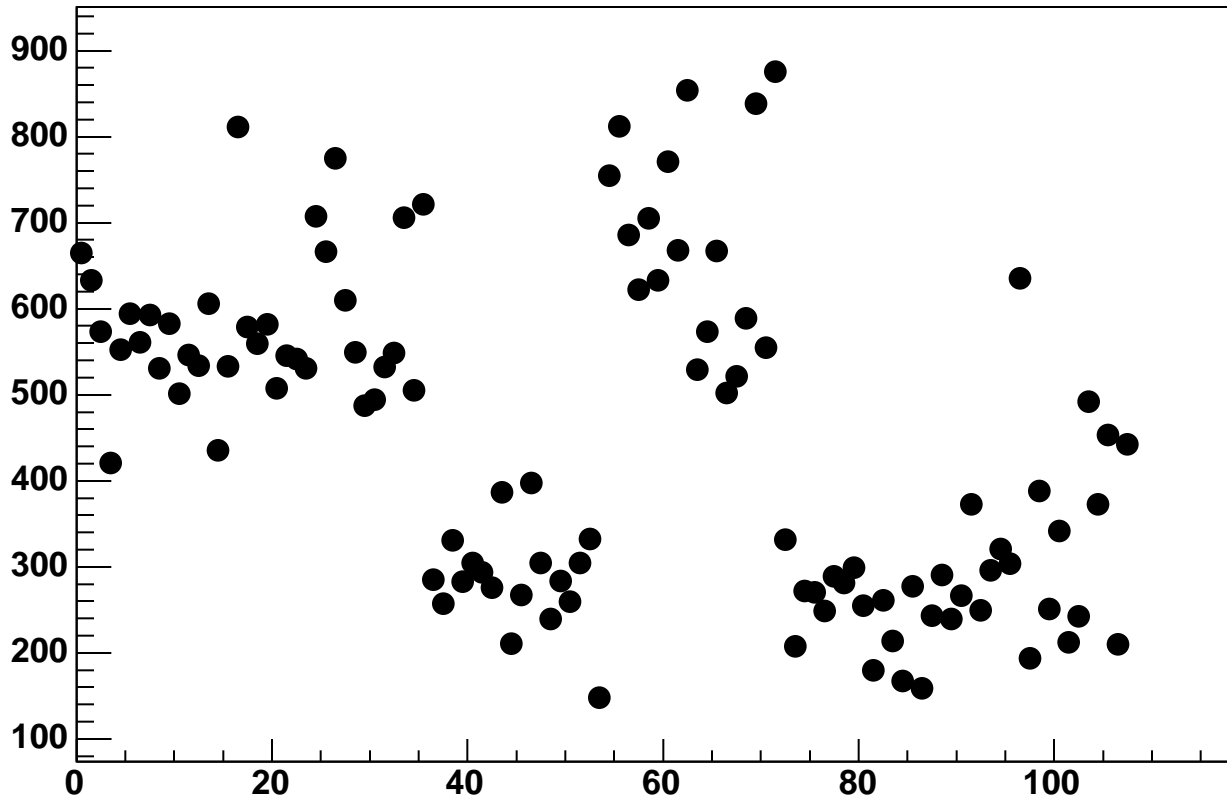
Enable 5, DAC=4000, Hold=155, ADC Mean vs 18\*Chip+Chan



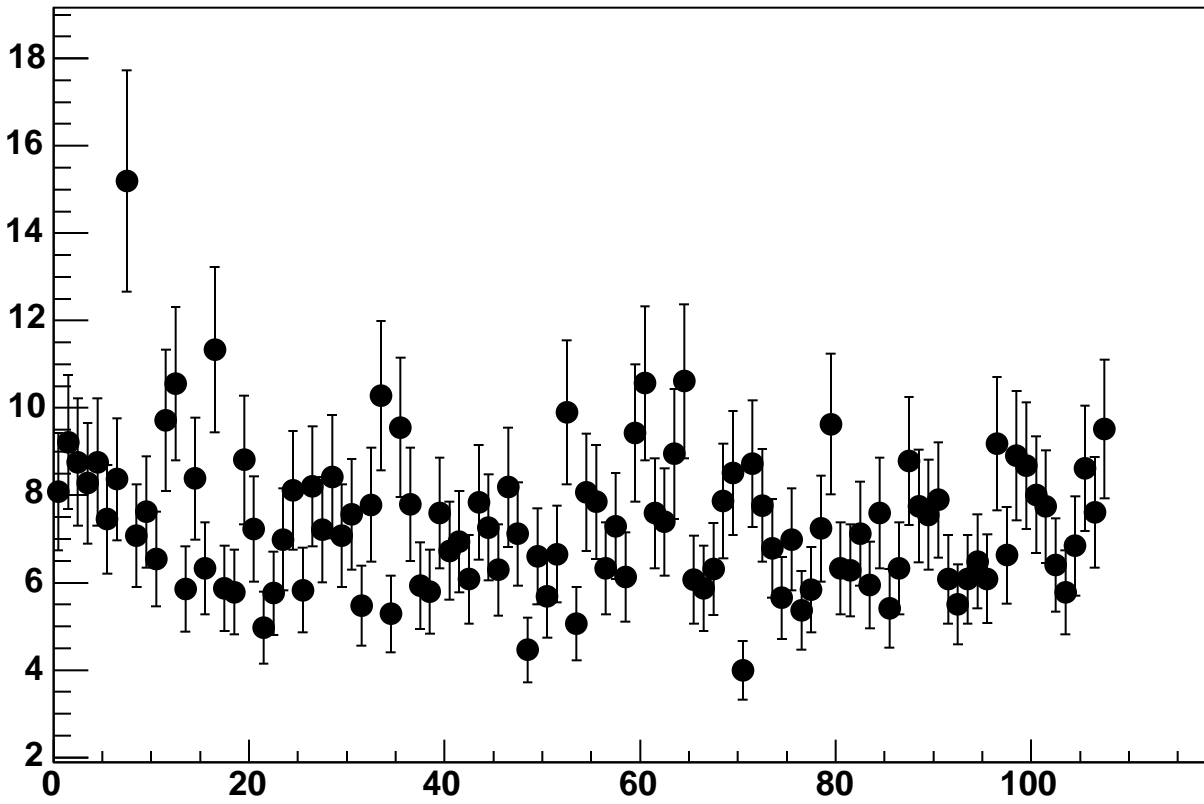
Enable 5, DAC=4000, Hold=155, ADC Noise vs 18\*Chip+Chan



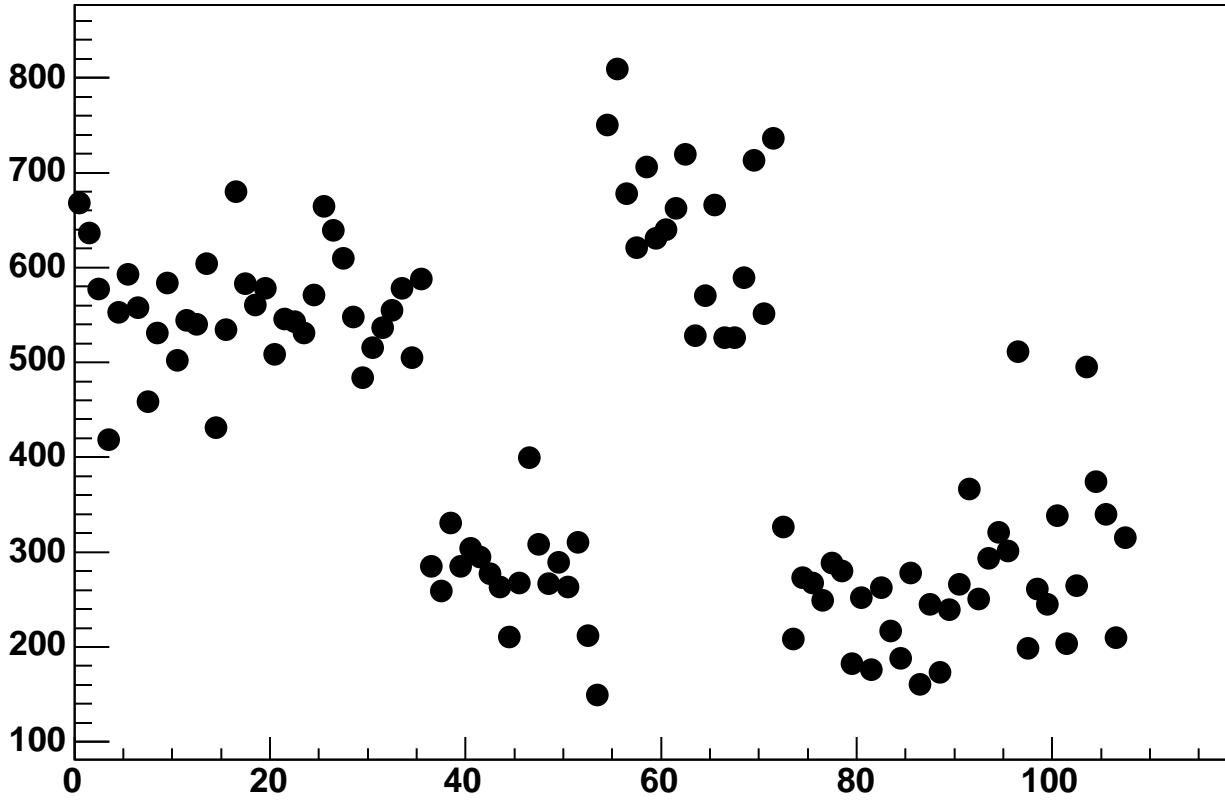
Enable 5, DAC=4000, Hold=160, ADC Mean vs 18\*Chip+Chan



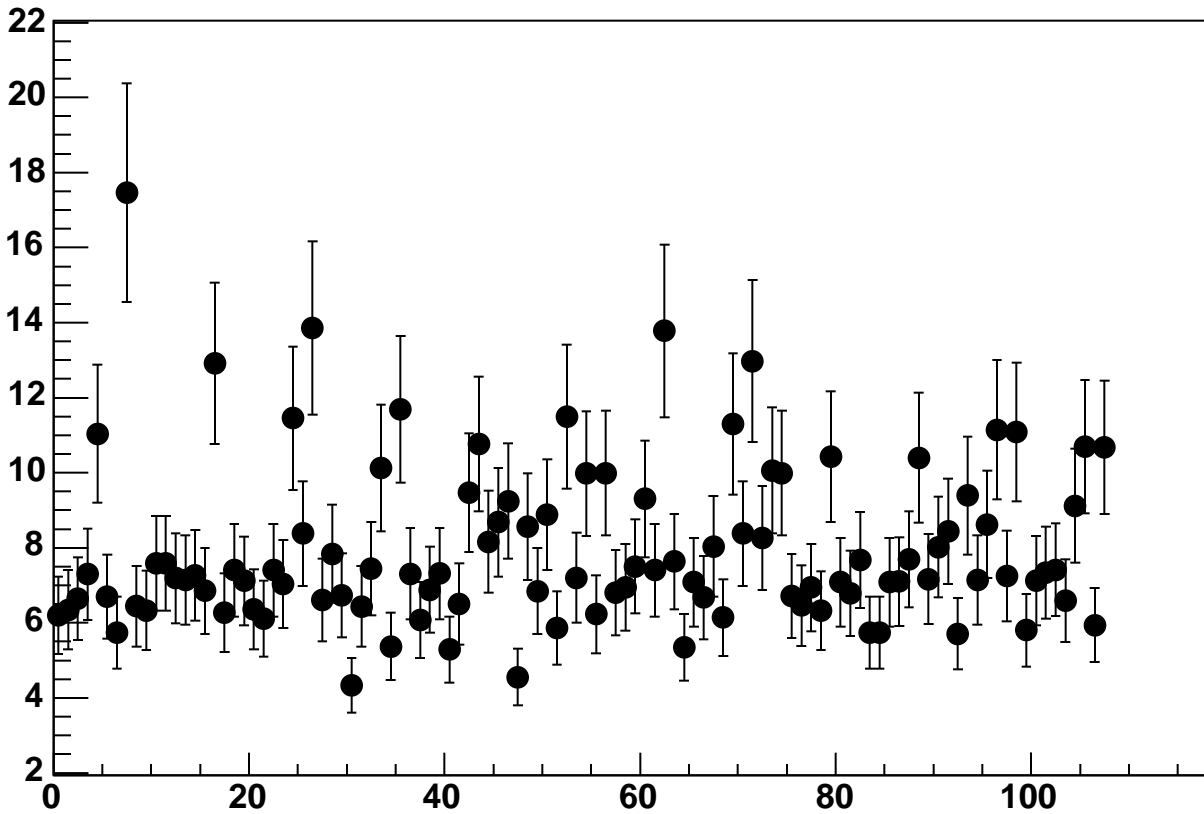
Enable 5, DAC=4000, Hold=160, ADC Noise vs 18\*Chip+Chan



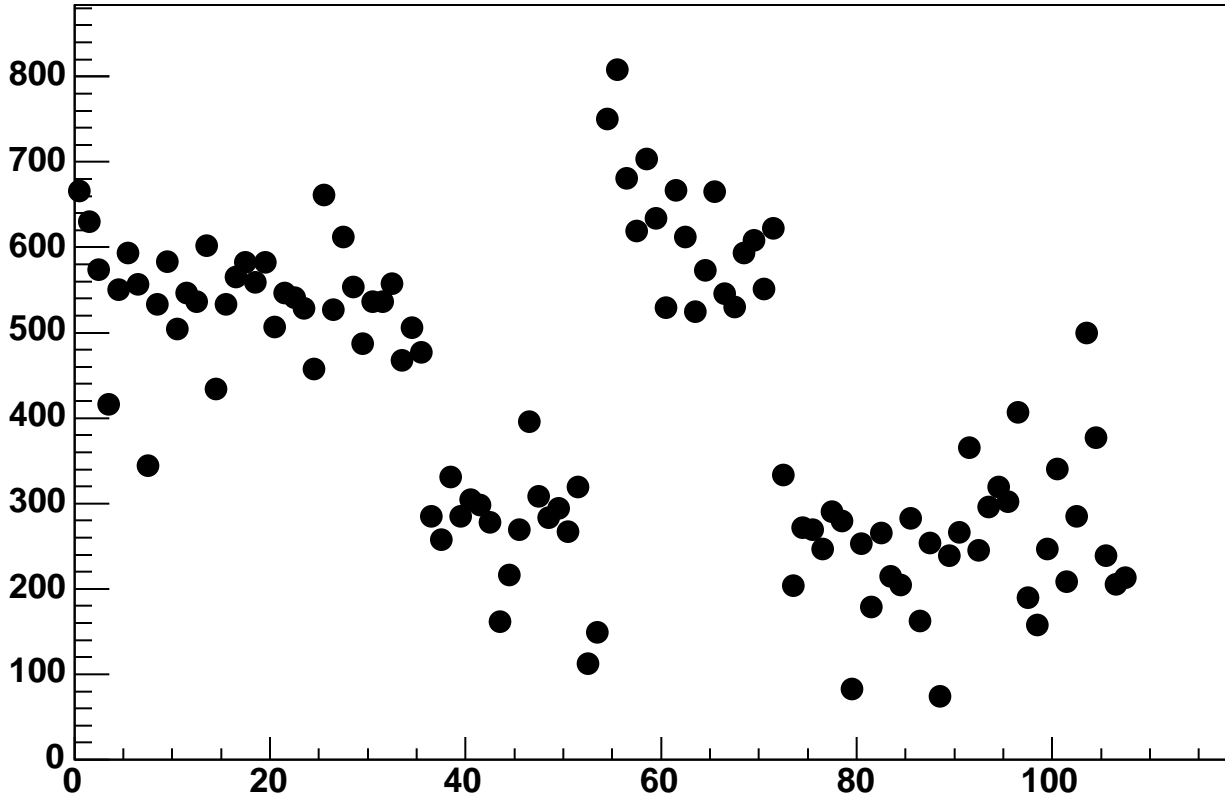
Enable 5, DAC=4000, Hold=165, ADC Mean vs 18\*Chip+Chan



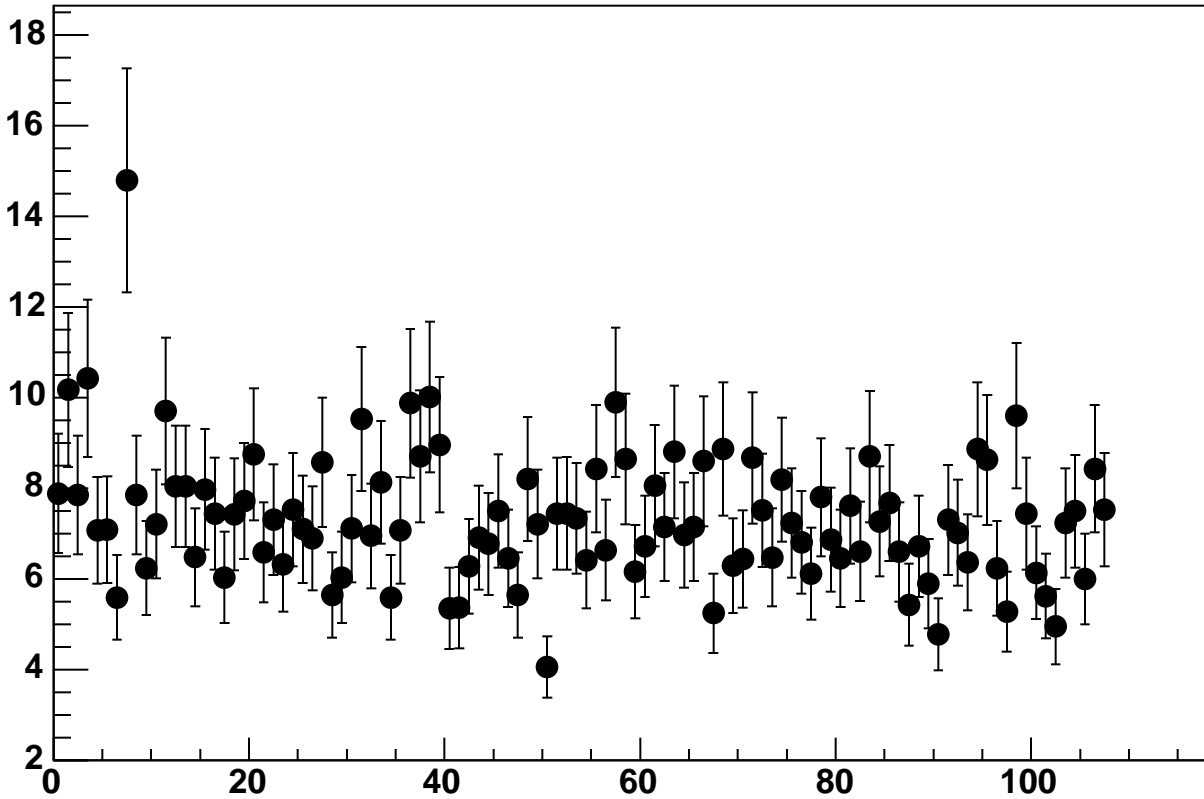
Enable 5, DAC=4000, Hold=165, ADC Noise vs 18\*Chip+Chan



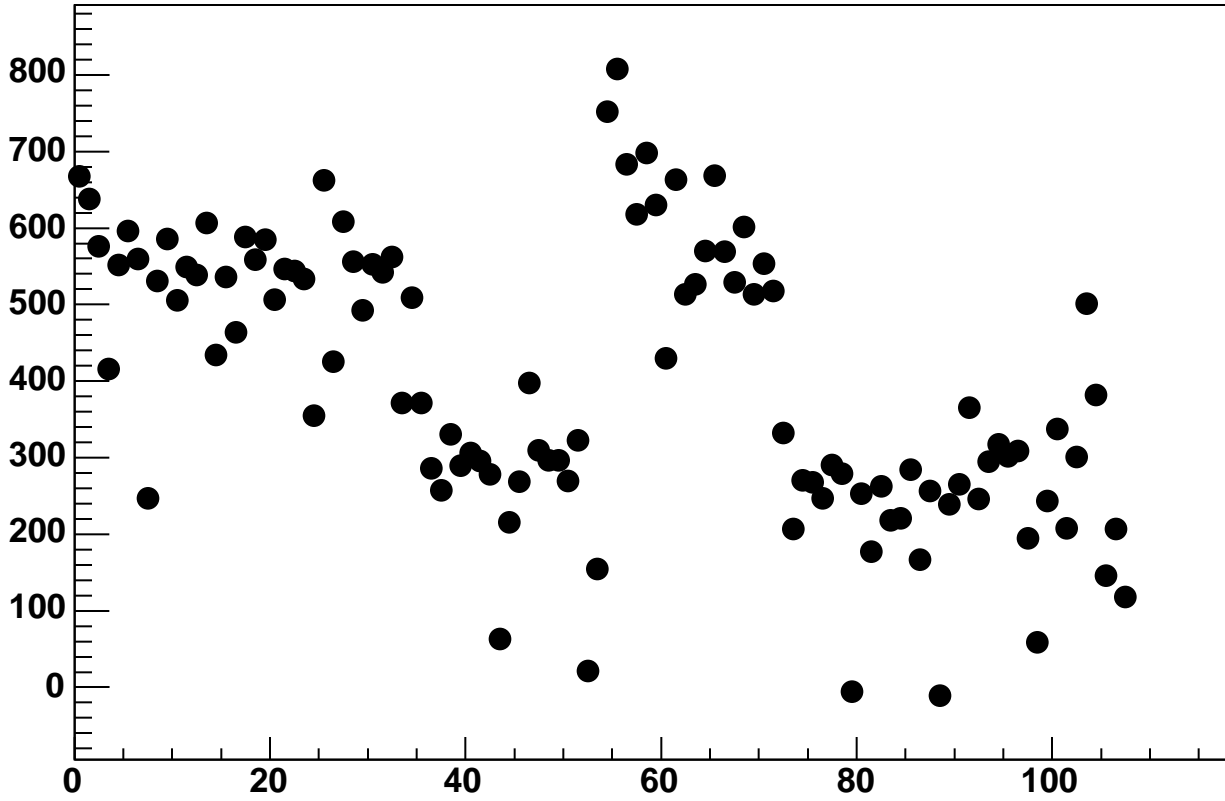
Enable 5, DAC=4000, Hold=170, ADC Mean vs 18\*Chip+Chan



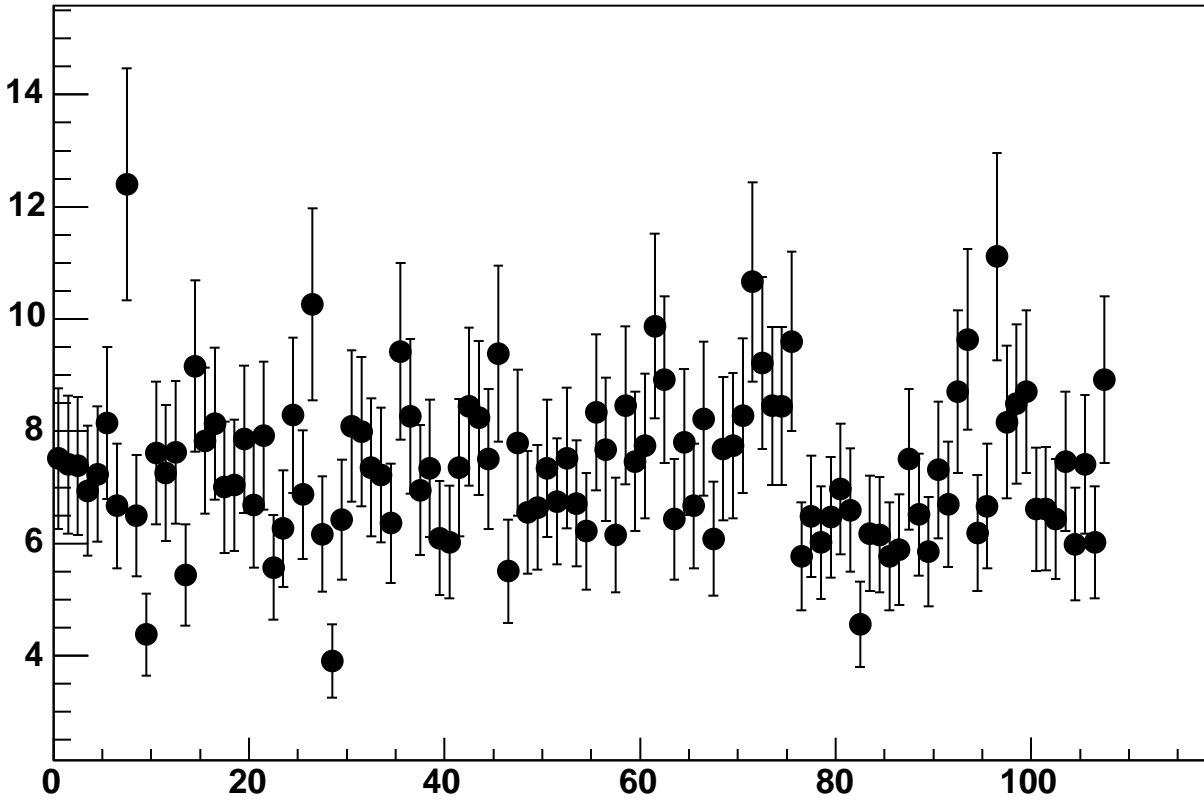
Enable 5, DAC=4000, Hold=170, ADC Noise vs 18\*Chip+Chan



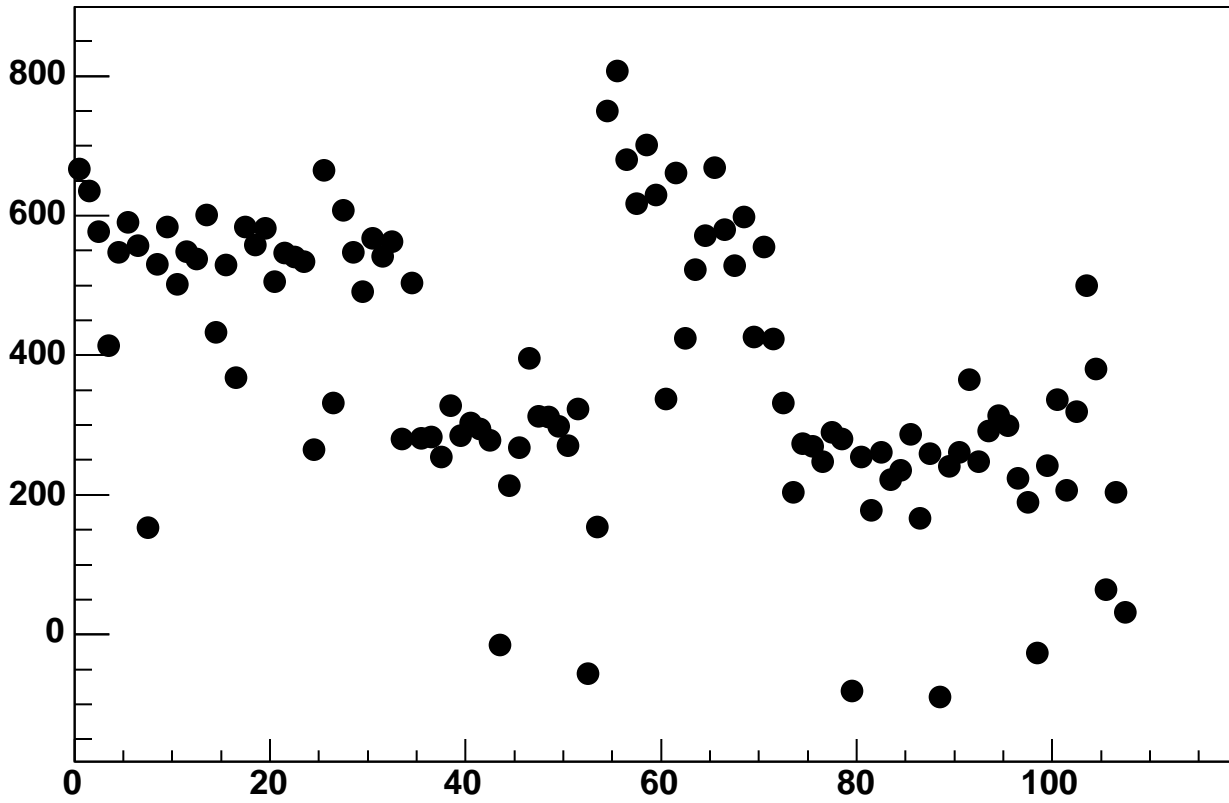
Enable 5, DAC=4000, Hold=175, ADC Mean vs 18\*Chip+Chan



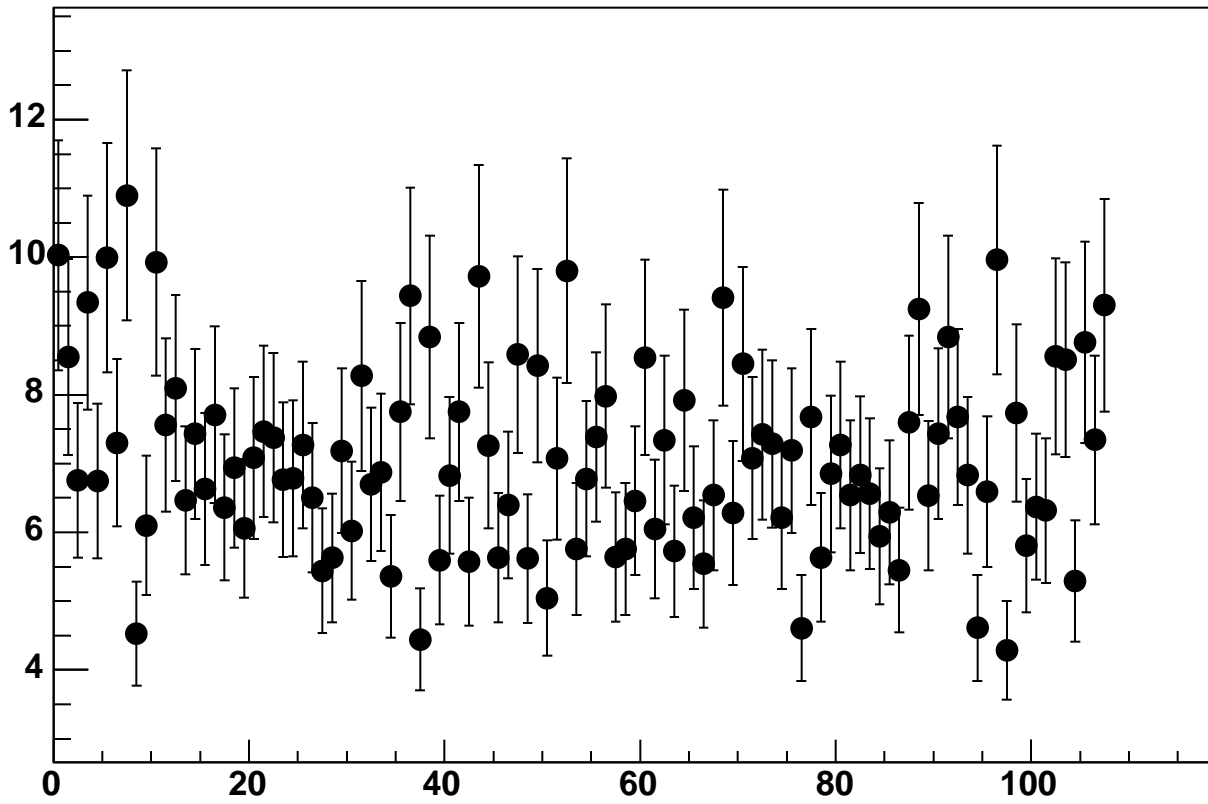
Enable 5, DAC=4000, Hold=175, ADC Noise vs 18\*Chip+Chan



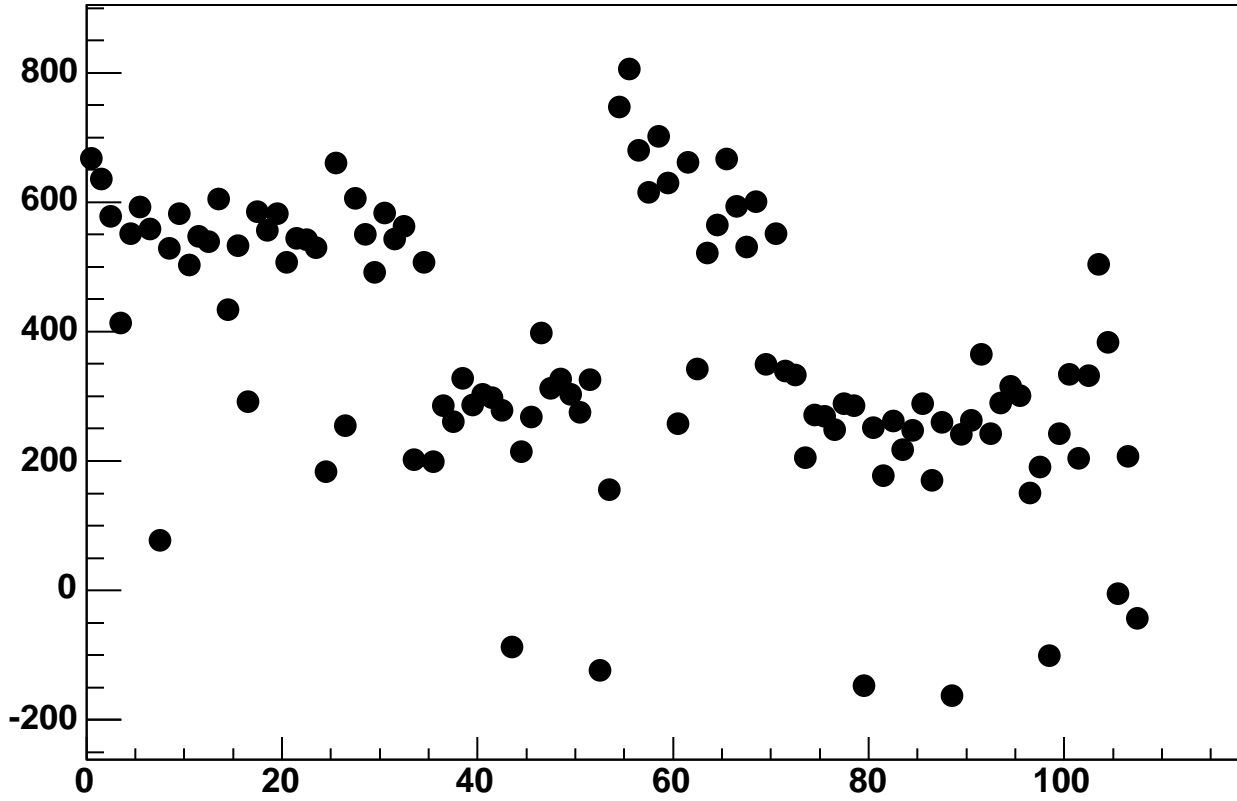
Enable 5, DAC=4000, Hold=180, ADC Mean vs 18\*Chip+Chan



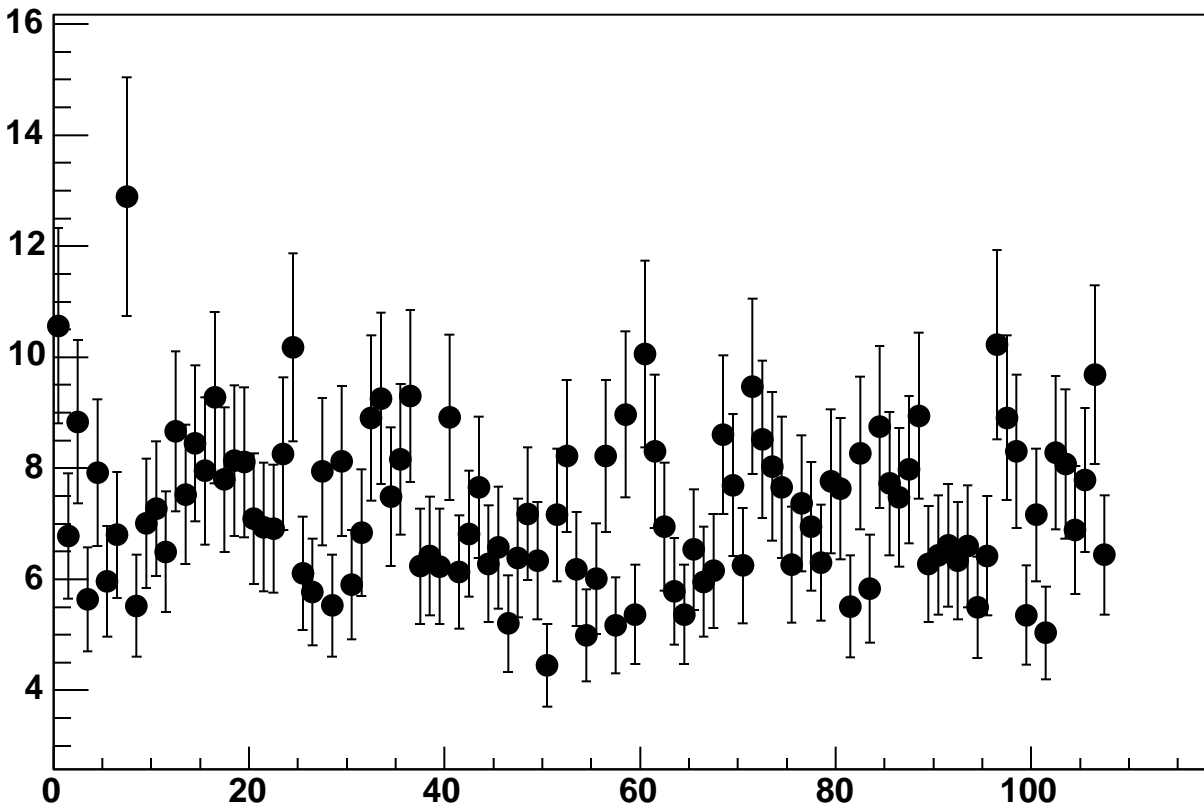
Enable 5, DAC=4000, Hold=180, ADC Noise vs 18\*Chip+Chan



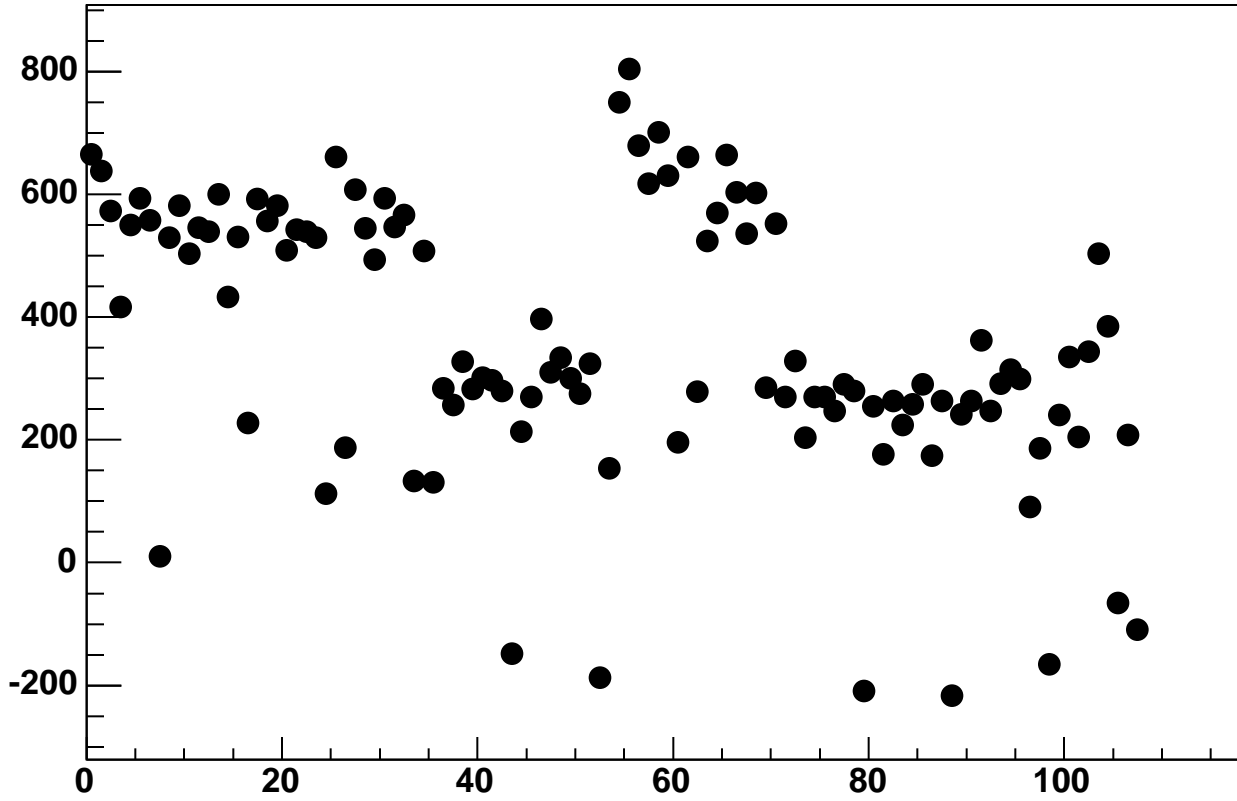
Enable 5, DAC=4000, Hold=185, ADC Mean vs 18\*Chip+Chan



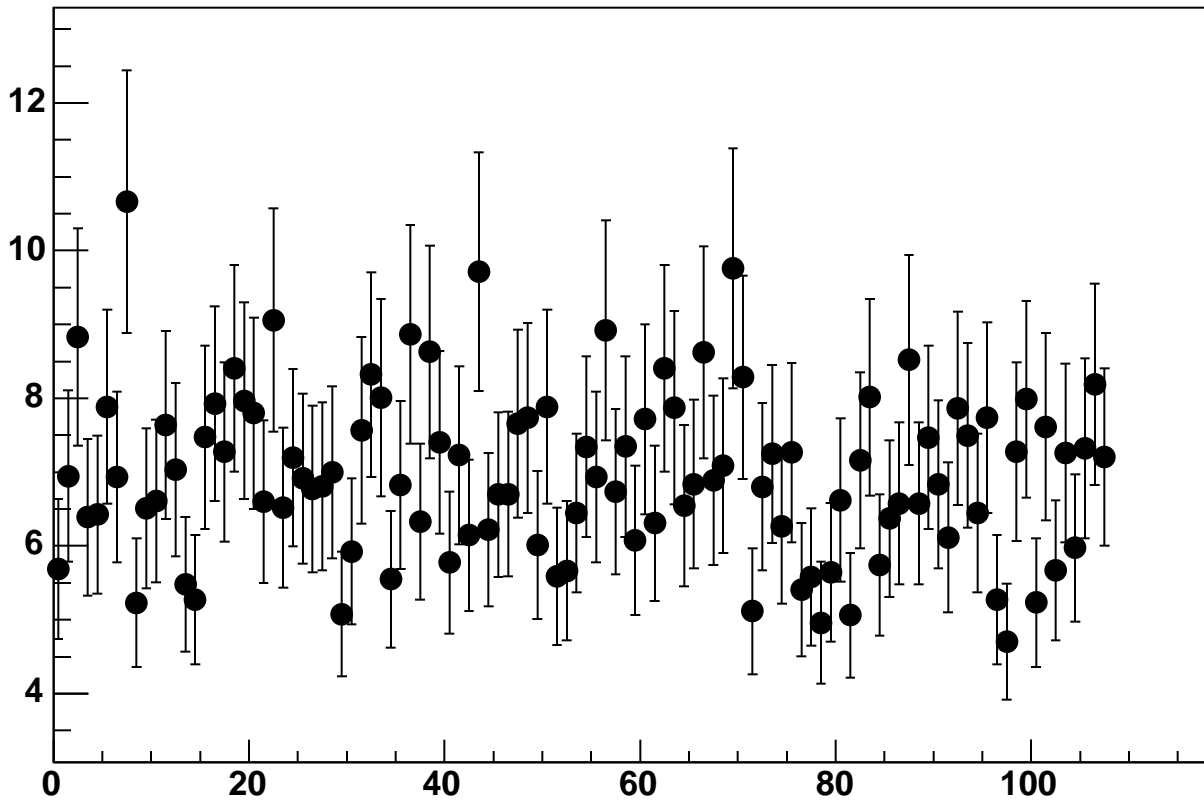
Enable 5, DAC=4000, Hold=185, ADC Noise vs 18\*Chip+Chan



Enable 5, DAC=4000, Hold=190, ADC Mean vs 18\*Chip+Chan

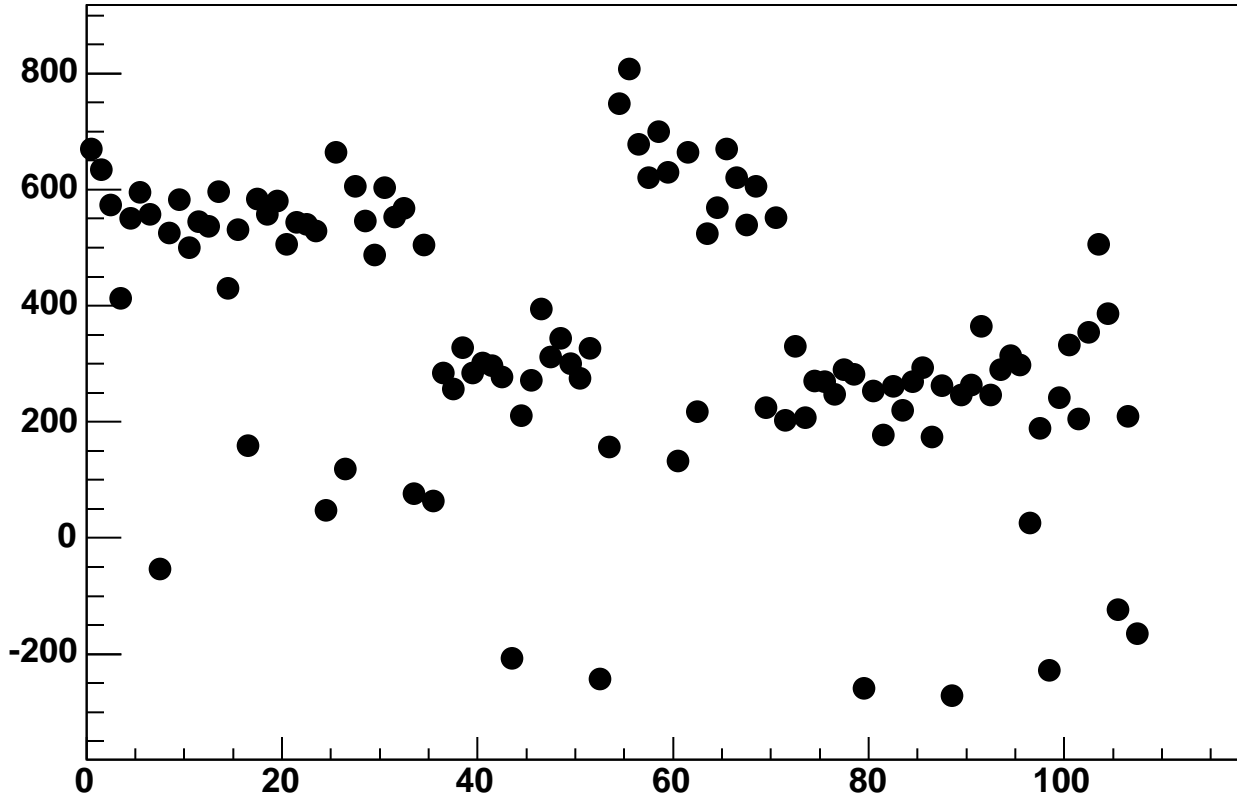


Enable 5, DAC=4000, Hold=190, ADC Noise vs 18\*Chip+Chan

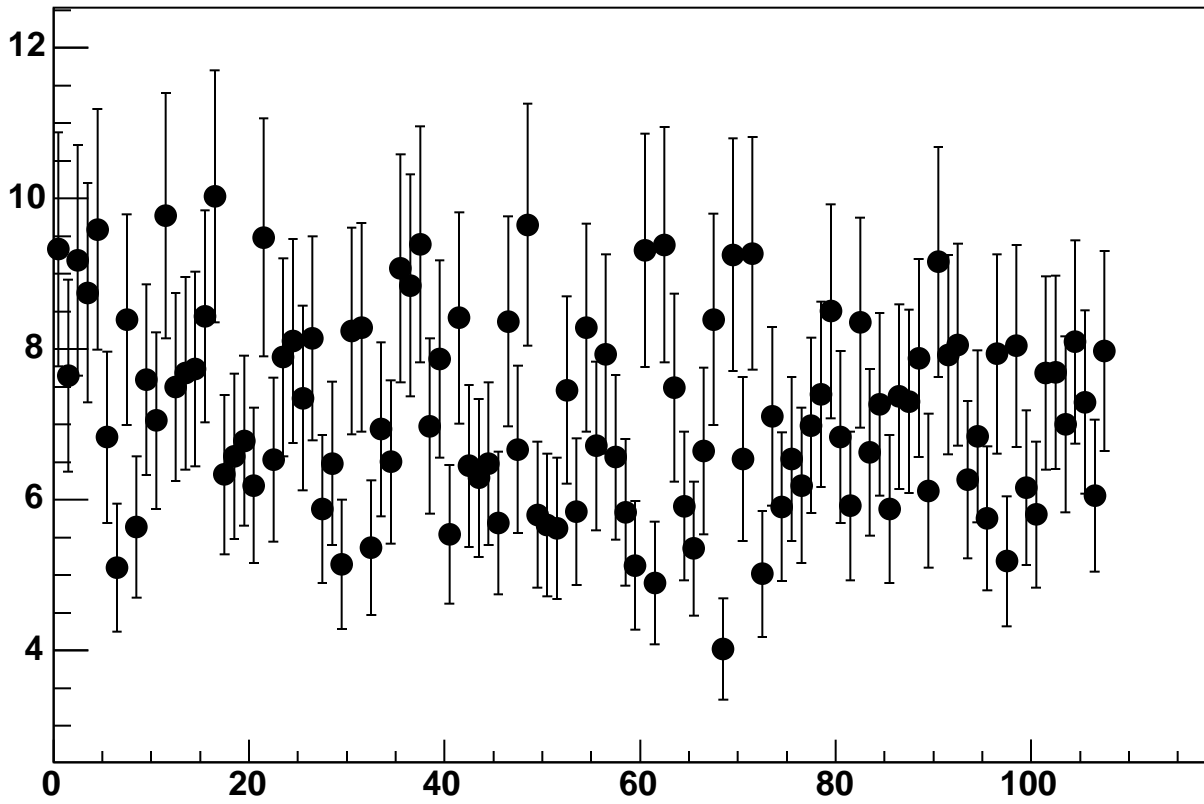




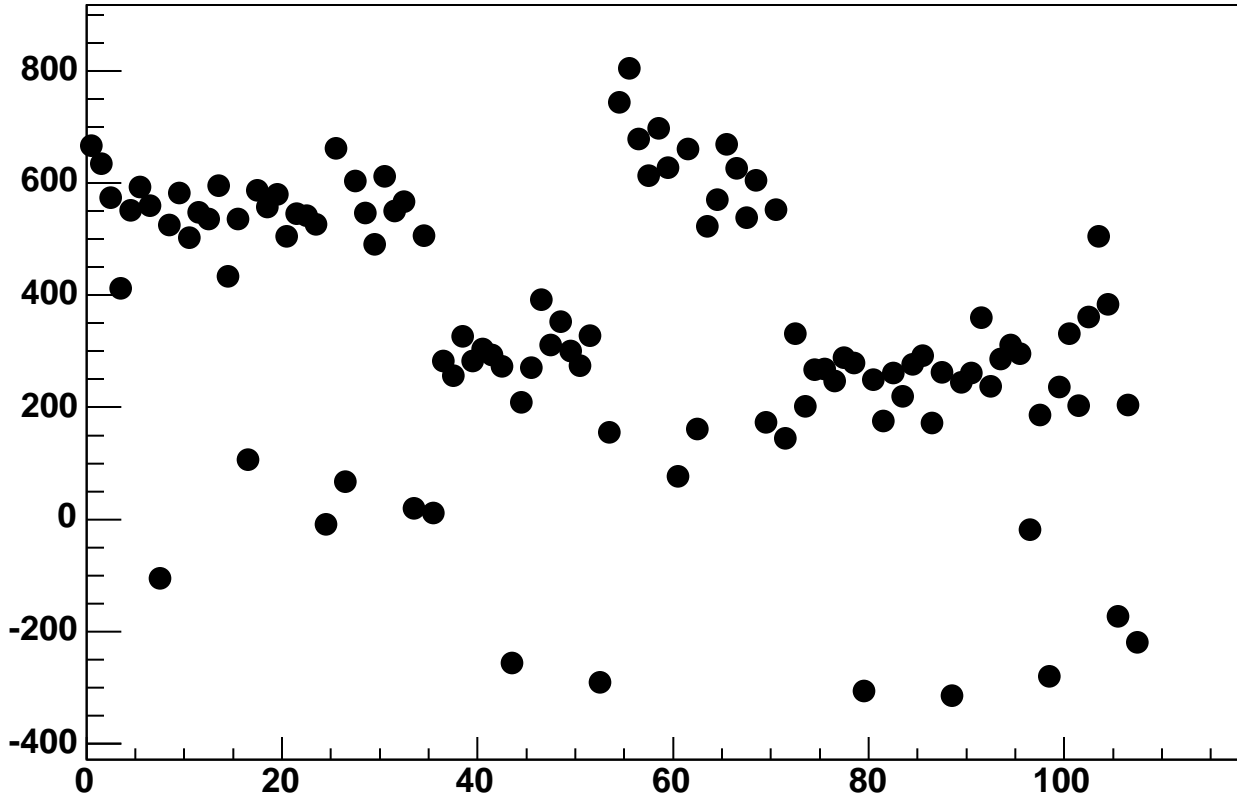
Enable 5, DAC=4000, Hold=195, ADC Mean vs 18\*Chip+Chan



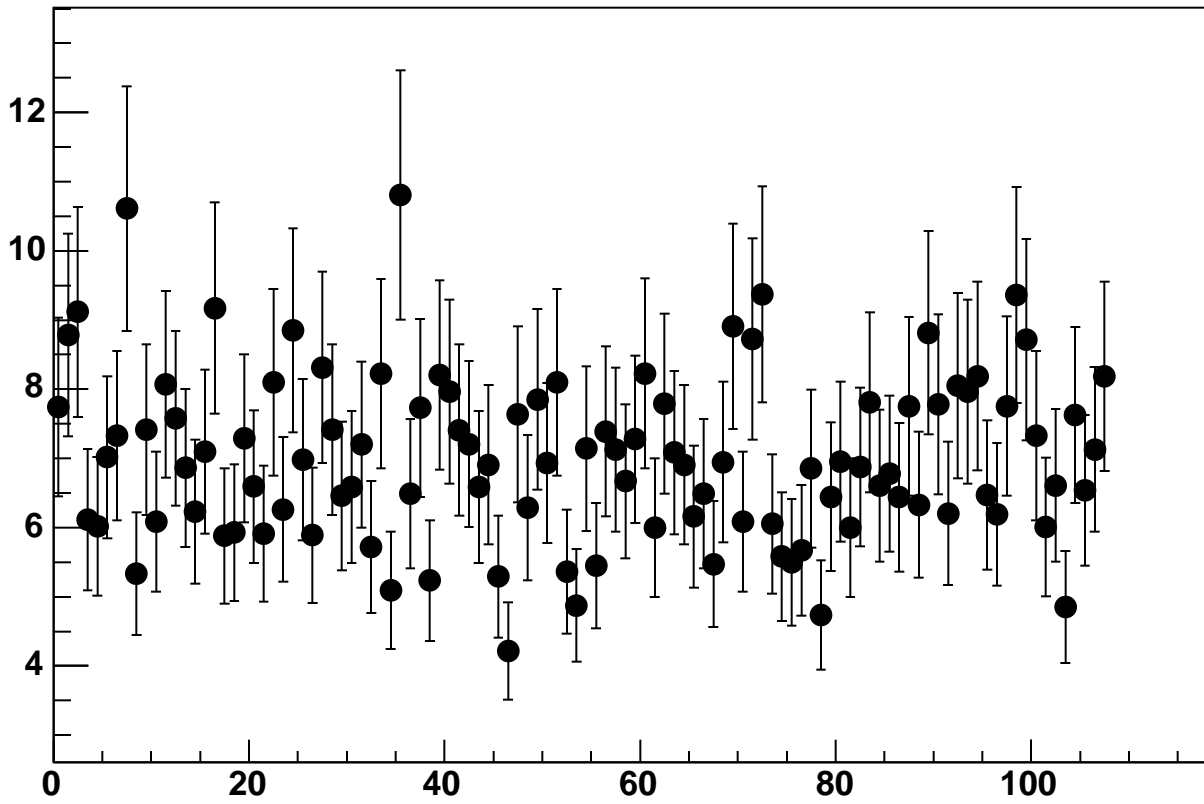
Enable 5, DAC=4000, Hold=195, ADC Noise vs 18\*Chip+Chan



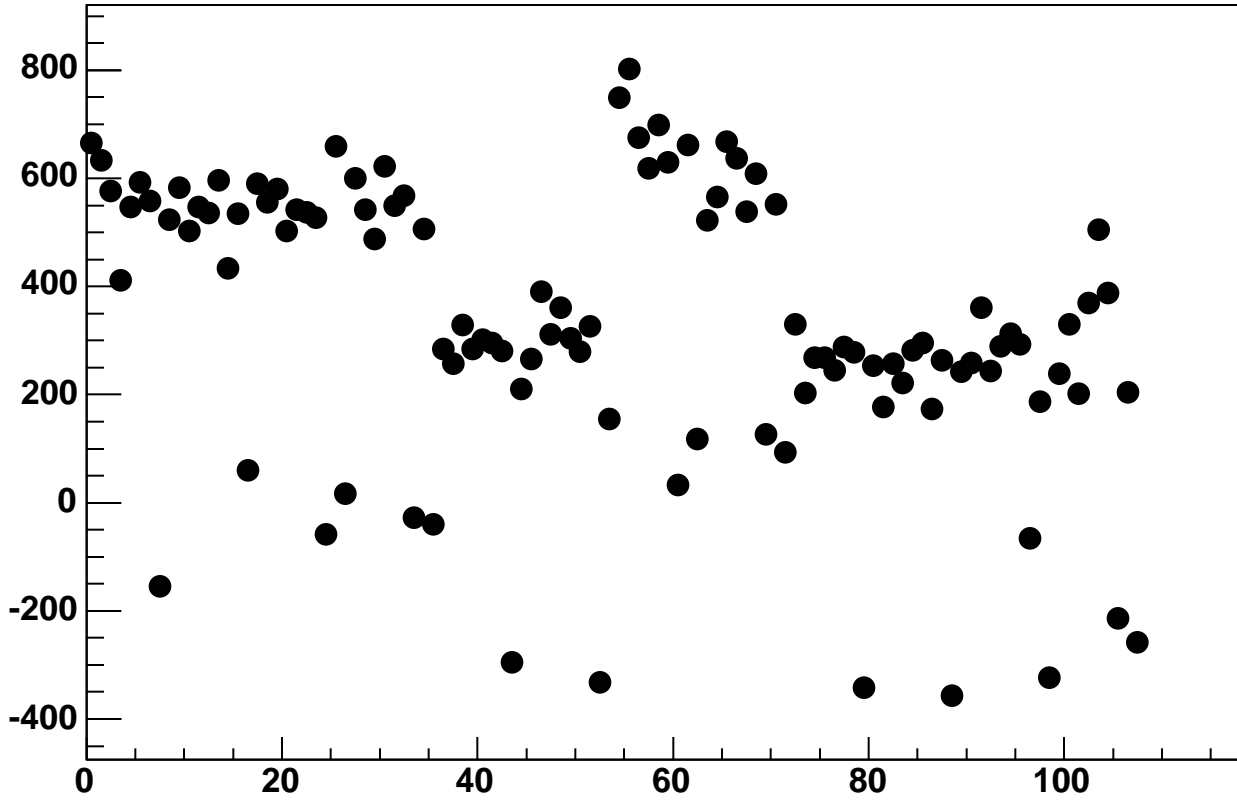
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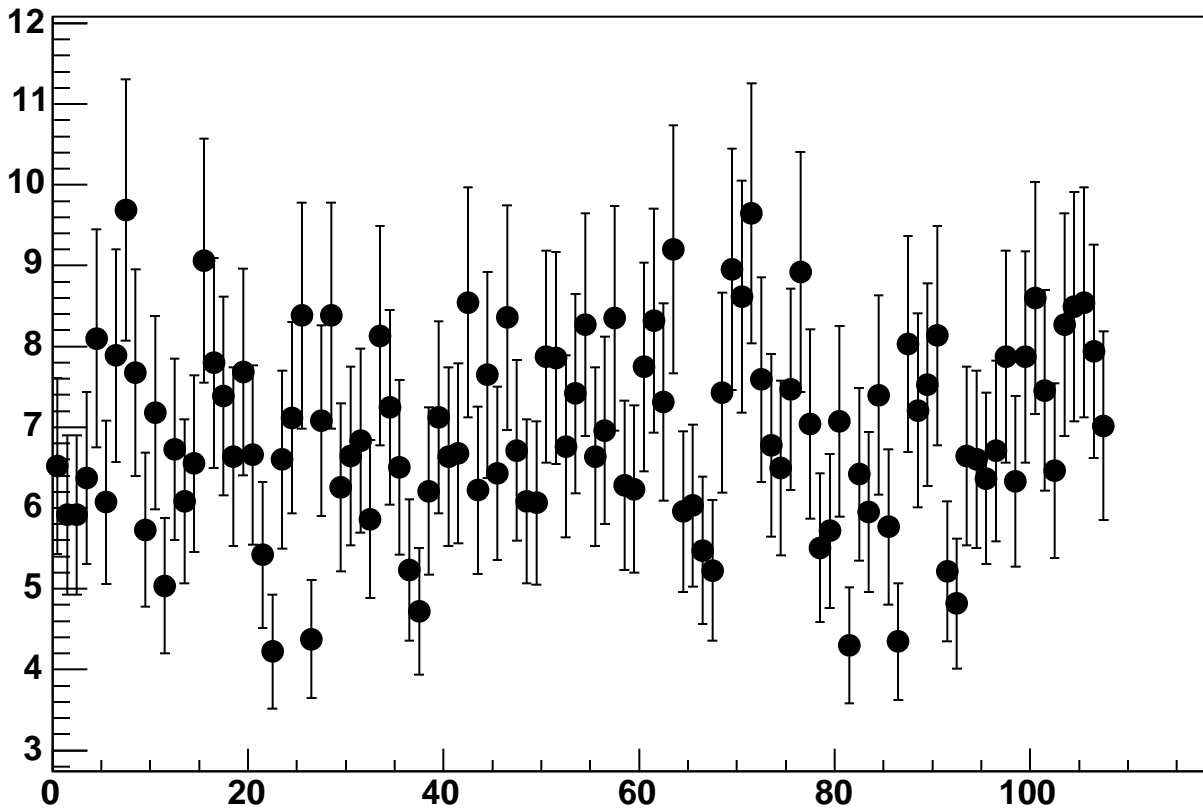
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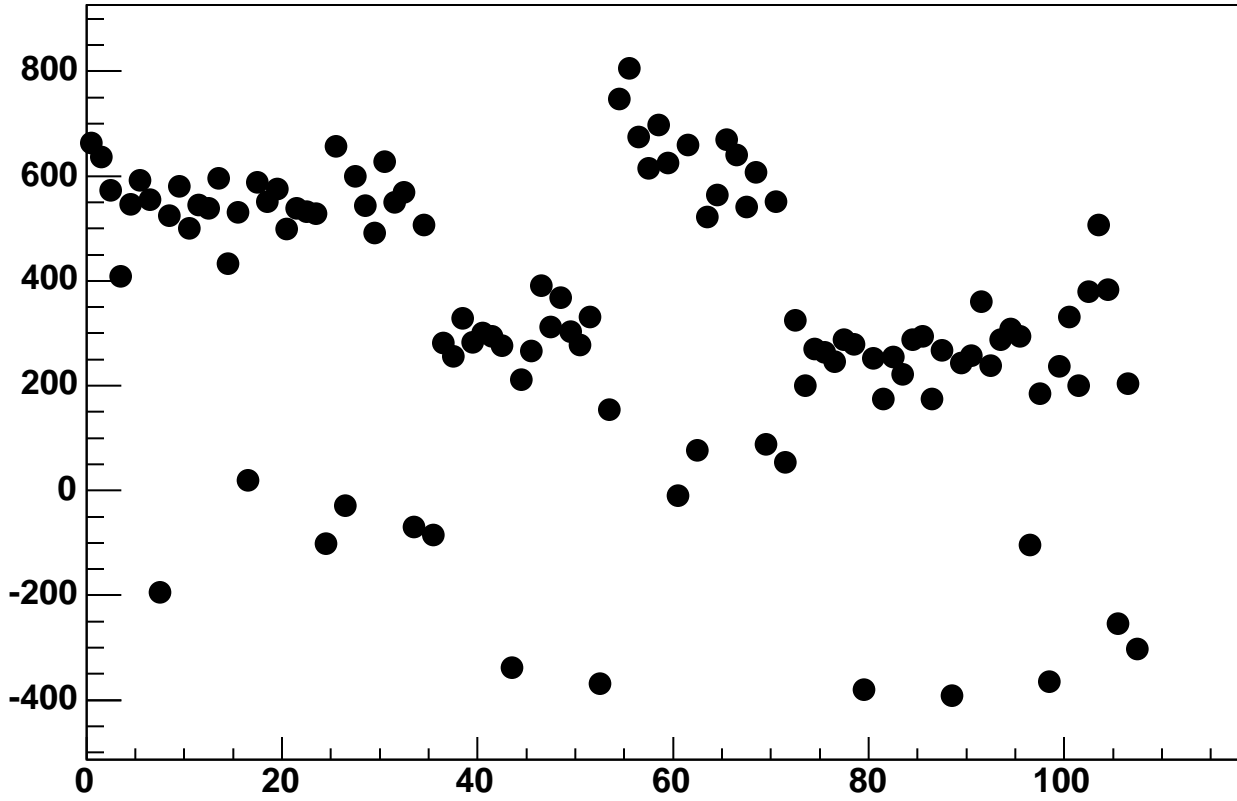
Enable 5, DAC=4000, Hold=205, ADC Mean vs 18\*Chip+Chan



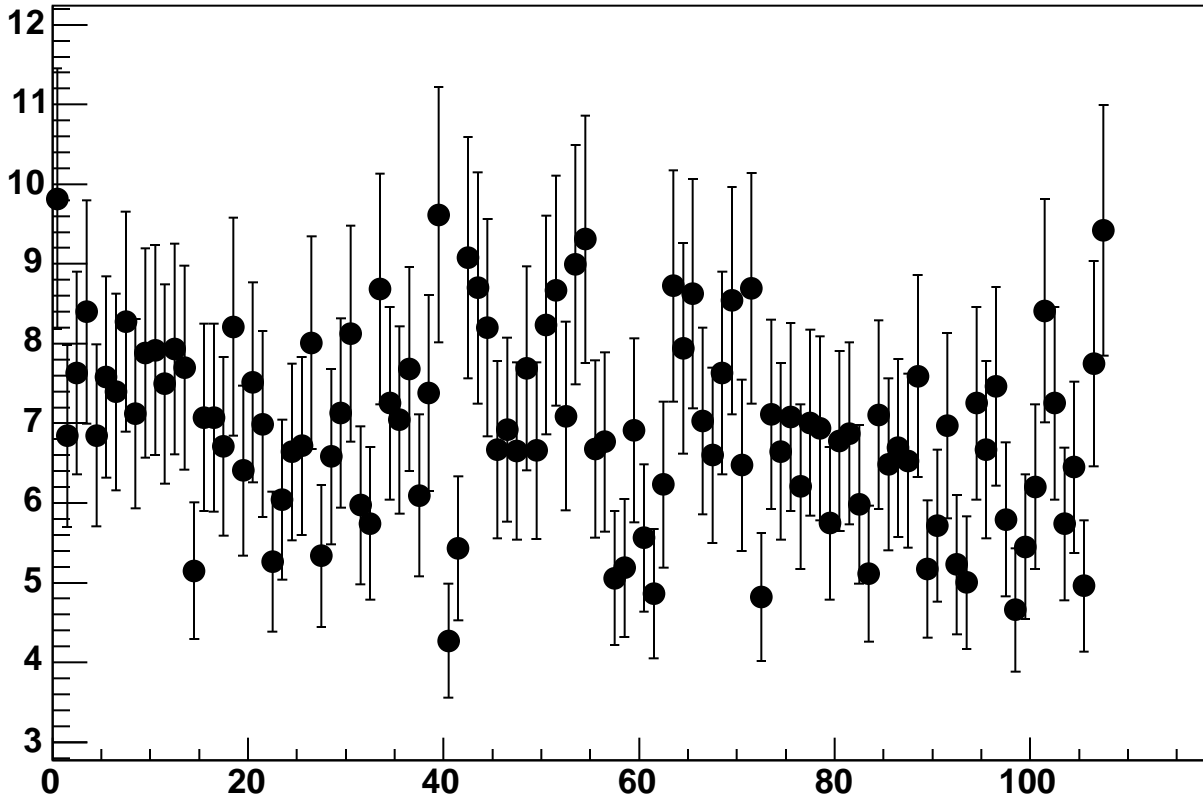
Enable 5, DAC=4000, Hold=205, ADC Noise vs 18\*Chip+Chan



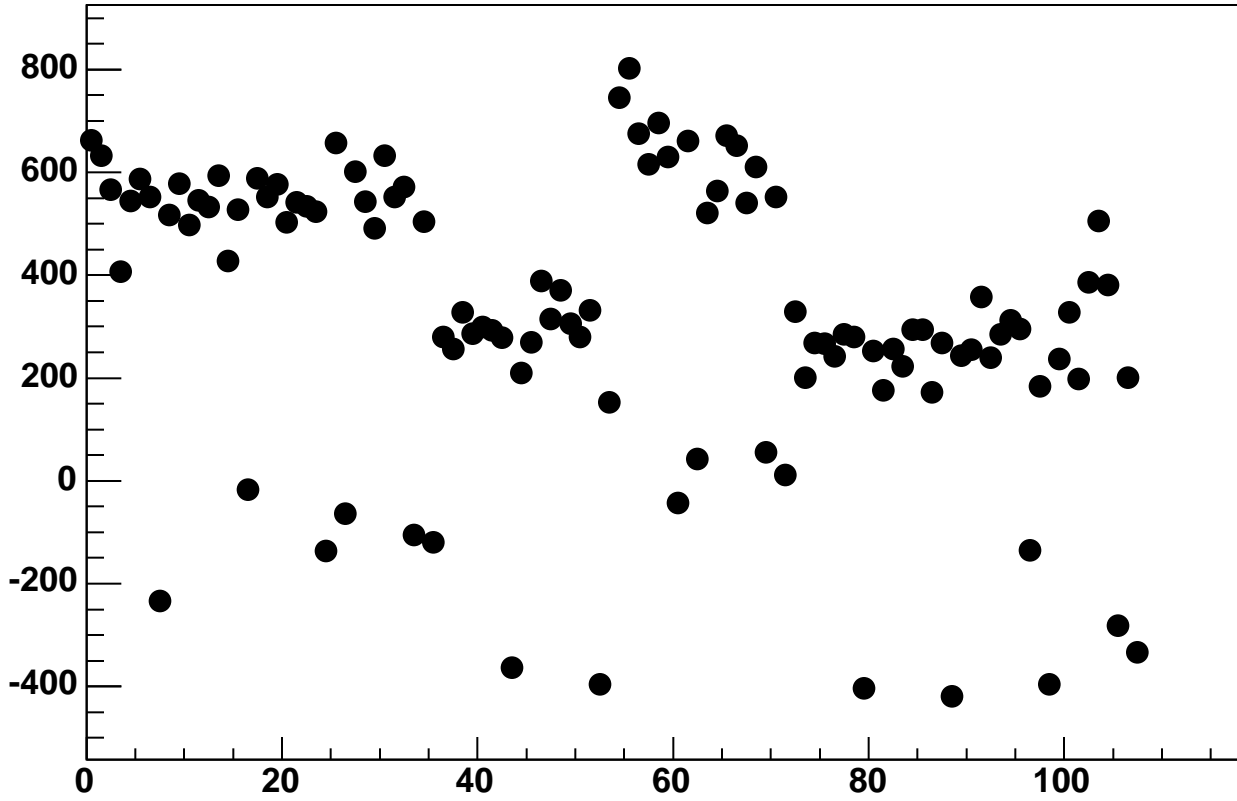
Enable 5, DAC=4000, Hold=210, ADC Mean vs 18\*Chip+Chan



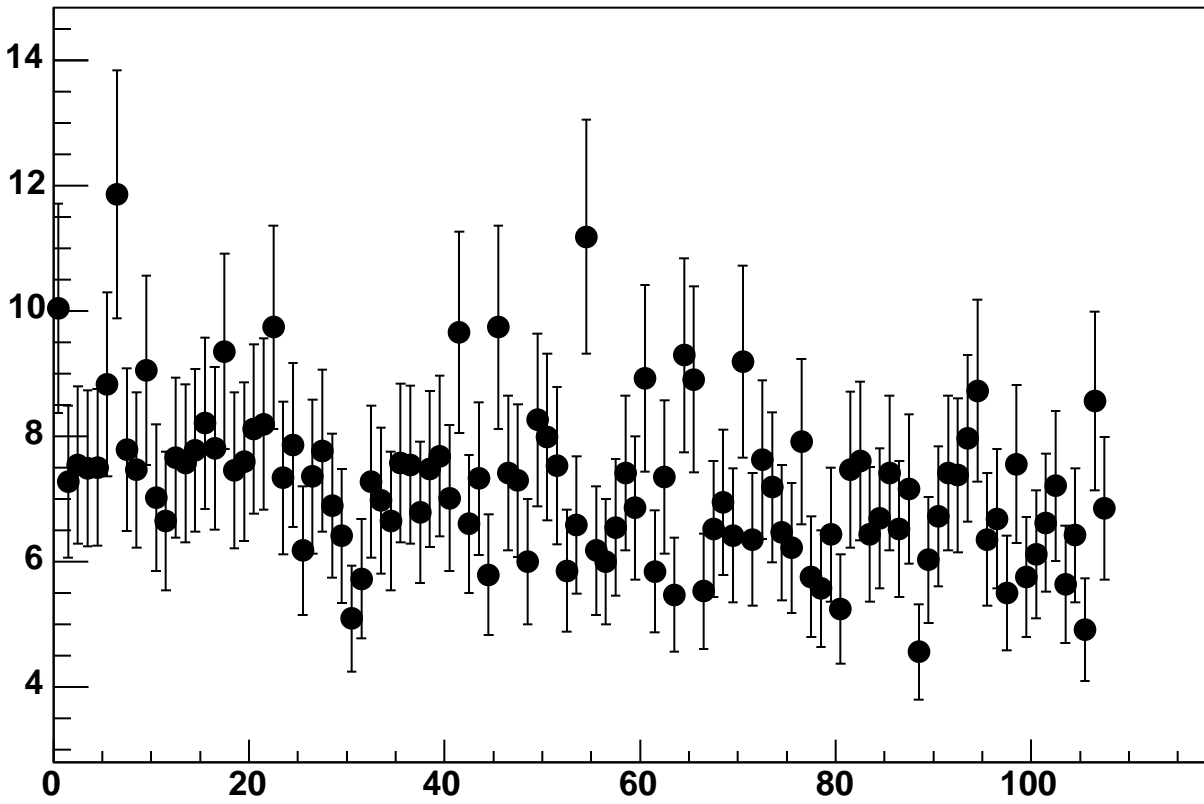
Enable 5, DAC=4000, Hold=210, ADC Noise vs 18\*Chip+Chan



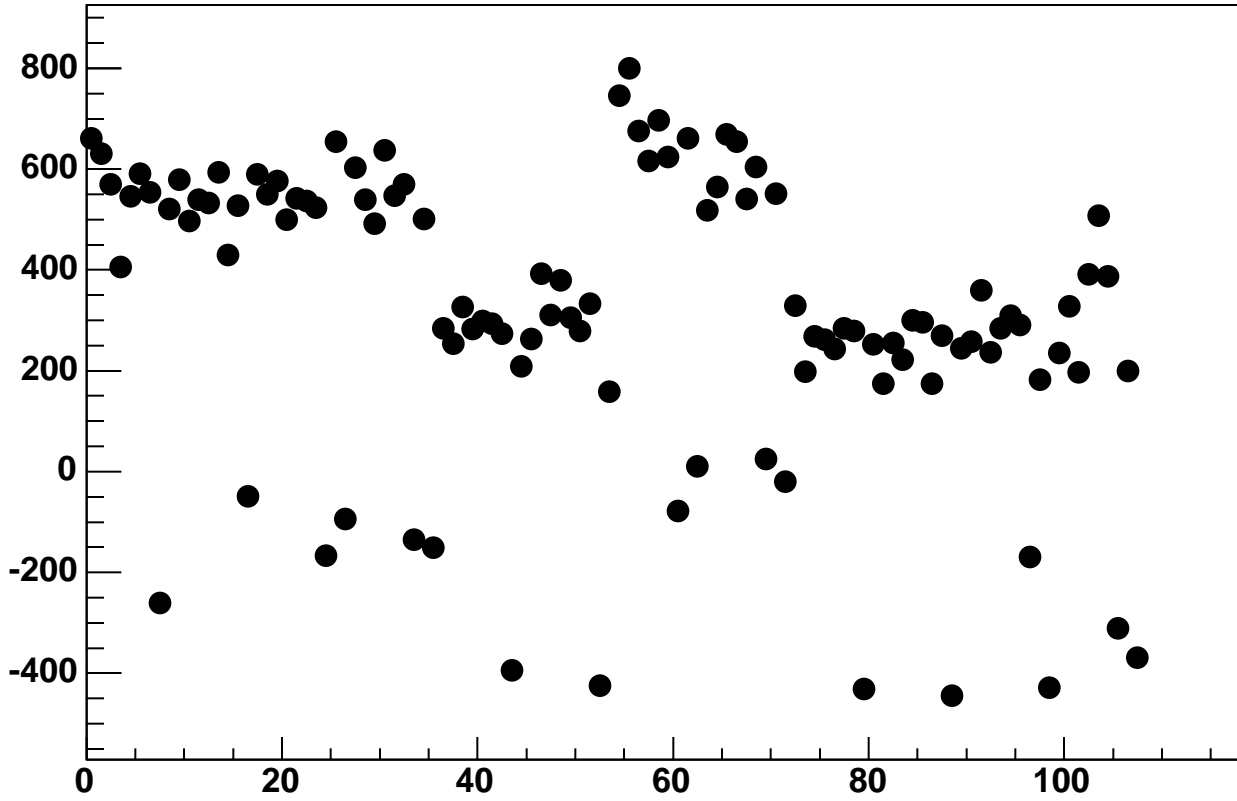
Enable 5, DAC=4000, Hold=215, ADC Mean vs 18\*Chip+Chan



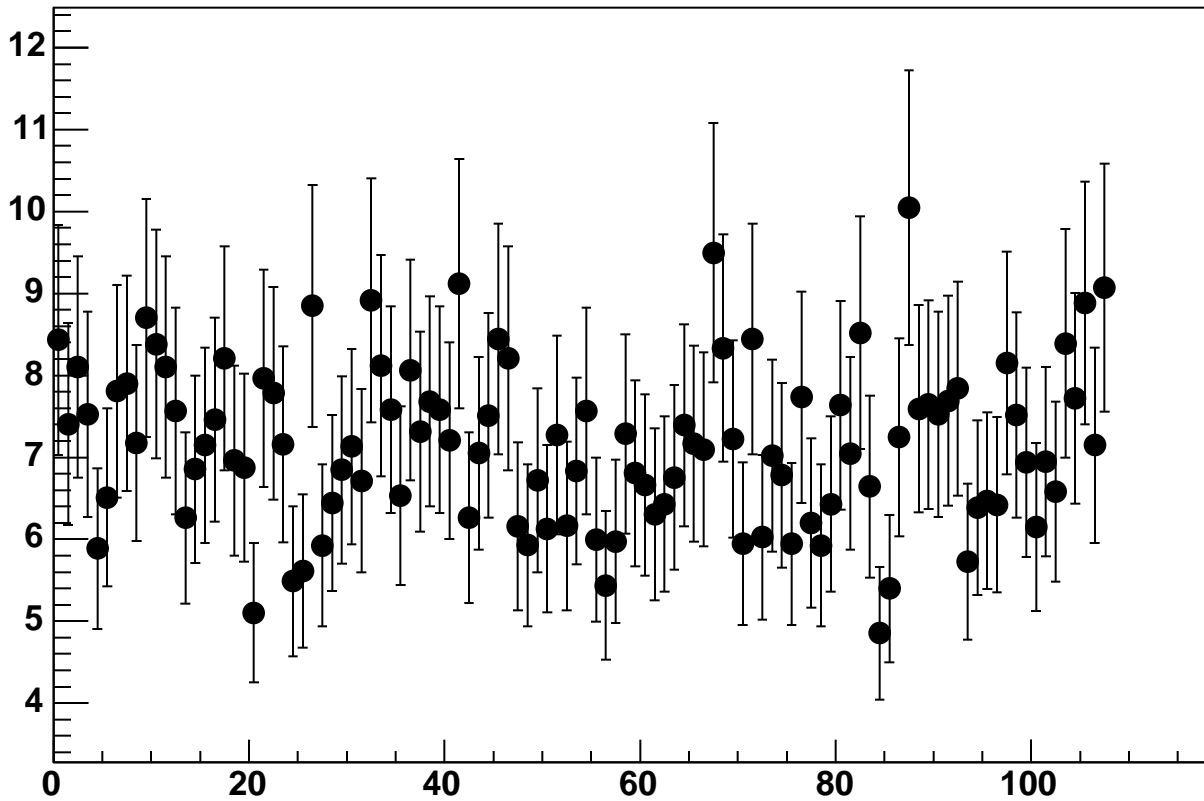
Enable 5, DAC=4000, Hold=215, ADC Noise vs 18\*Chip+Chan



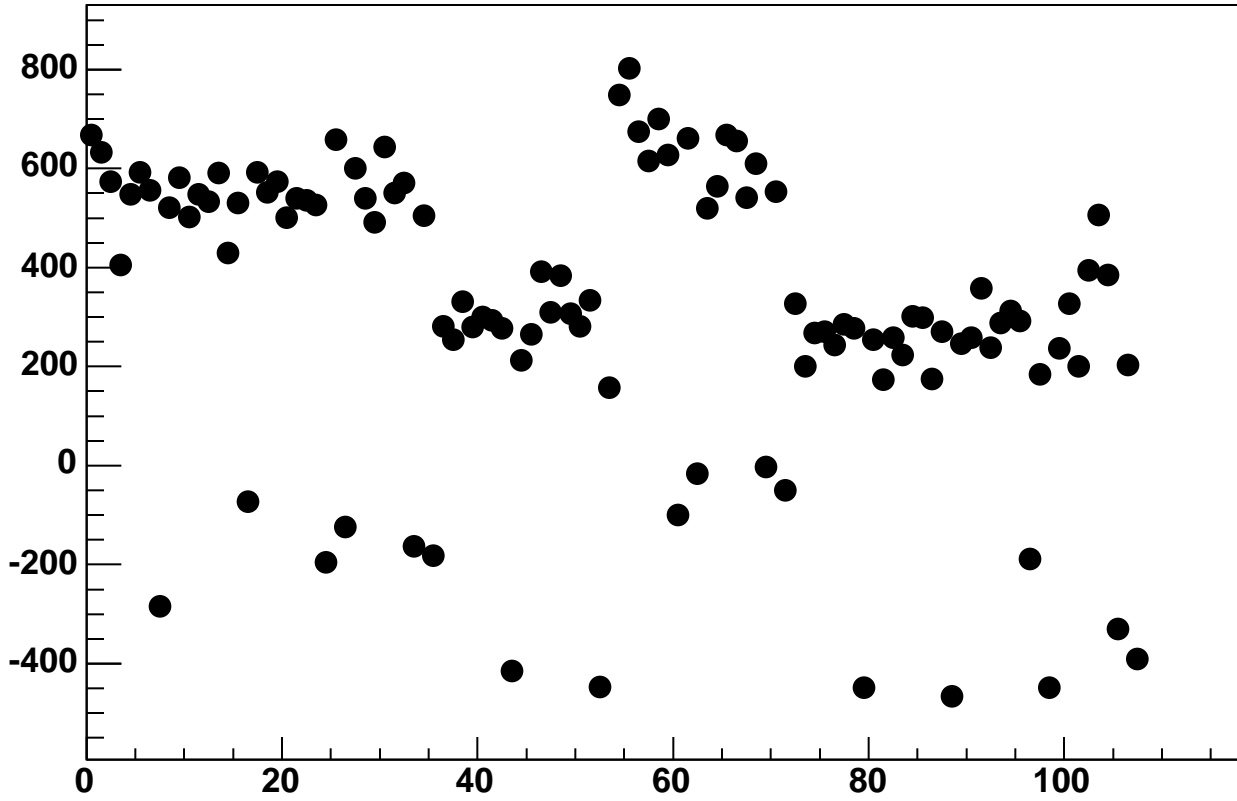
Enable 5, DAC=4000, Hold=220, ADC Mean vs 18\*Chip+Chan



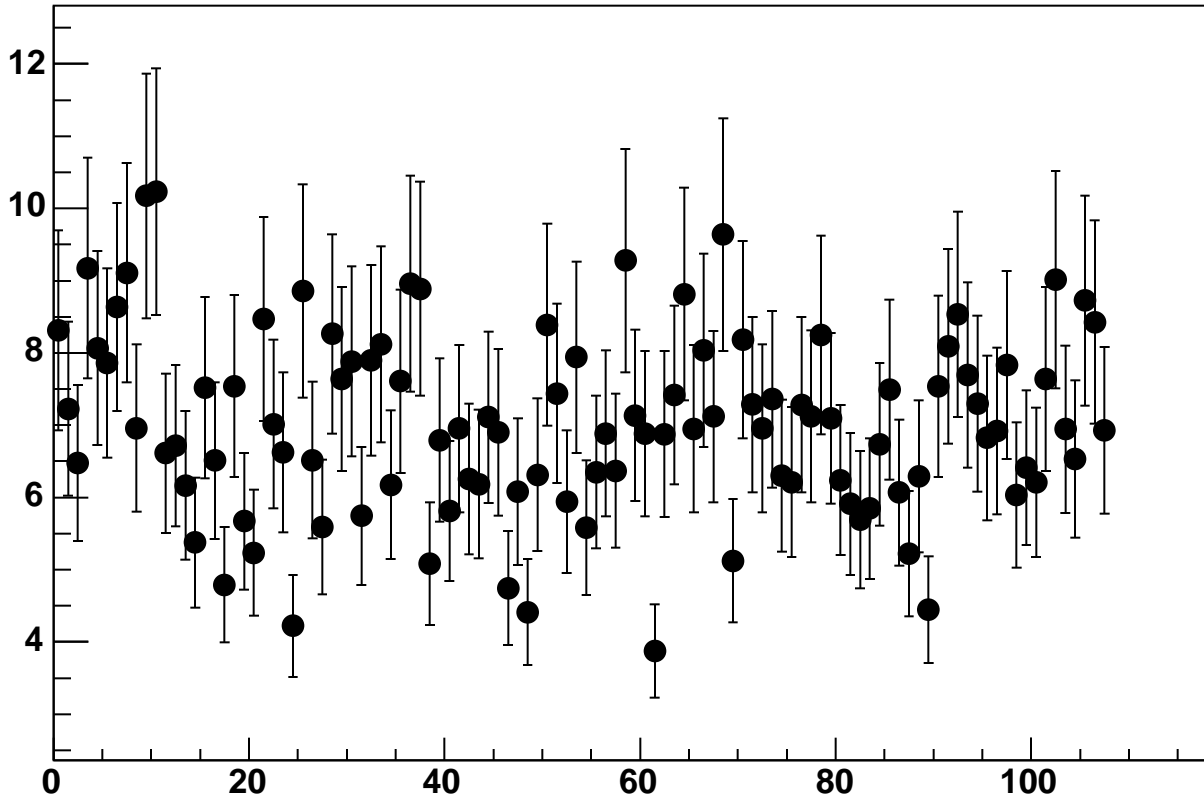
Enable 5, DAC=4000, Hold=220, ADC Noise vs 18\*Chip+Chan



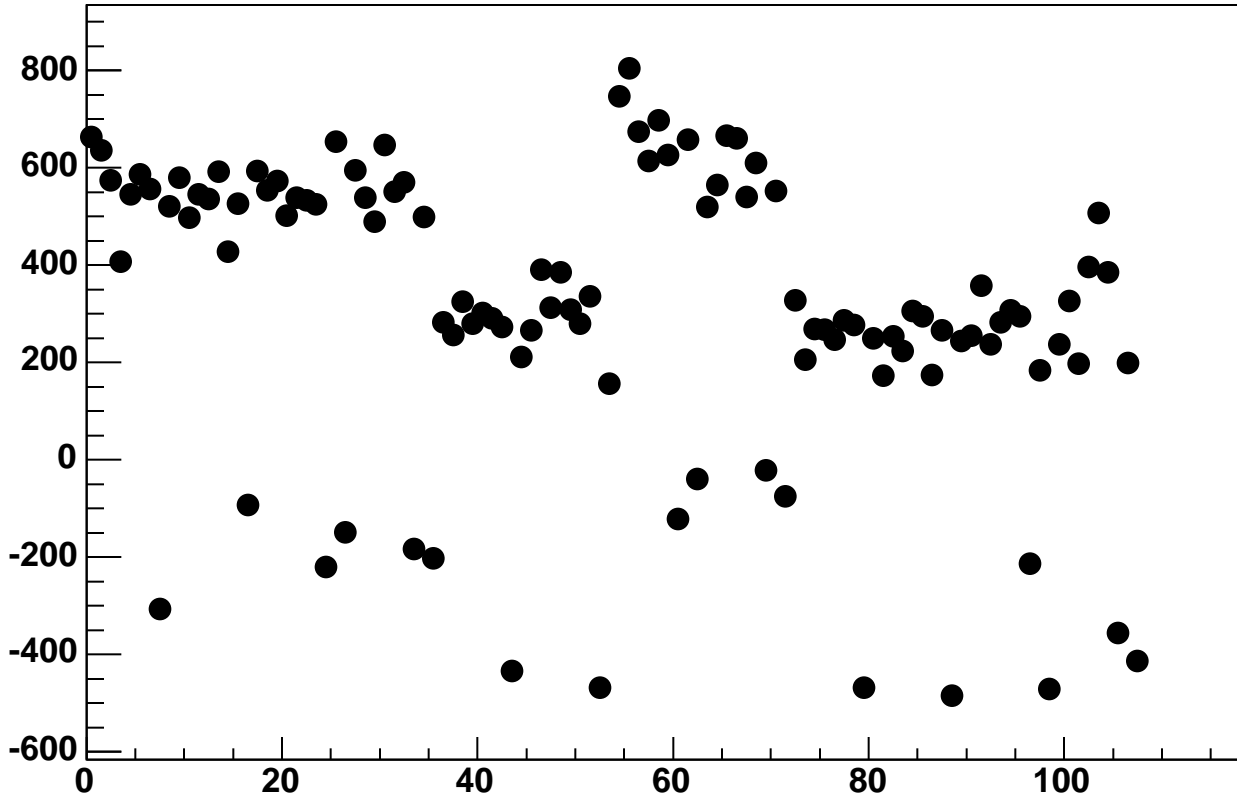
Enable 5, DAC=4000, Hold=225, ADC Mean vs 18\*Chip+Chan



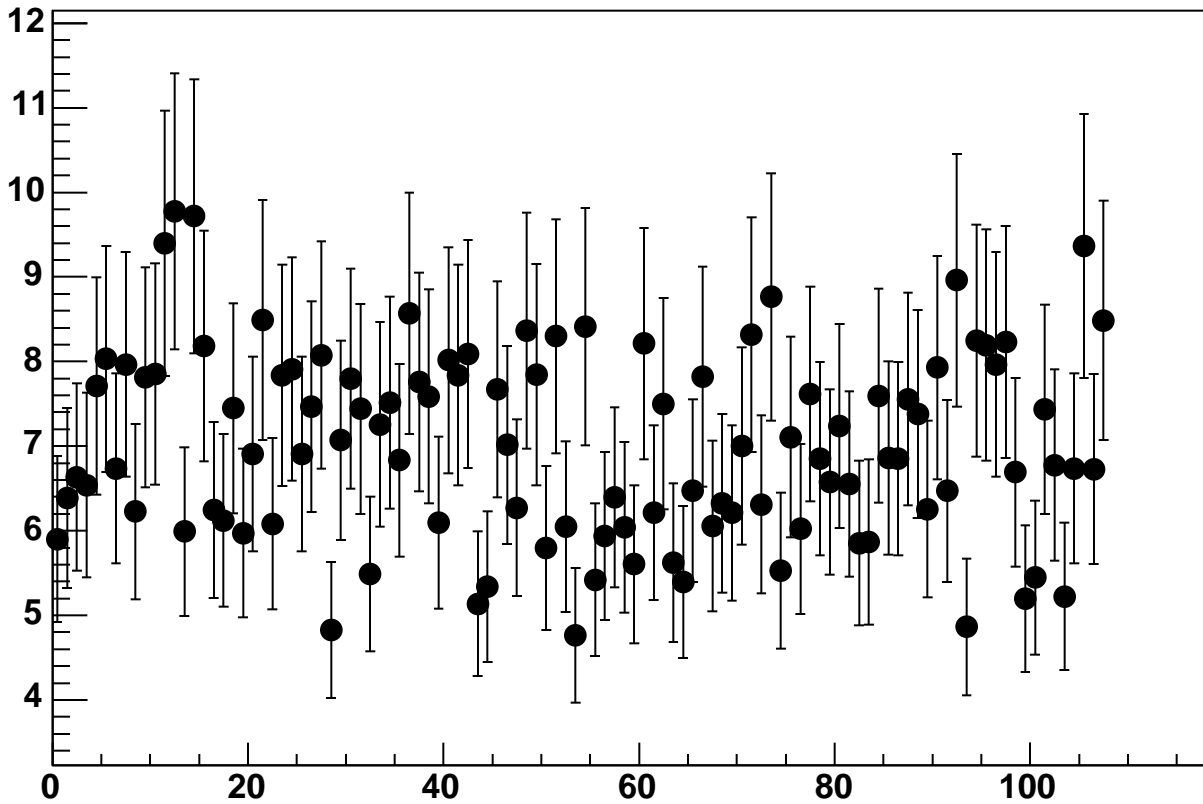
Enable 5, DAC=4000, Hold=225, ADC Noise vs 18\*Chip+Chan



Enable 5, DAC=4000, Hold=230, ADC Mean vs 18\*Chip+Chan

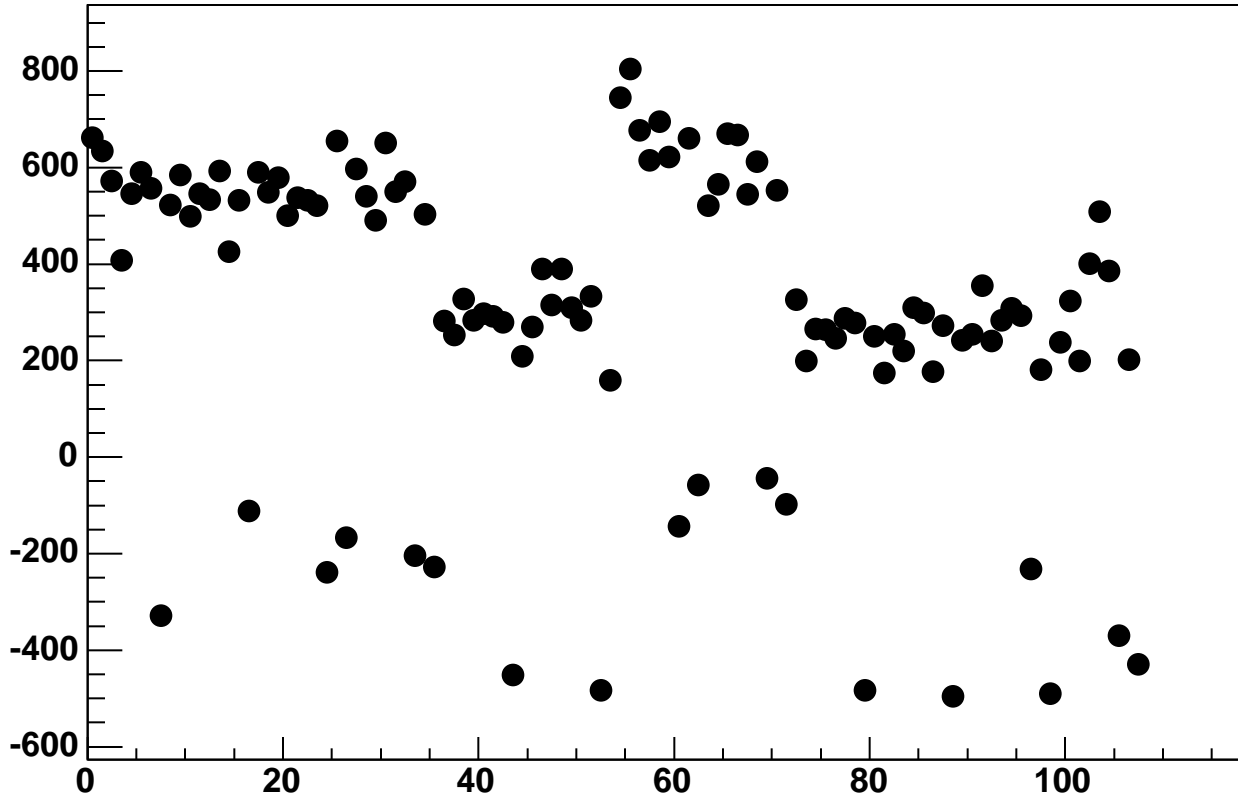


Enable 5, DAC=4000, Hold=230, ADC Noise vs 18\*Chip+Chan

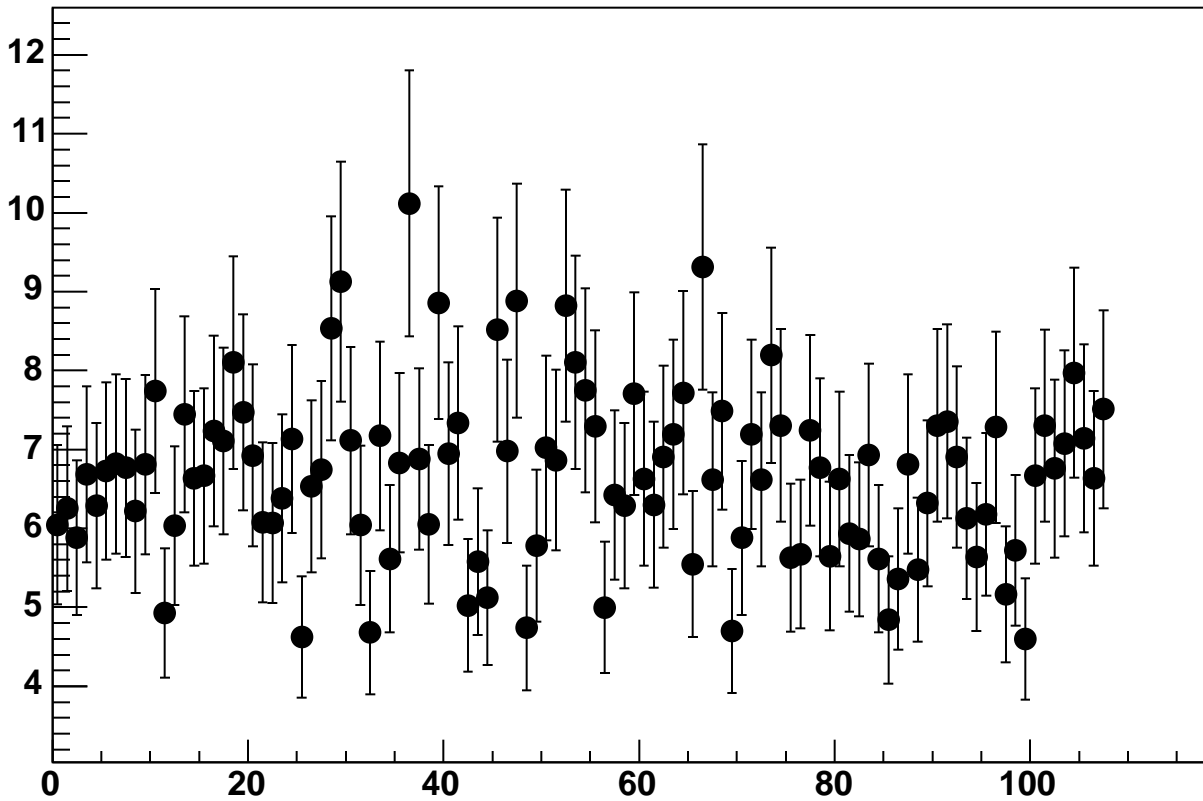




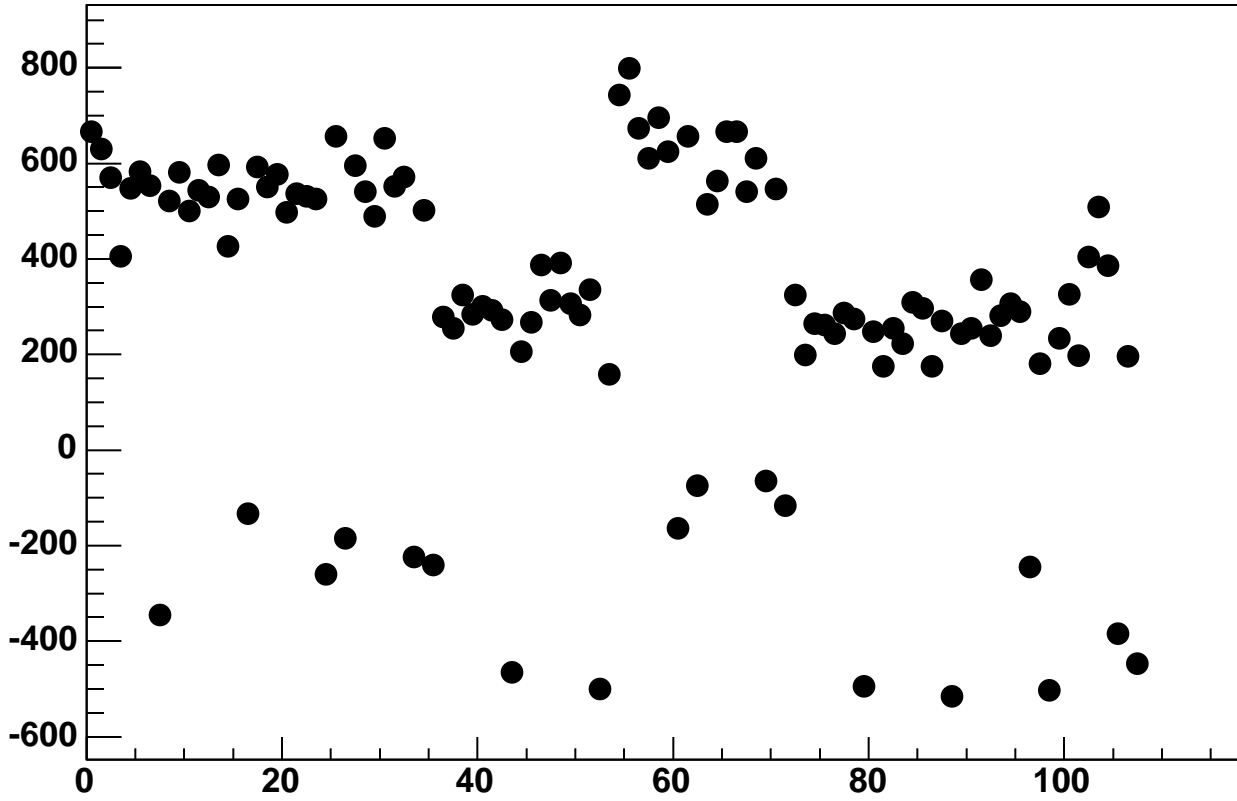
Enable 5, DAC=4000, Hold=235, ADC Mean vs 18\*Chip+Chan



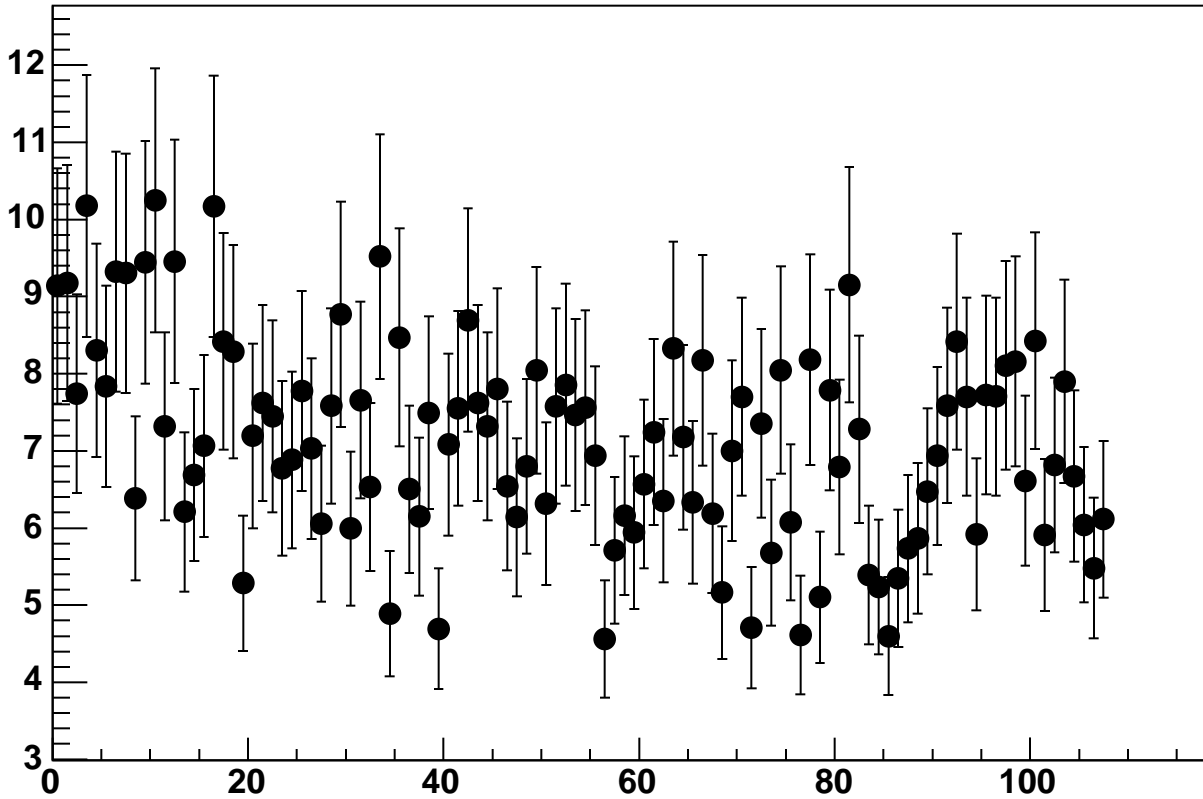
Enable 5, DAC=4000, Hold=235, ADC Noise vs 18\*Chip+Chan



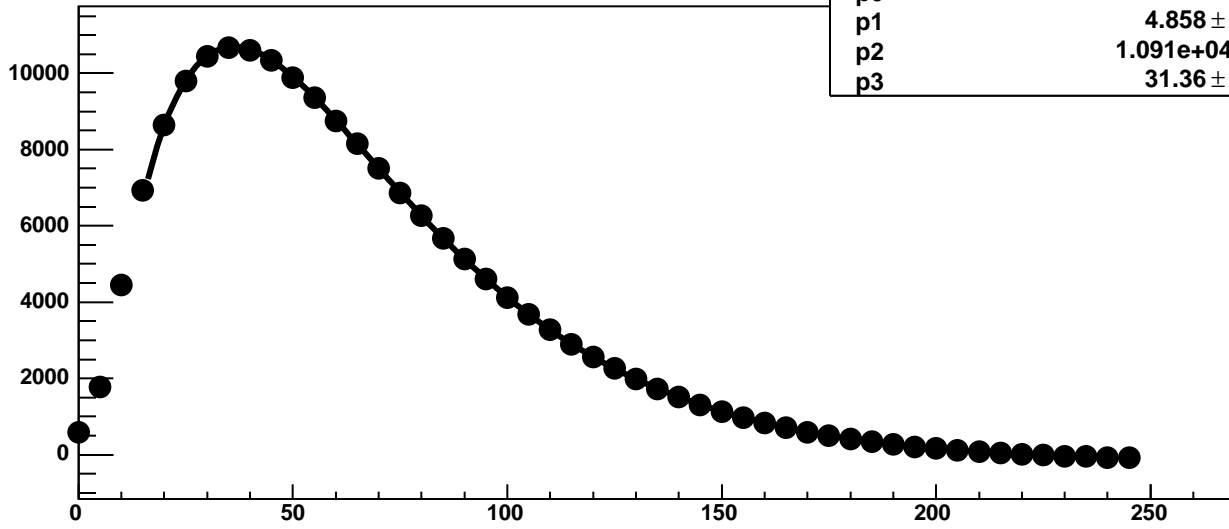
Enable 5, DAC=4000, Hold=240, ADC Mean vs 18\*Chip+Chan



Enable 5, DAC=4000, Hold=240, ADC Noise vs 18\*Chip+Chan

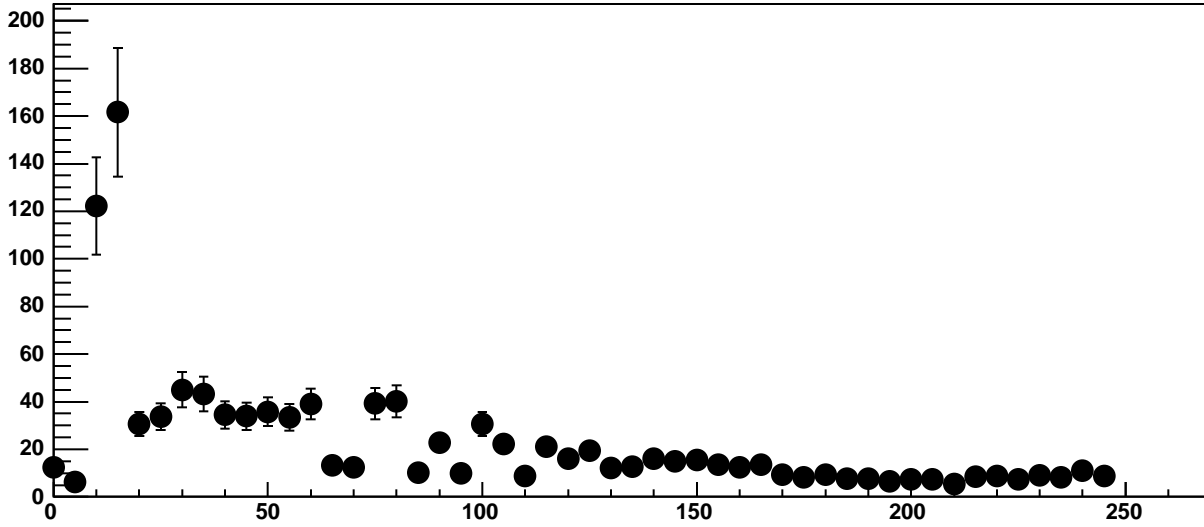


Chip 0, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold

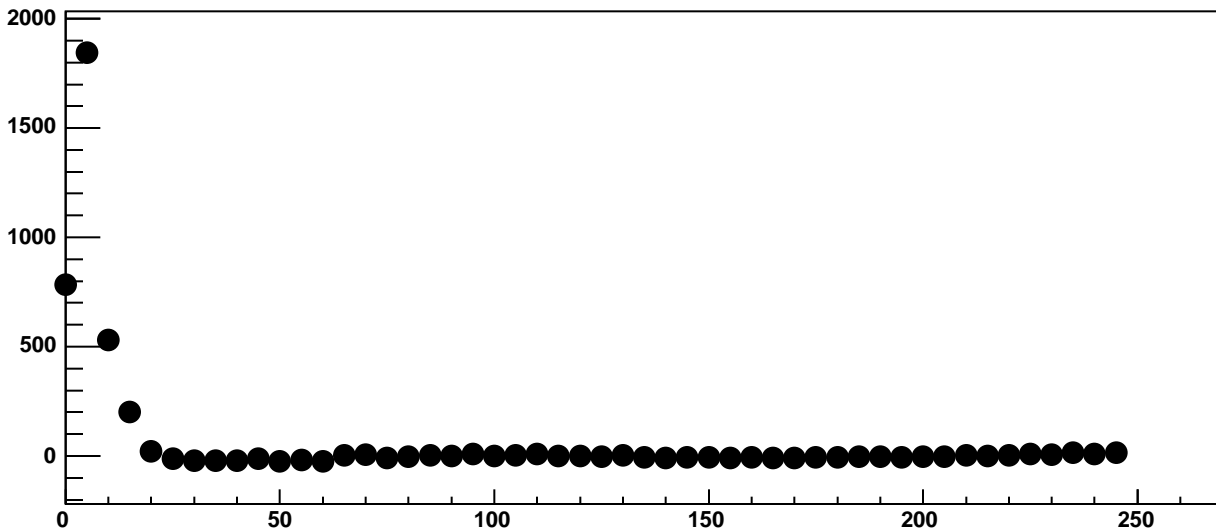


$\chi^2 / \text{ndf}$	351.2 / 42
p0	$-203.6 \pm 0.7325$
p1	$4.858 \pm 0.02079$
p2	$1.091\text{e}+04 \pm 2.493$
p3	$31.36 \pm 0.01072$

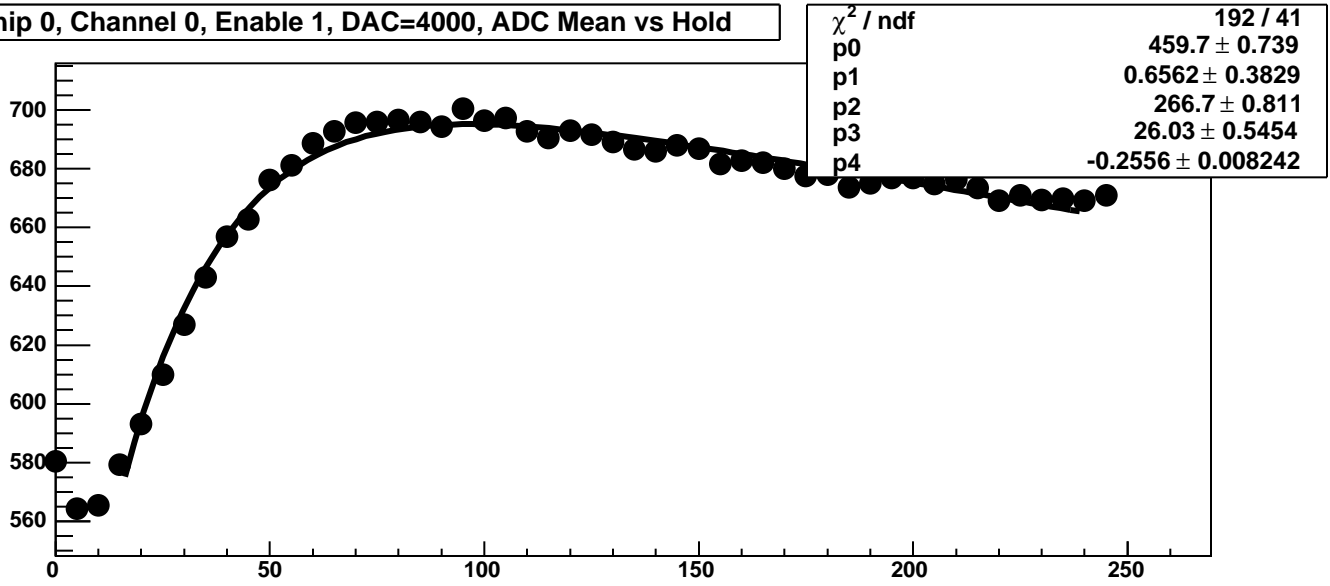
Chip 0, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold



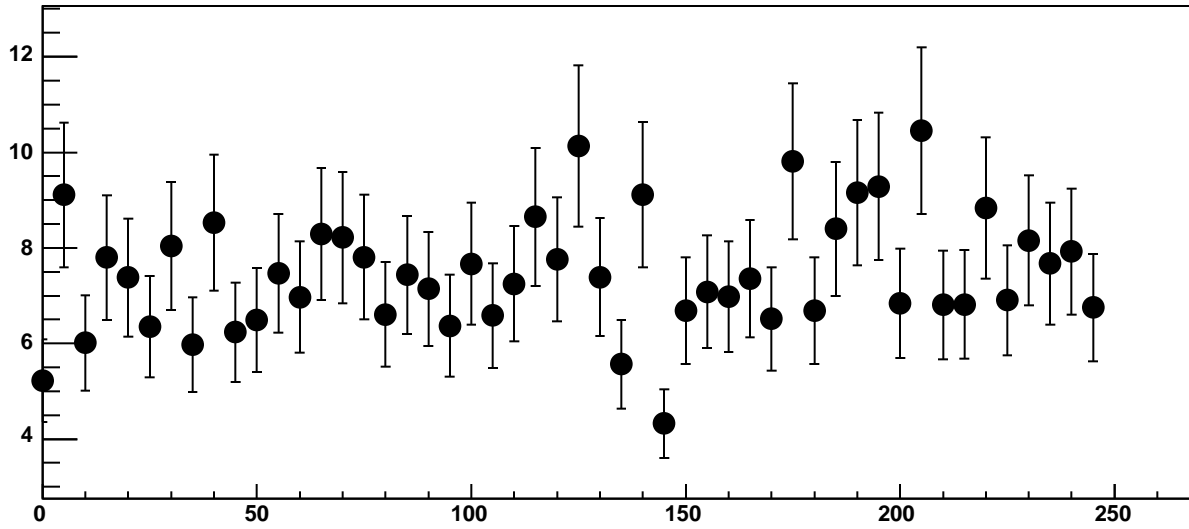
Chip 0, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold



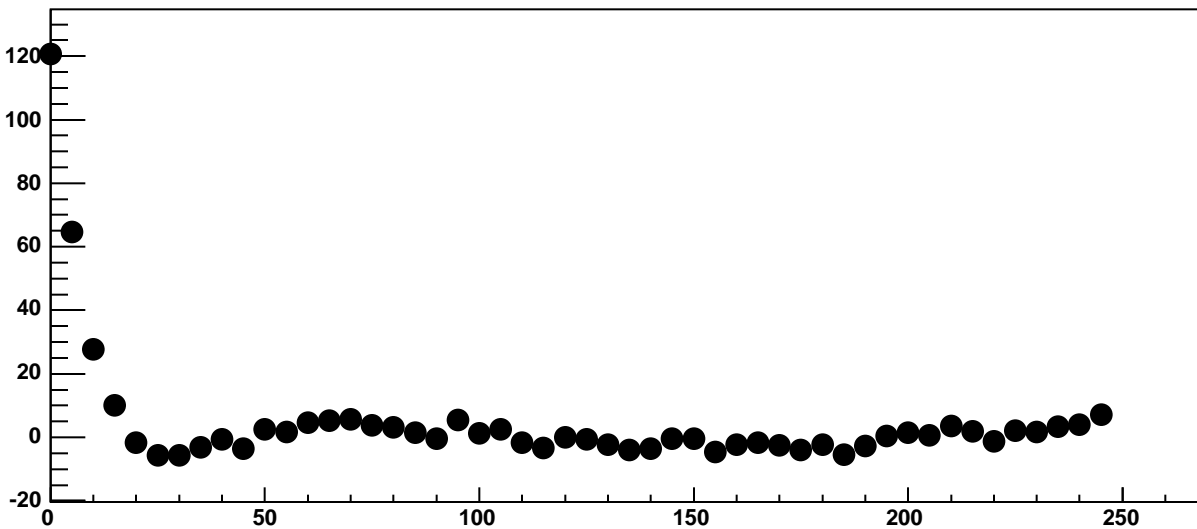
Chip 0, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold



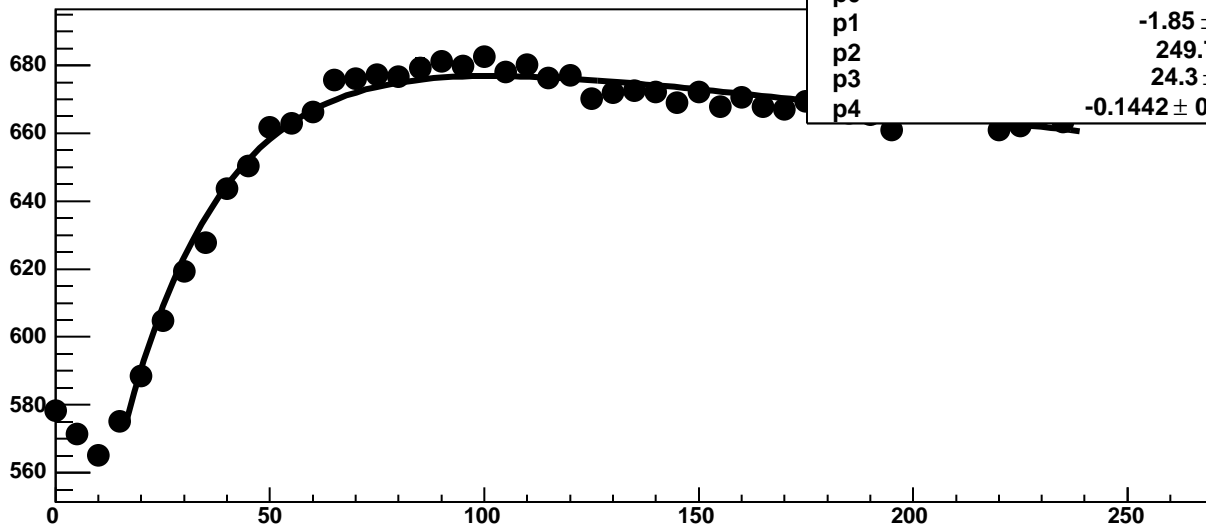
Chip 0, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold

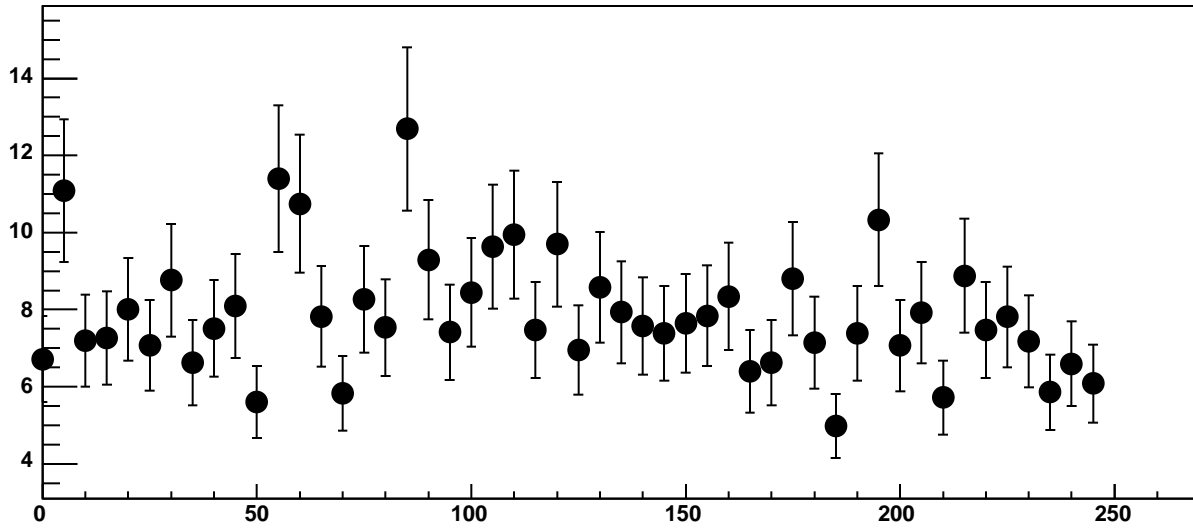


Chip 0, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

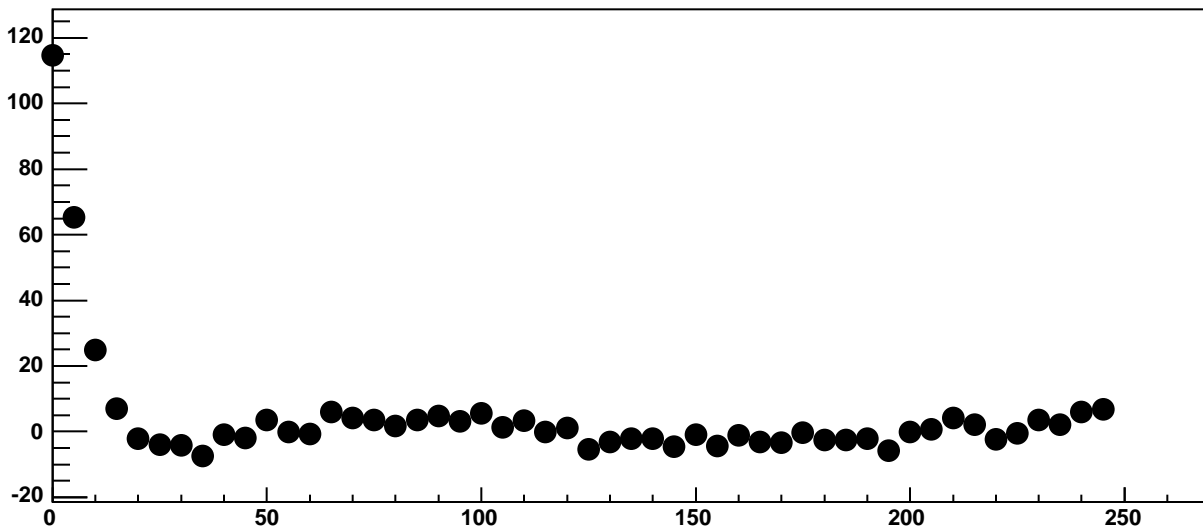


$\chi^2 / \text{ndf}$	201.1 / 41
p0	$445.7 \pm 3.229$
p1	$-1.85 \pm 0.5805$
p2	$249.7 \pm 3.28$
p3	$24.3 \pm 0.5847$
p4	$-0.1442 \pm 0.007256$

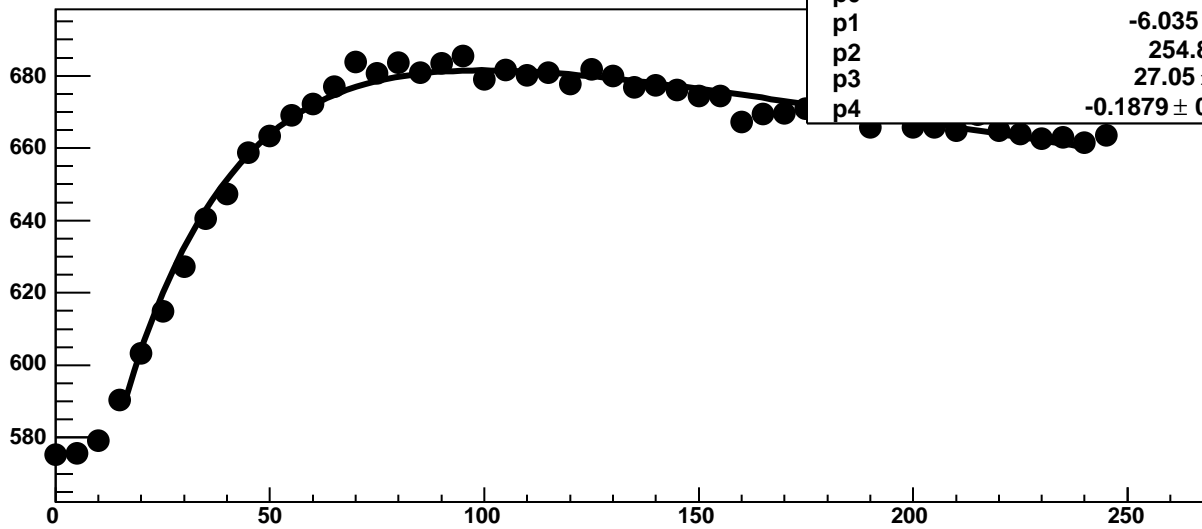
Chip 0, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

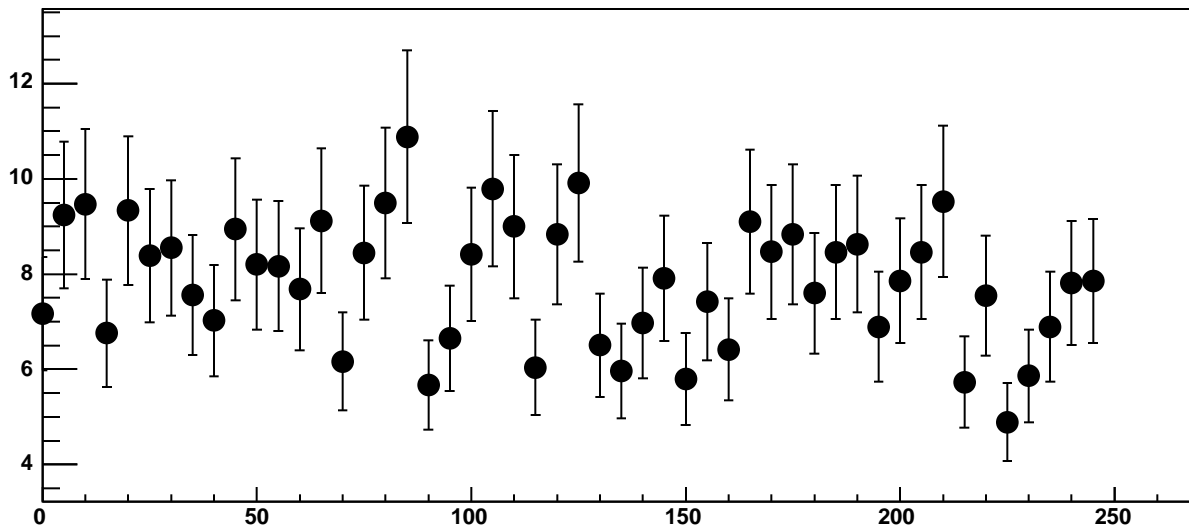


Chip 0, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold

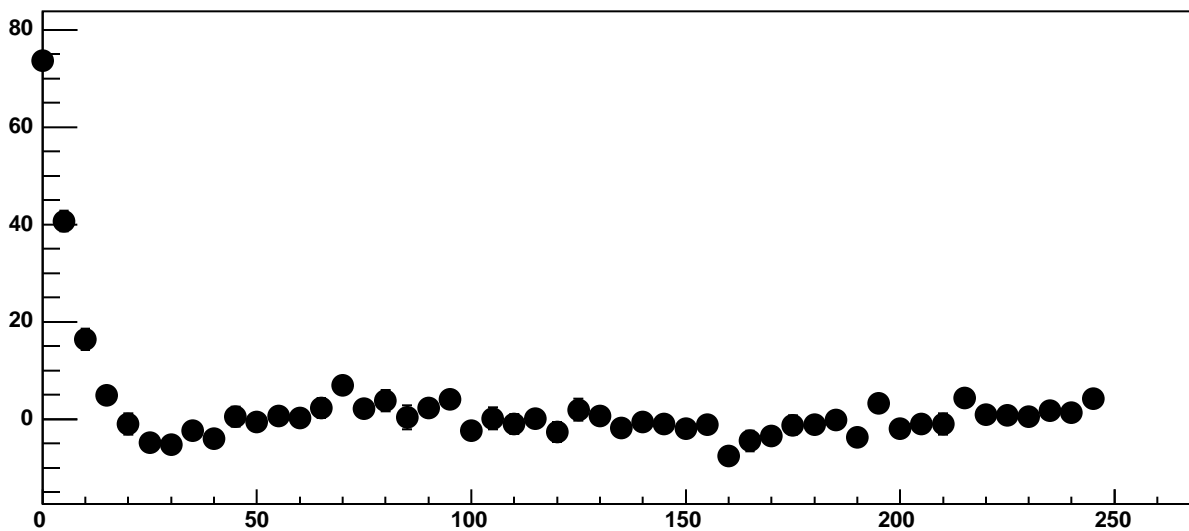


$\chi^2 / \text{ndf}$	139.1 / 41
p0	$451.7 \pm 3.198$
p1	$-6.035 \pm 0.7097$
p2	$254.8 \pm 3.265$
p3	$27.05 \pm 0.7184$
p4	$-0.1879 \pm 0.007523$

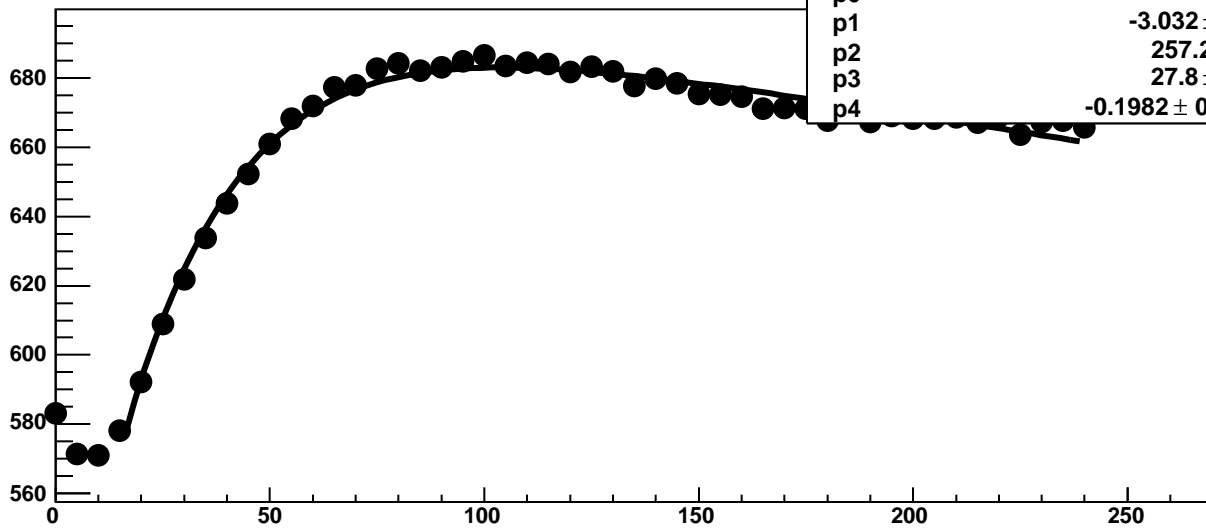
Chip 0, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold

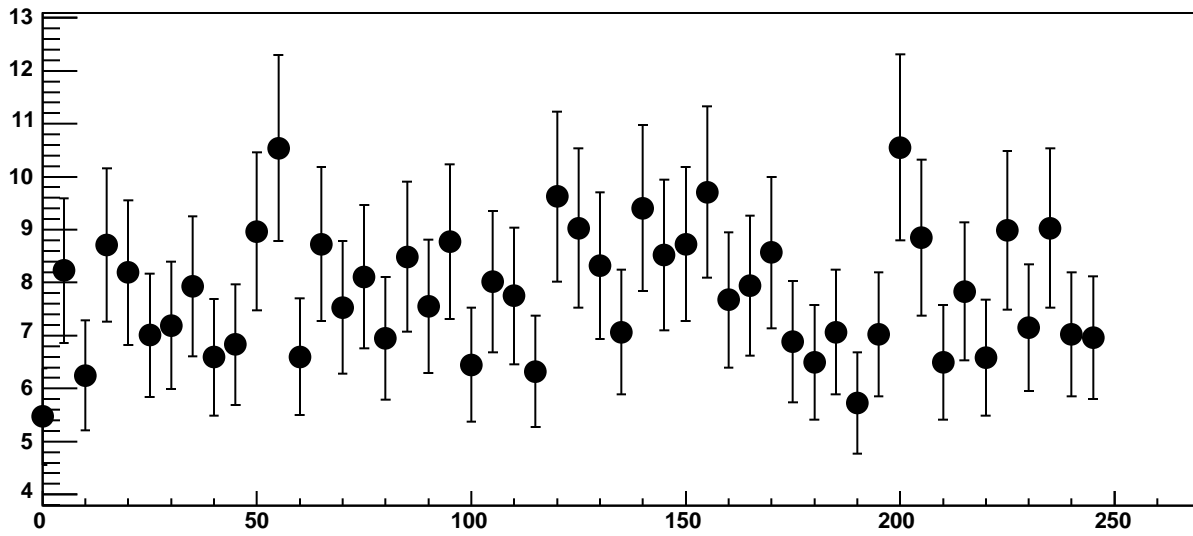


Chip 0, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

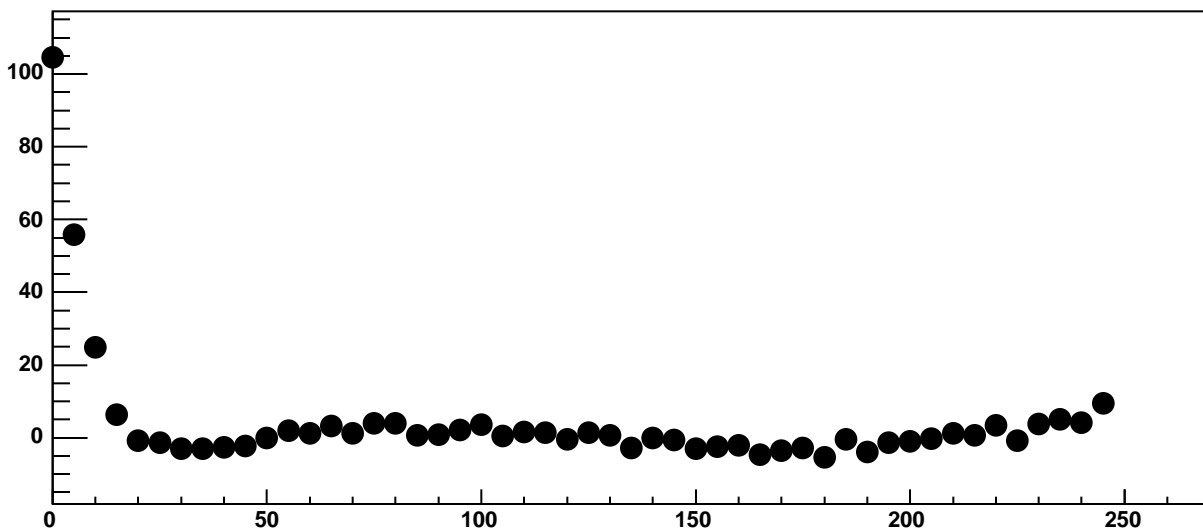


$\chi^2 / \text{ndf}$	115.6 / 41
p0	$452.5 \pm 3.292$
p1	$-3.032 \pm 0.6493$
p2	$257.2 \pm 3.371$
p3	$27.8 \pm 0.6972$
p4	$-0.1982 \pm 0.008396$

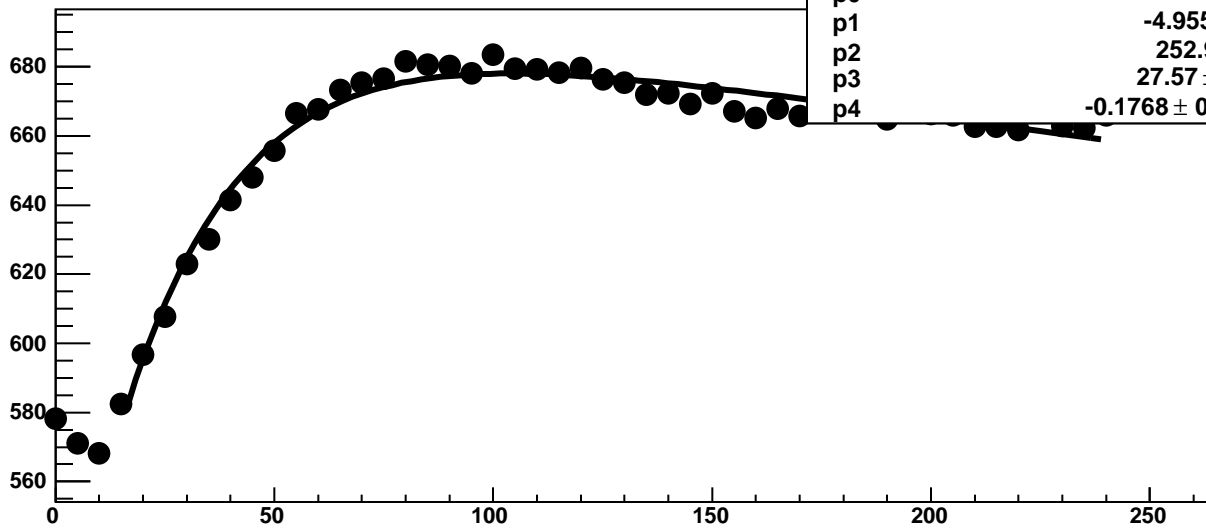
Chip 0, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

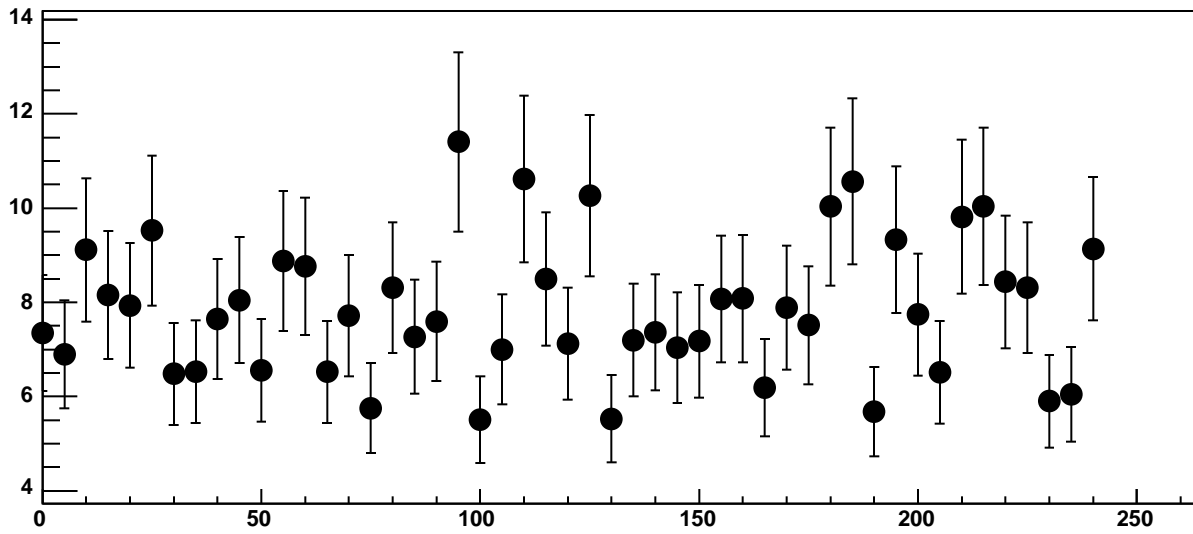


Chip 0, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold

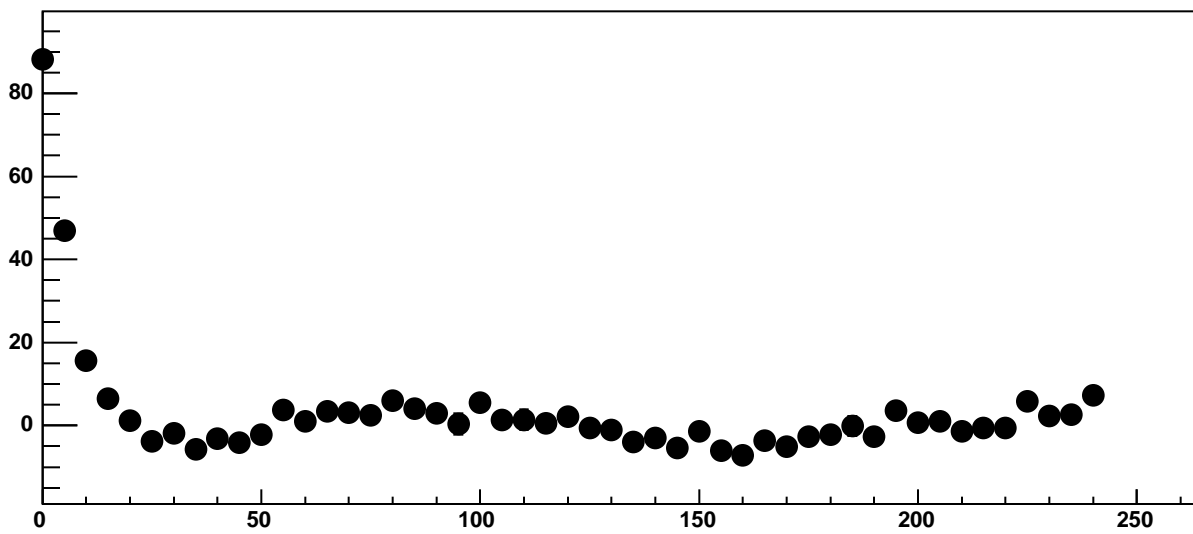


$\chi^2 / \text{ndf}$	198.2 / 41
p0	$449.3 \pm 3.192$
p1	$-4.955 \pm 0.691$
p2	$252.9 \pm 3.26$
p3	$27.57 \pm 0.6979$
p4	$-0.1768 \pm 0.007906$

Chip 0, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold

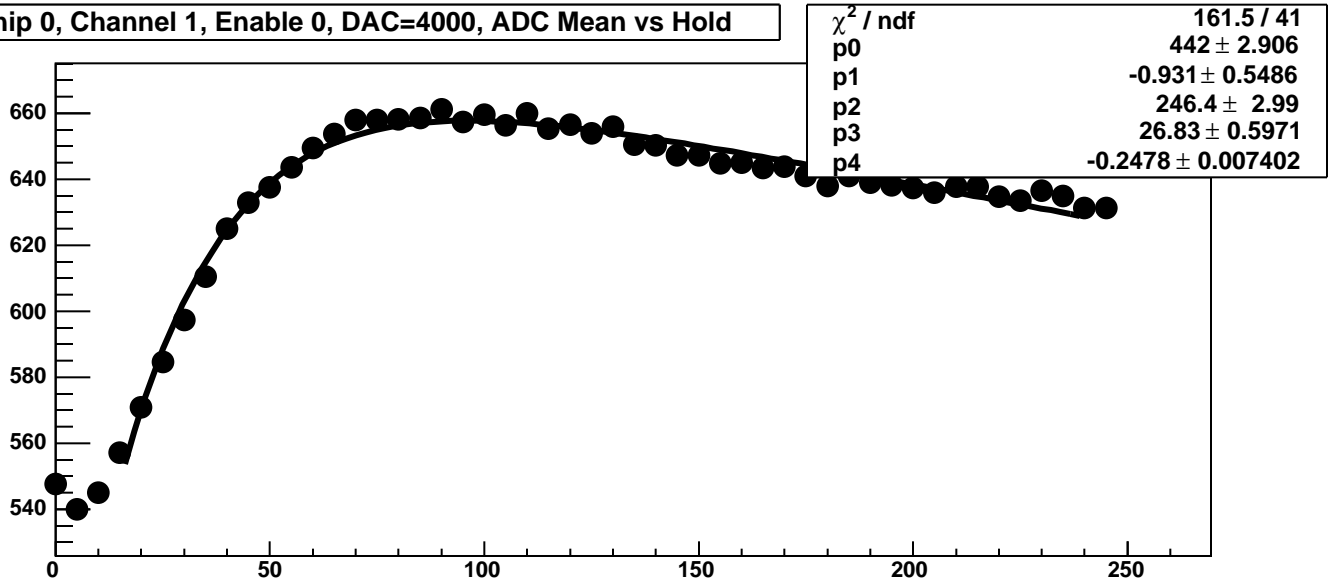


Chip 0, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold

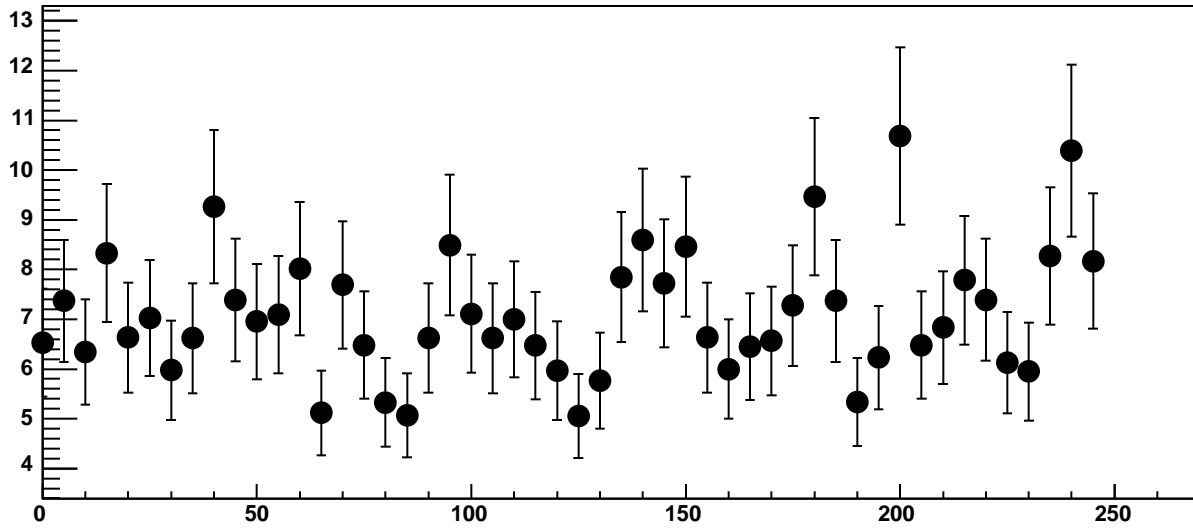




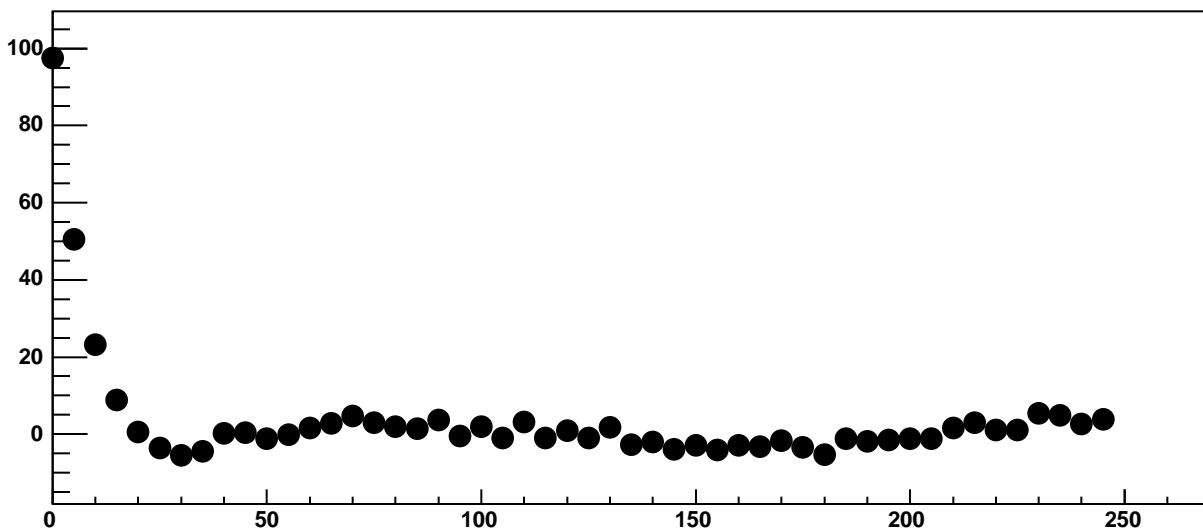
Chip 0, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold



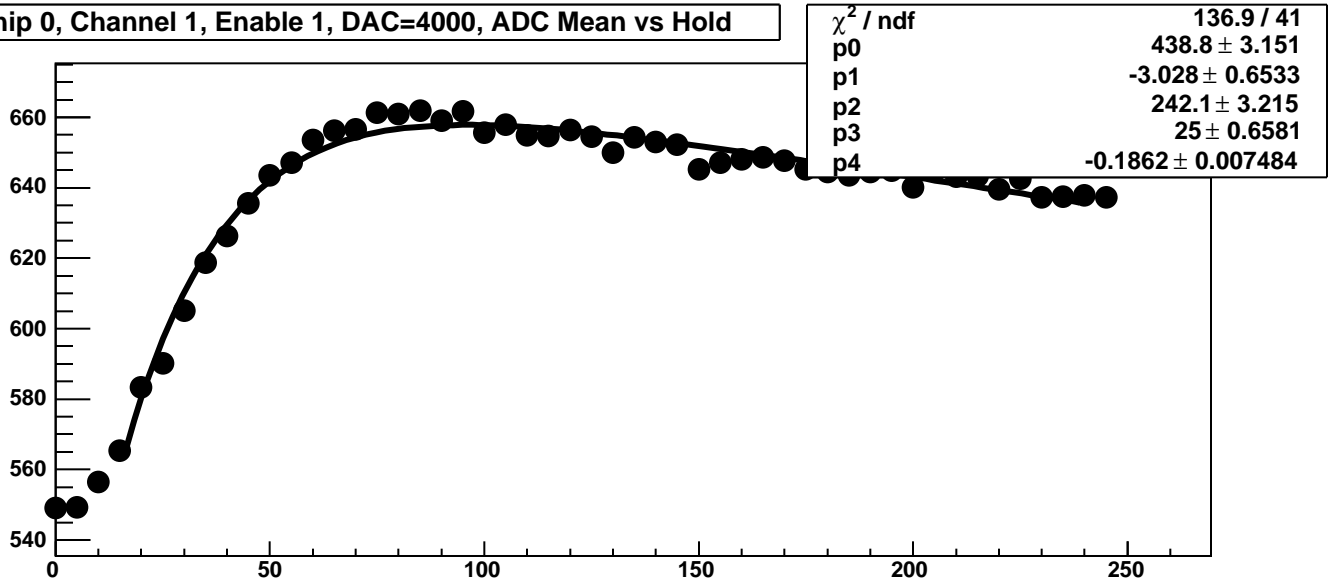
Chip 0, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold



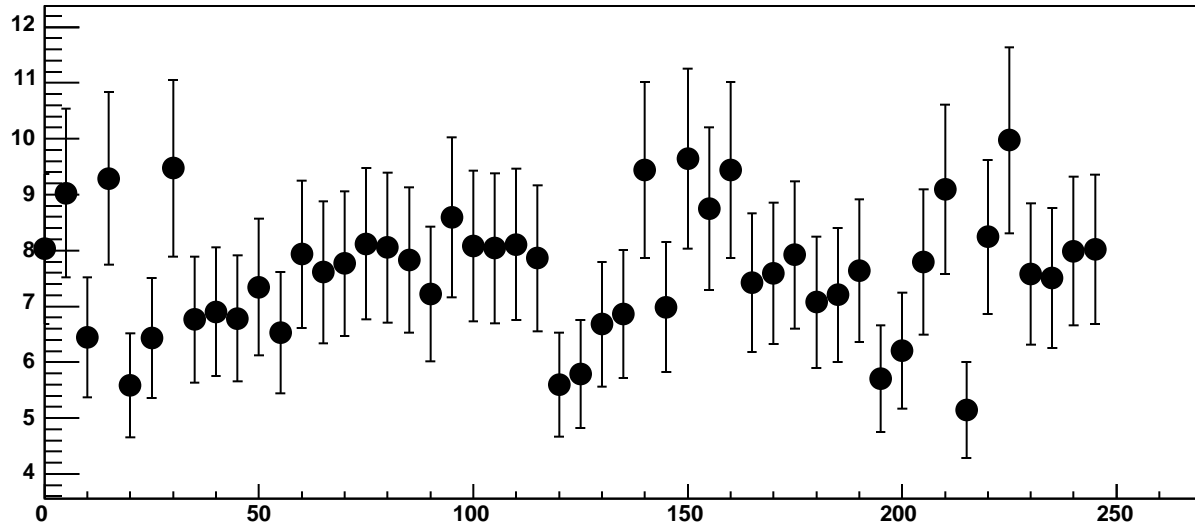
Chip 0, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold



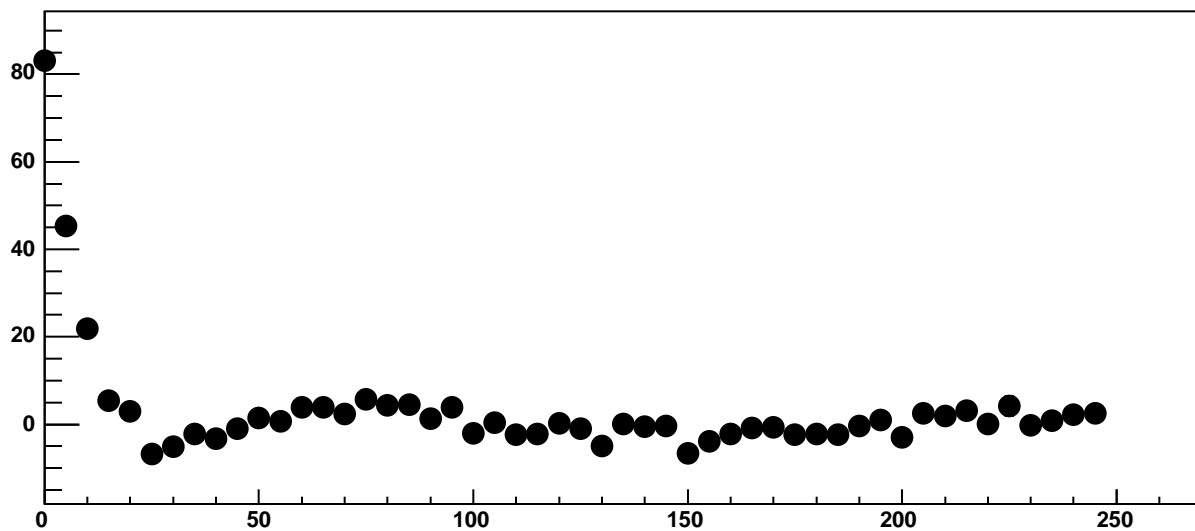
Chip 0, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold



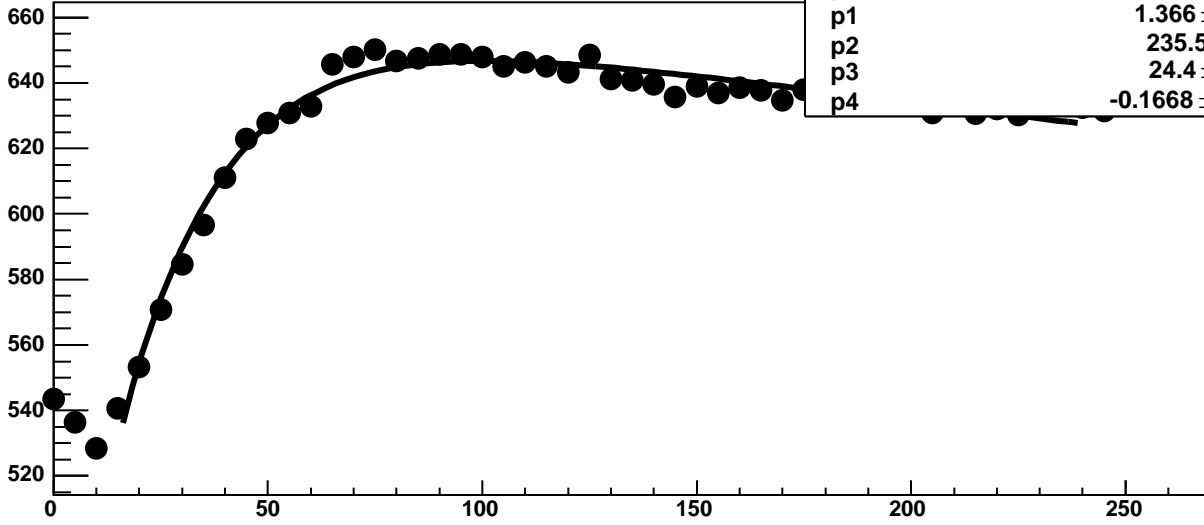
Chip 0, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

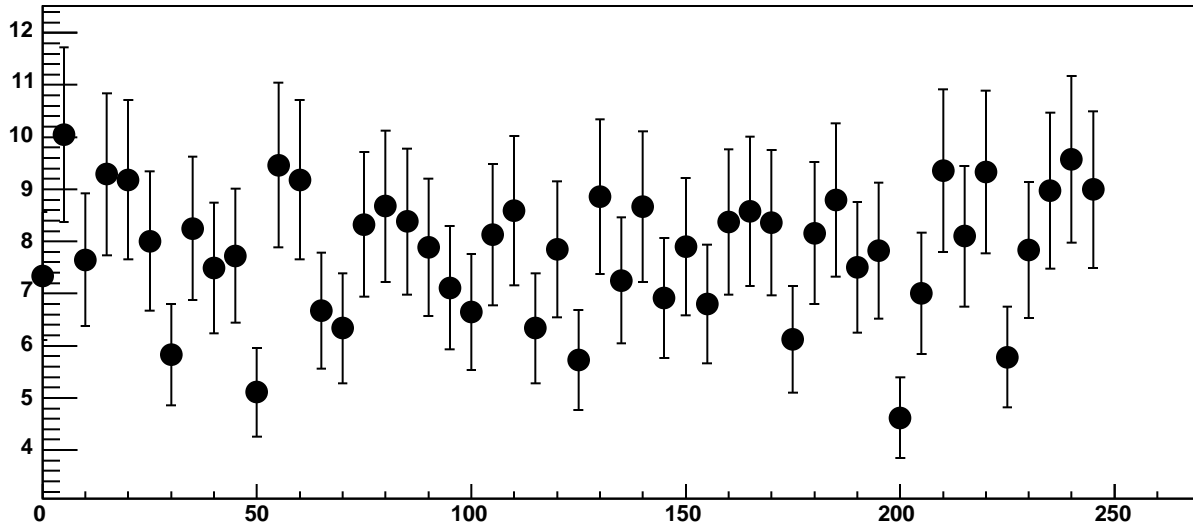


Chip 0, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

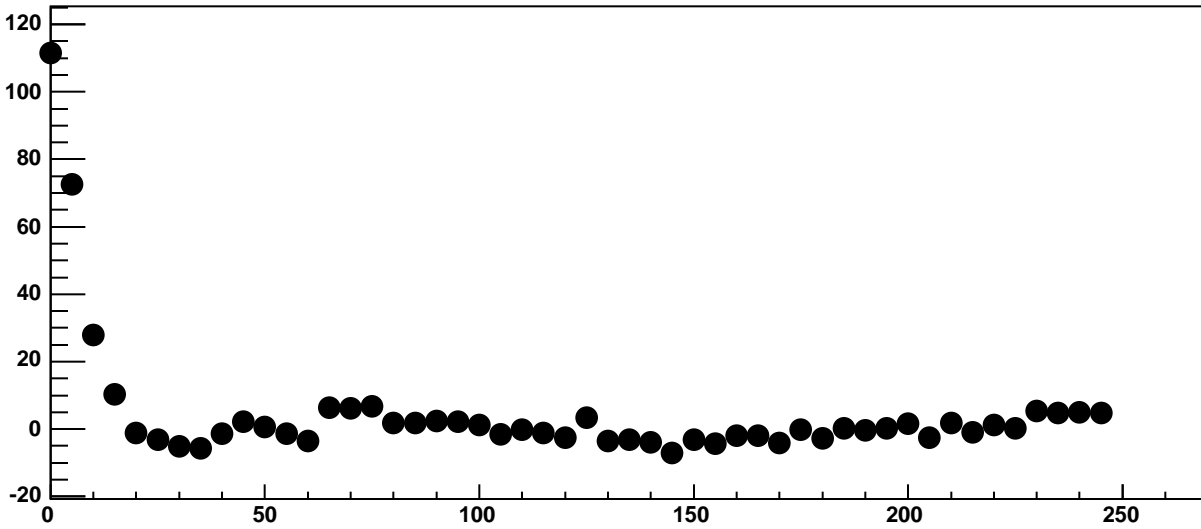


$\chi^2 / \text{ndf}$	194.9 / 41
p0	$431.8 \pm 3.177$
p1	$1.366 \pm 0.5638$
p2	$235.5 \pm 3.238$
p3	$24.4 \pm 0.5723$
p4	$-0.1668 \pm 0.0075$

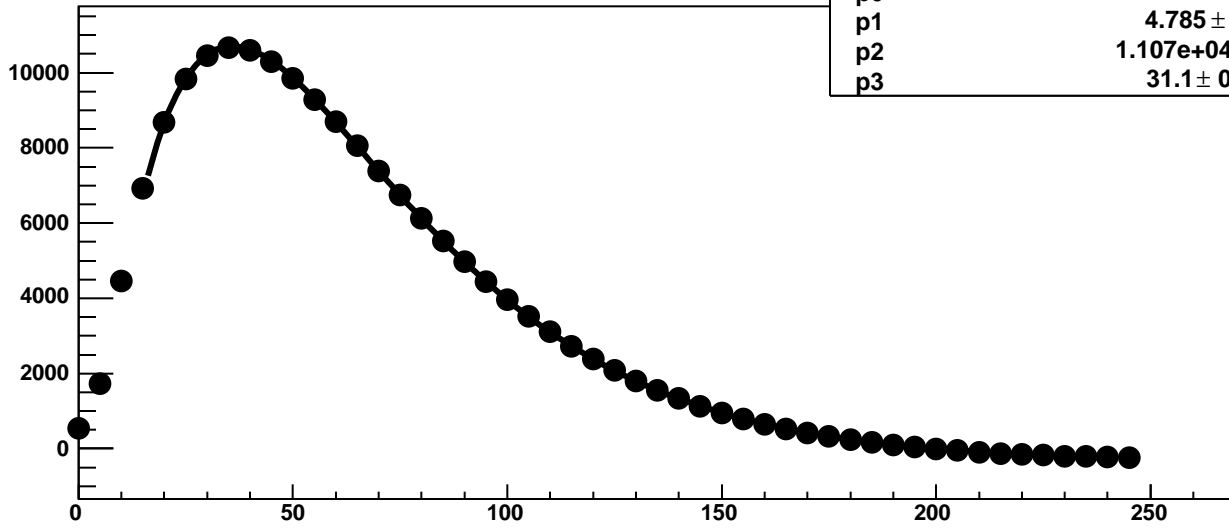
Chip 0, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

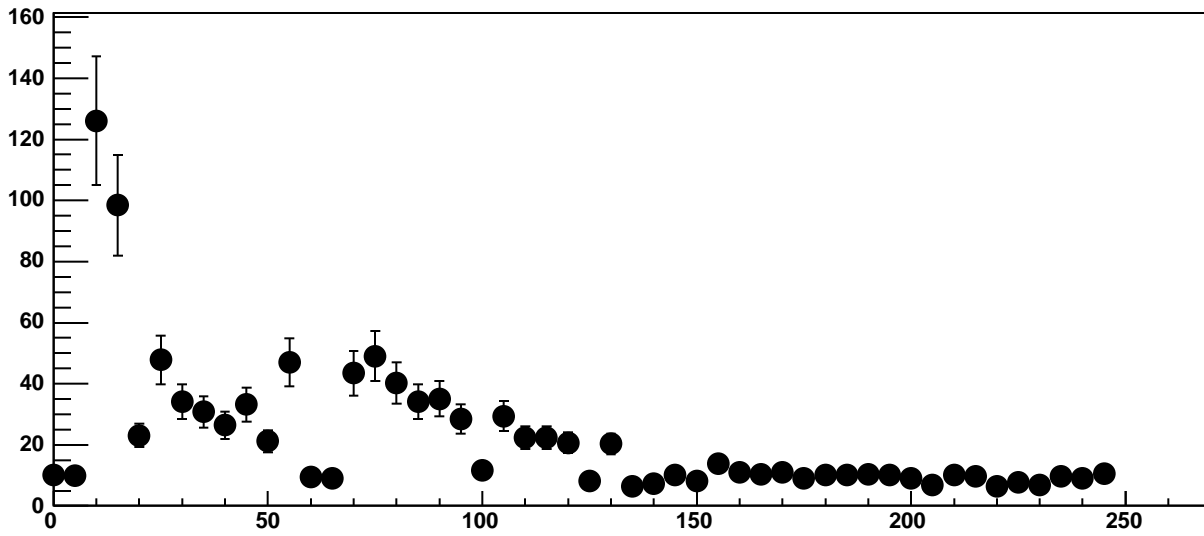


Chip 0, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold

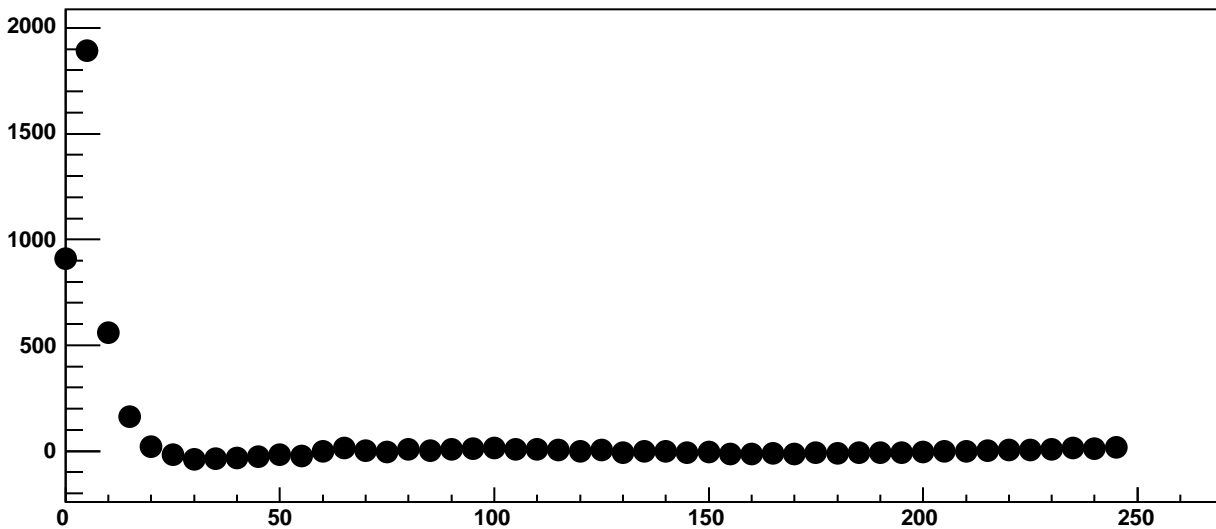


$\chi^2 / \text{ndf}$	597.8 / 42
p0	$-360.9 \pm 0.7371$
p1	$4.785 \pm 0.01586$
p2	$1.107\text{e}+04 \pm 1.955$
p3	$31.1 \pm 0.008516$

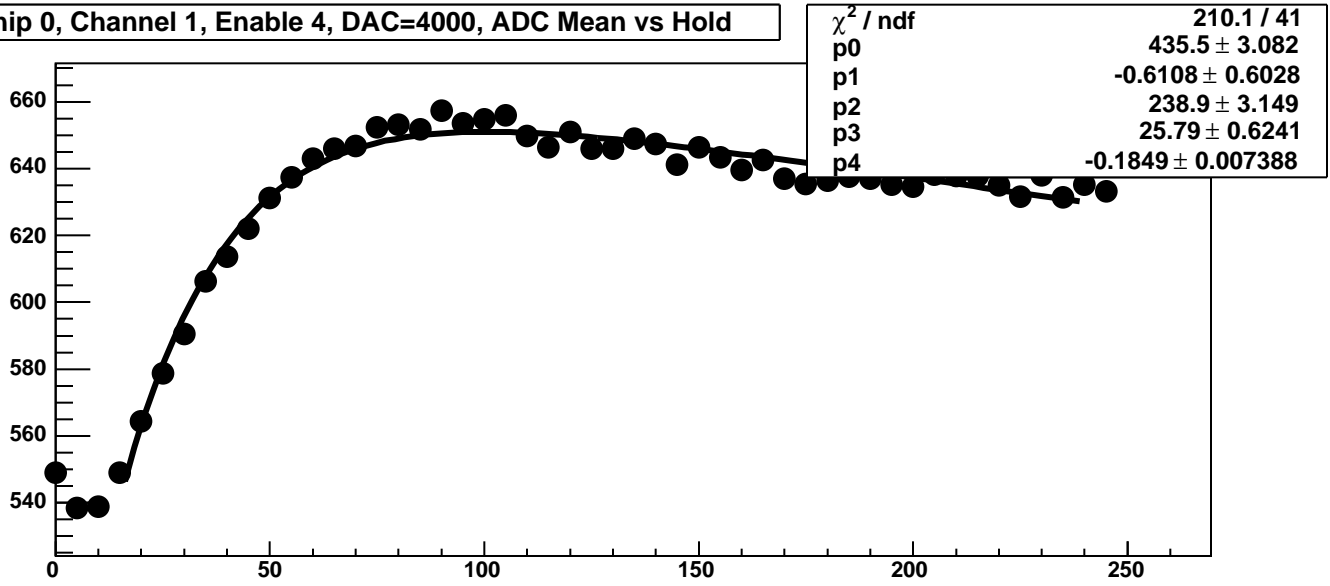
Chip 0, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold



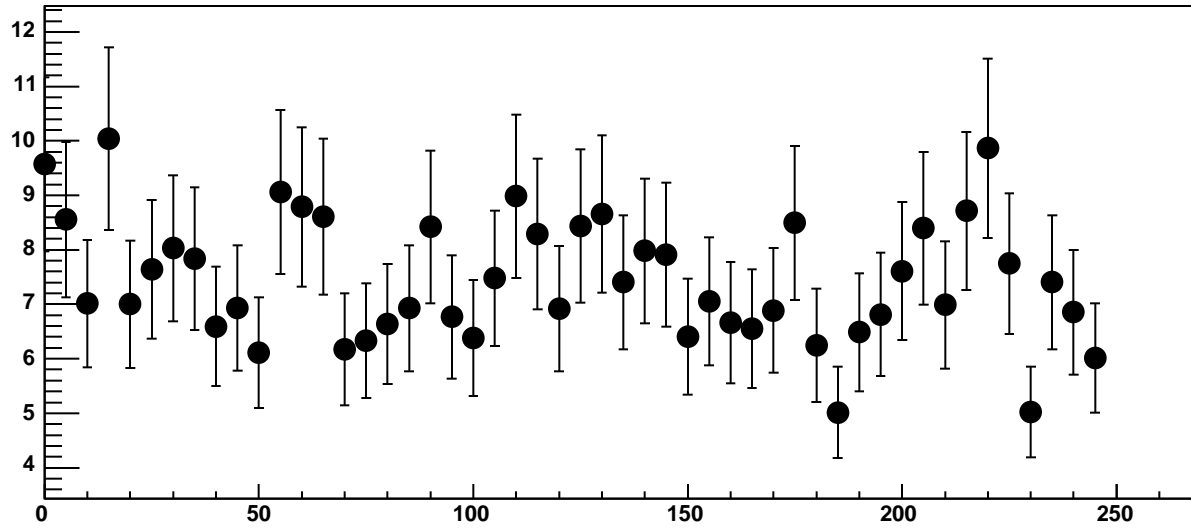
Chip 0, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold



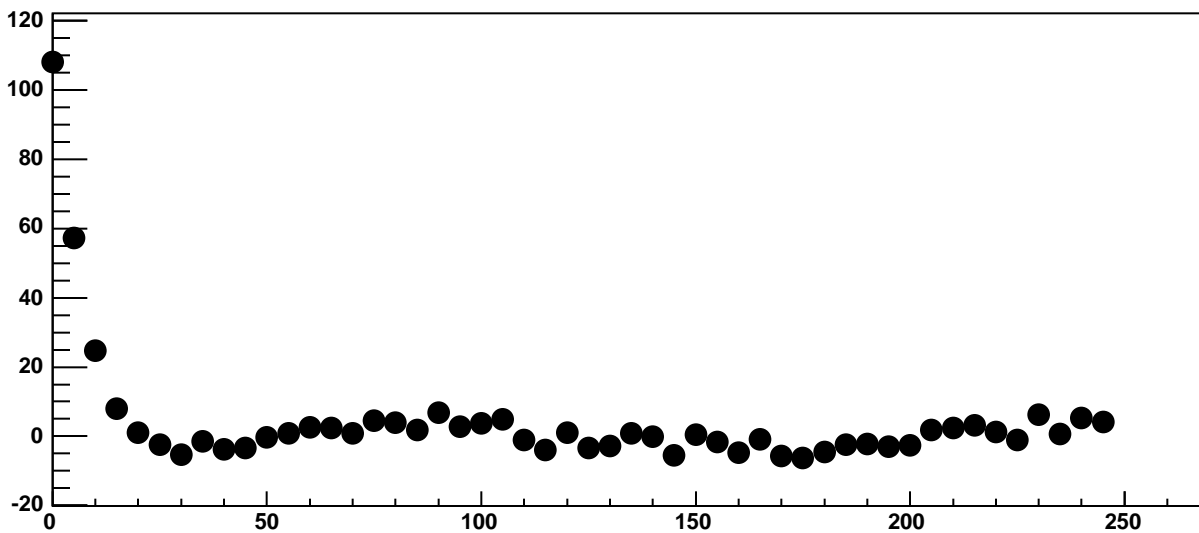
Chip 0, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold



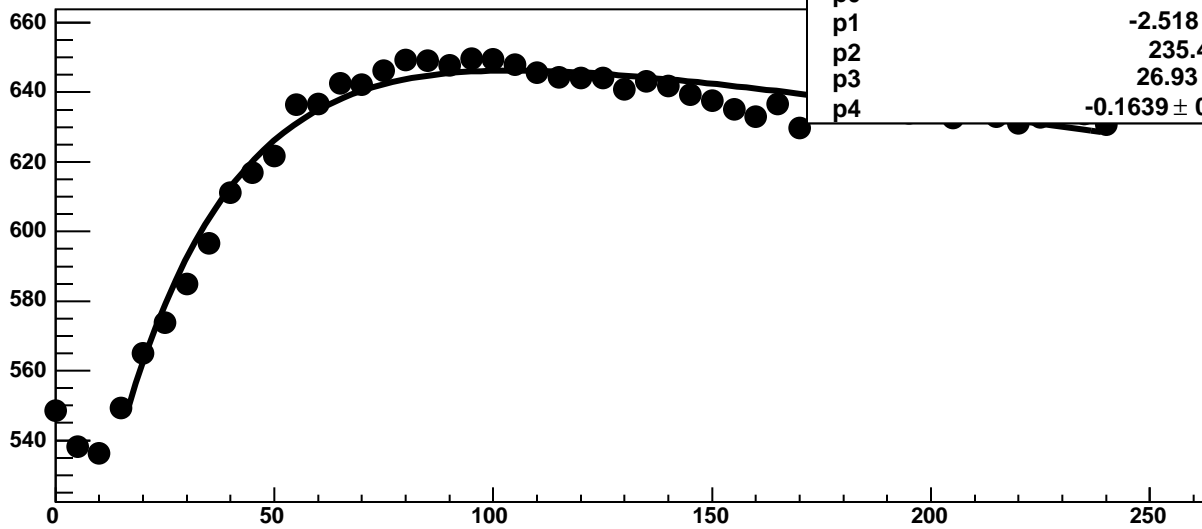
Chip 0, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

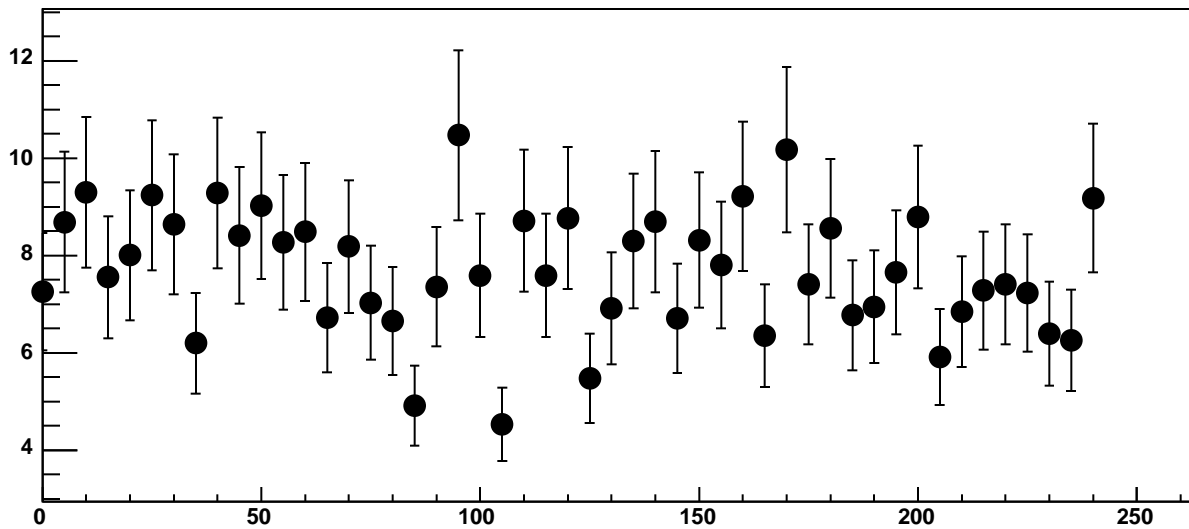


Chip 0, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

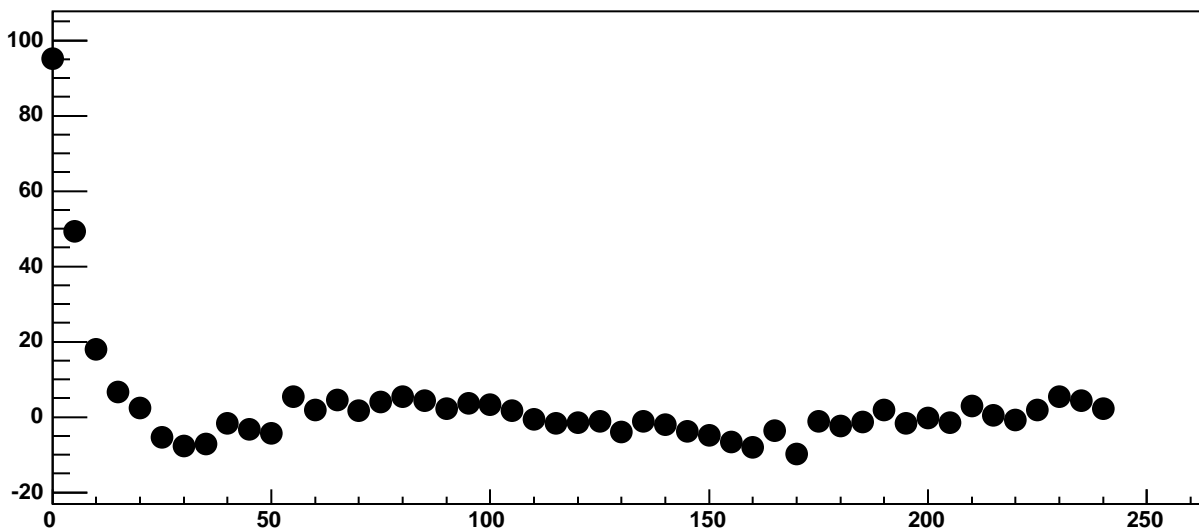


$\chi^2 / \text{ndf}$	244.5 / 41
p0	$432.9 \pm 3.123$
p1	$-2.518 \pm 0.6407$
p2	$235.4 \pm 3.186$
p3	$26.93 \pm 0.6772$
p4	$-0.1639 \pm 0.007443$

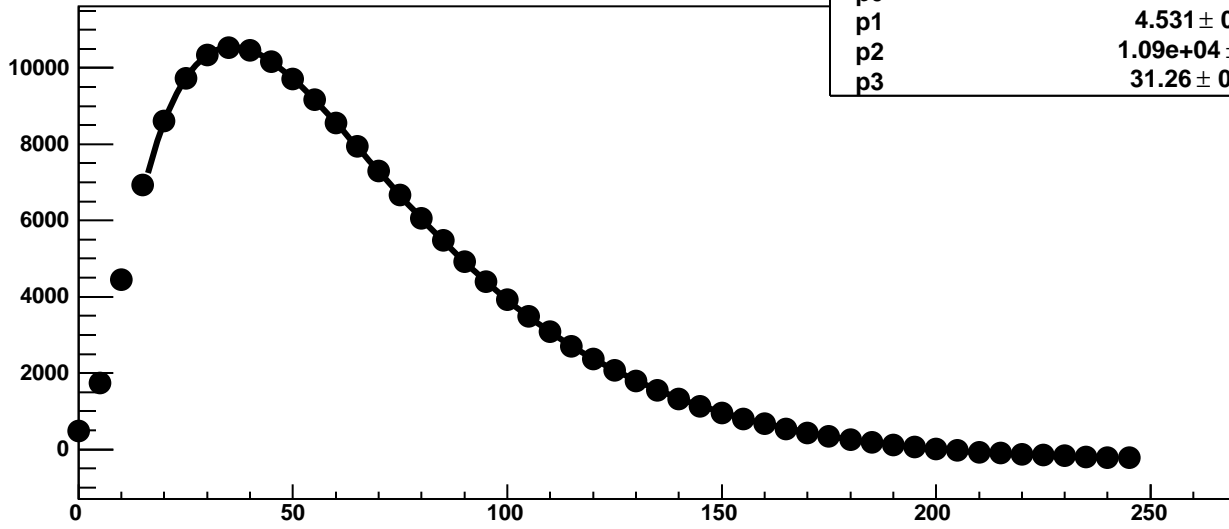
Chip 0, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold

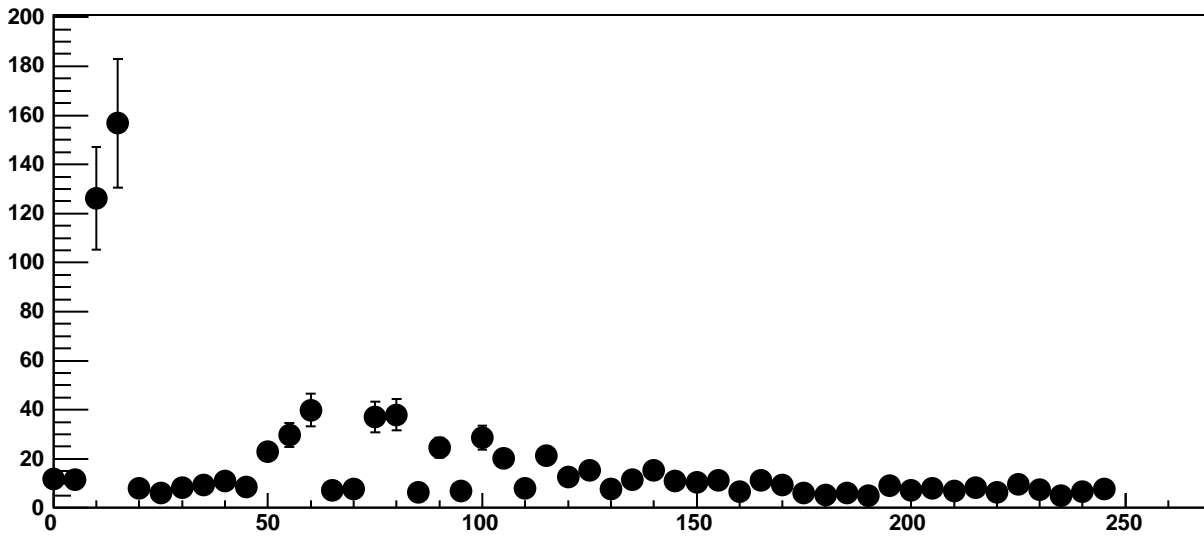


Chip 0, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold

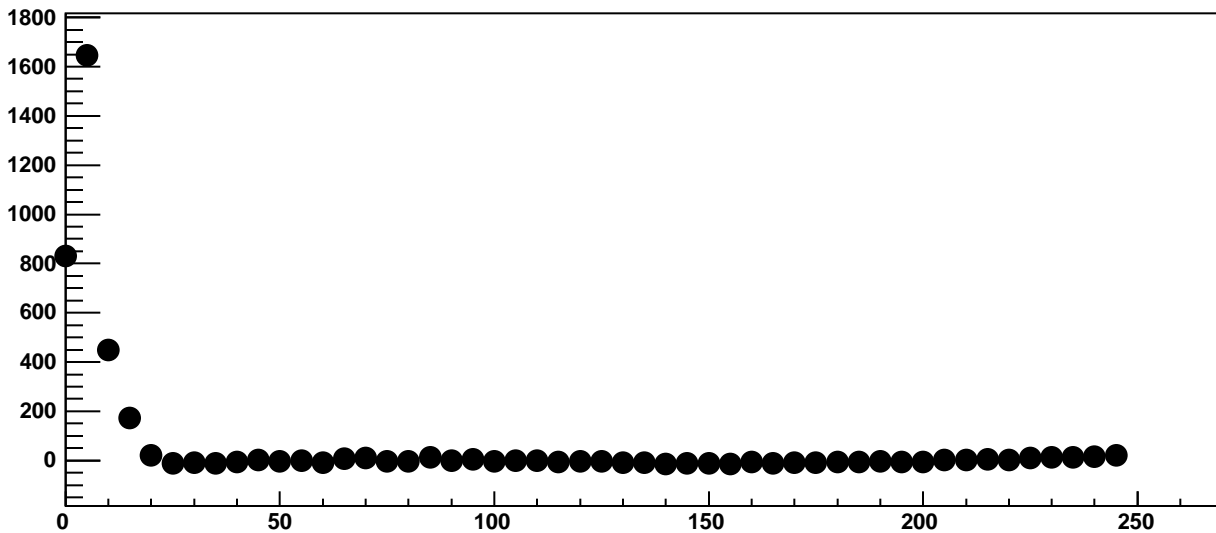


$\chi^2 / \text{ndf}$	1091 / 42
p0	$-343.5 \pm 0.539$
p1	$4.531 \pm 0.008112$
p2	$1.09\text{e}+04 \pm 0.8852$
p3	$31.26 \pm 0.005697$

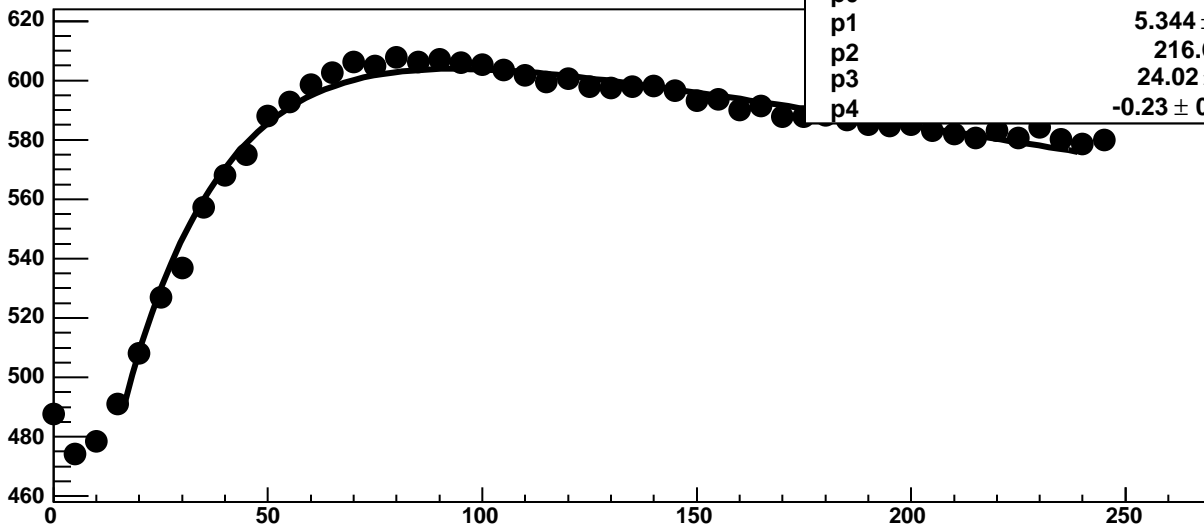
Chip 0, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold

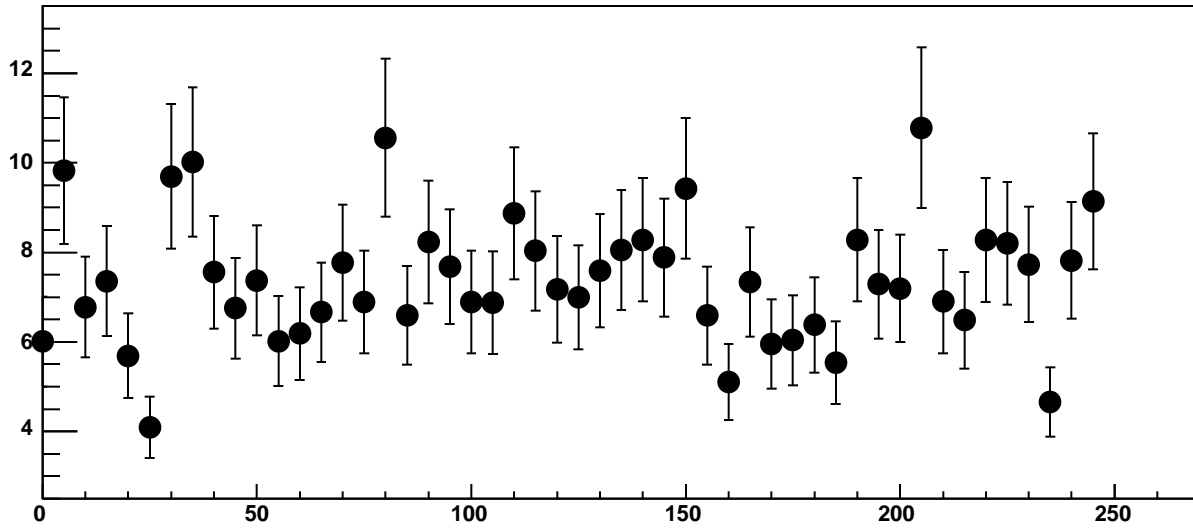


Chip 0, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

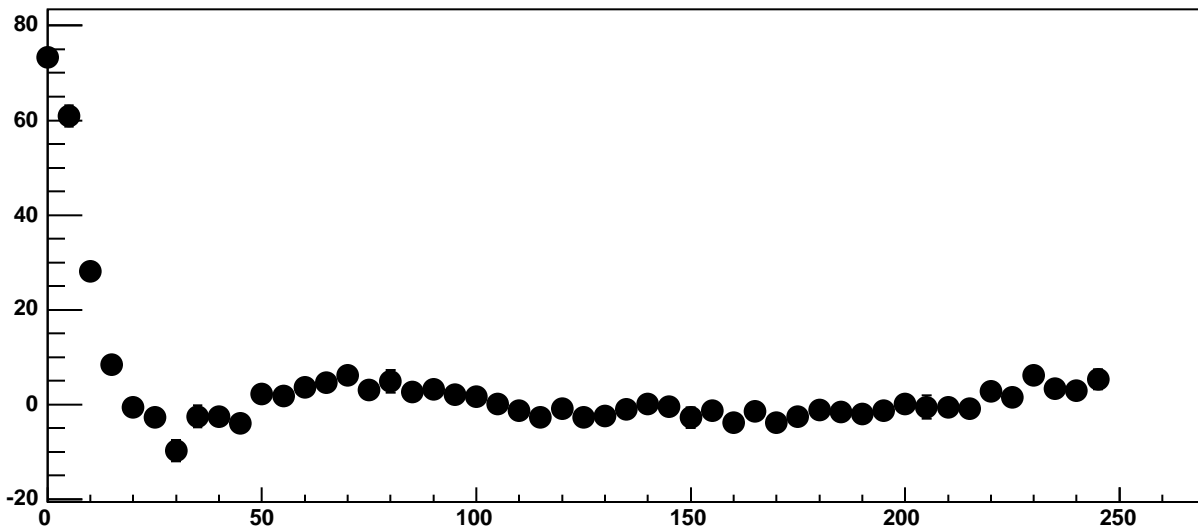


$\chi^2 / \text{ndf}$	175.7 / 41
p0	$413.1 \pm 2.994$
p1	$5.344 \pm 0.4403$
p2	$216.6 \pm 3.08$
p3	$24.02 \pm 0.4911$
p4	$-0.23 \pm 0.006952$

Chip 0, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold

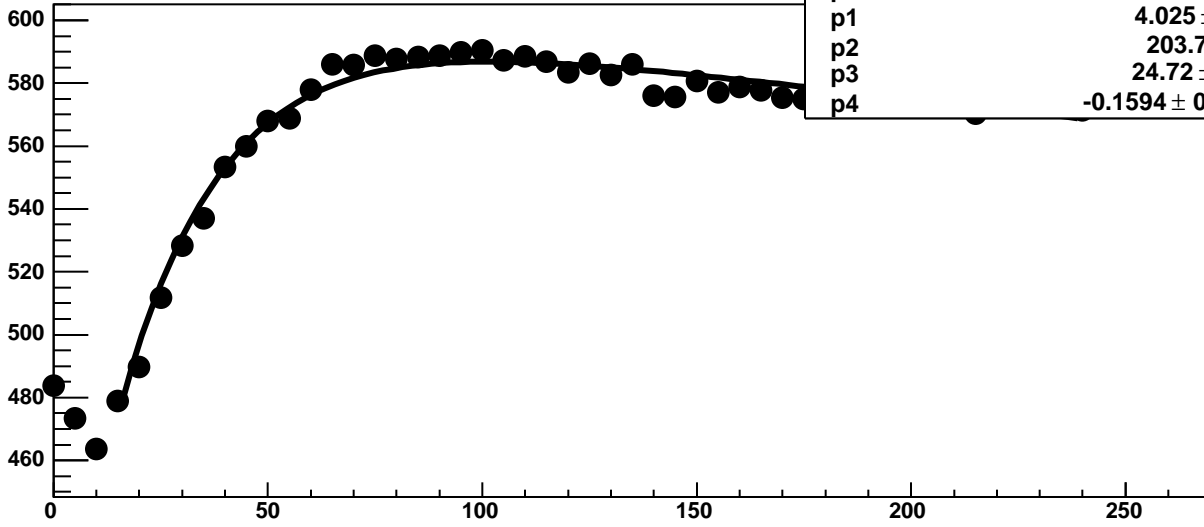


Chip 0, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold



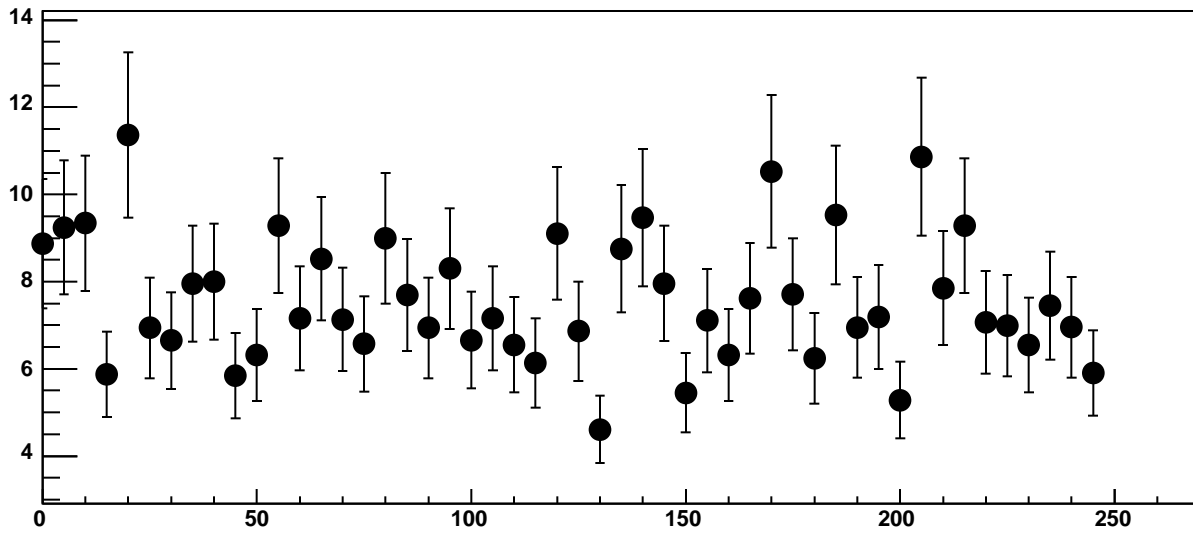


Chip 0, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

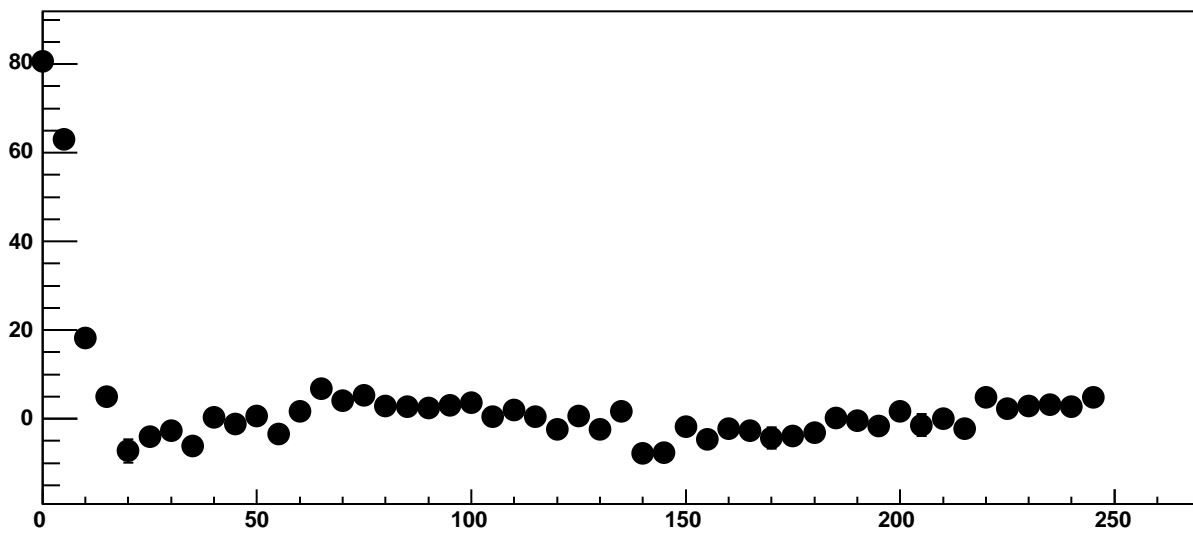


$\chi^2 / \text{ndf}$	185.2 / 41
p0	$402.6 \pm 3.066$
p1	$4.025 \pm 0.5008$
p2	$203.7 \pm 3.134$
p3	$24.72 \pm 0.5385$
p4	$-0.1594 \pm 0.007109$

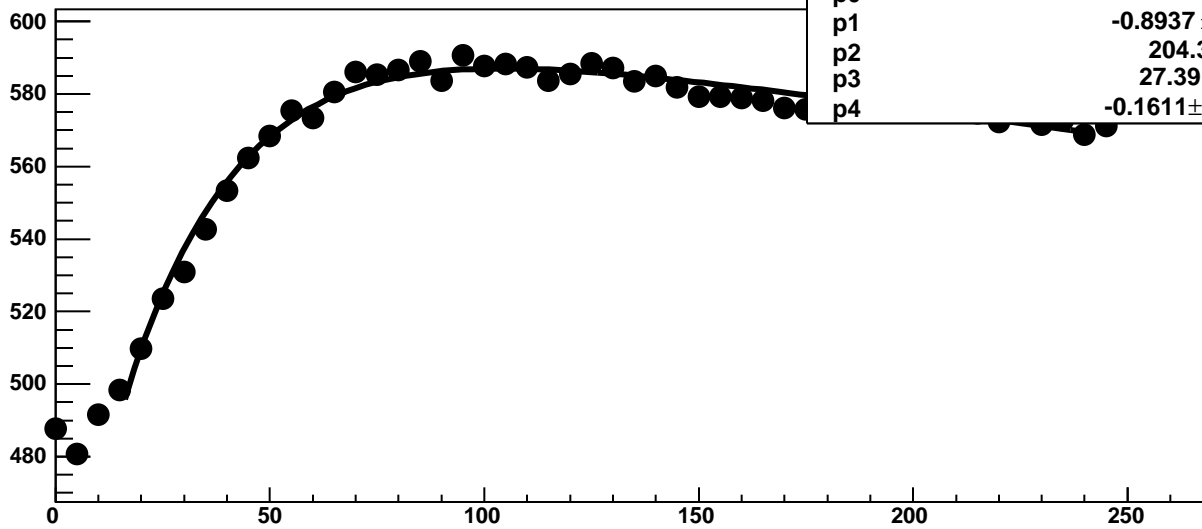
Chip 0, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

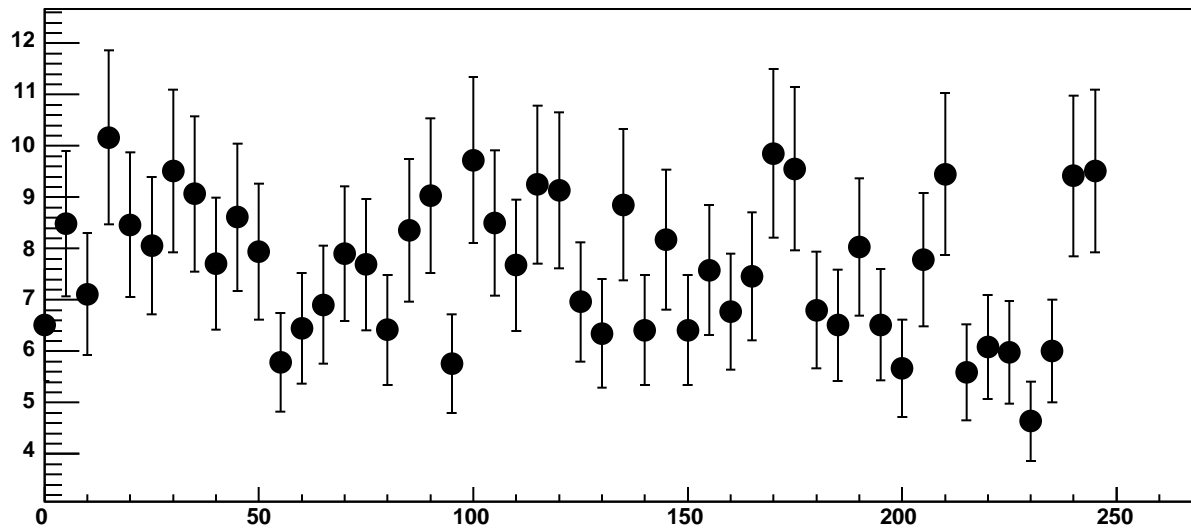


Chip 0, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold

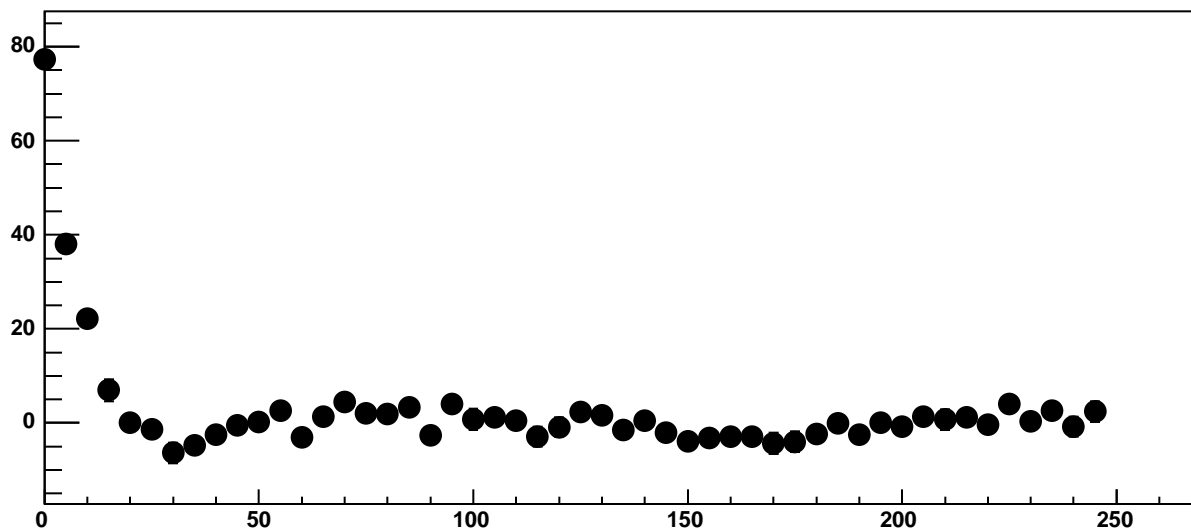


$\chi^2 / \text{ndf}$	106.9 / 41
p0	$404.1 \pm 3.119$
p1	$-0.8937 \pm 0.7524$
p2	$204.3 \pm 3.192$
p3	$27.39 \pm 0.8081$
p4	$-0.1611 \pm 0.00768$

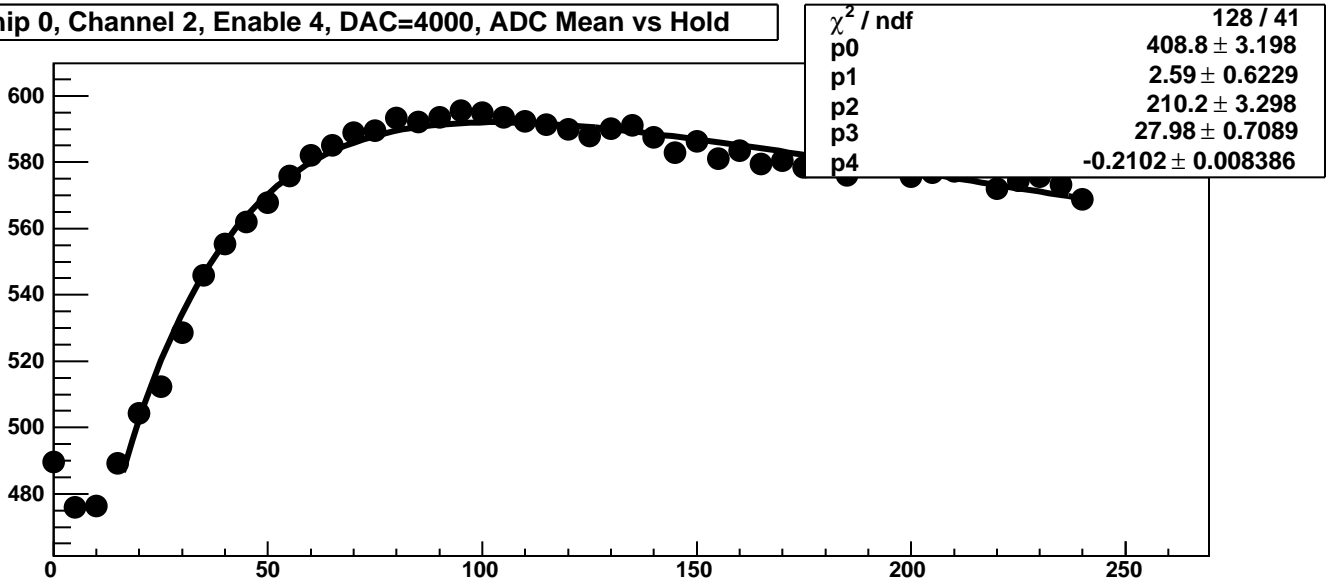
Chip 0, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold



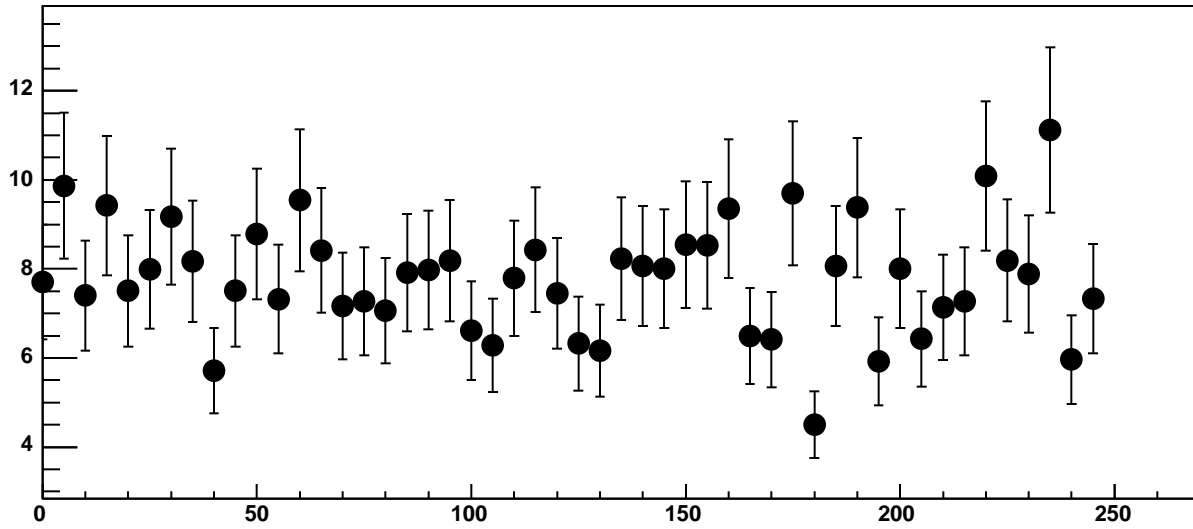
Chip 0, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold



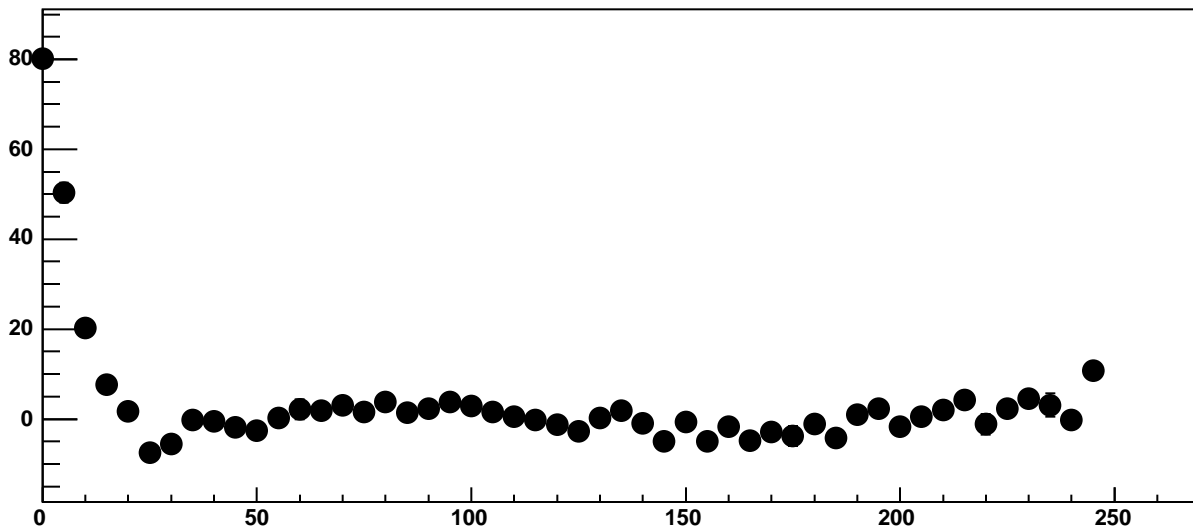
Chip 0, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold



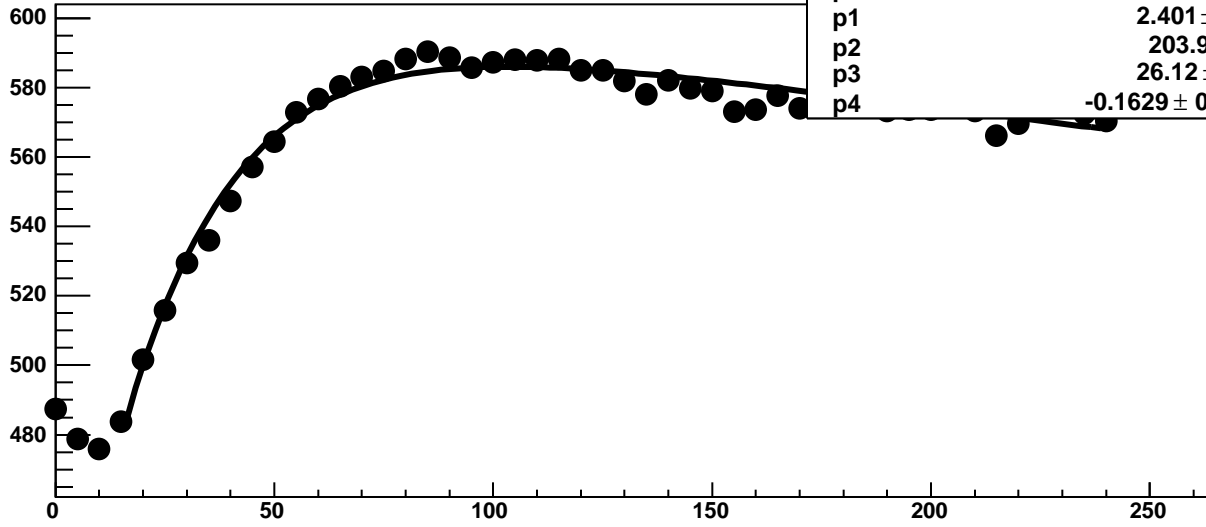
Chip 0, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

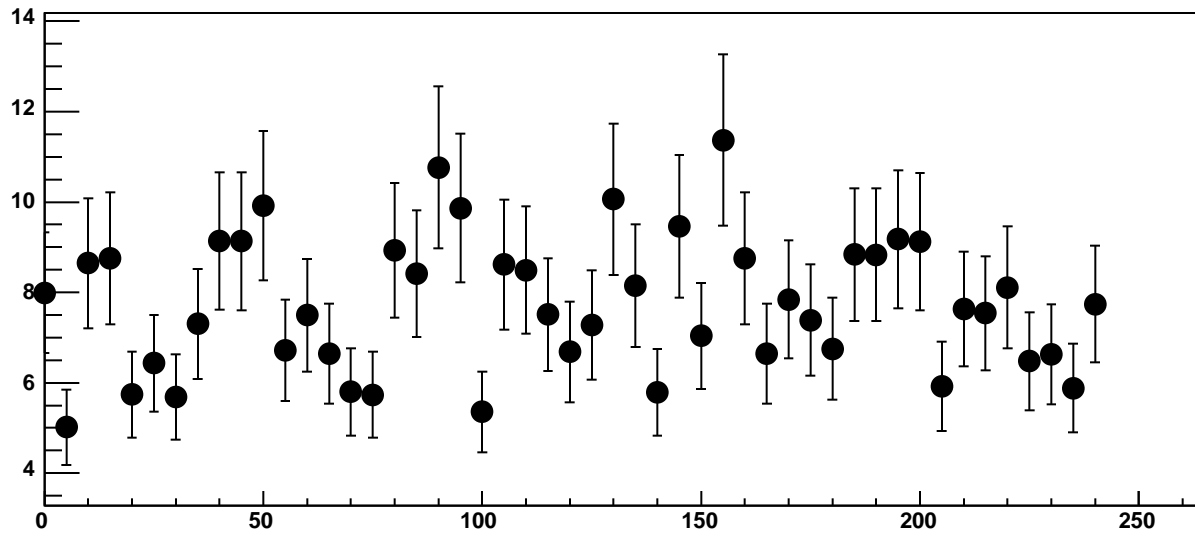


Chip 0, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

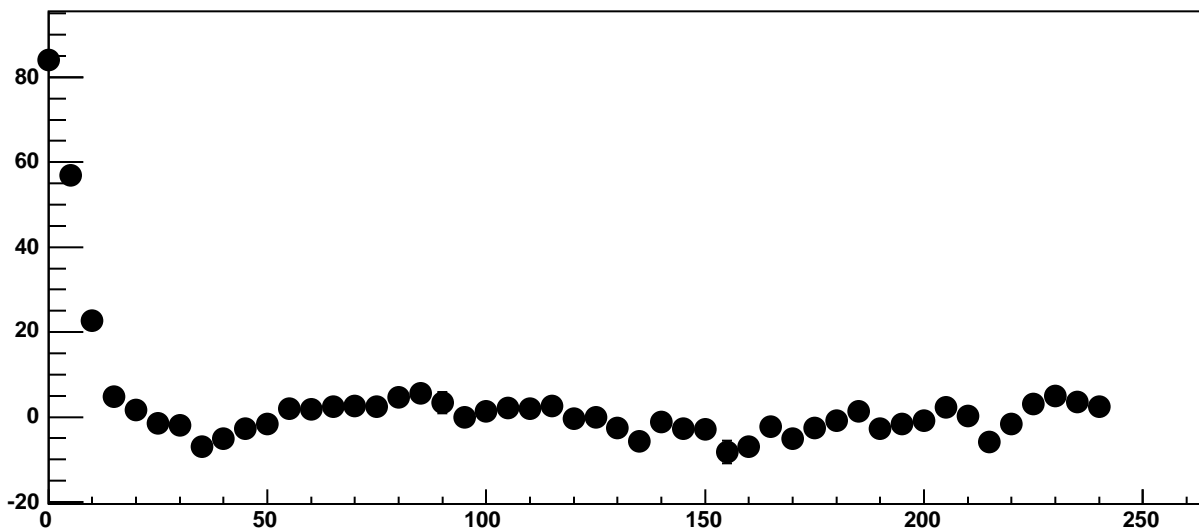


$\chi^2 / \text{ndf}$	163 / 41
p0	$402.9 \pm 3.163$
p1	$2.401 \pm 0.5886$
p2	$203.9 \pm 3.239$
p3	$26.12 \pm 0.6673$
p4	$-0.1629 \pm 0.007595$

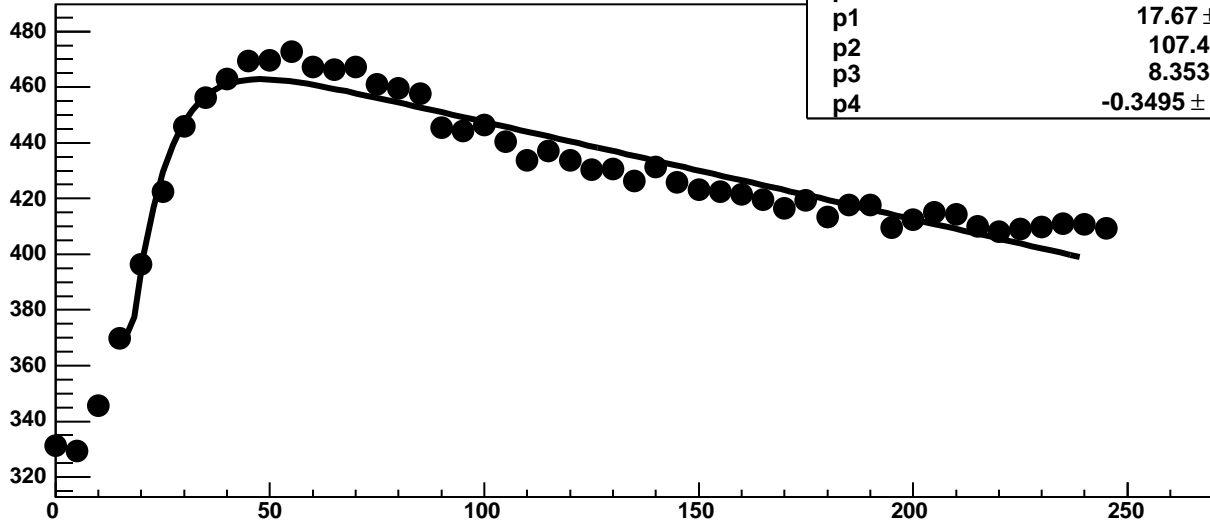
Chip 0, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

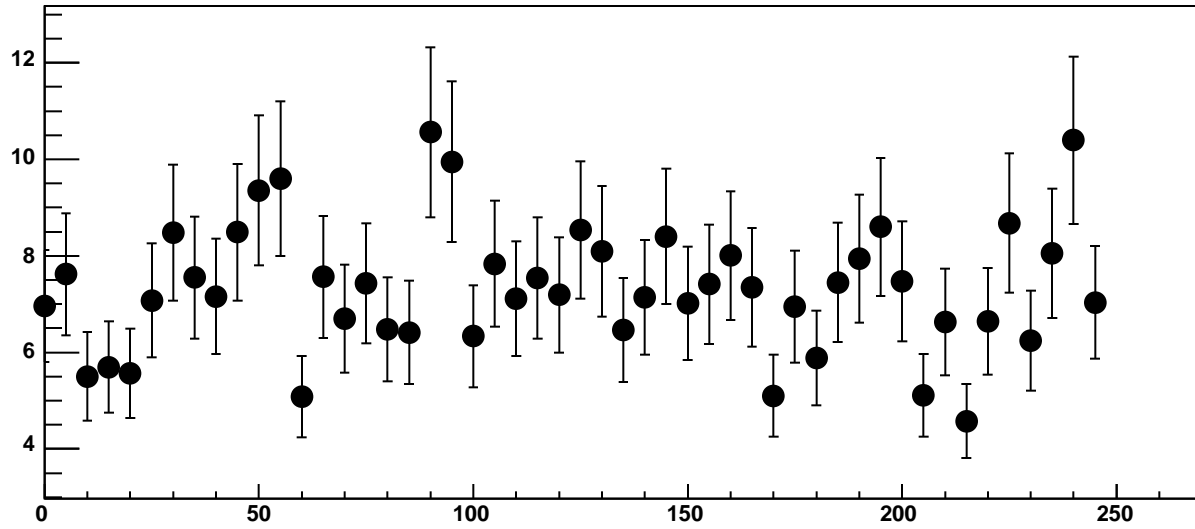


Chip 0, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold

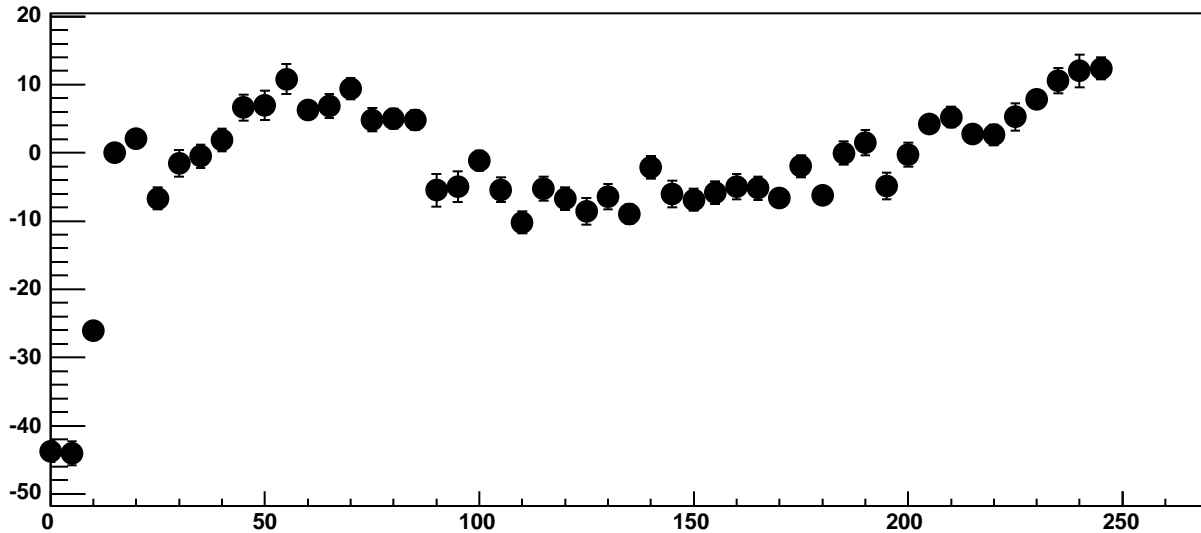


$\chi^2 / \text{ndf}$	581.7 / 41
p0	$369 \pm 1.272$
p1	$17.67 \pm 0.1957$
p2	$107.4 \pm 1.468$
p3	$8.353 \pm 0.295$
p4	$-0.3495 \pm 0.00461$

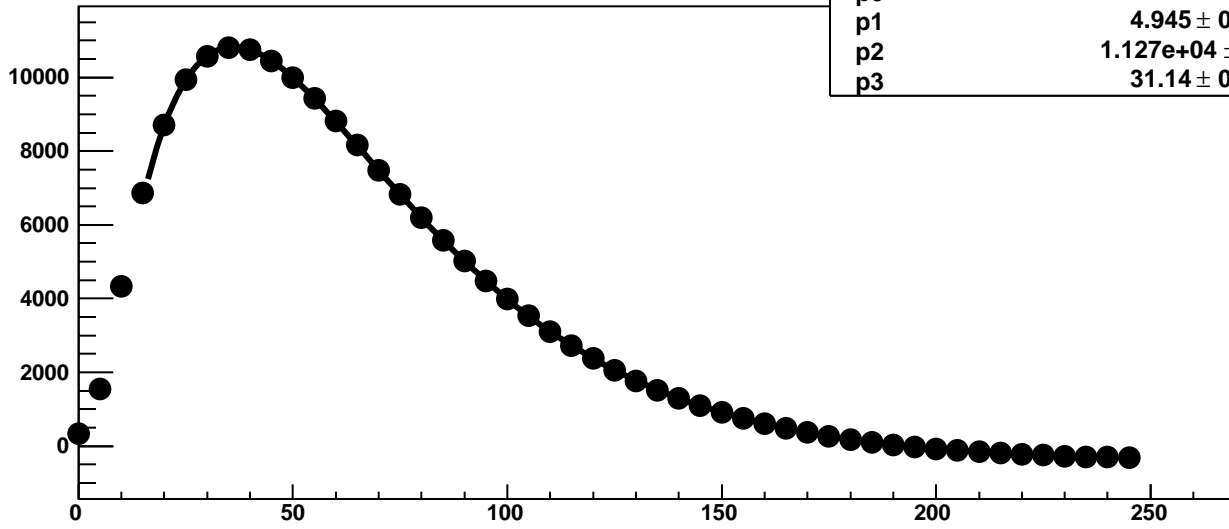
Chip 0, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

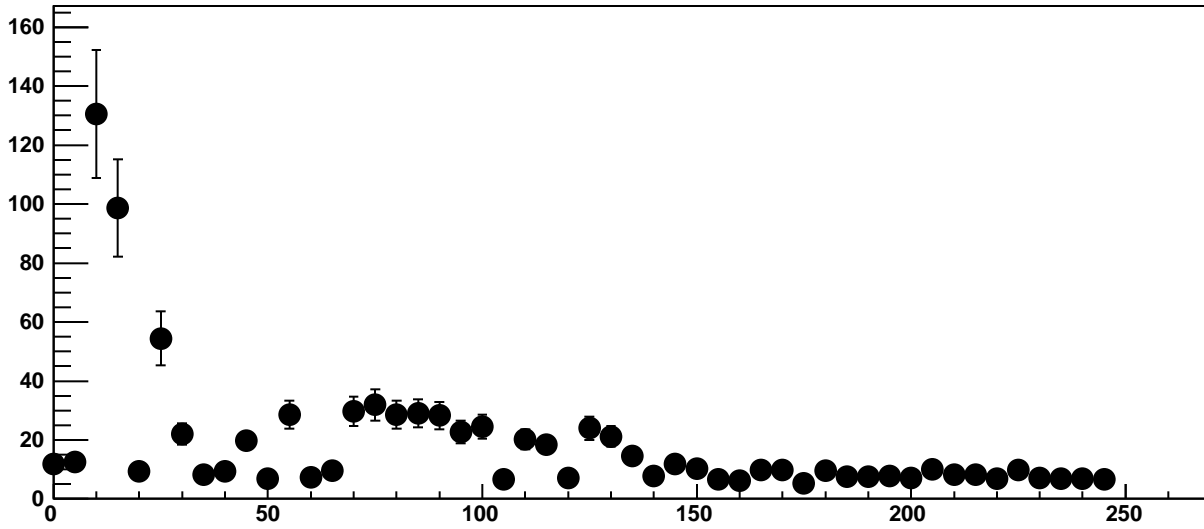


Chip 0, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold



$\chi^2 / \text{ndf}$	420.3 / 42
p0	-437 ± 0.6188
p1	4.945 ± 0.008375
p2	1.127e+04 ± 0.9875
p3	31.14 ± 0.005969

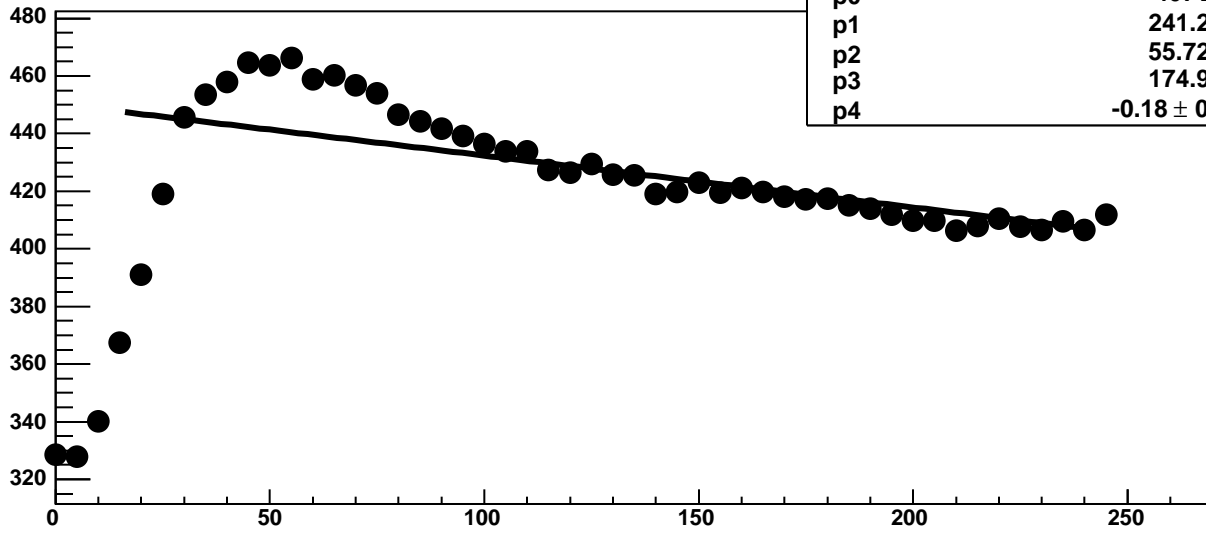
Chip 0, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold

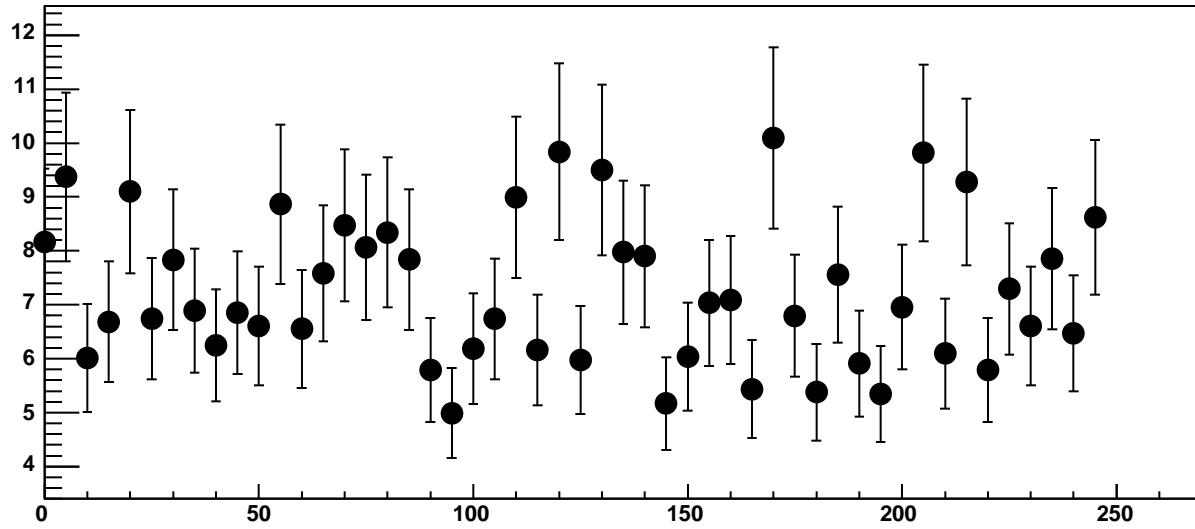


Chip 0, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

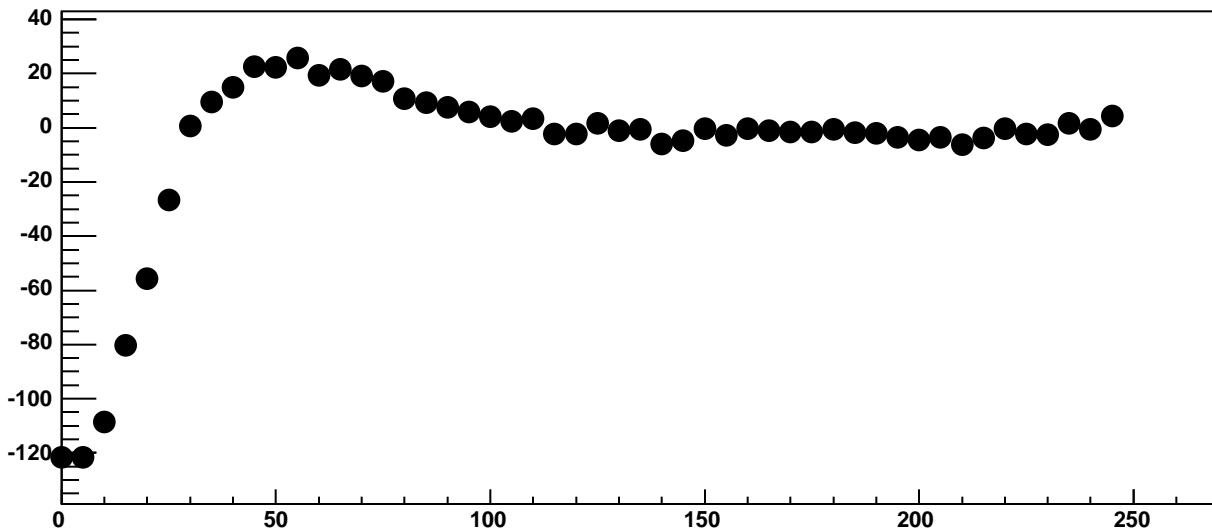


$\chi^2 / \text{ndf}$	5188 / 41
p0	$407 \pm 0.7614$
p1	$241.2 \pm 4.344$
p2	$55.72 \pm 2.579$
p3	$174.9 \pm 1.418$
p4	$-0.18 \pm 0.003548$

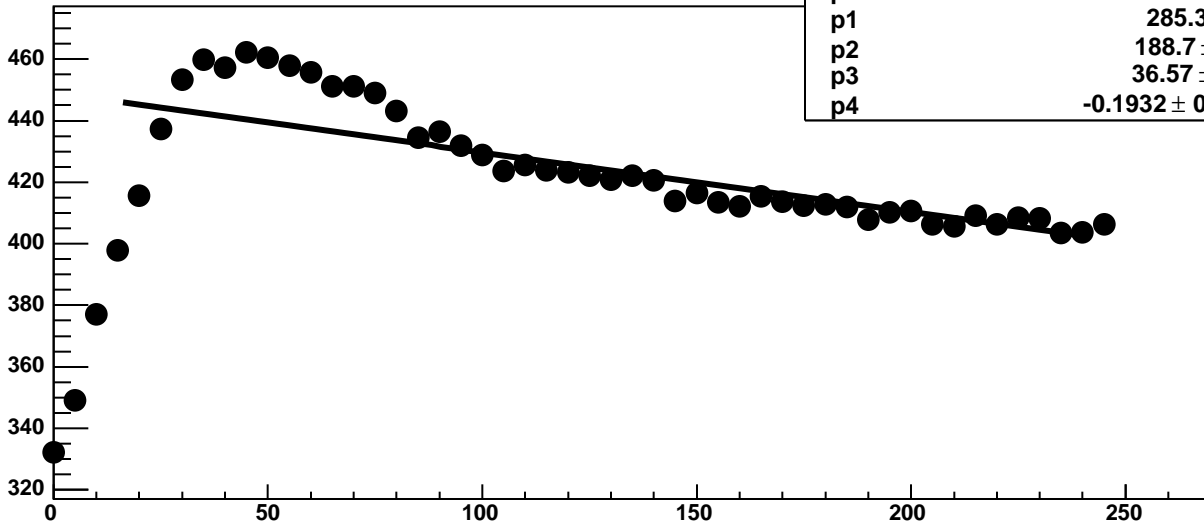
Chip 0, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

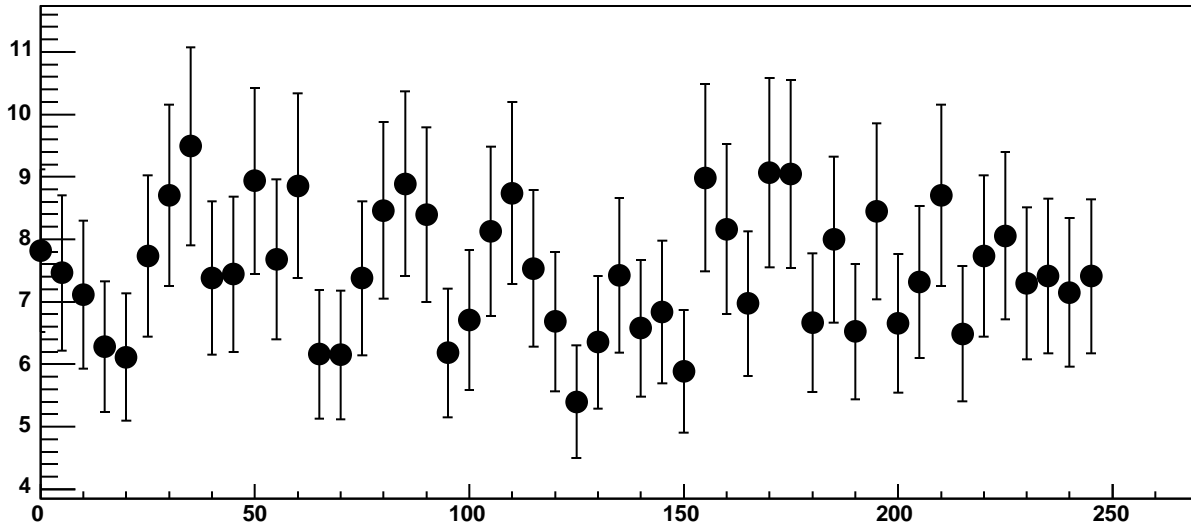


Chip 0, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

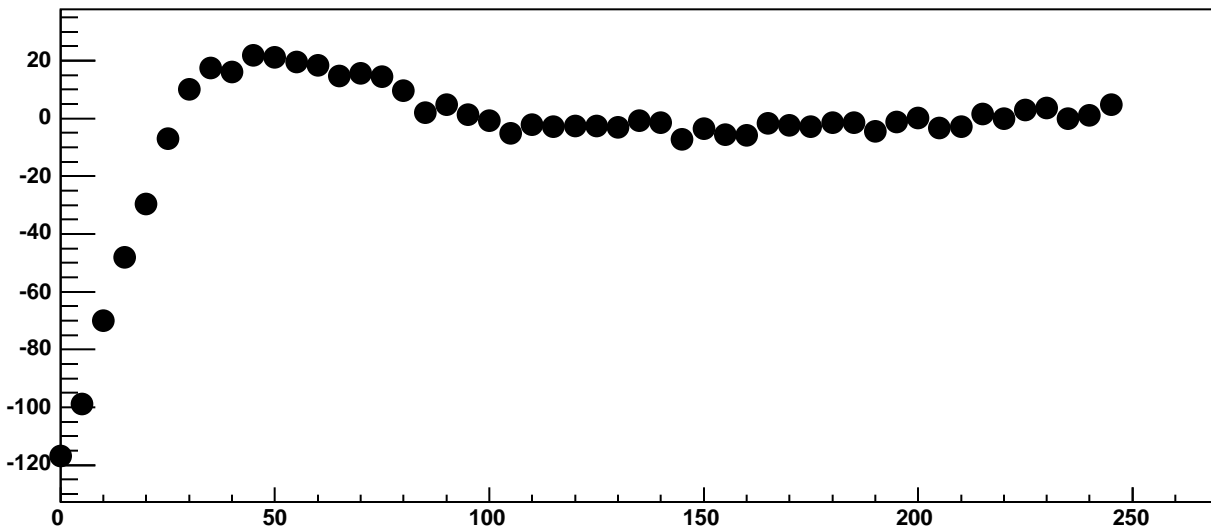


$\chi^2 / \text{ndf}$	2668 / 41
p0	$393.9 \pm 0.3621$
p1	$285.3 \pm 1.705$
p2	$188.7 \pm 0.3423$
p3	$36.57 \pm 0.2294$
p4	$-0.1932 \pm 0.003754$

Chip 0, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold

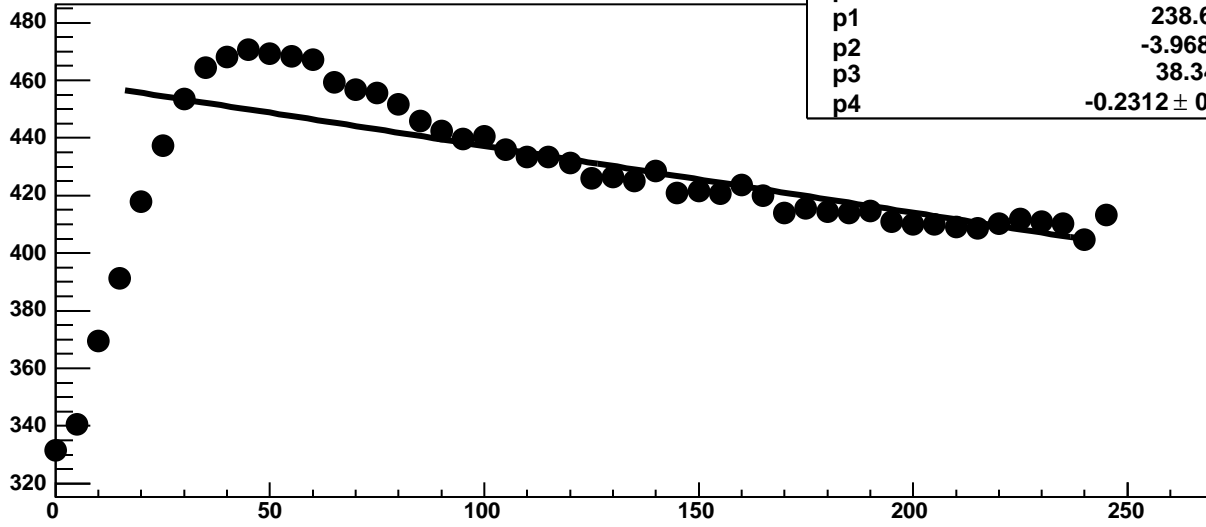


Chip 0, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold



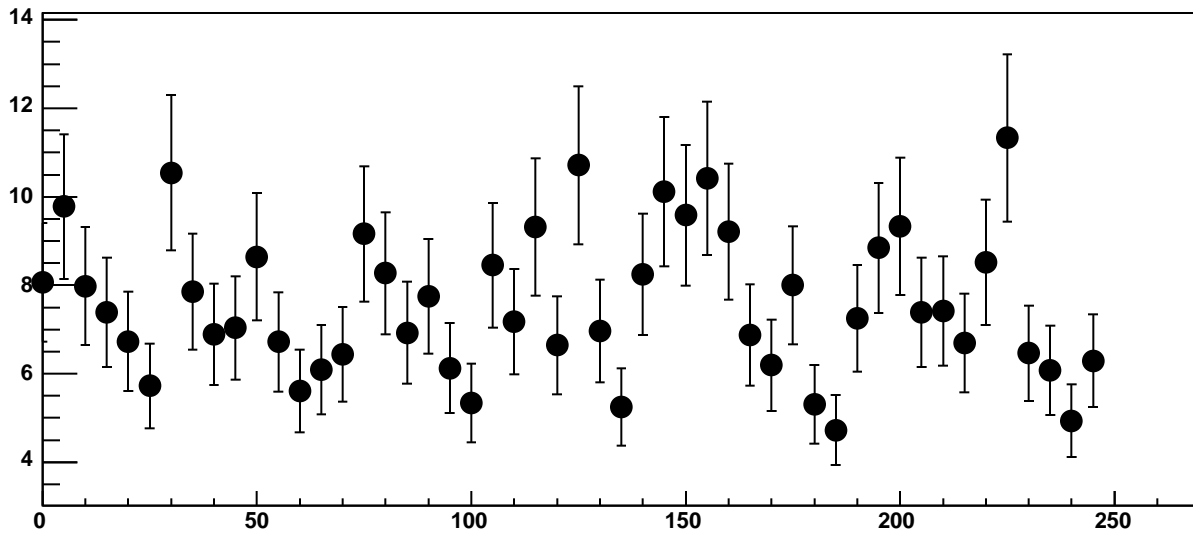


Chip 0, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold

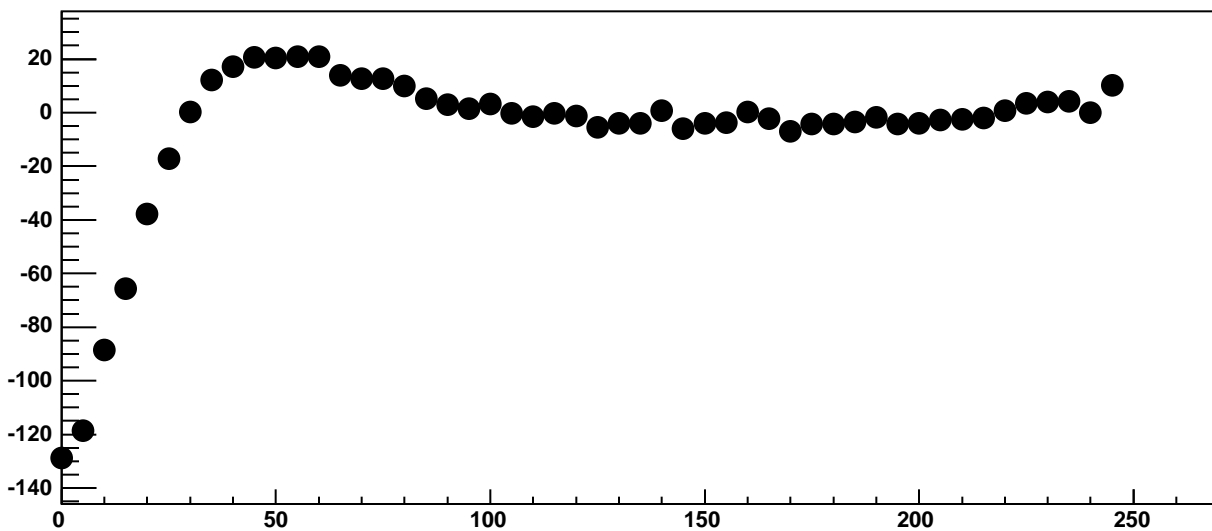


$\chi^2 / \text{ndf}$	3537 / 41
p0	$405.1 \pm 4.148$
p1	$238.6 \pm 17.51$
p2	$-3.968 \pm 45.92$
p3	$38.34 \pm 367$
p4	$-0.2312 \pm 0.003787$

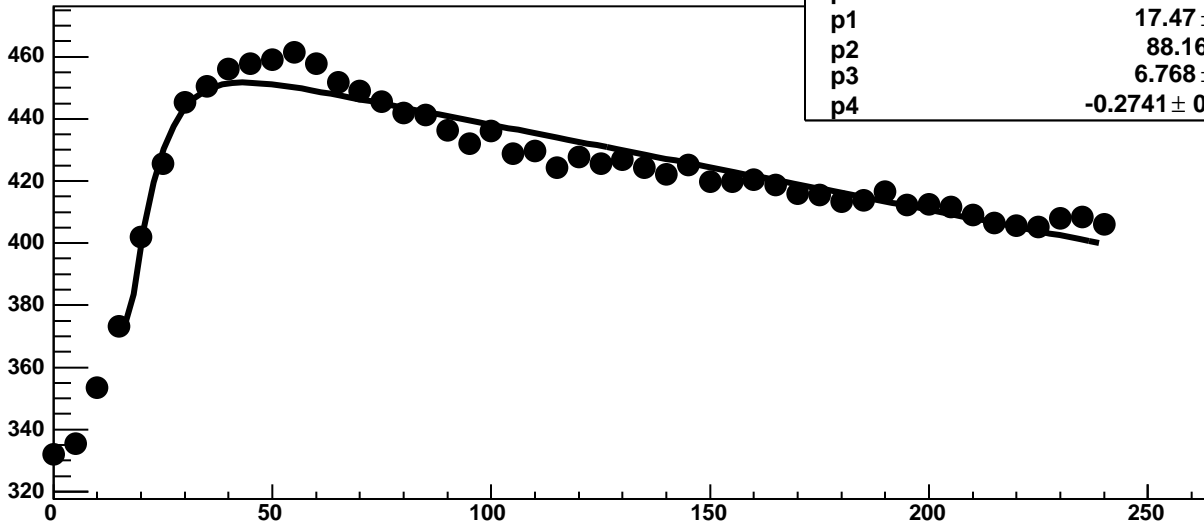
Chip 0, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold

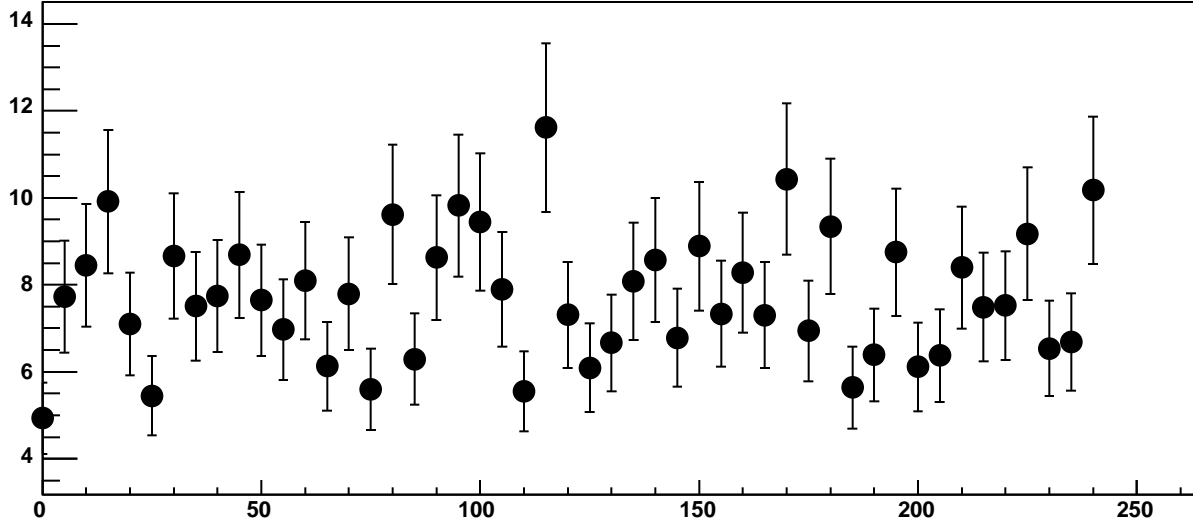


Chip 0, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold

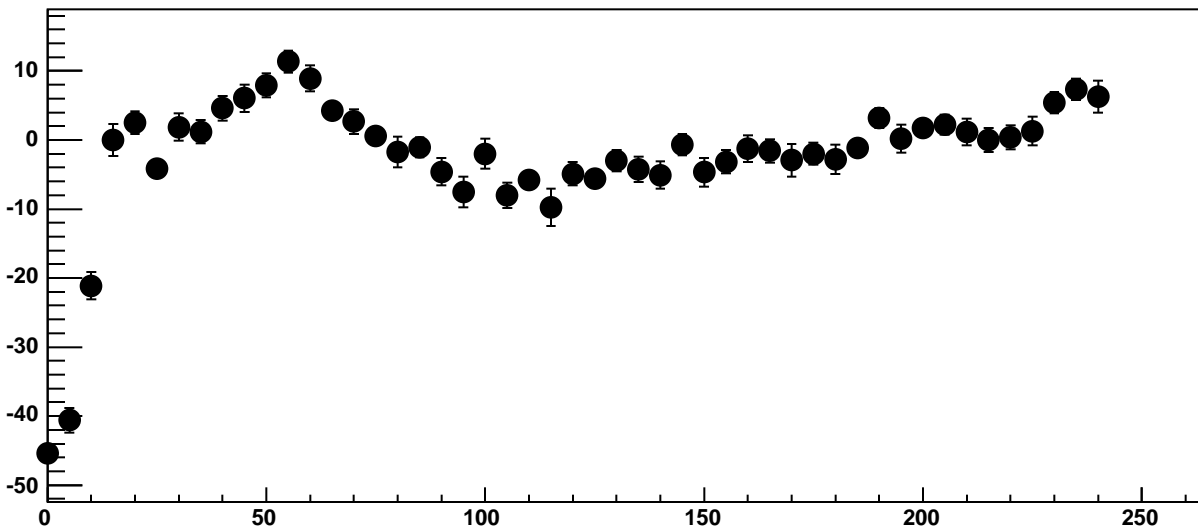


$\chi^2 / \text{ndf}$	316.4 / 41
p0	$372.6 \pm 2.235$
p1	$17.47 \pm 0.2992$
p2	$88.16 \pm 2.367$
p3	$6.768 \pm 0.3269$
p4	$-0.2741 \pm 0.004599$

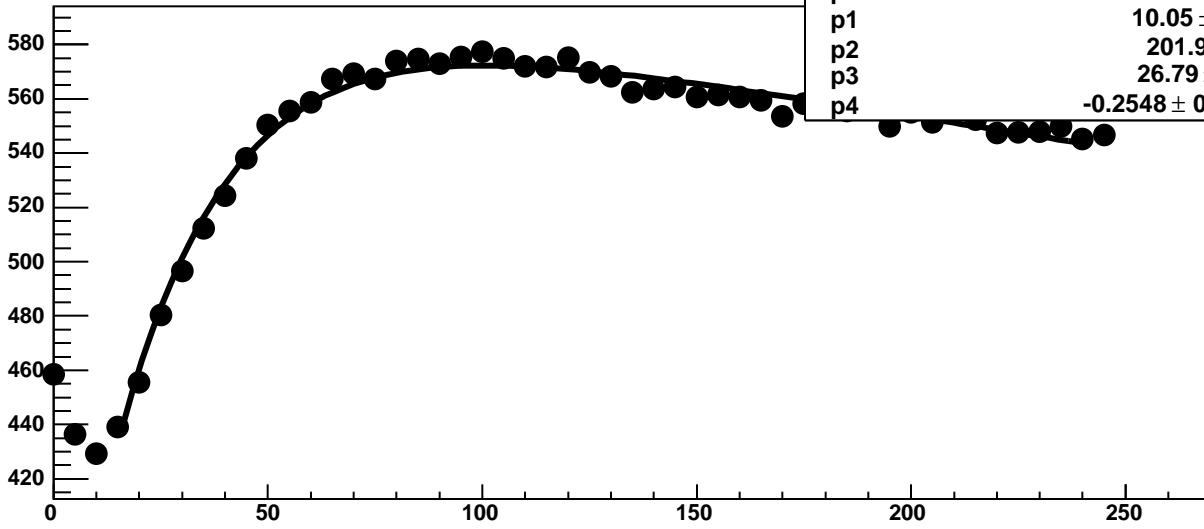
Chip 0, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold

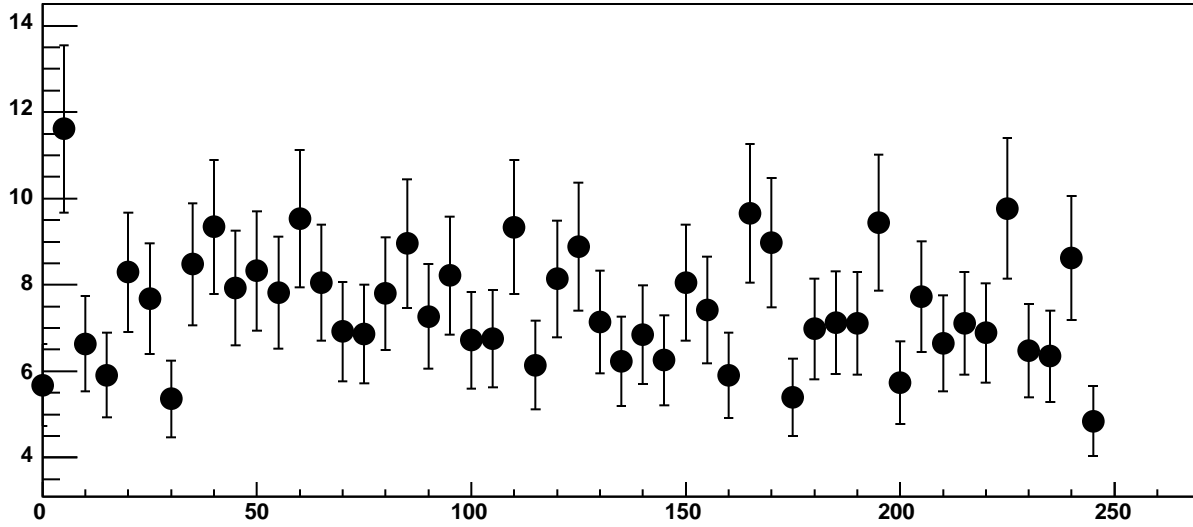


Chip 0, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

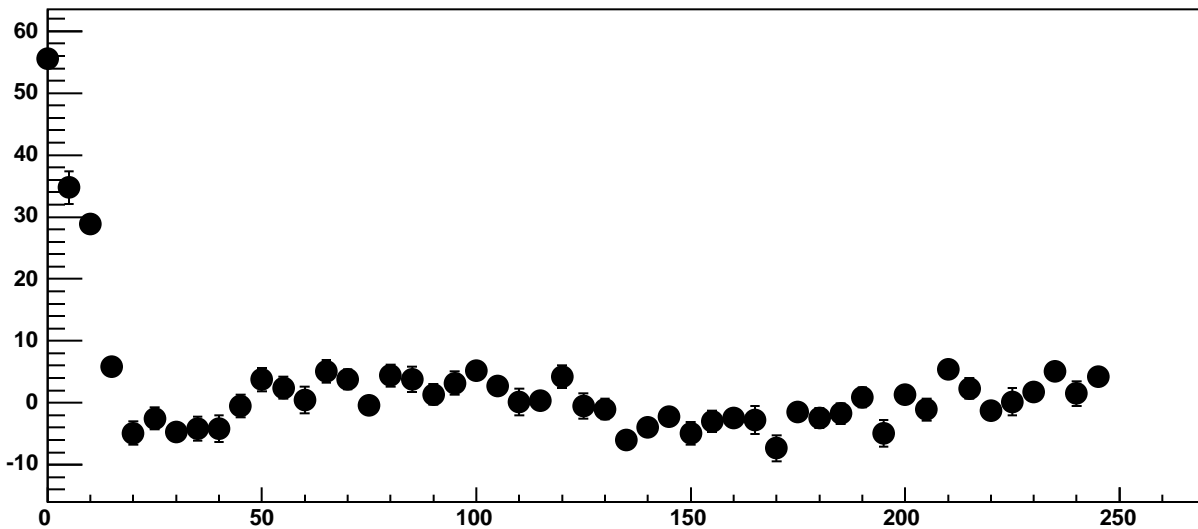


$\chi^2 / \text{ndf}$	198.2 / 41
p0	$400.4 \pm 3.13$
p1	$10.05 \pm 0.4704$
p2	$201.9 \pm 3.256$
p3	$26.79 \pm 0.5031$
p4	$-0.2548 \pm 0.007865$

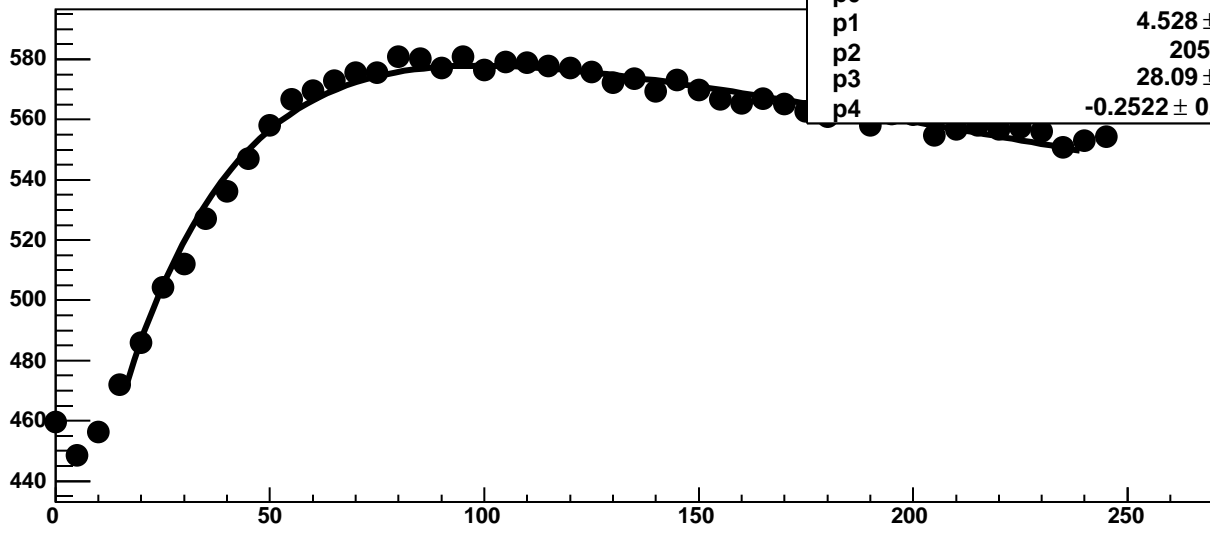
Chip 0, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



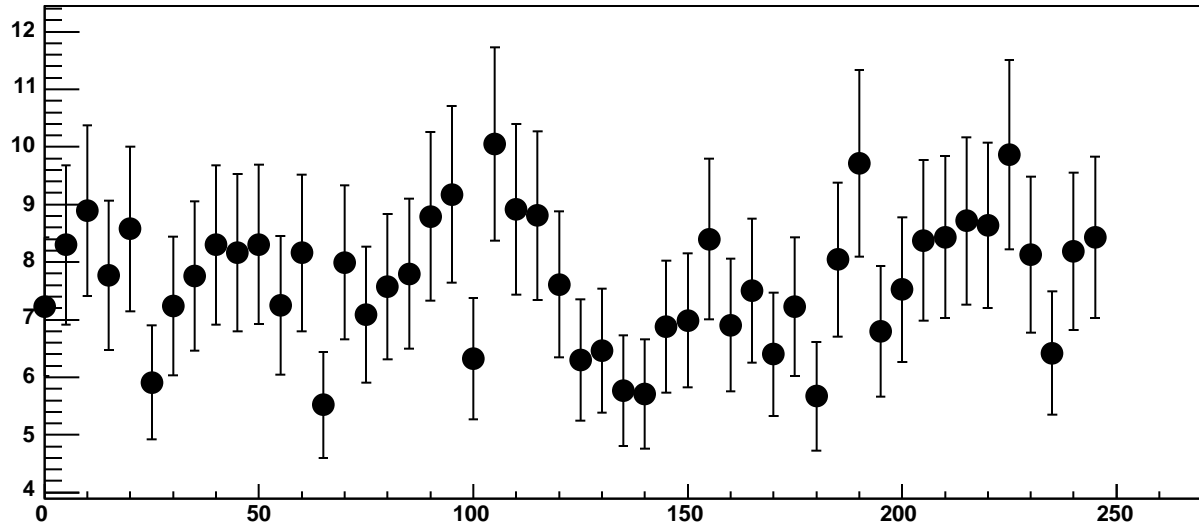
Chip 0, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold



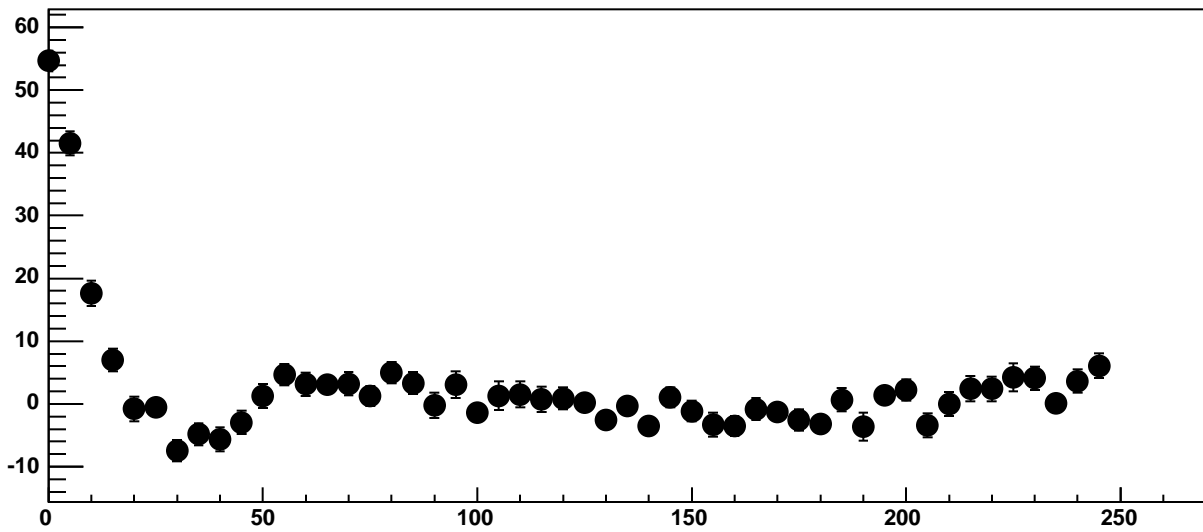
Chip 0, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold



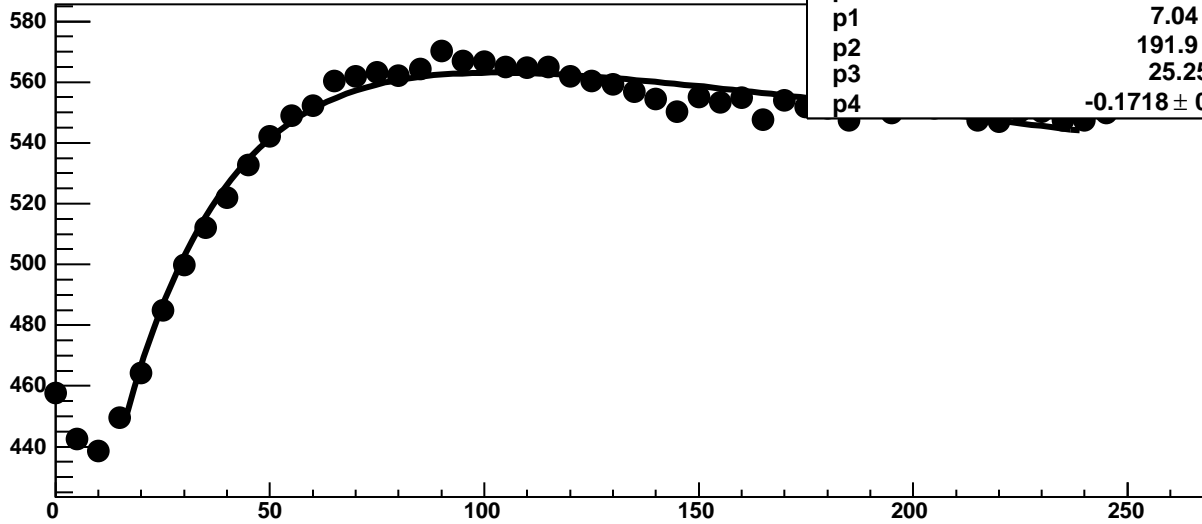
Chip 0, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold

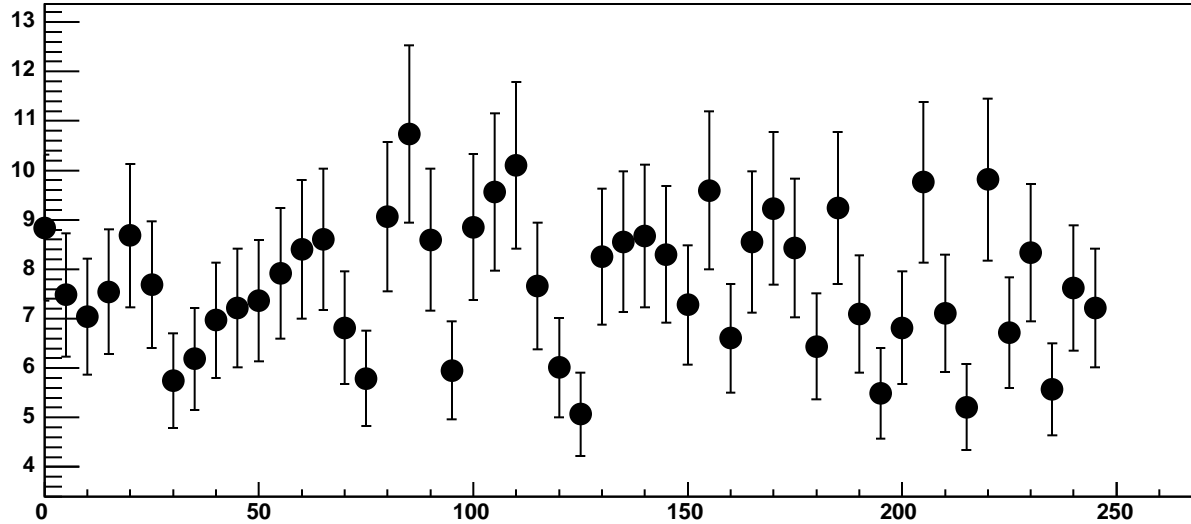


Chip 0, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

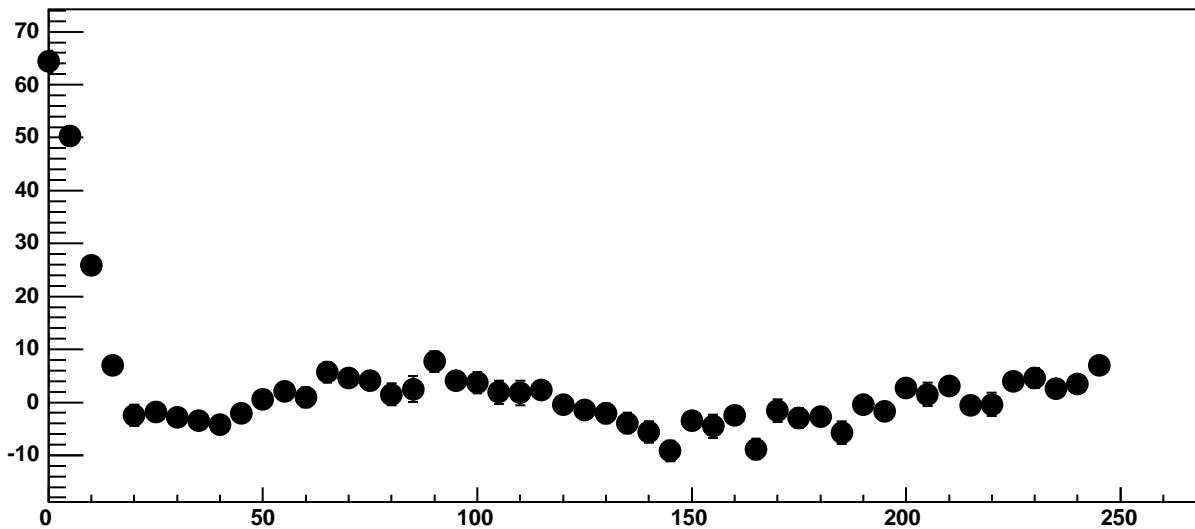


$\chi^2 / \text{ndf}$	211.7 / 41
p0	$392 \pm 0.6777$
p1	$7.04 \pm 0.3262$
p2	$191.9 \pm 0.7477$
p3	$25.25 \pm 0.576$
p4	$-0.1718 \pm 0.007747$

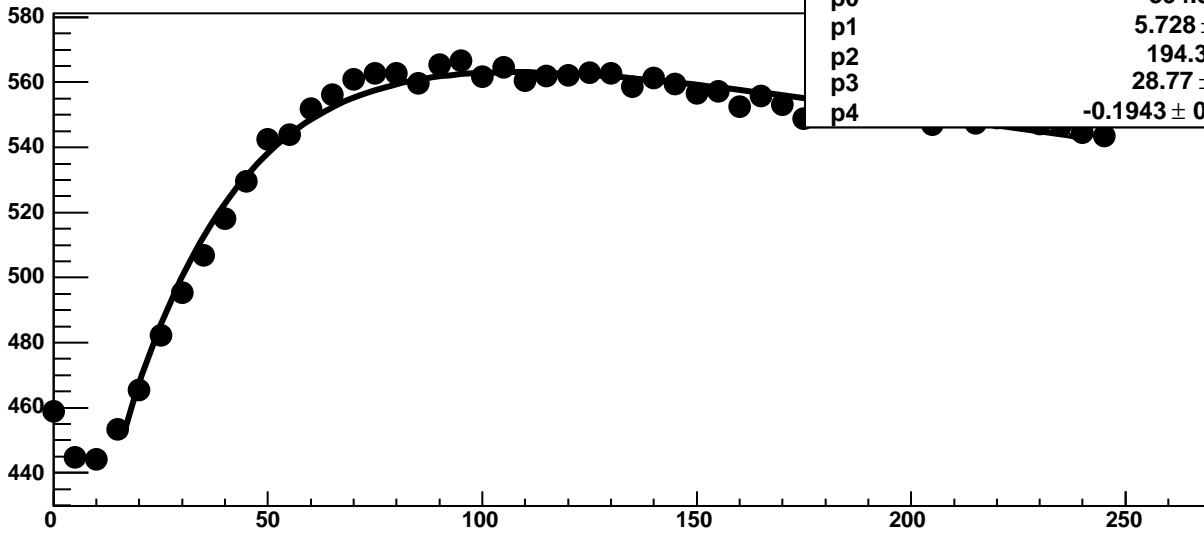
Chip 0, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



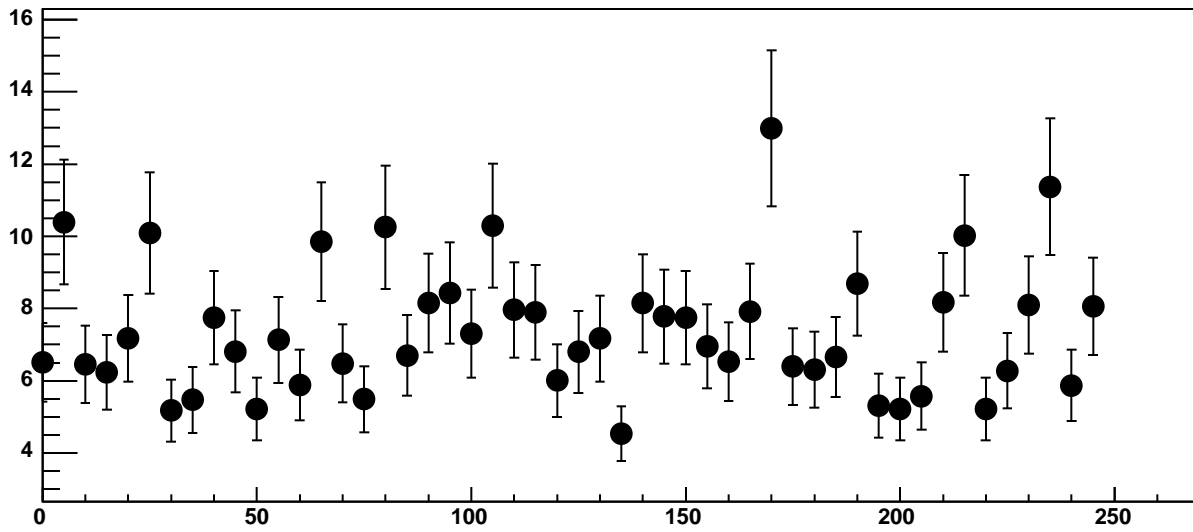
Chip 0, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold



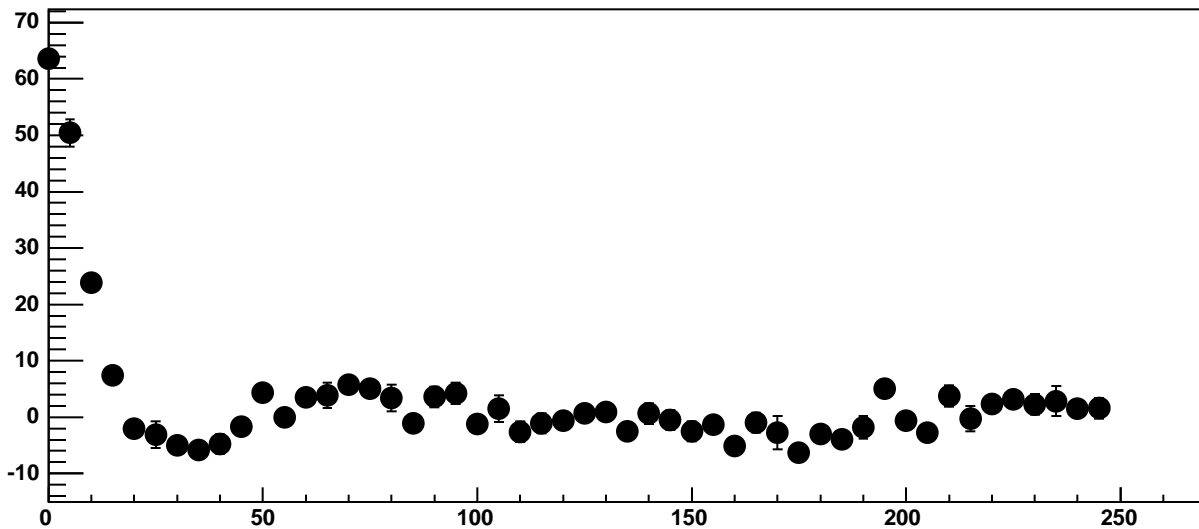
Chip 0, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold



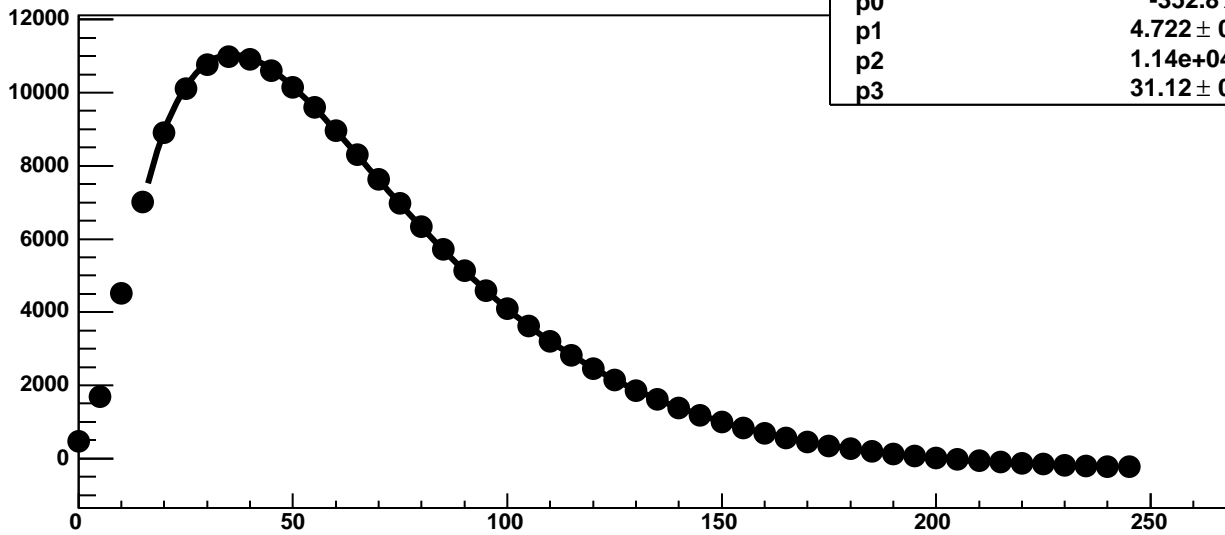
Chip 0, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

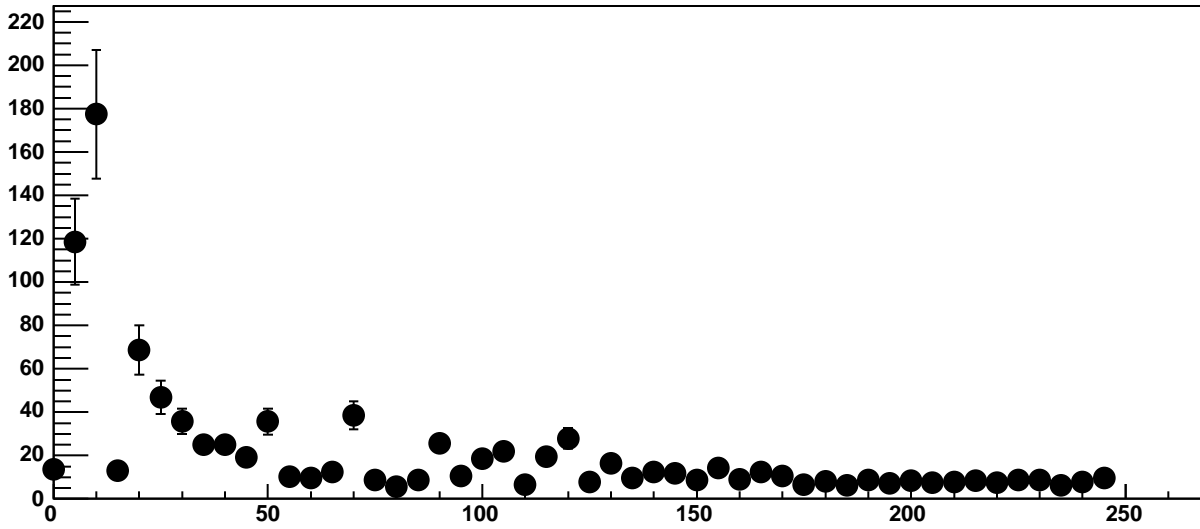


Chip 0, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold

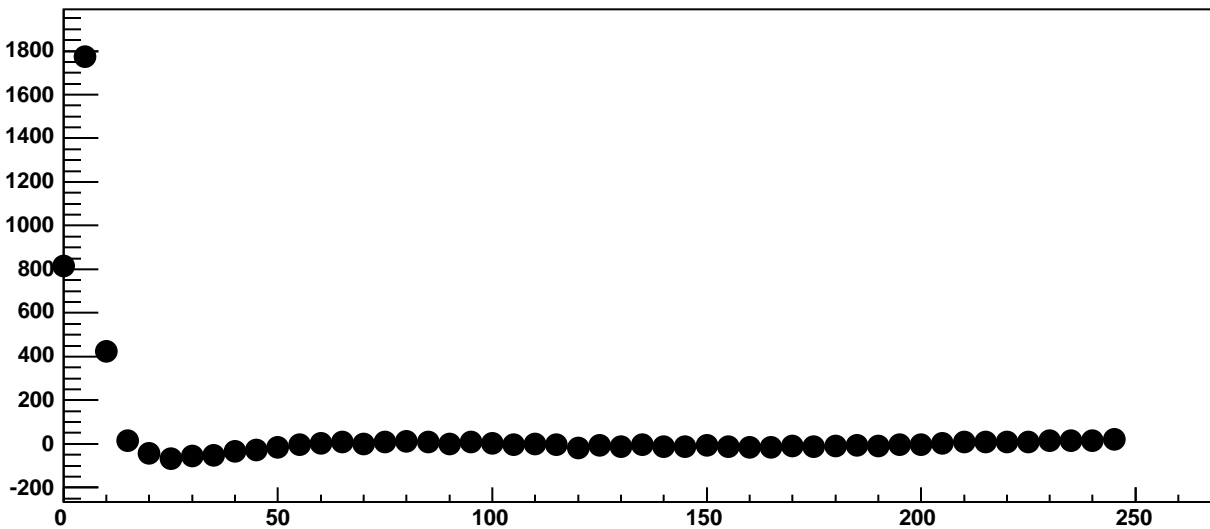


$\chi^2 / \text{ndf}$	1027 / 42
p0	$-352.8 \pm 0.6246$
p1	$4.722 \pm 0.007569$
p2	$1.14\text{e}+04 \pm 1.605$
p3	$31.12 \pm 0.006247$

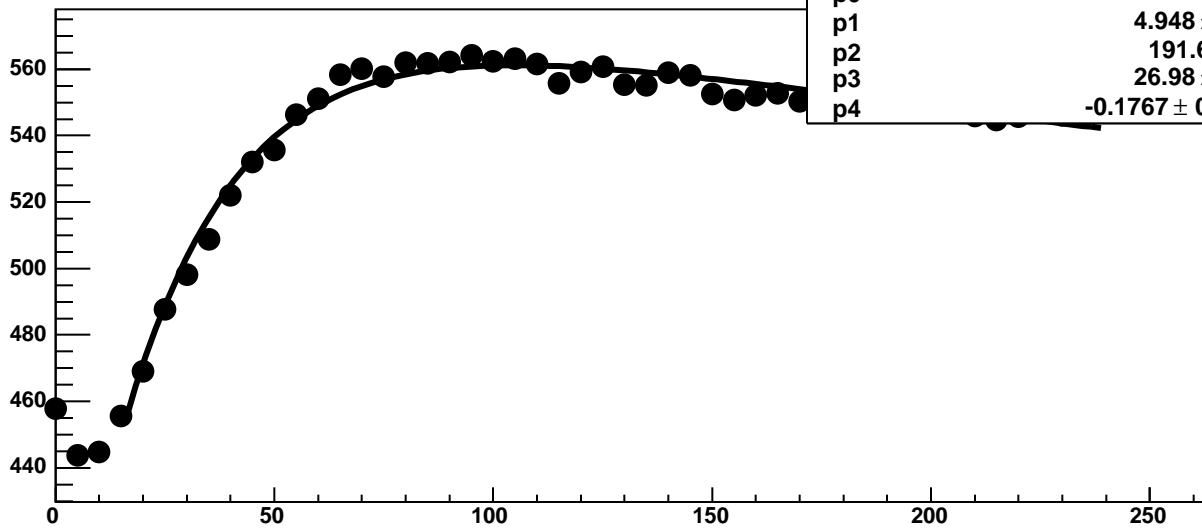
Chip 0, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold

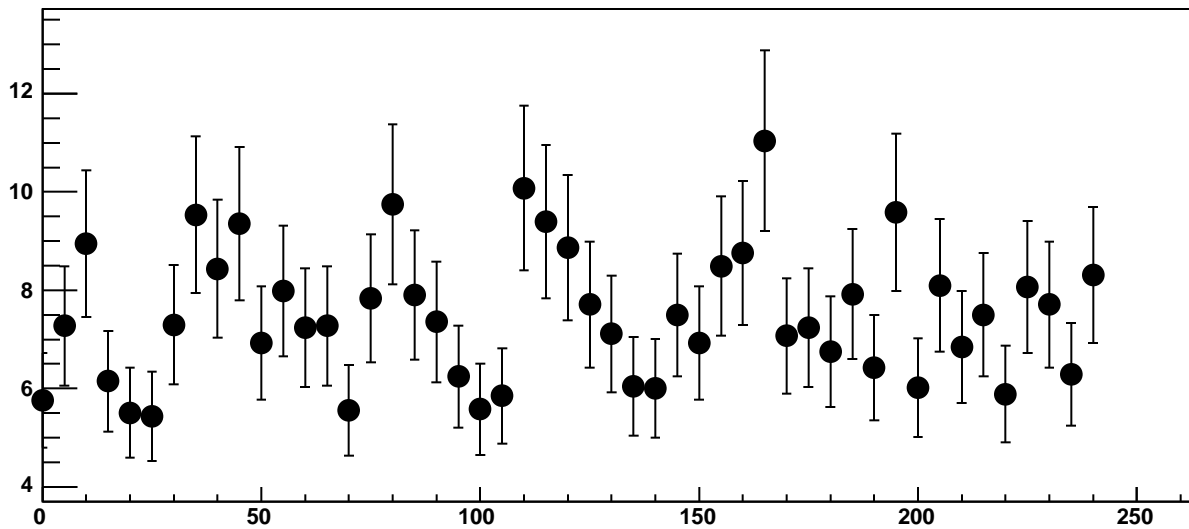


Chip 0, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold

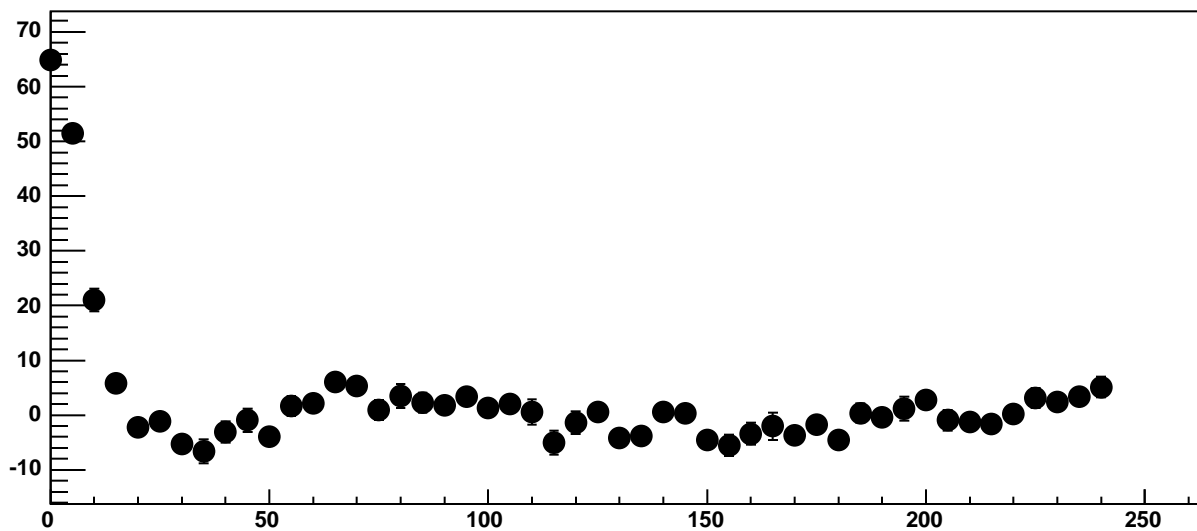


$\chi^2 / \text{ndf}$	175.8 / 41
p0	$391.9 \pm 3.079$
p1	$4.948 \pm 0.5345$
p2	$191.6 \pm 3.172$
p3	$26.98 \pm 0.6325$
p4	$-0.1767 \pm 0.007726$

Chip 0, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold

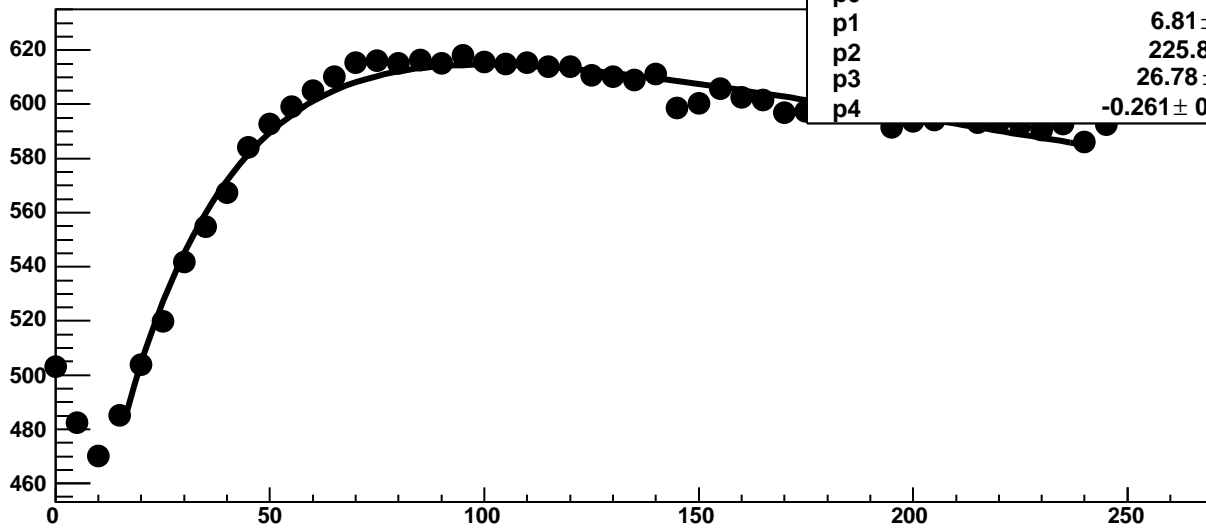


Chip 0, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold



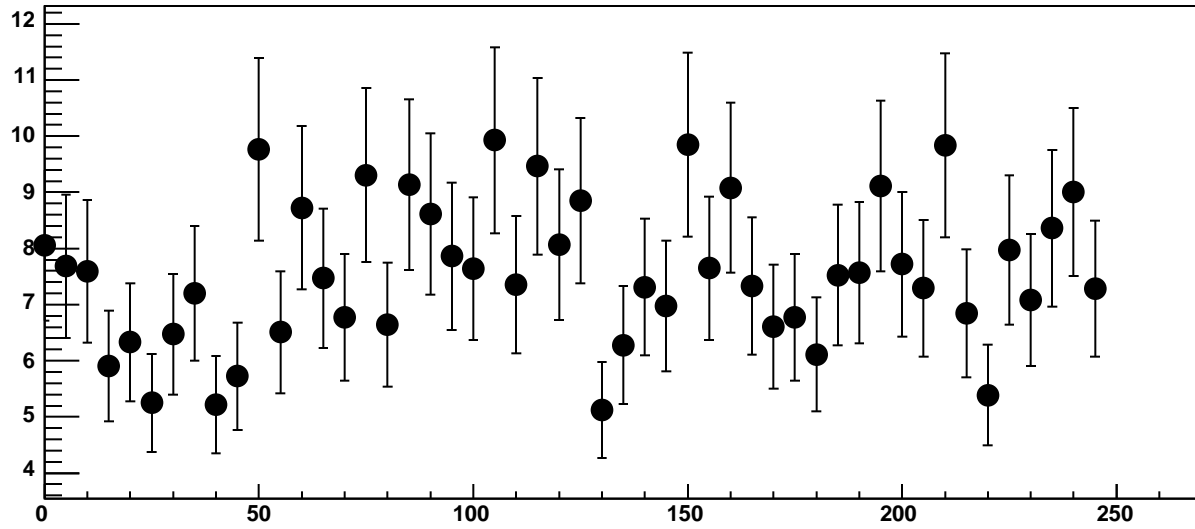


Chip 0, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold

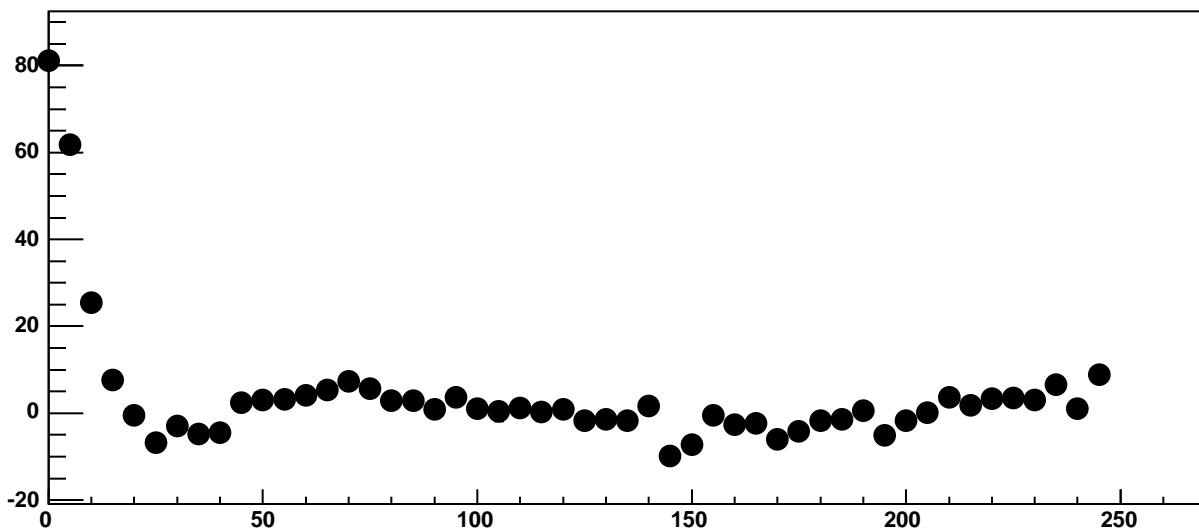


$\chi^2 / \text{ndf}$	271.7 / 41
p0	$420.1 \pm 3.098$
p1	$6.81 \pm 0.4335$
p2	$225.8 \pm 3.216$
p3	$26.78 \pm 0.4673$
p4	$-0.261 \pm 0.007943$

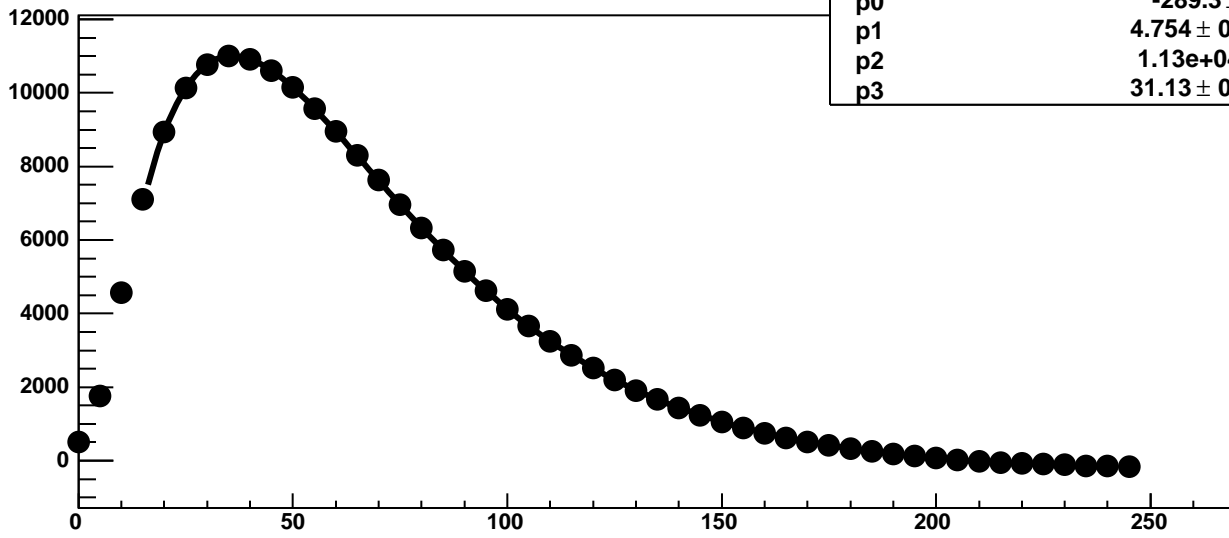
Chip 0, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

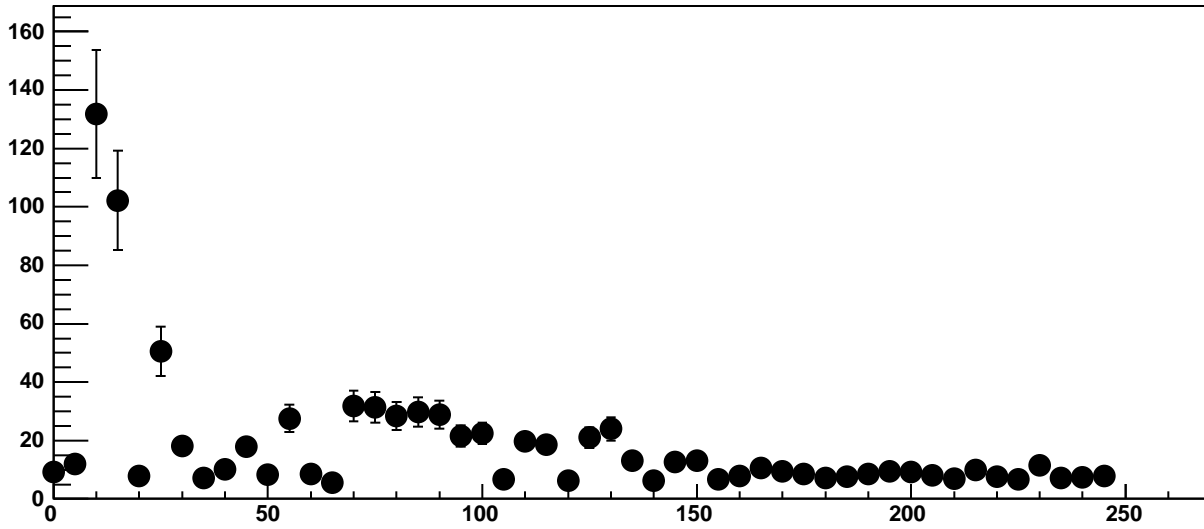


Chip 0, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold

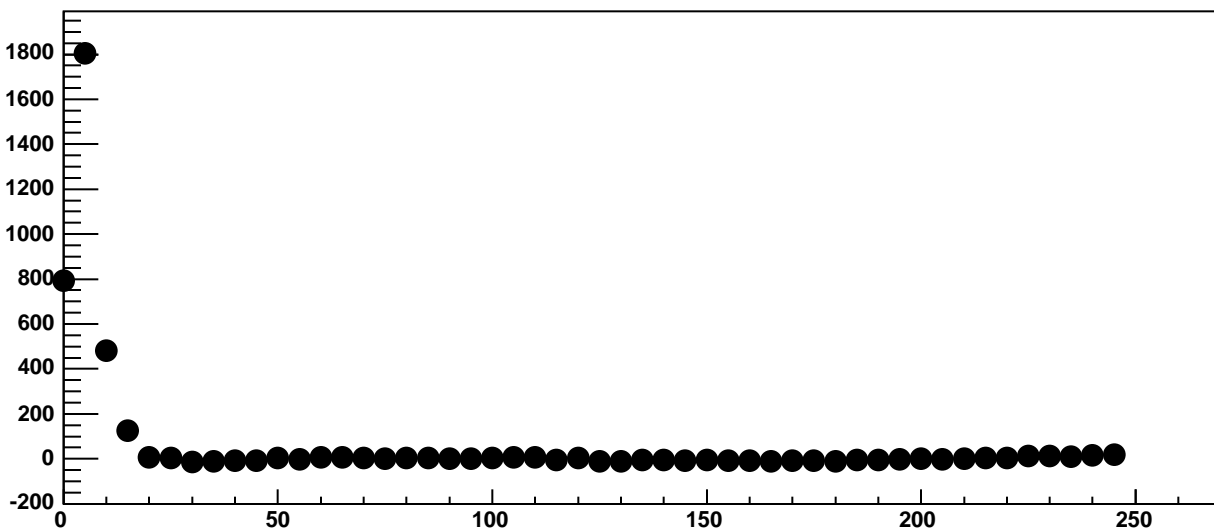


$\chi^2 / \text{ndf}$	575.5 / 42
p0	-289.3 ± 0.6359
p1	4.754 ± 0.007817
p2	1.13e+04 ± 0.96
p3	31.13 ± 0.005783

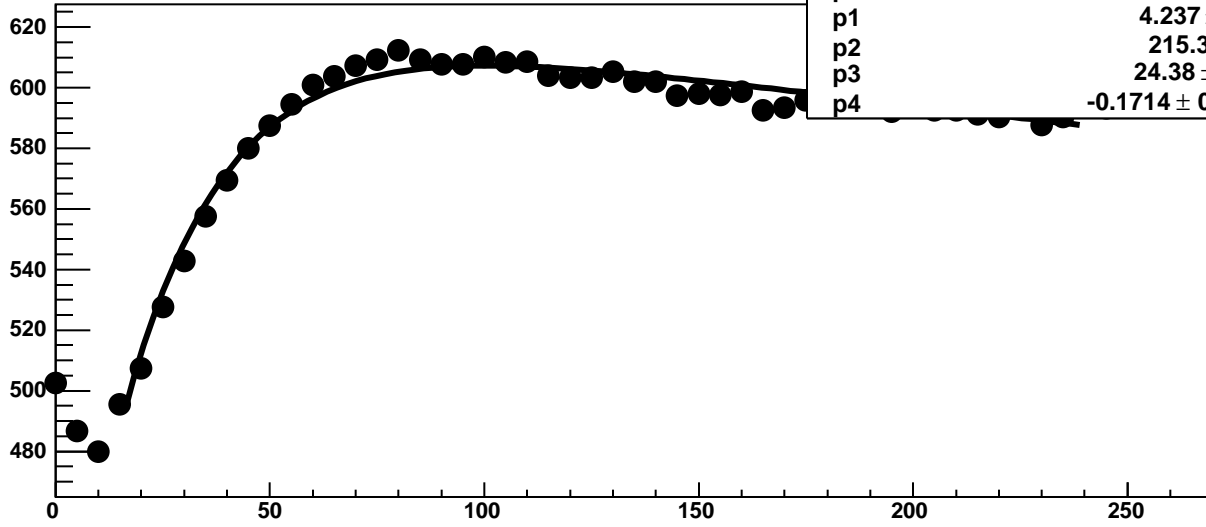
Chip 0, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold

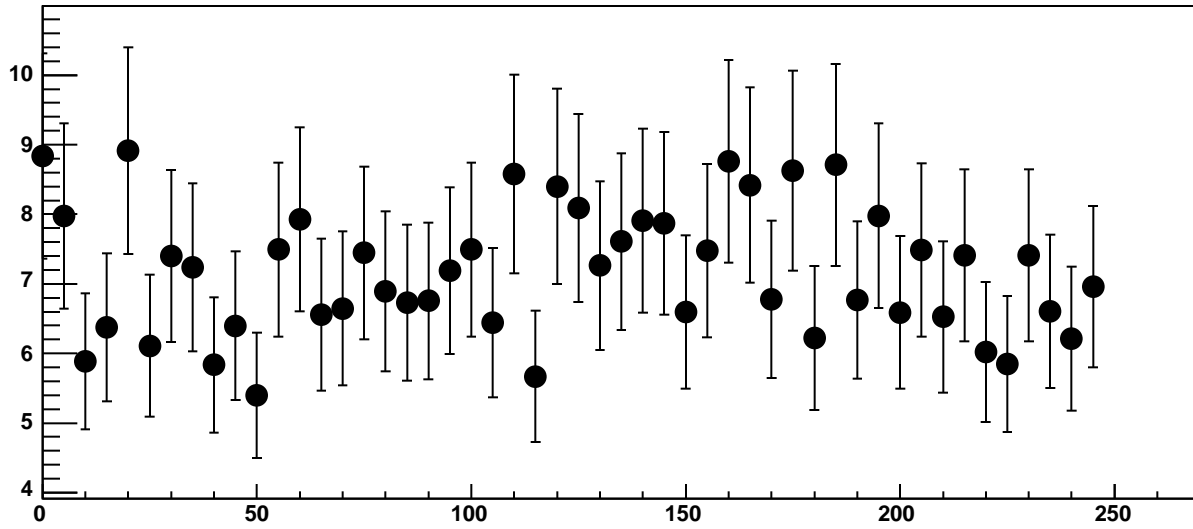


Chip 0, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

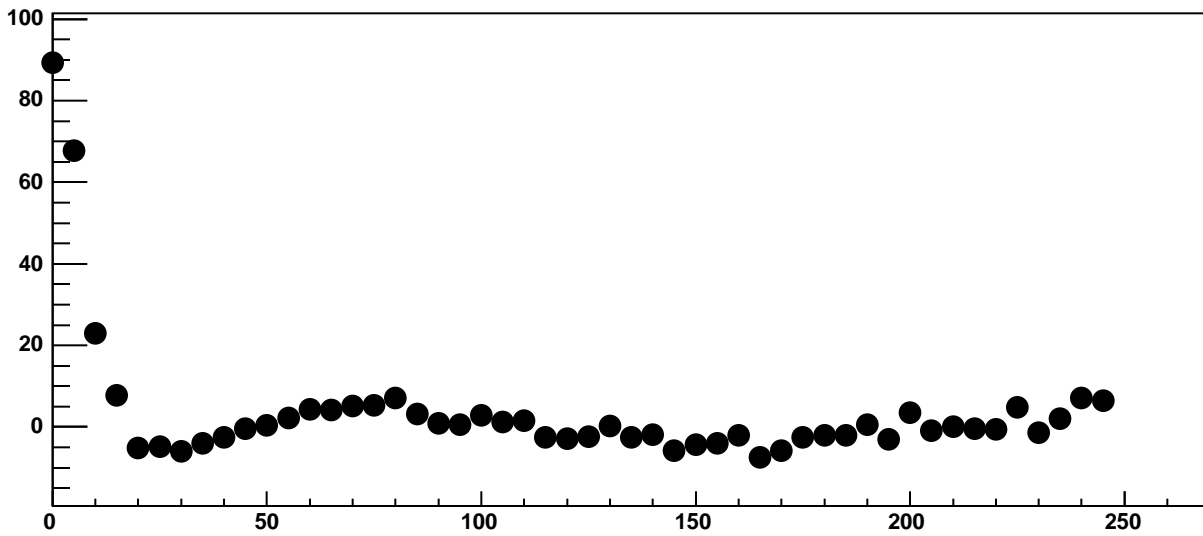


$\chi^2 / \text{ndf}$	253.2 / 41
p0	$412.6 \pm 2.998$
p1	$4.237 \pm 0.4621$
p2	$215.3 \pm 3.066$
p3	$24.38 \pm 0.4856$
p4	$-0.1714 \pm 0.006722$

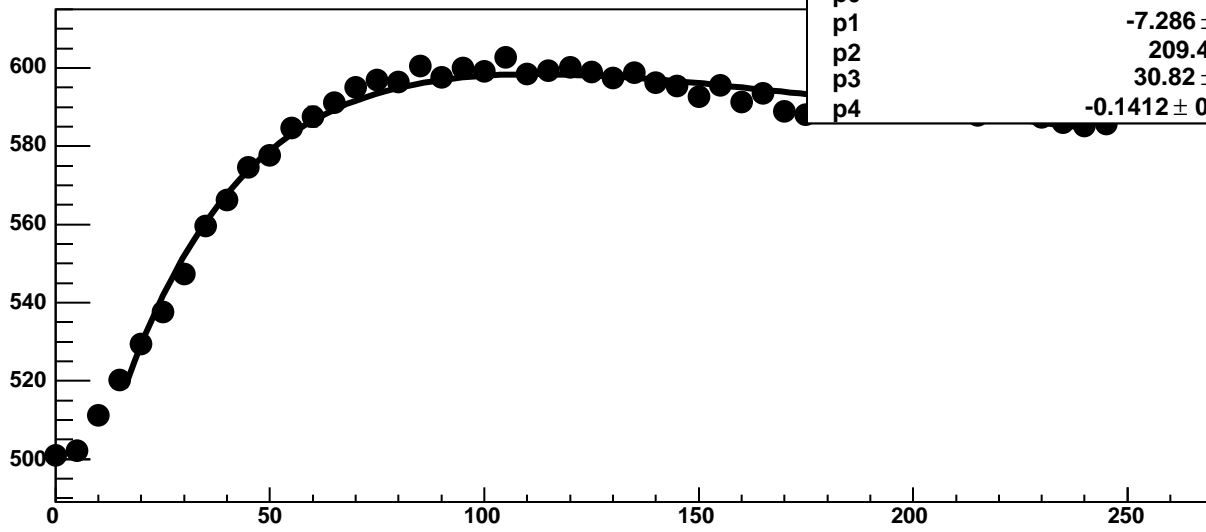
Chip 0, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

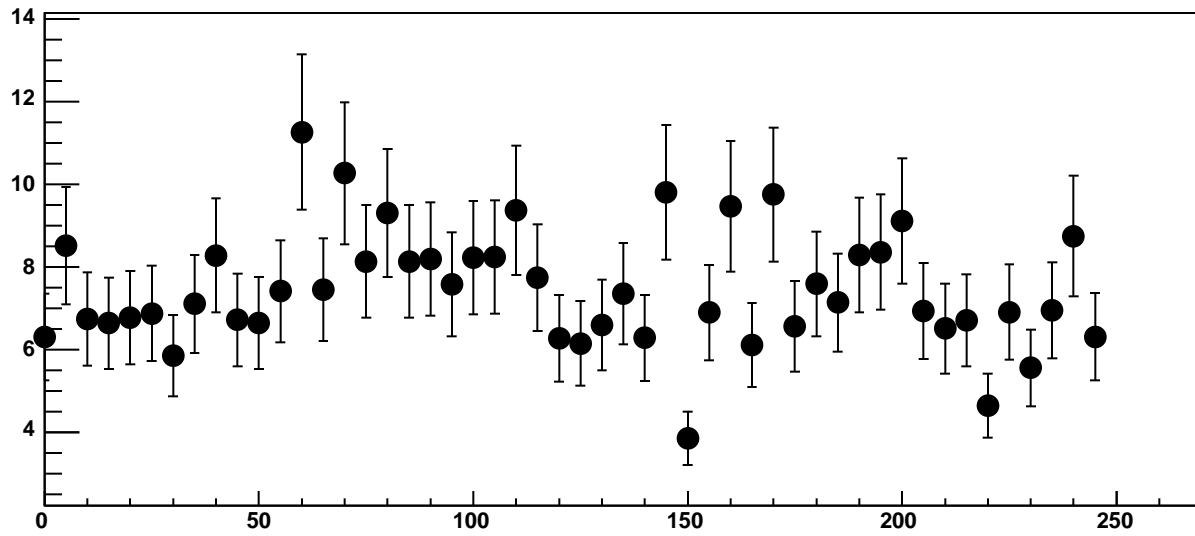


Chip 0, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

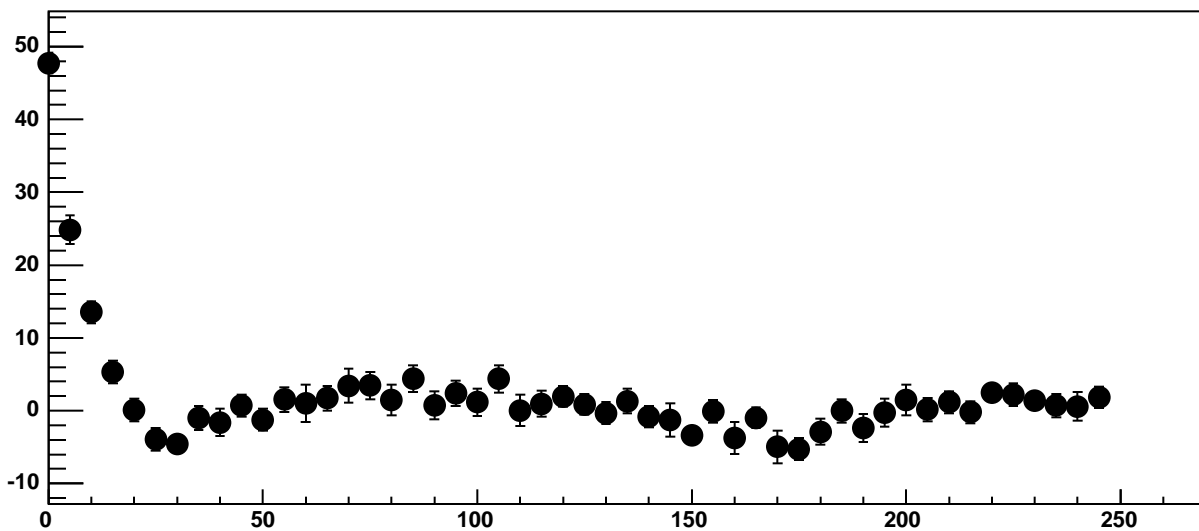


$\chi^2 / \text{ndf}$	106.8 / 41
p0	$410.2 \pm 3.036$
p1	$-7.286 \pm 0.8445$
p2	$209.4 \pm 3.109$
p3	$30.82 \pm 0.9246$
p4	$-0.1412 \pm 0.008068$

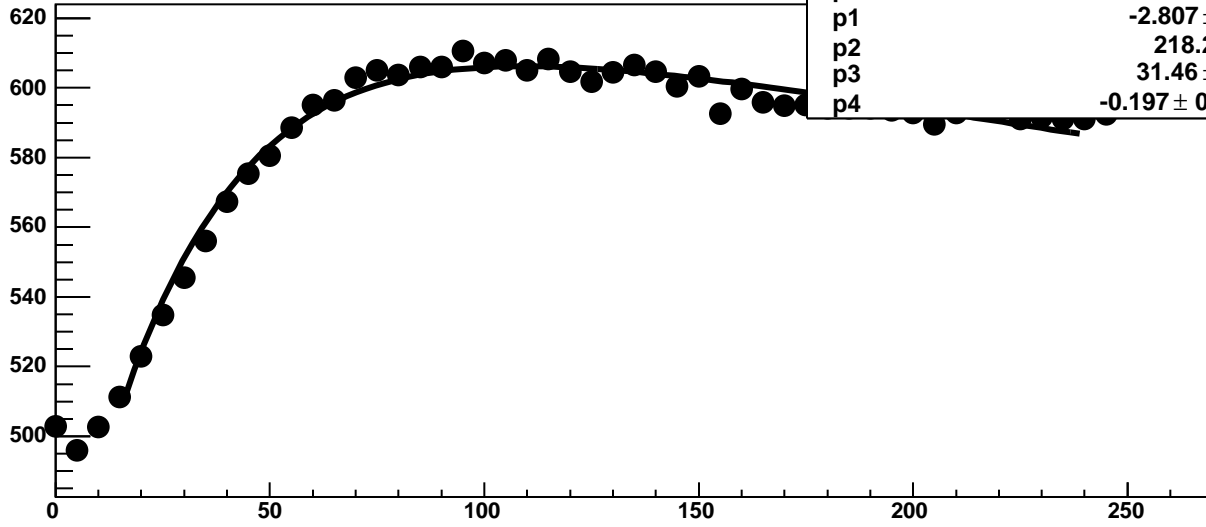
Chip 0, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

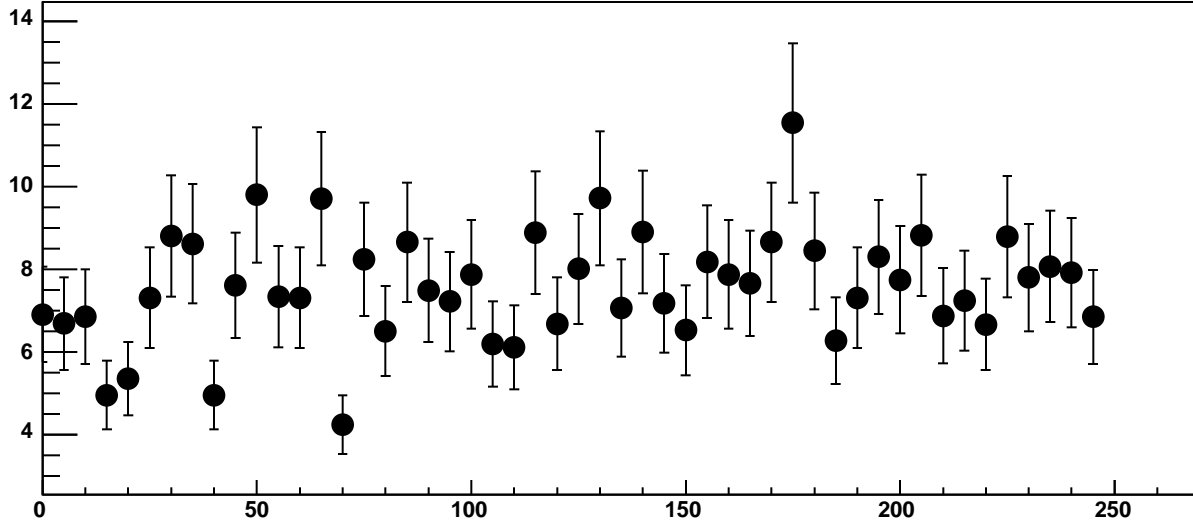


Chip 0, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold

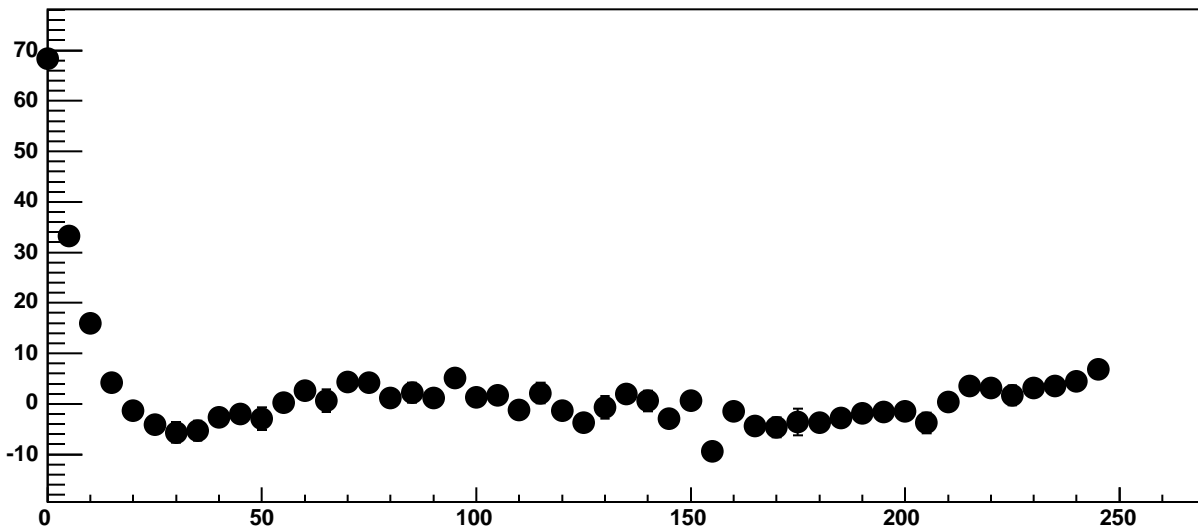


$\chi^2 / \text{ndf}$	172.8 / 41
p0	416.3 ± 3.098
p1	-2.807 ± 0.6112
p2	218.2 ± 3.211
p3	31.46 ± 0.7658
p4	-0.197 ± 0.008878

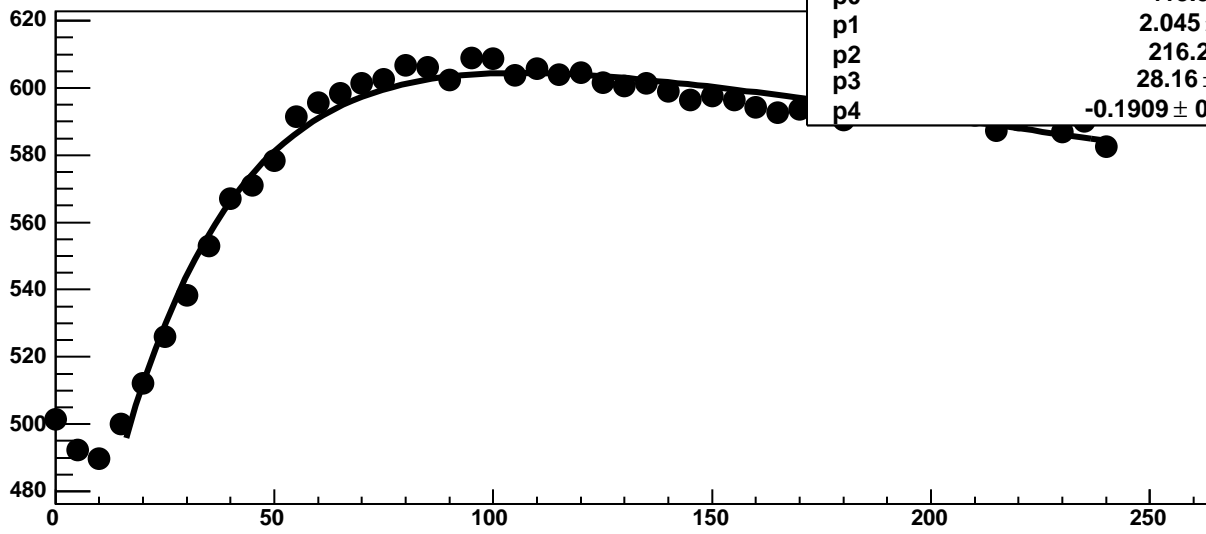
Chip 0, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold

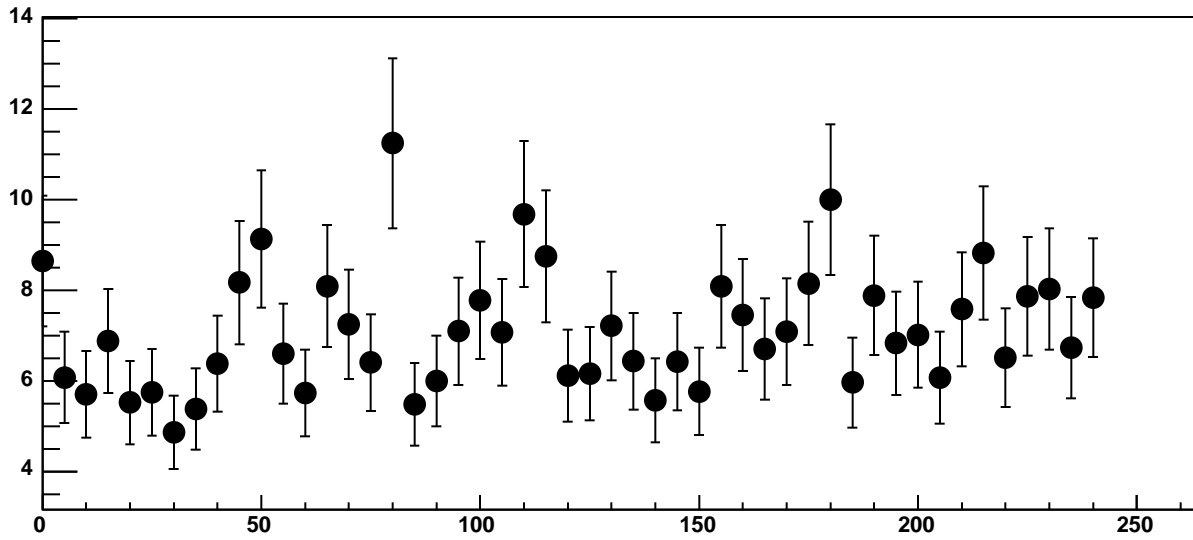


Chip 0, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

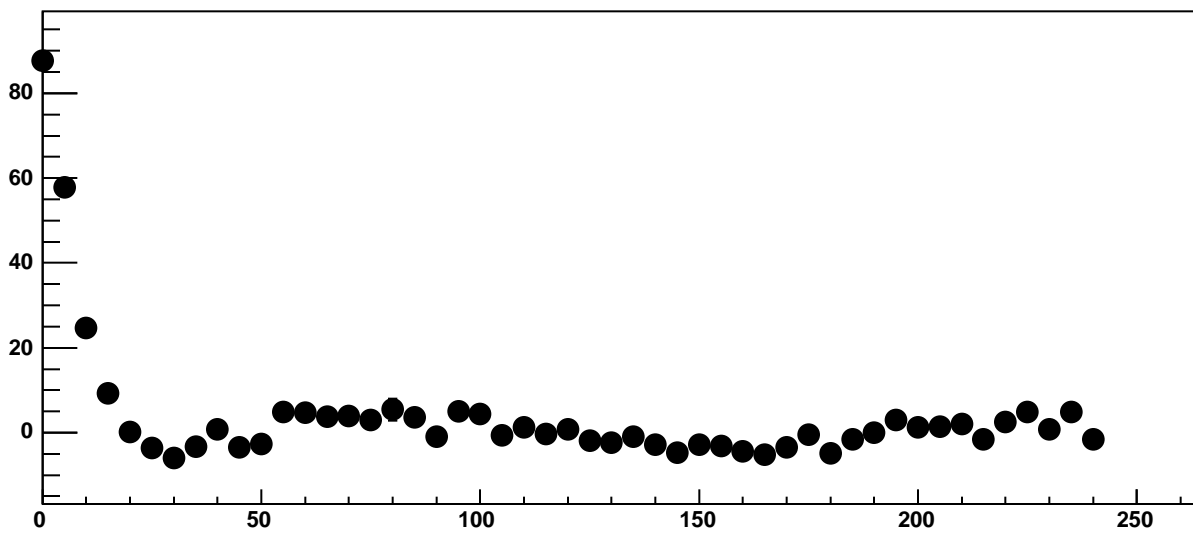


$\chi^2 / \text{ndf}$	233.6 / 41
p0	$413.5 \pm 2.916$
p1	$2.045 \pm 0.5181$
p2	$216.2 \pm 3.006$
p3	$28.16 \pm 0.5949$
p4	$-0.1909 \pm 0.007738$

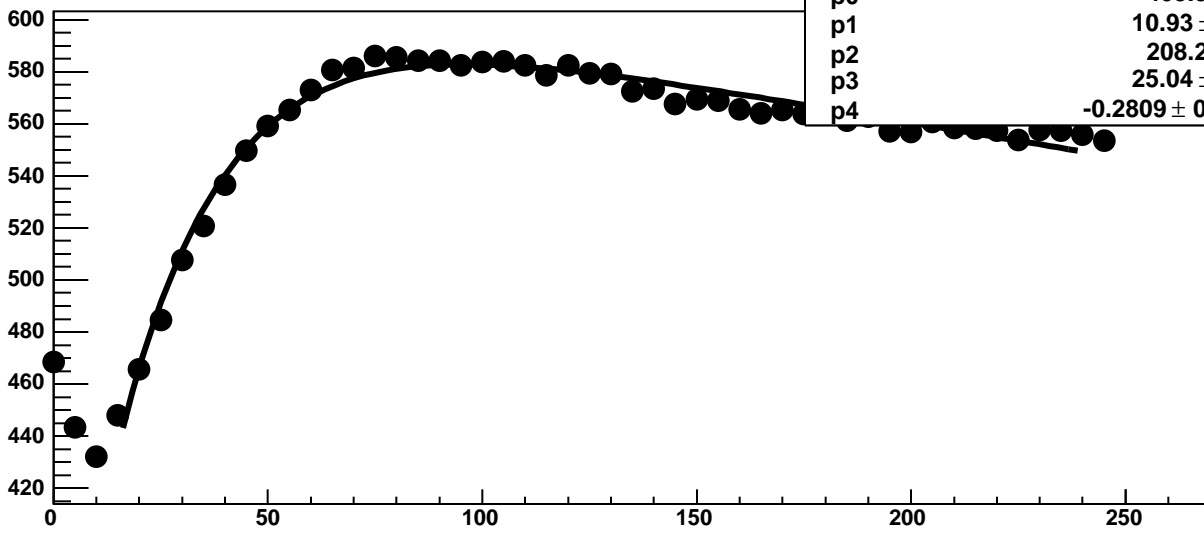
Chip 0, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

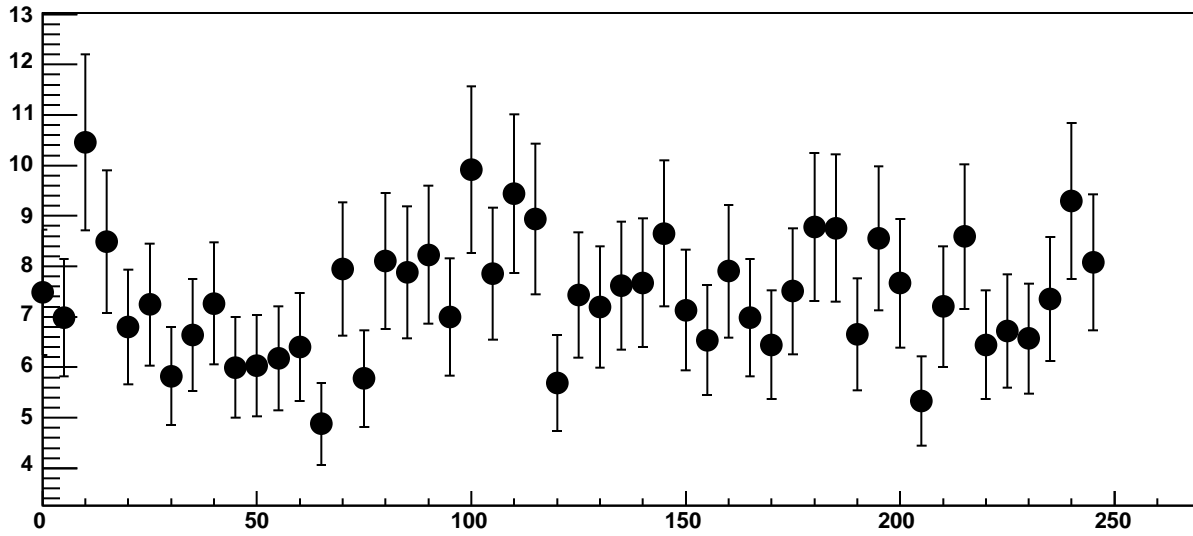


Chip 0, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

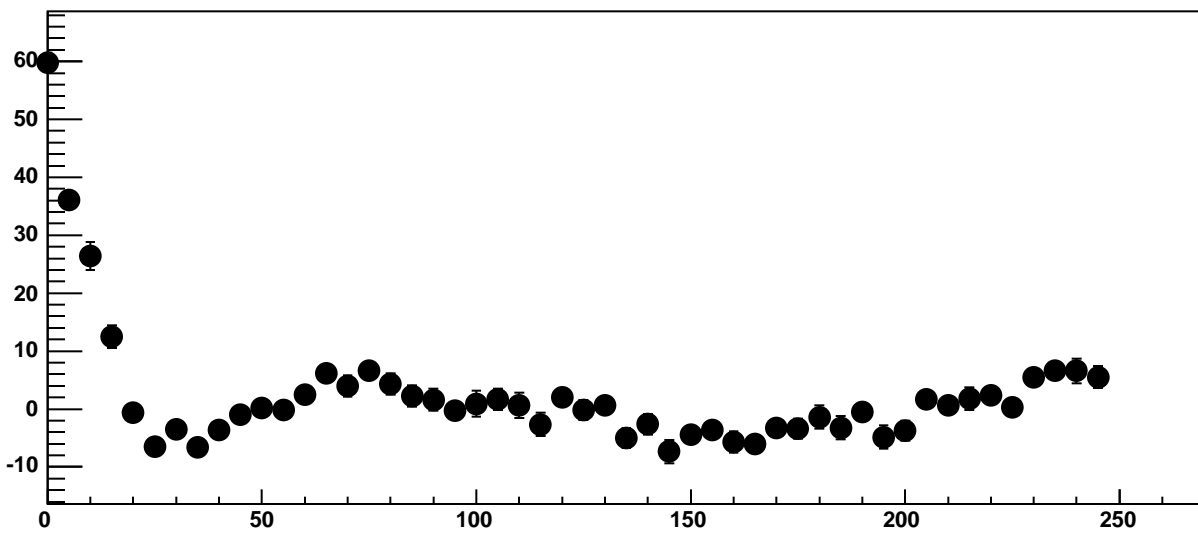


$\chi^2 / \text{ndf}$	290.3 / 41
p0	$405.5 \pm 3.046$
p1	$10.93 \pm 0.4265$
p2	$208.2 \pm 3.167$
p3	$25.04 \pm 0.4266$
p4	$-0.2809 \pm 0.007464$

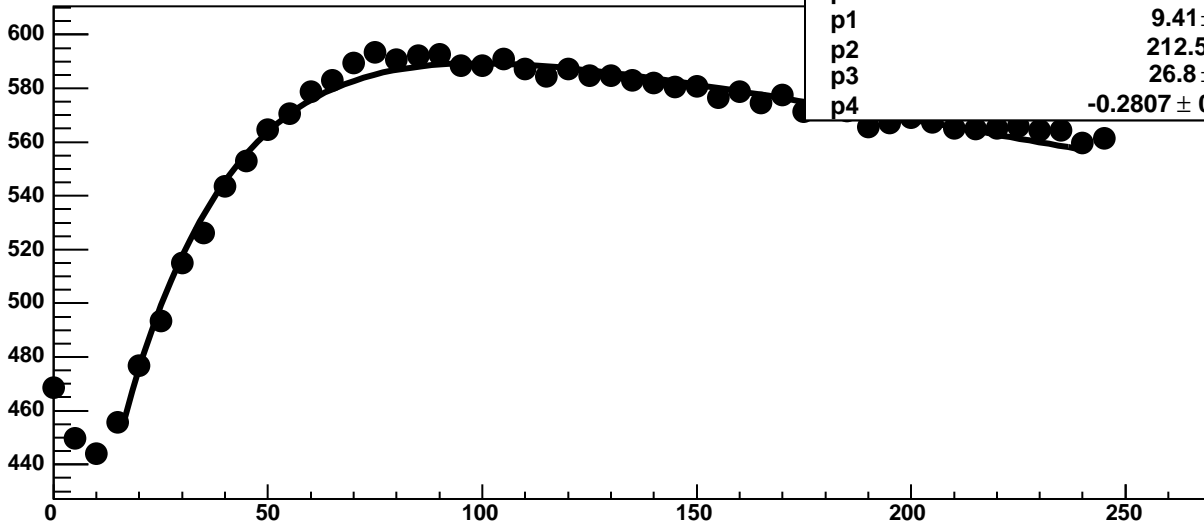
Chip 0, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold

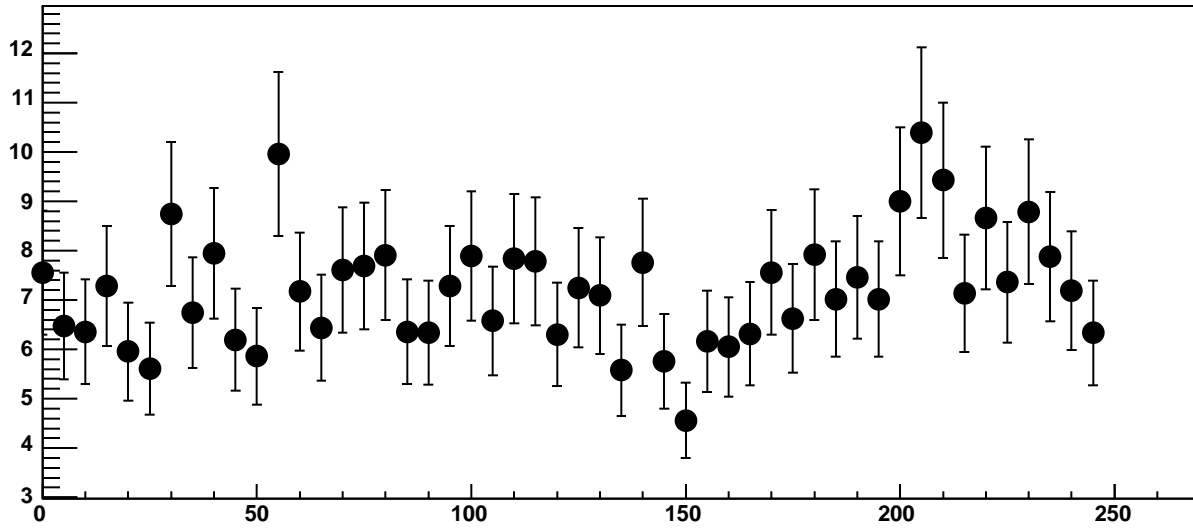


Chip 0, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold

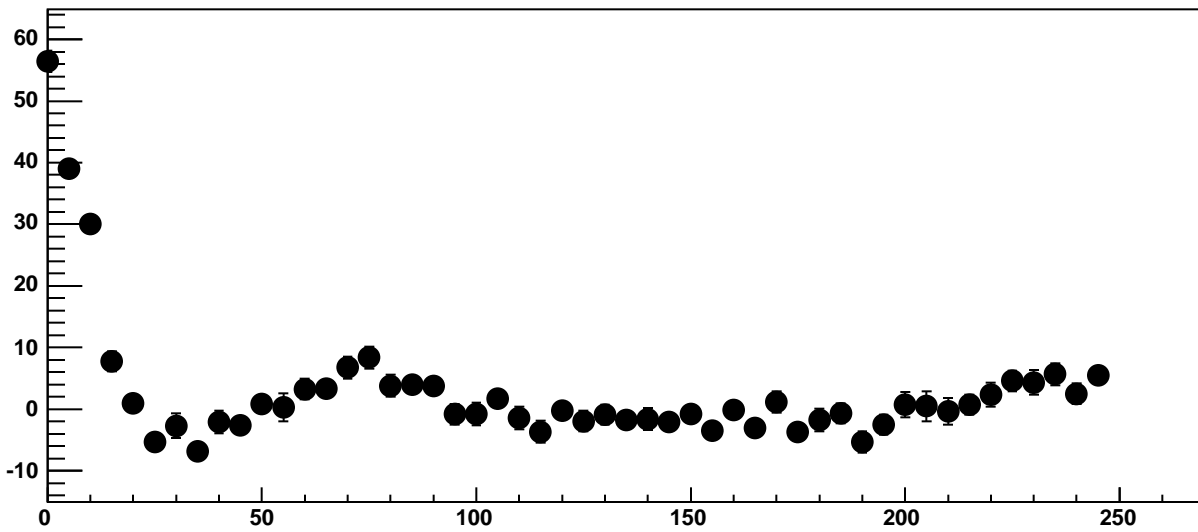


$\chi^2 / \text{ndf}$	201.1 / 41
p0	$409.4 \pm 2.999$
p1	$9.41 \pm 0.4389$
p2	$212.5 \pm 3.124$
p3	$26.8 \pm 0.4753$
p4	$-0.2807 \pm 0.007991$

Chip 0, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold

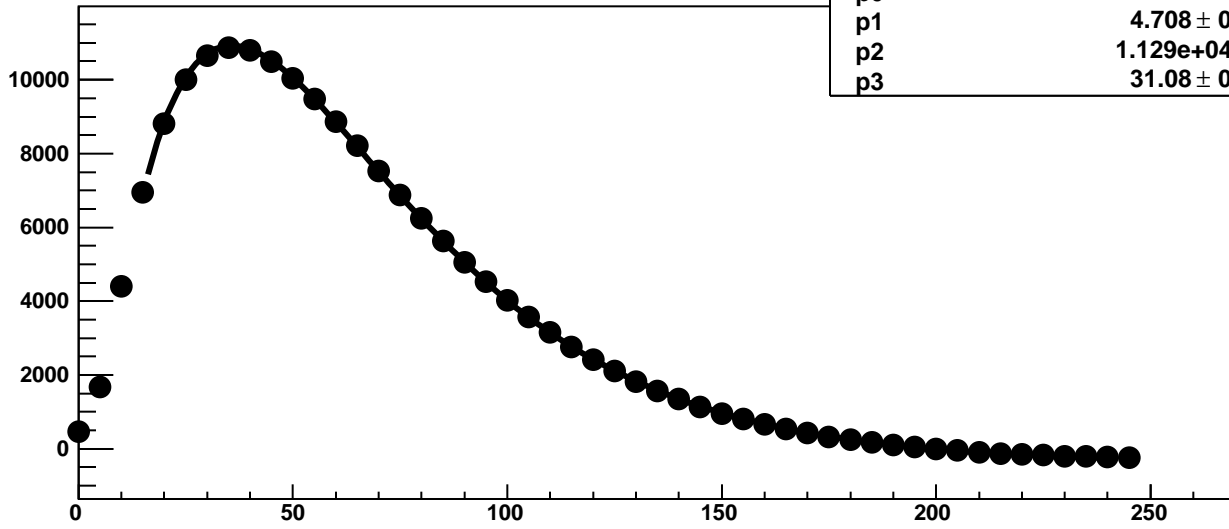


Chip 0, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold



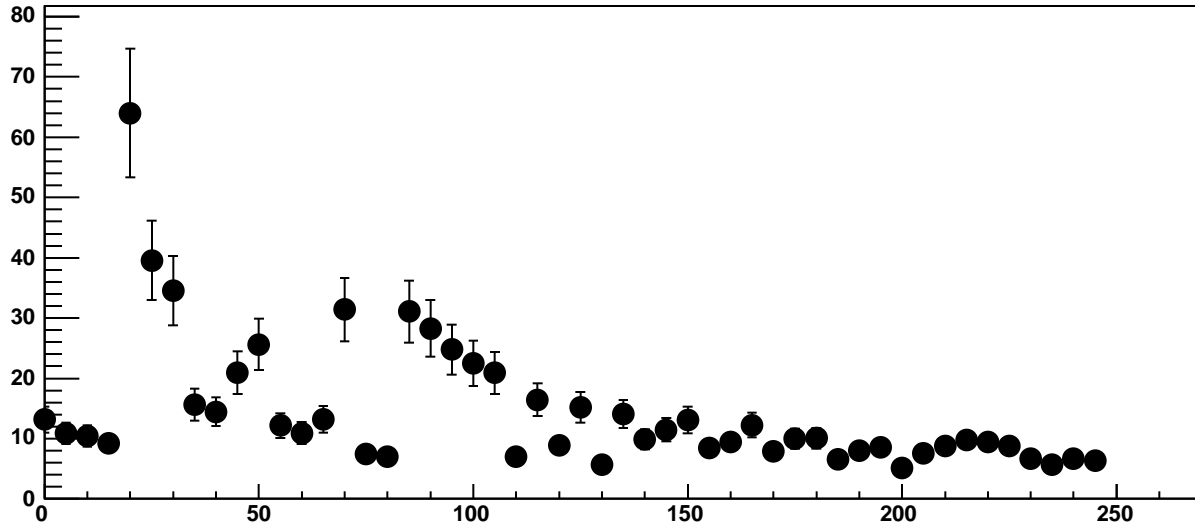


Chip 0, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold

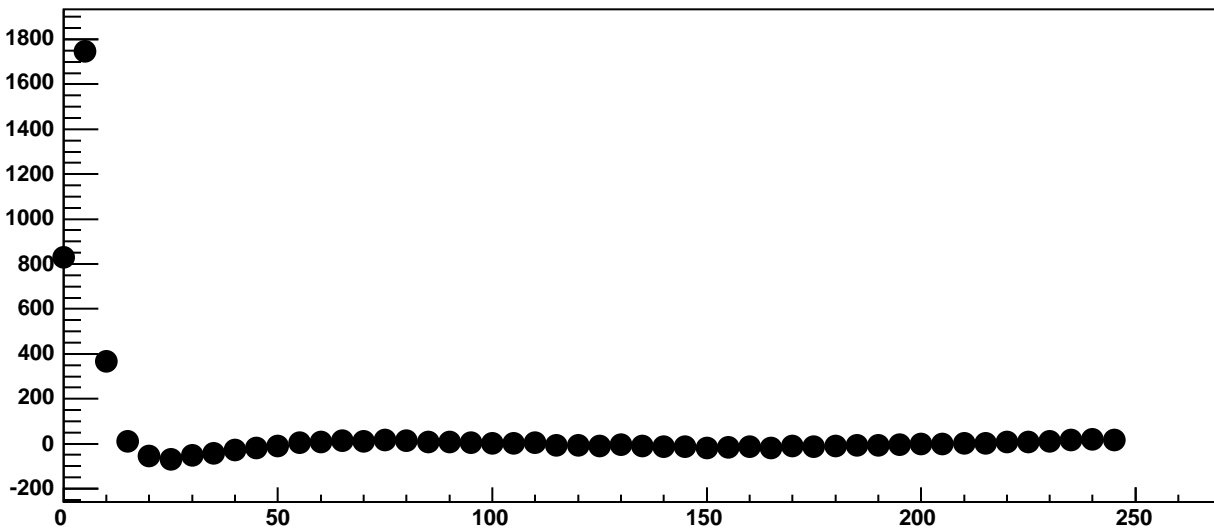


$\chi^2 / \text{ndf}$	1346 / 42
p0	-362.6 ± 0.5902
p1	4.708 ± 0.006043
p2	1.129e+04 ± 1.427
p3	31.08 ± 0.005655

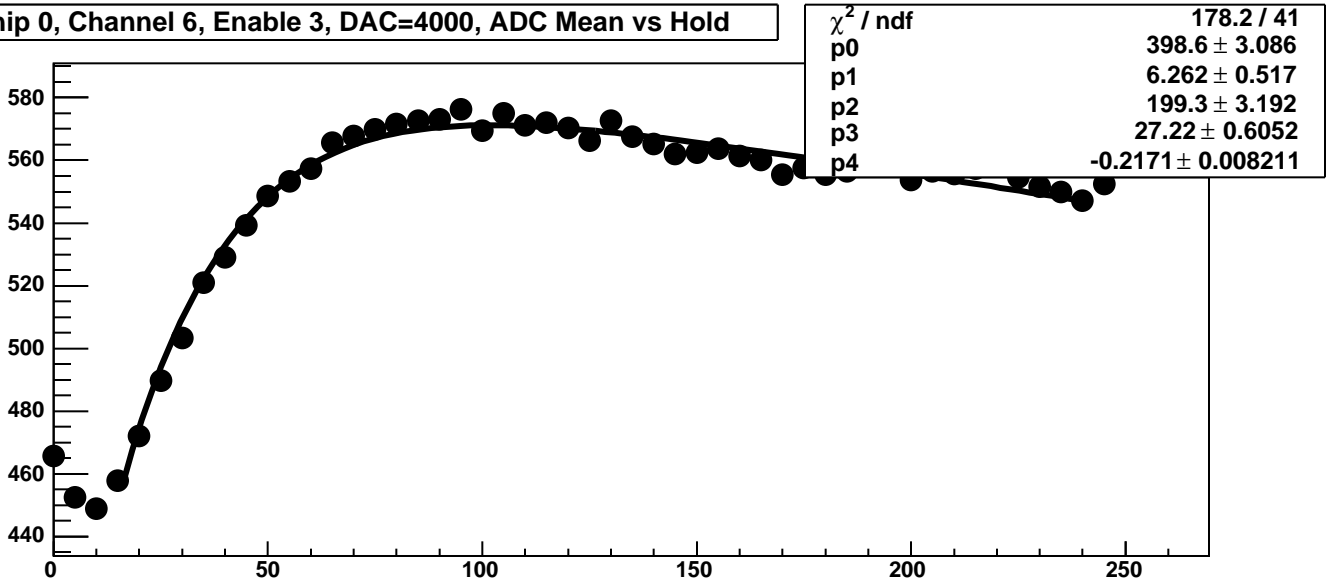
Chip 0, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold



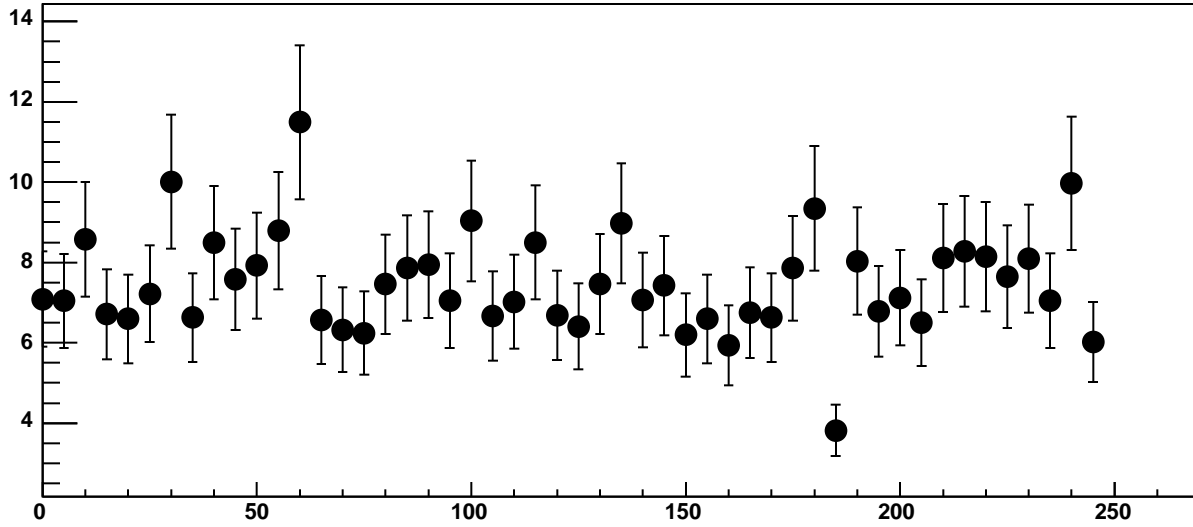
Chip 0, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold



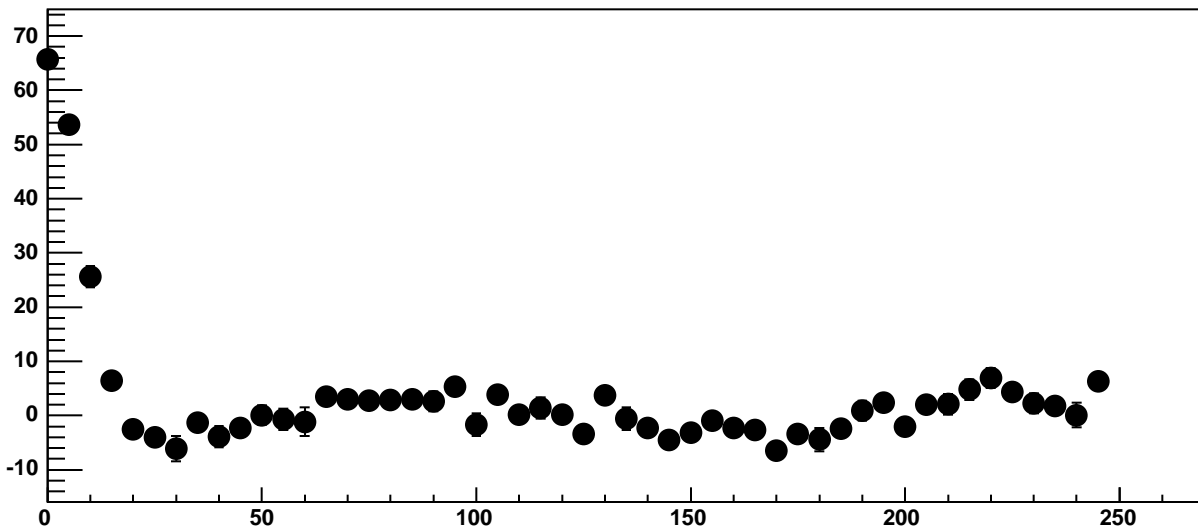
Chip 0, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold



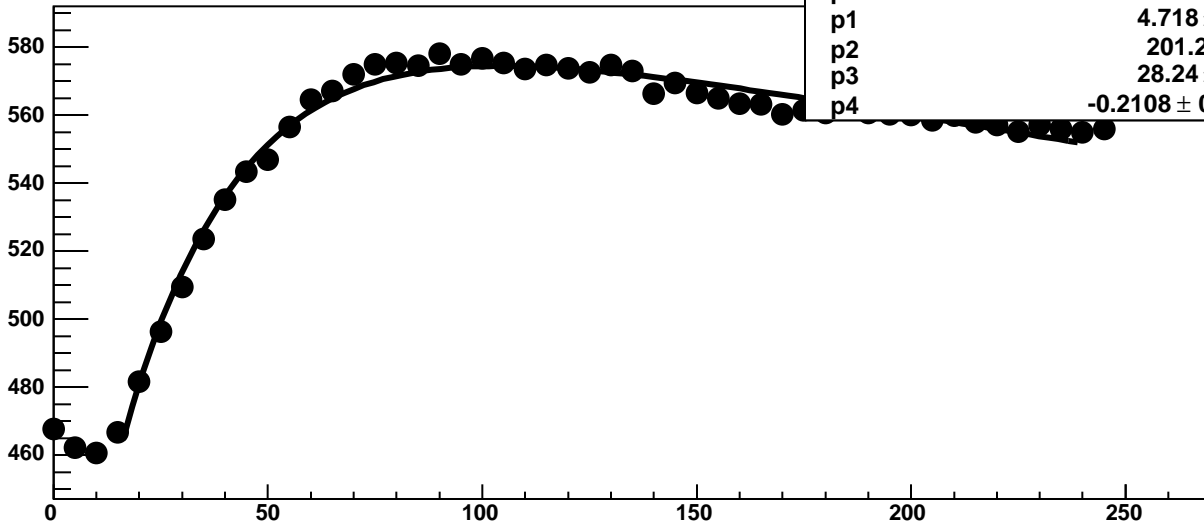
Chip 0, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

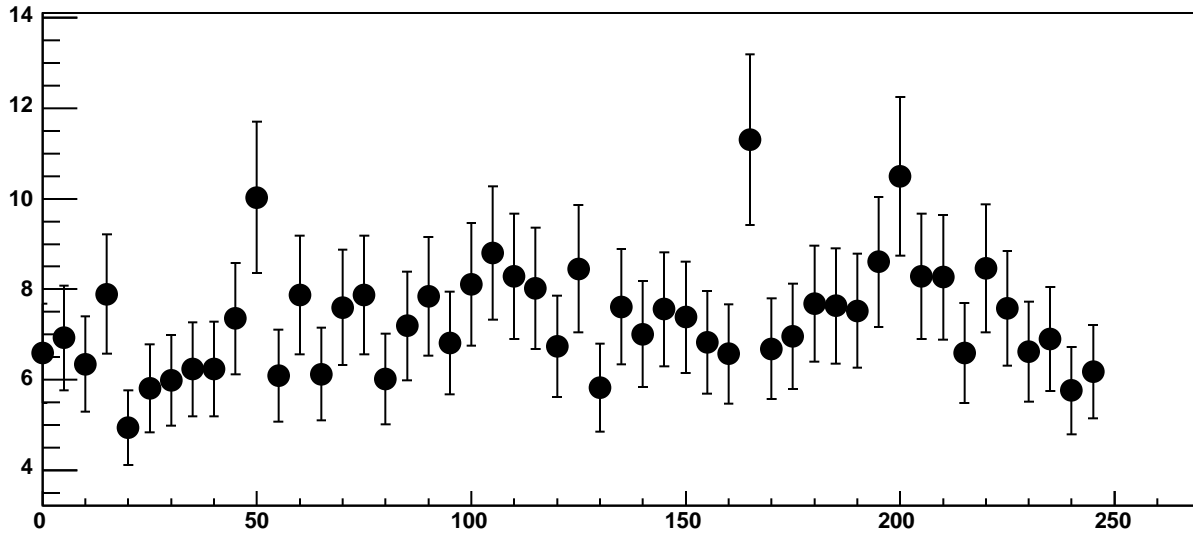


Chip 0, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

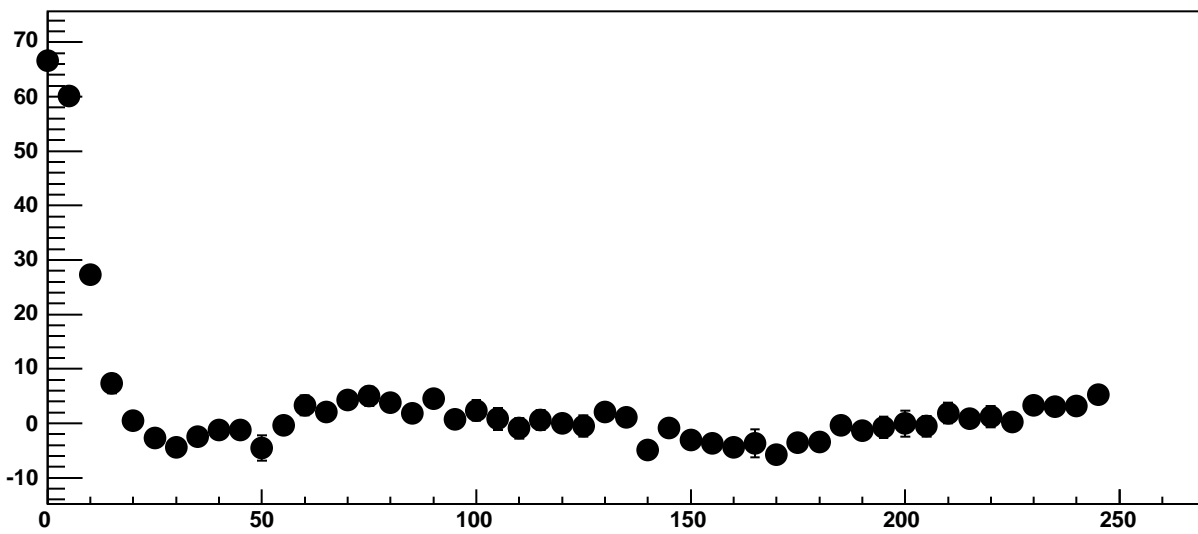


$\chi^2 / \text{ndf}$	147.6 / 41
p0	$400.2 \pm 3.062$
p1	$4.718 \pm 0.5381$
p2	$201.2 \pm 3.173$
p3	$28.24 \pm 0.6231$
p4	$-0.2108 \pm 0.007941$

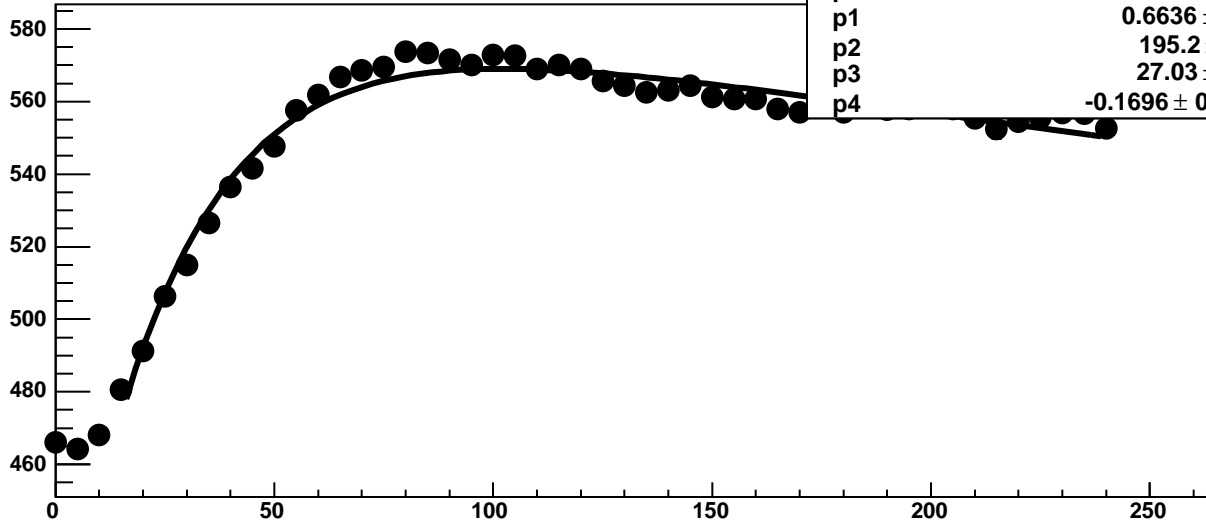
Chip 0, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

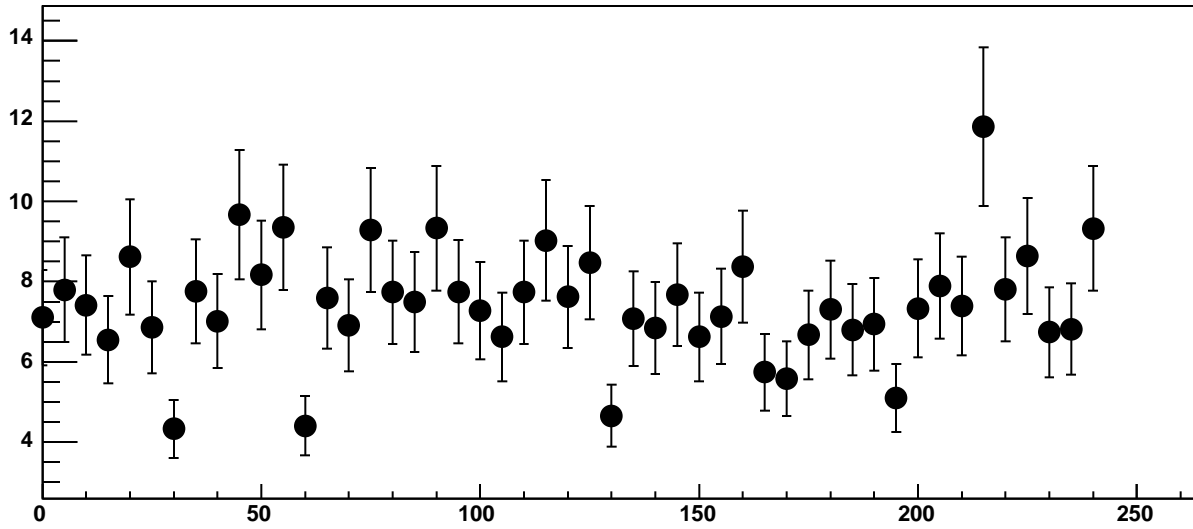


Chip 0, Channel 6, Enable 5, DAC=4000, ADC Mean vs Hold

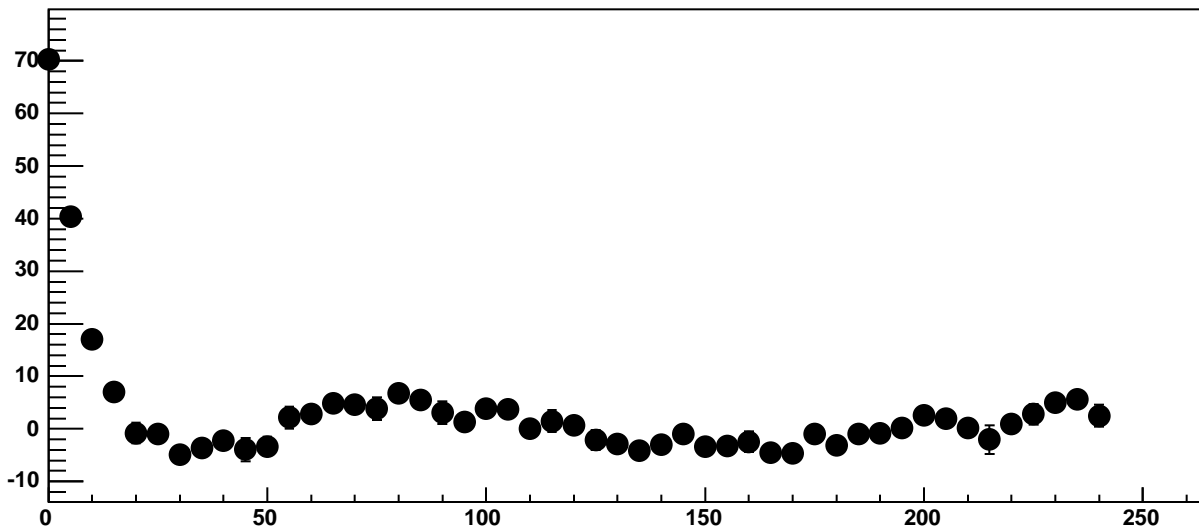


$\chi^2 / \text{ndf}$	218.3 / 41
p0	$395.6 \pm 0.7522$
p1	$0.6636 \pm 0.4709$
p2	$195.2 \pm 0.8171$
p3	$27.03 \pm 0.7177$
p4	$-0.1696 \pm 0.008355$

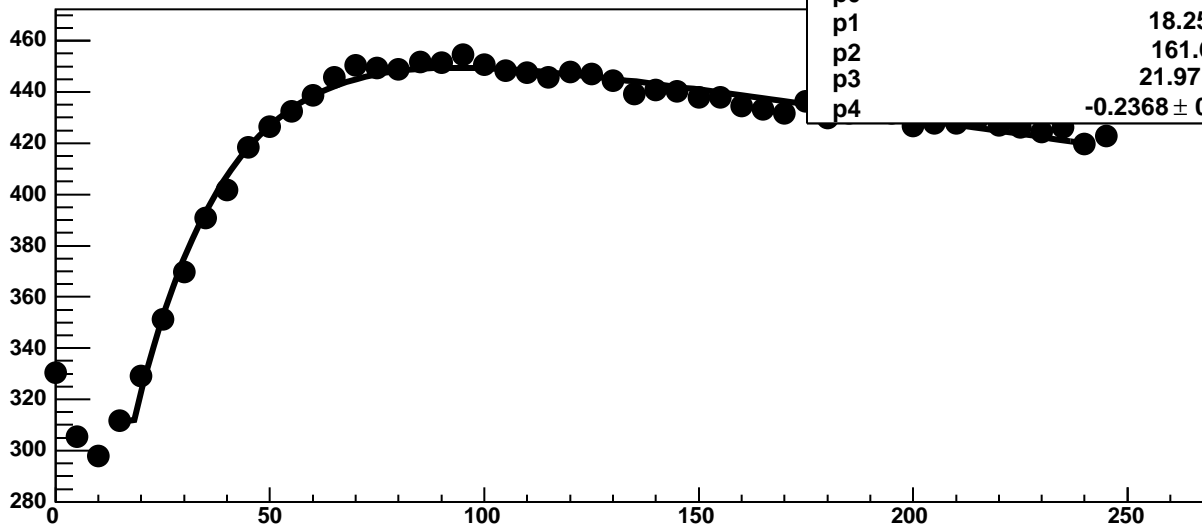
Chip 0, Channel 6, Enable 5, DAC=4000, ADC Noise vs Hold



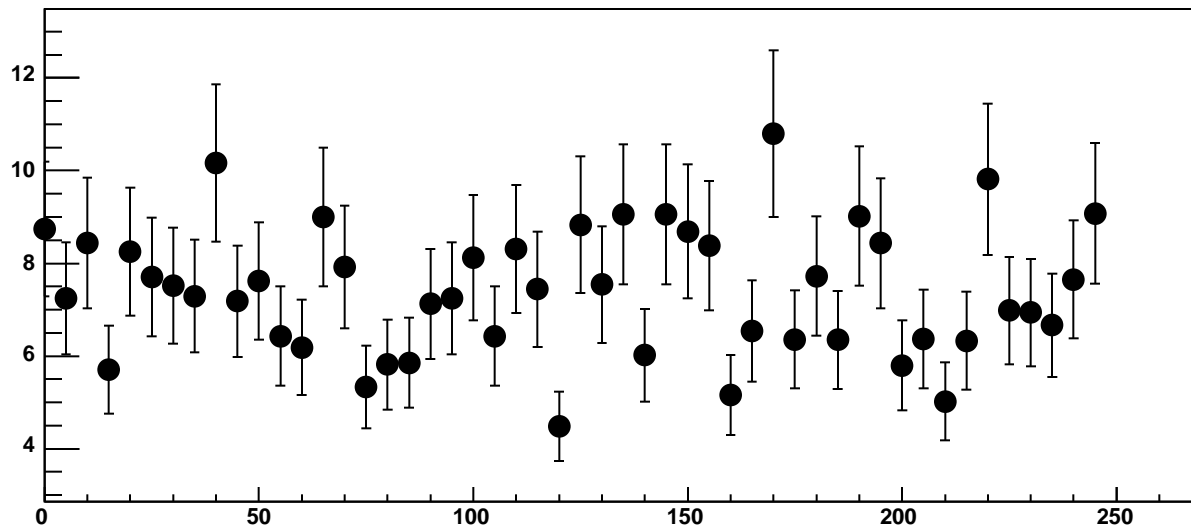
Chip 0, Channel 6, Enable 5, DAC=4000, ADC Residuals vs Hold



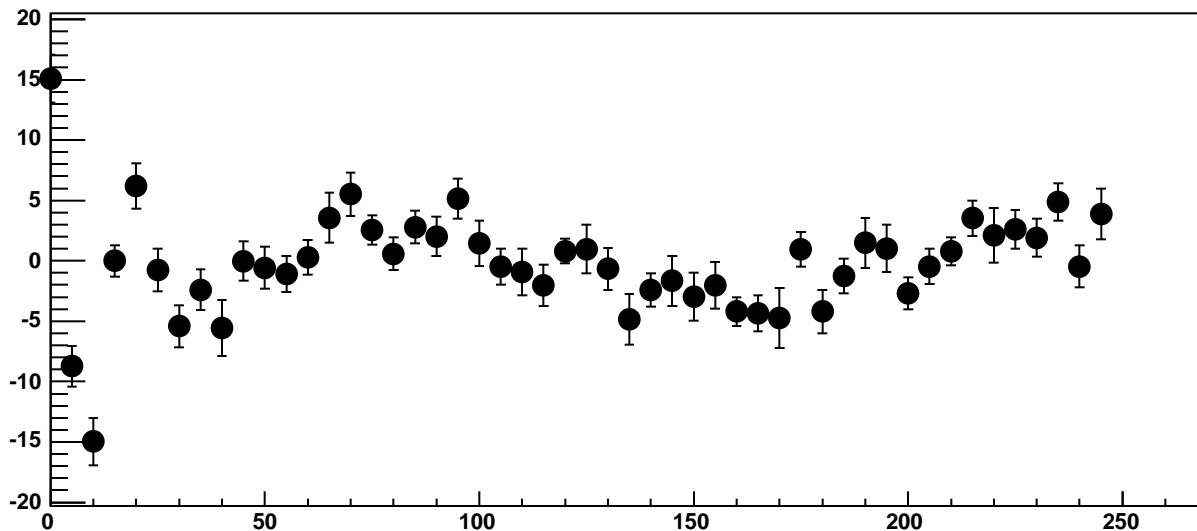
Chip 0, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold



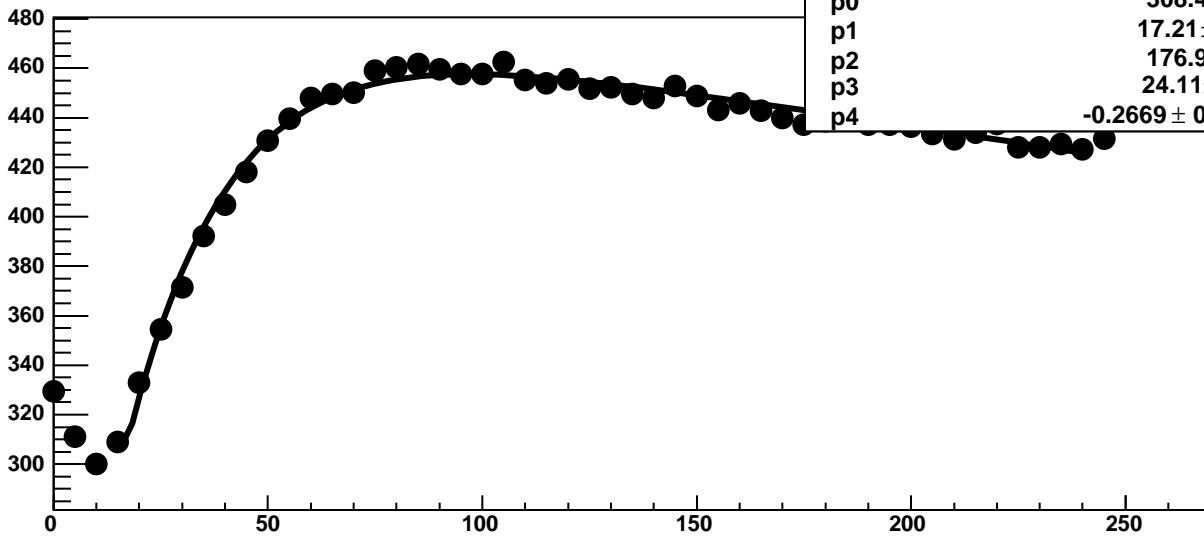
Chip 0, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

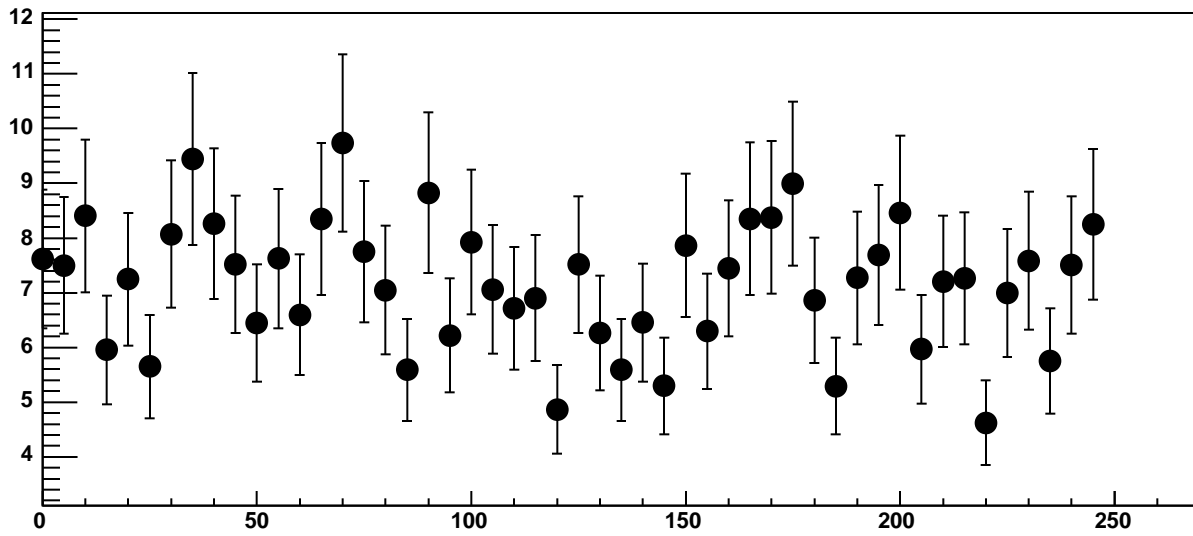


Chip 0, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold

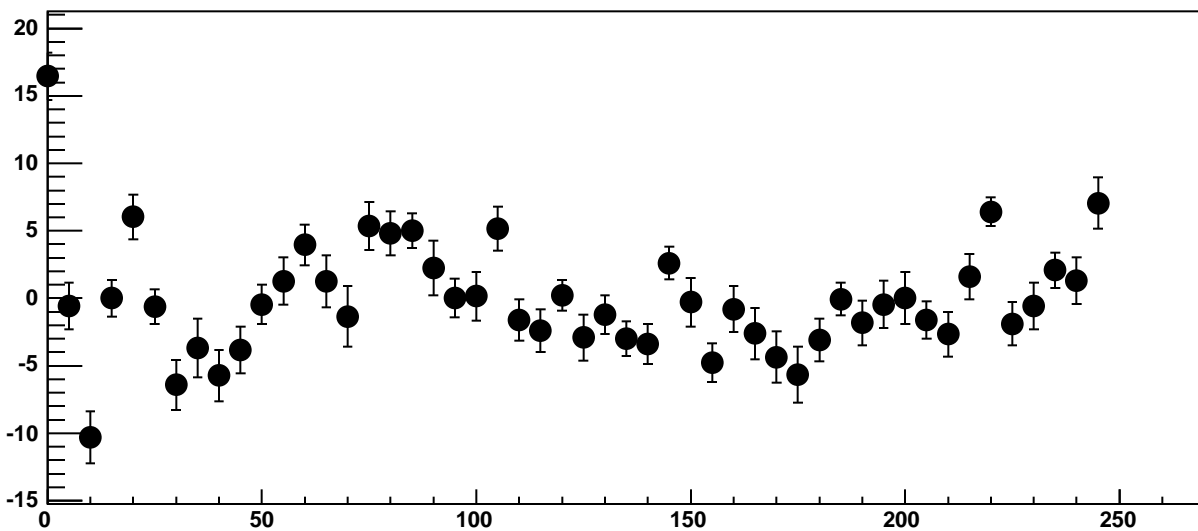


$\chi^2 / \text{ndf}$	194.2 / 41
p0	$308.4 \pm 1.317$
p1	$17.21 \pm 0.2839$
p2	$176.9 \pm 1.846$
p3	$24.11 \pm 0.4891$
p4	$-0.2669 \pm 0.007414$

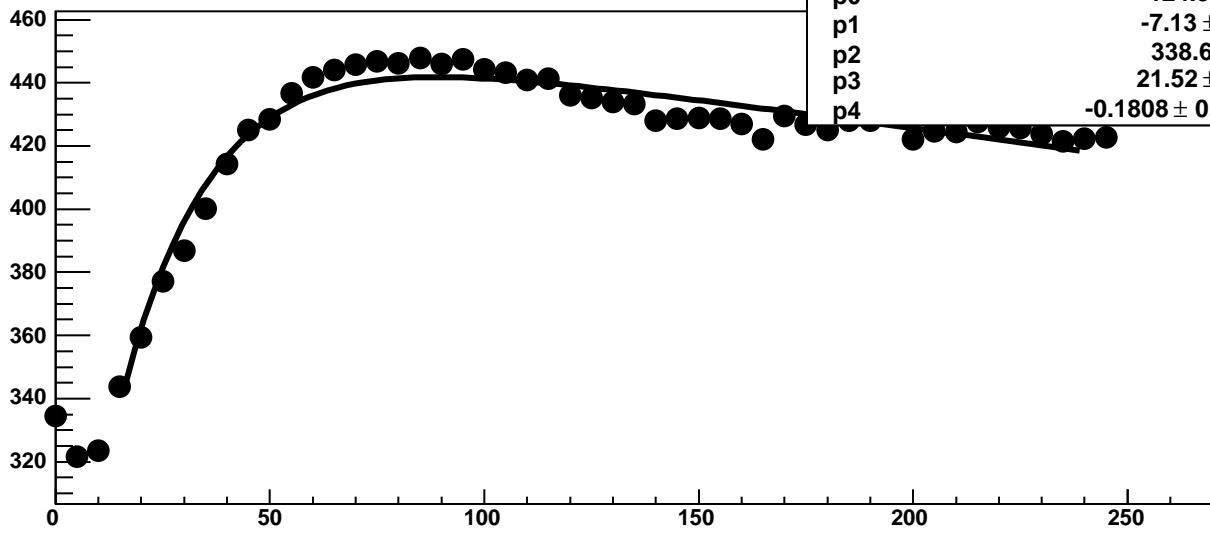
Chip 0, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

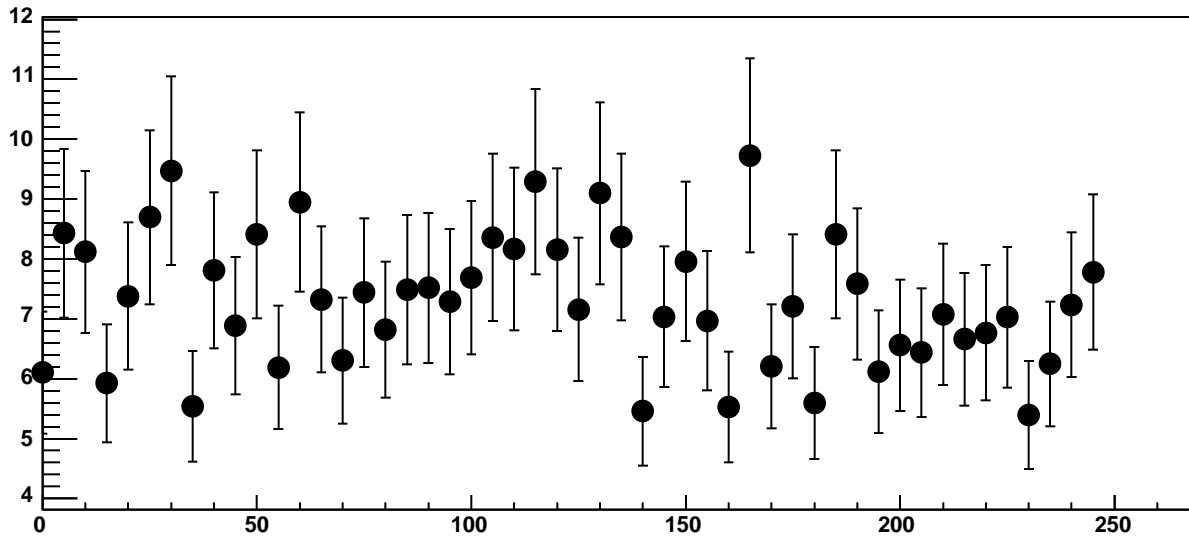


Chip 0, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold

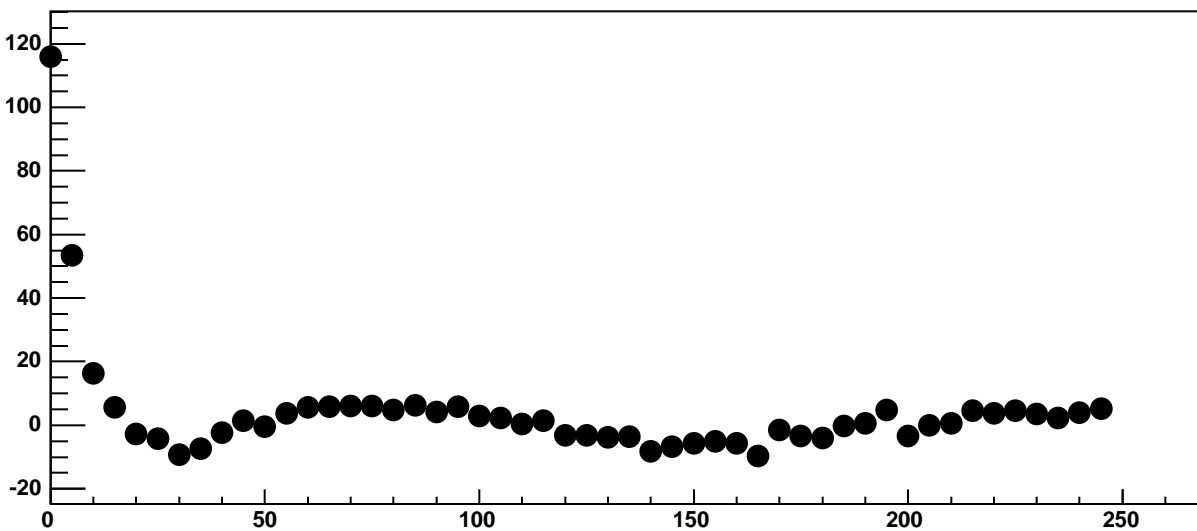


$\chi^2 / \text{ndf}$	384.6 / 41
p0	$124.5 \pm 3.026$
p1	$-7.13 \pm 0.5472$
p2	$338.6 \pm 3.061$
p3	$21.52 \pm 0.4578$
p4	$-0.1808 \pm 0.006096$

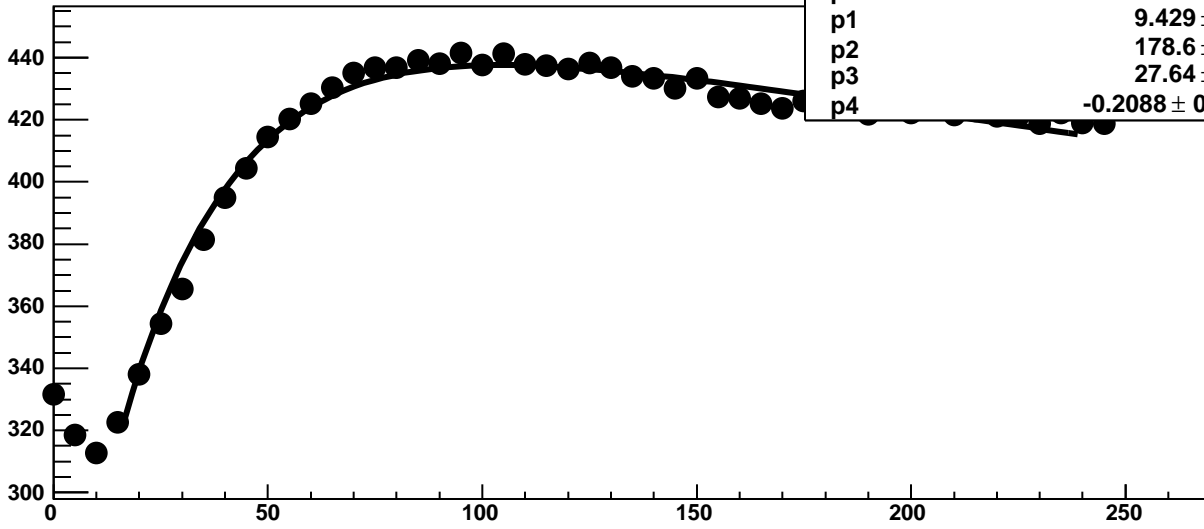
Chip 0, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold

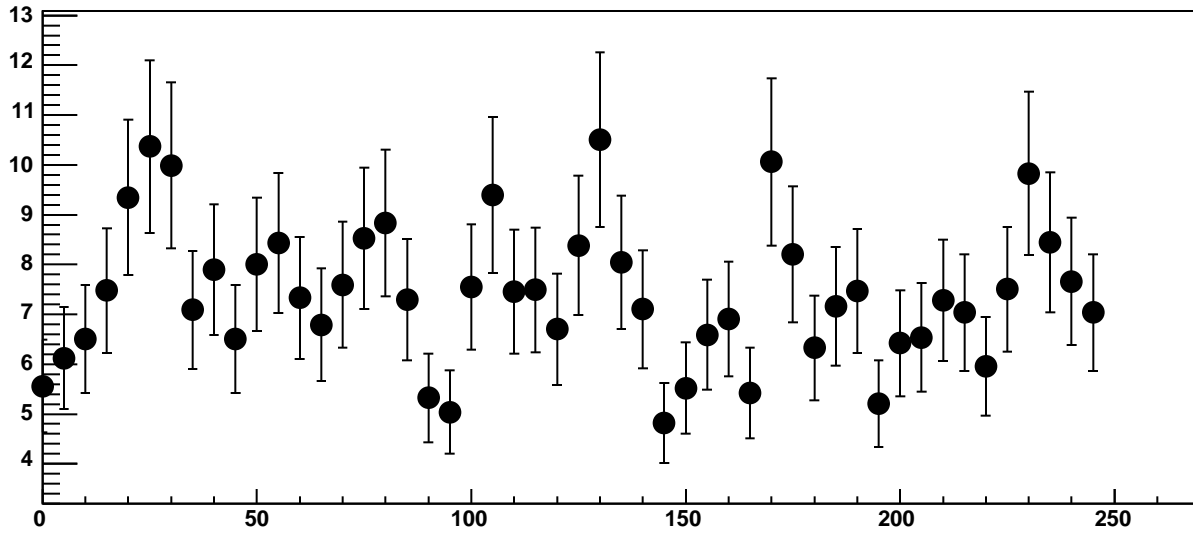


Chip 0, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

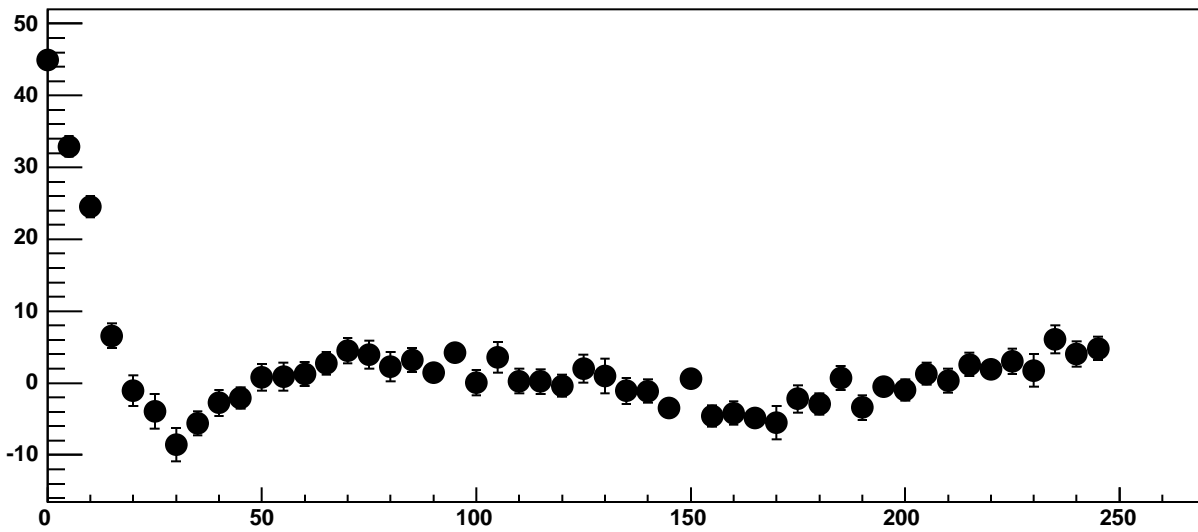


$\chi^2 / \text{ndf}$	168.3 / 41
p0	$284.6 \pm 1.731$
p1	$9.429 \pm 0.3318$
p2	$178.6 \pm 0.3889$
p3	$27.64 \pm 0.6459$
p4	$-0.2088 \pm 0.008656$

Chip 0, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold

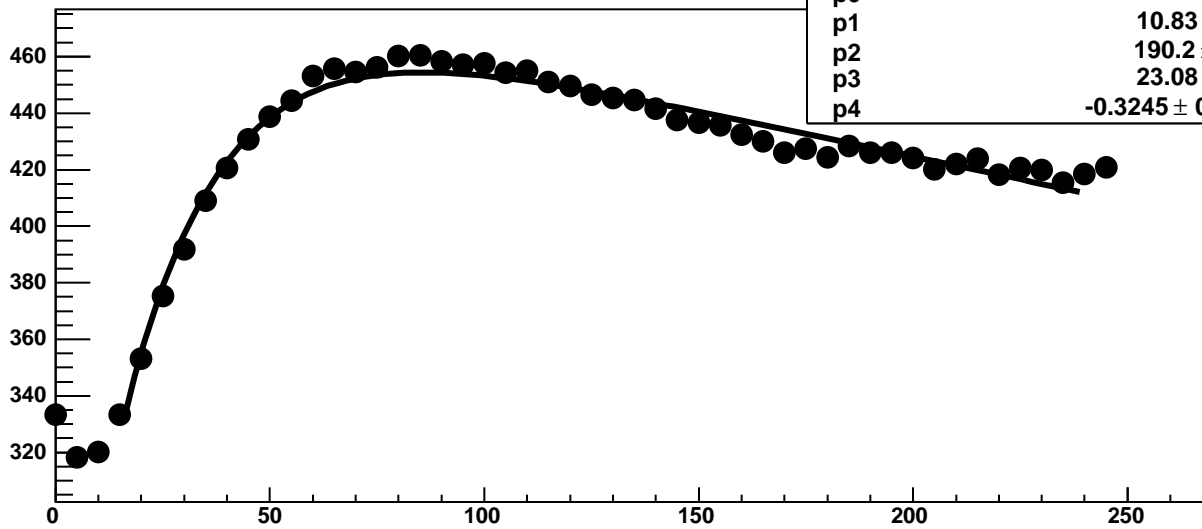


Chip 0, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

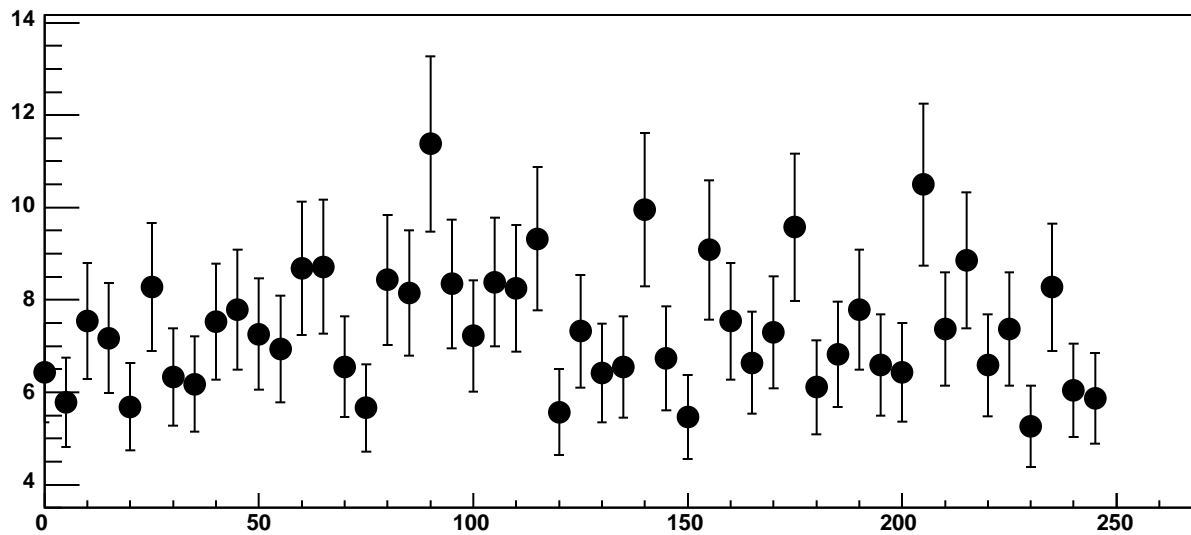




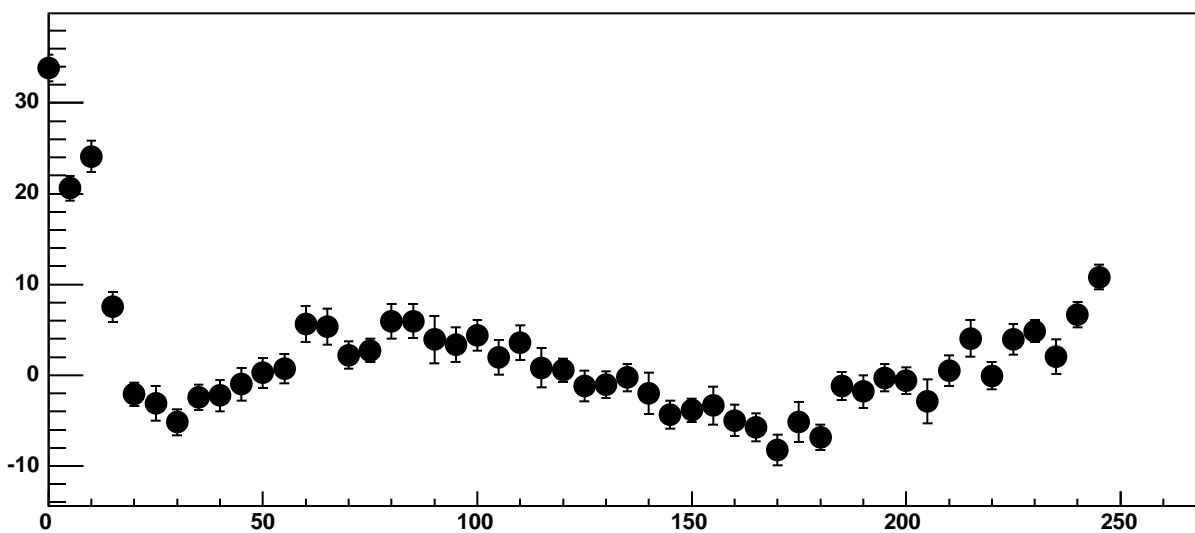
Chip 0, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold



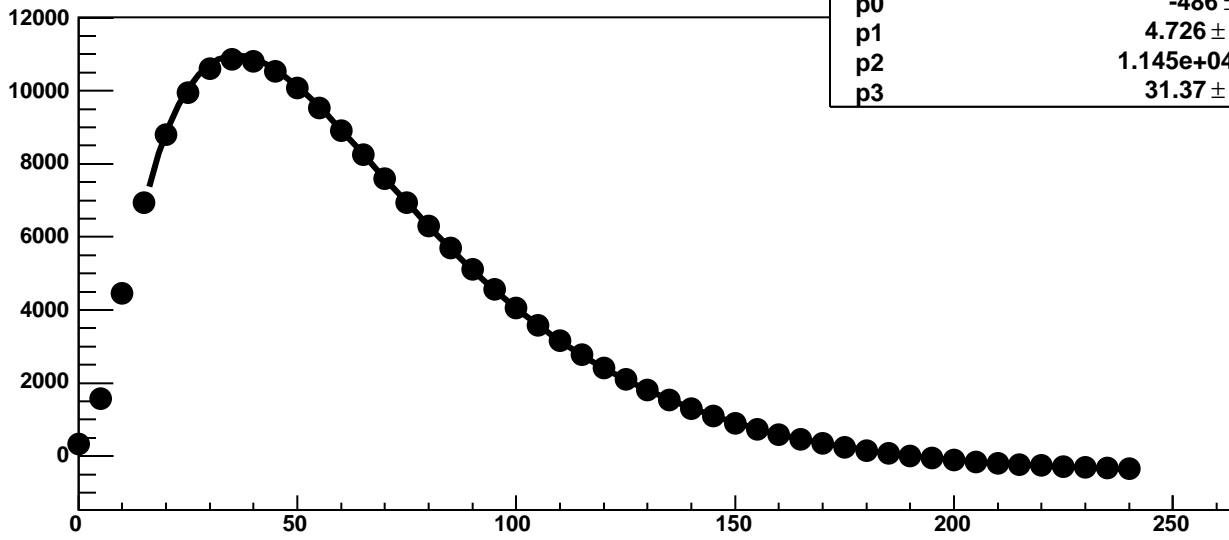
Chip 0, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

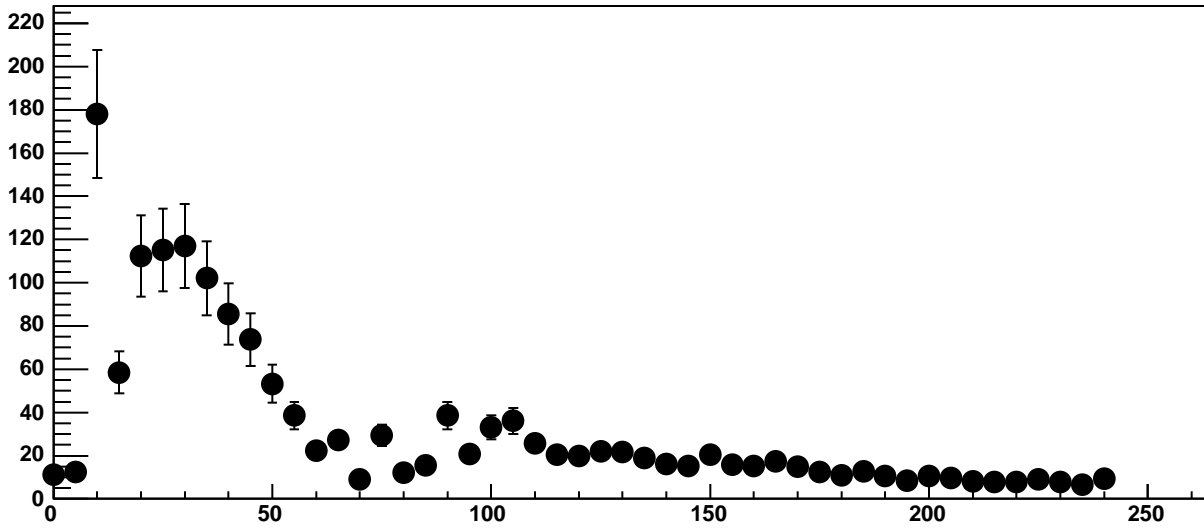


Chip 0, Channel 7, Enable 5!, DAC=4000, ADC Mean vs Hold

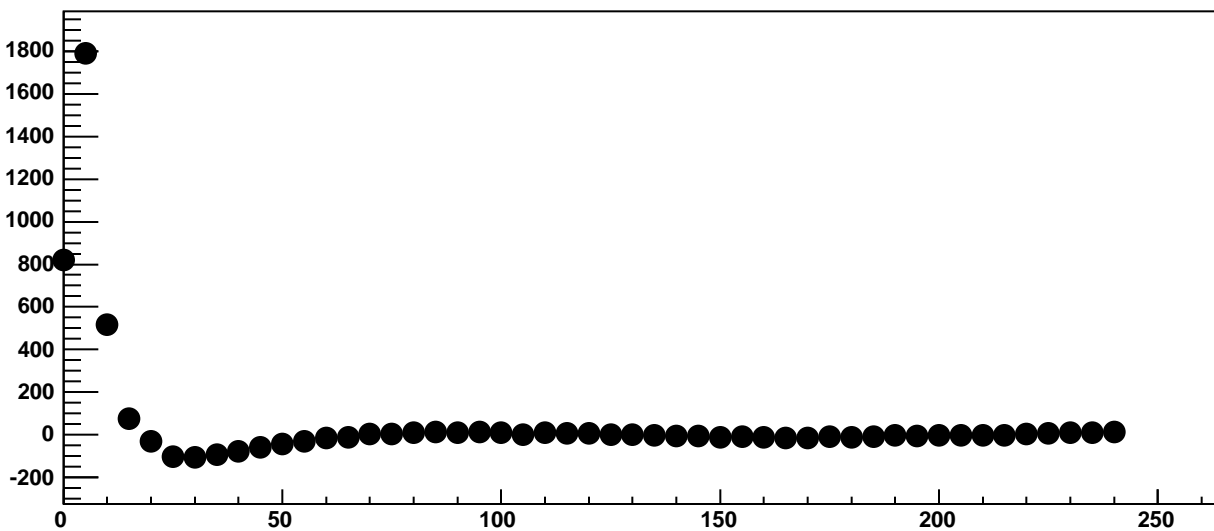


$\chi^2 / \text{ndf}$	423 / 42
p0	$-486 \pm 0.8374$
p1	$4.726 \pm 0.02468$
p2	$1.145\text{e}+04 \pm 4.061$
p3	$31.37 \pm 0.01269$

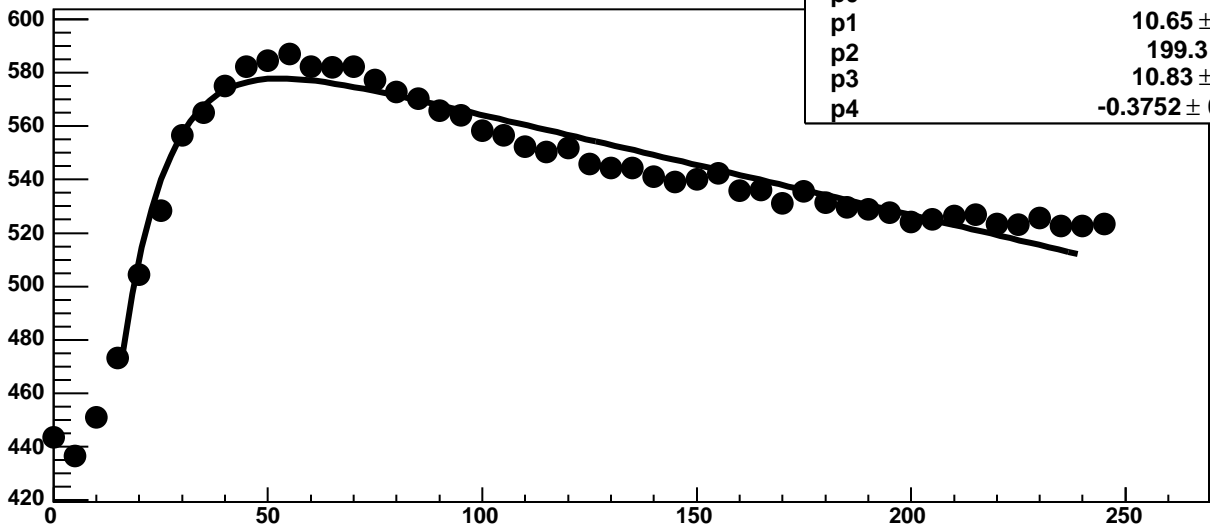
Chip 0, Channel 7, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 7, Enable 5!, DAC=4000, ADC Residuals vs Hold

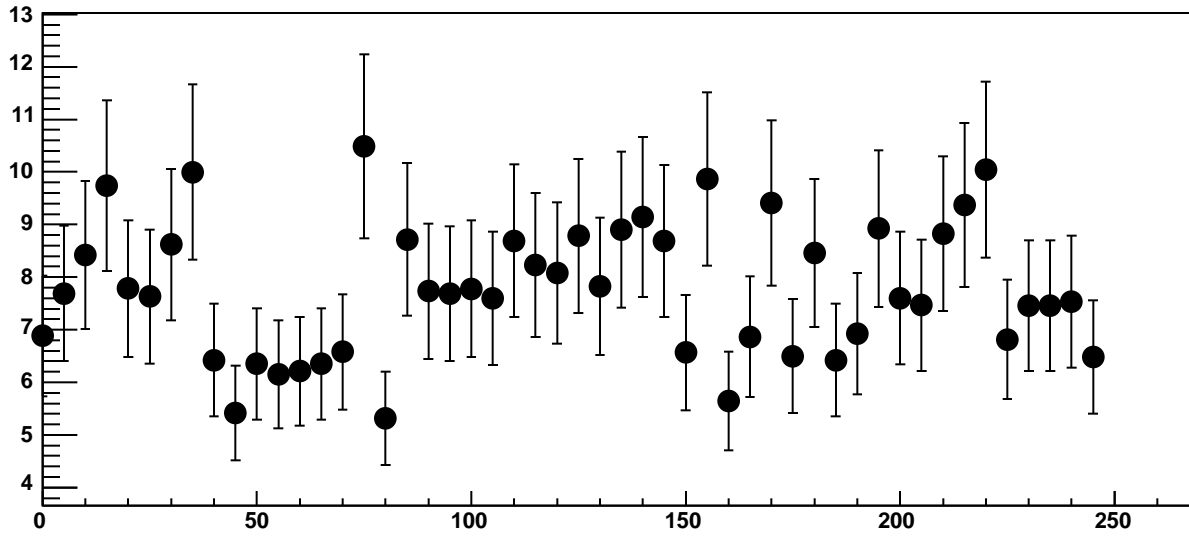


Chip 0, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

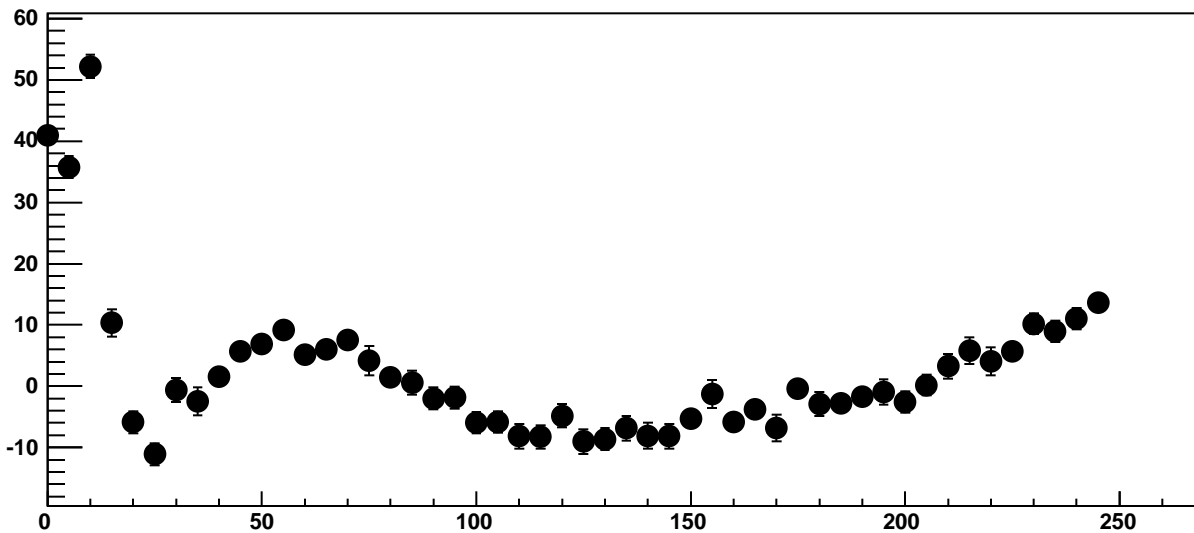


$\chi^2 / \text{ndf}$	562.4 / 41
p0	$398.5 \pm 3.296$
p1	$10.65 \pm 0.2776$
p2	$199.3 \pm 3.364$
p3	$10.83 \pm 0.2454$
p4	$-0.3752 \pm 0.00482$

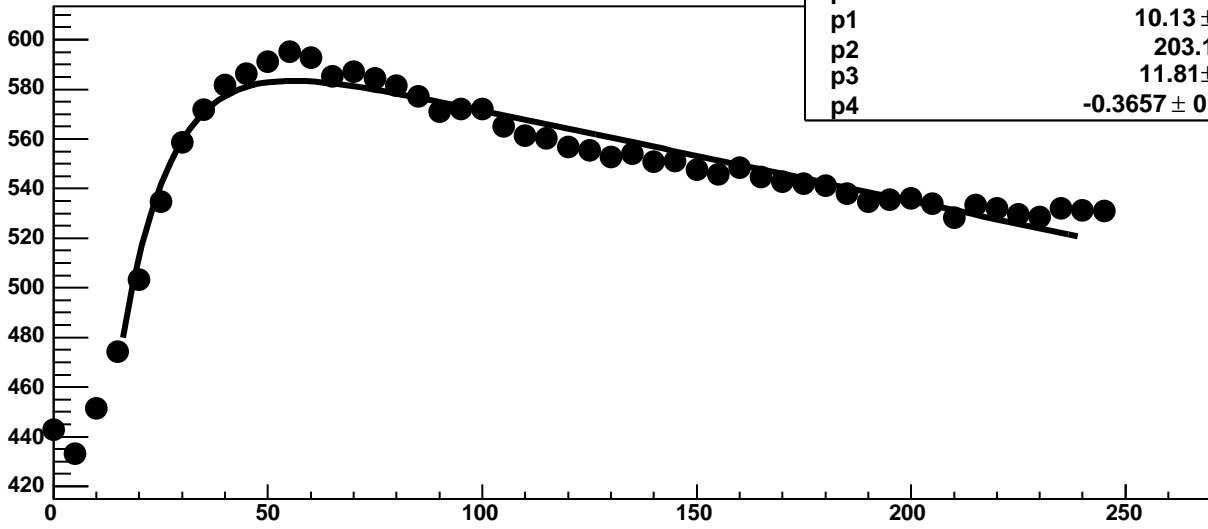
Chip 0, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

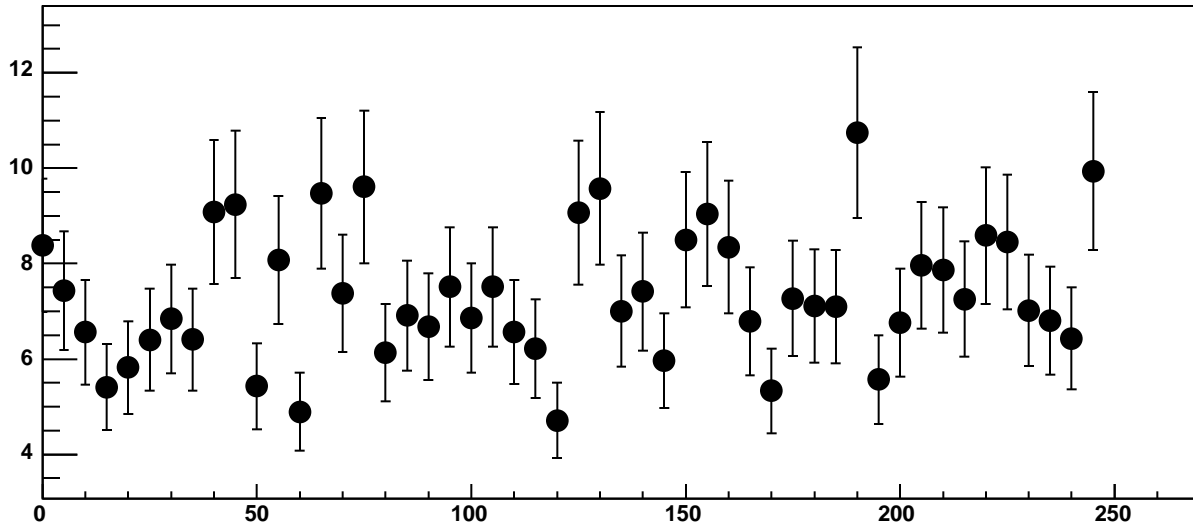


Chip 0, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

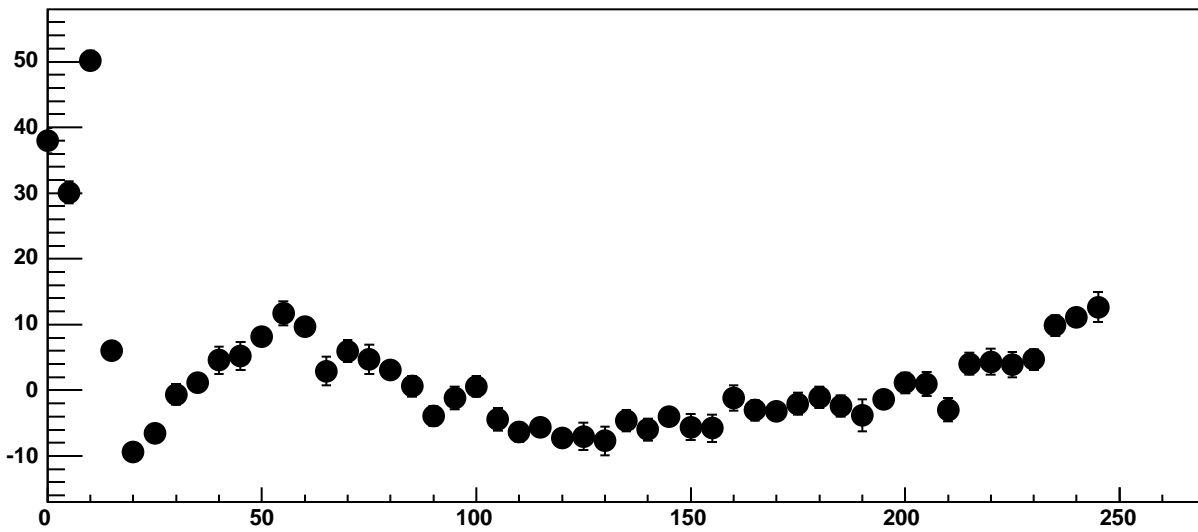


$\chi^2 / \text{ndf}$	588.9 / 41
p0	$401.2 \pm 3.044$
p1	$10.13 \pm 0.2329$
p2	$203.1 \pm 3.111$
p3	$11.81 \pm 0.2149$
p4	$-0.3657 \pm 0.004718$

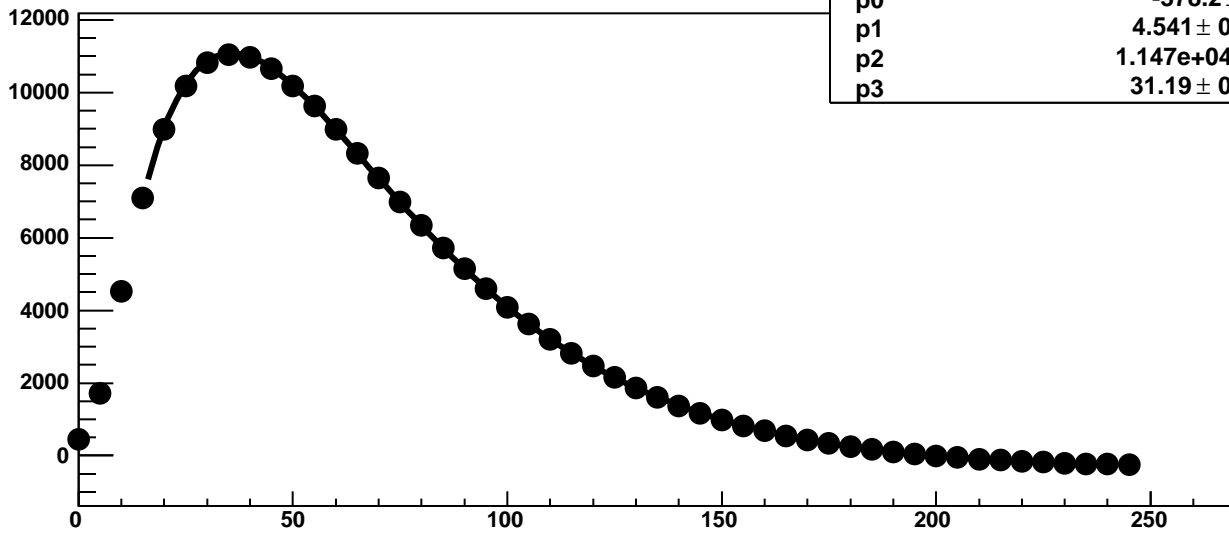
Chip 0, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

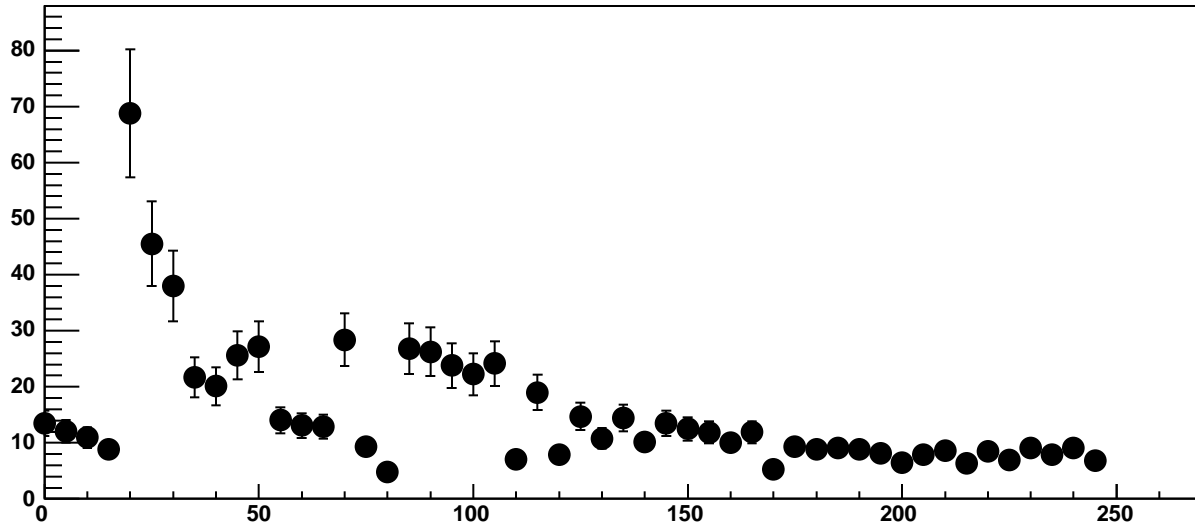


Chip 0, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold

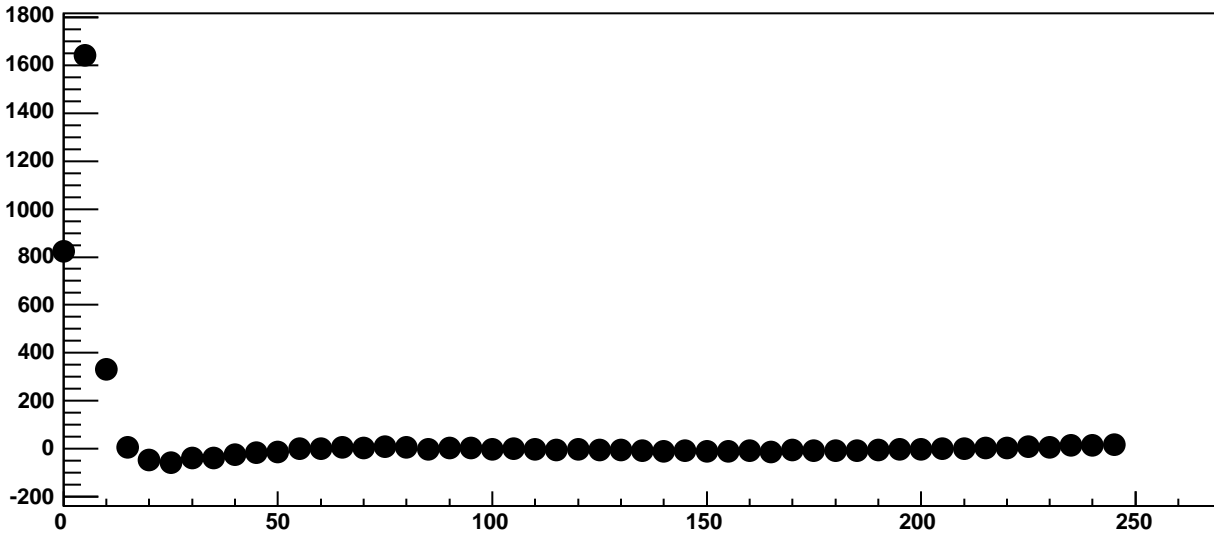


$\chi^2 / \text{ndf}$	545.6 / 42
p0	$-378.2 \pm 0.6362$
p1	$4.541 \pm 0.006425$
p2	$1.147\text{e}+04 \pm 1.664$
p3	$31.19 \pm 0.006376$

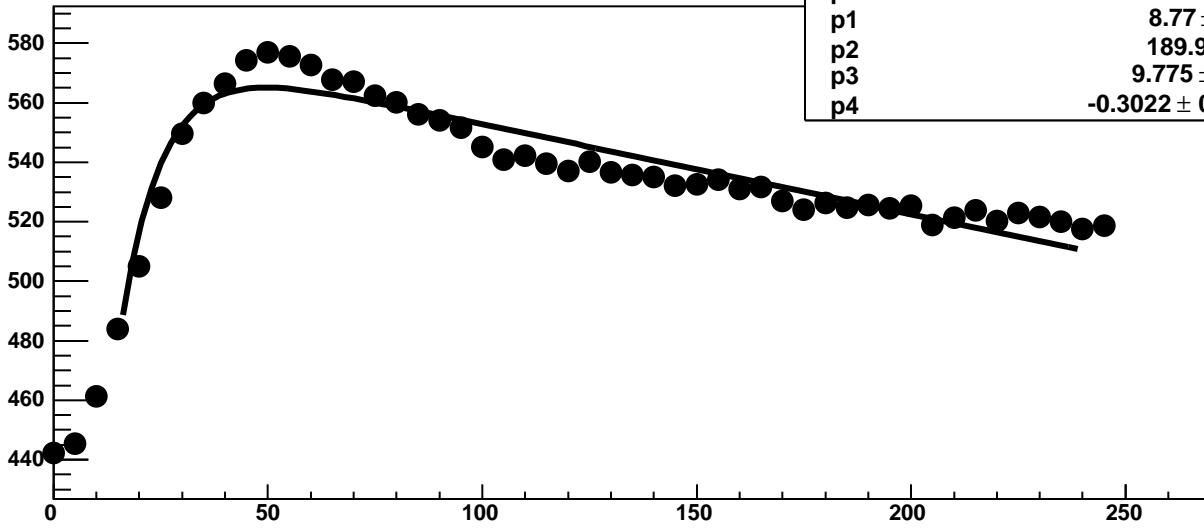
Chip 0, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold

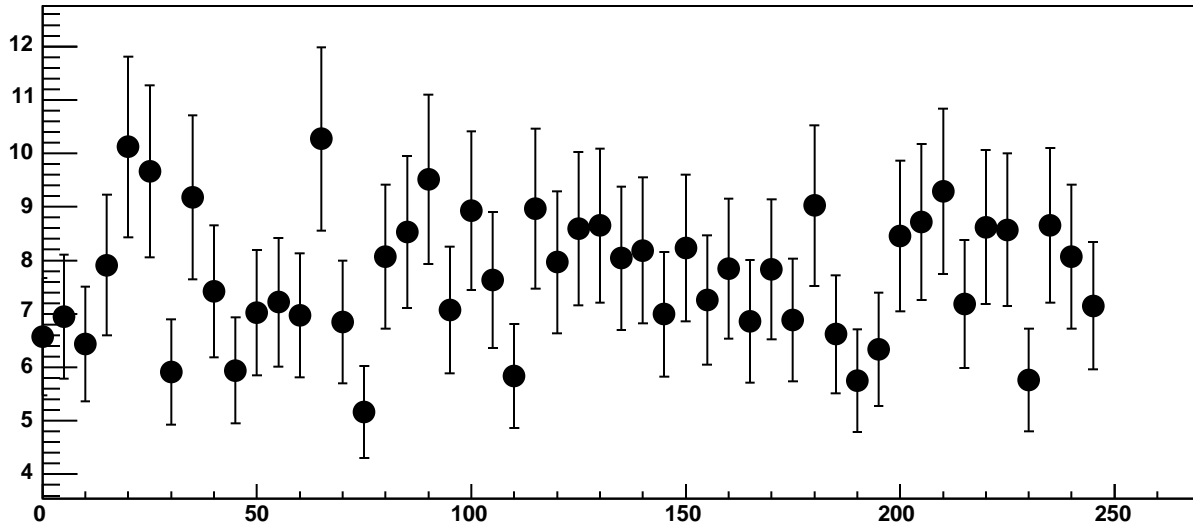


Chip 0, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

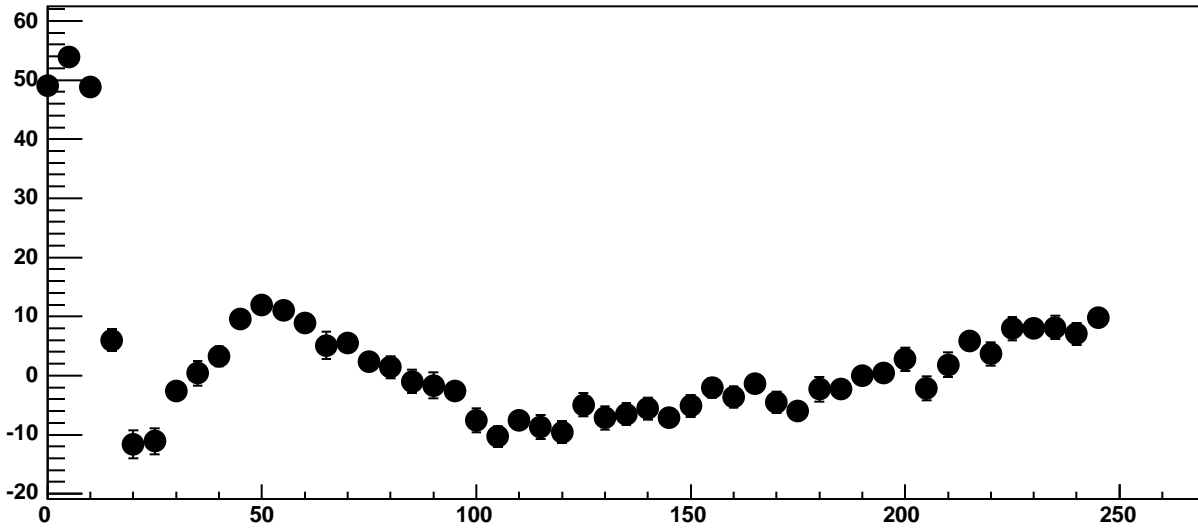


$\chi^2 / \text{ndf}$	600.7 / 41
p0	$390.4 \pm 3.315$
p1	$8.77 \pm 0.3162$
p2	$189.9 \pm 3.366$
p3	$9.775 \pm 0.2715$
p4	$-0.3022 \pm 0.004701$

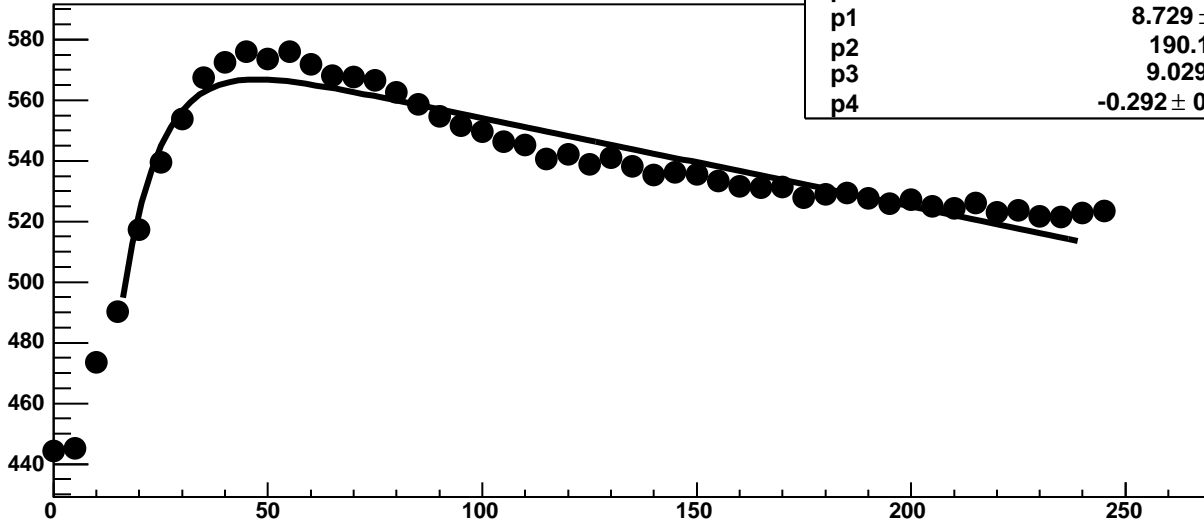
Chip 0, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

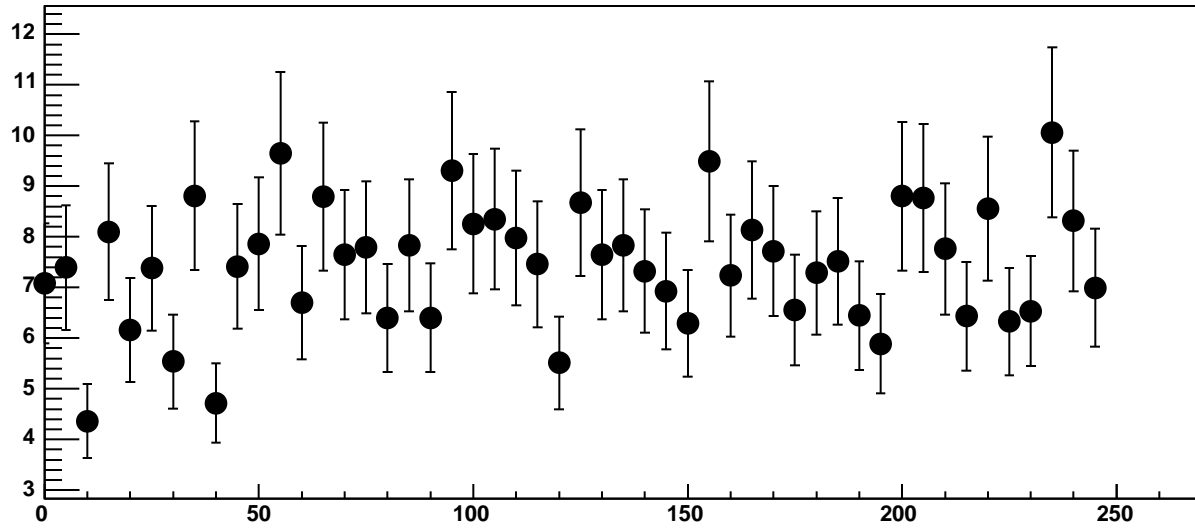


Chip 0, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

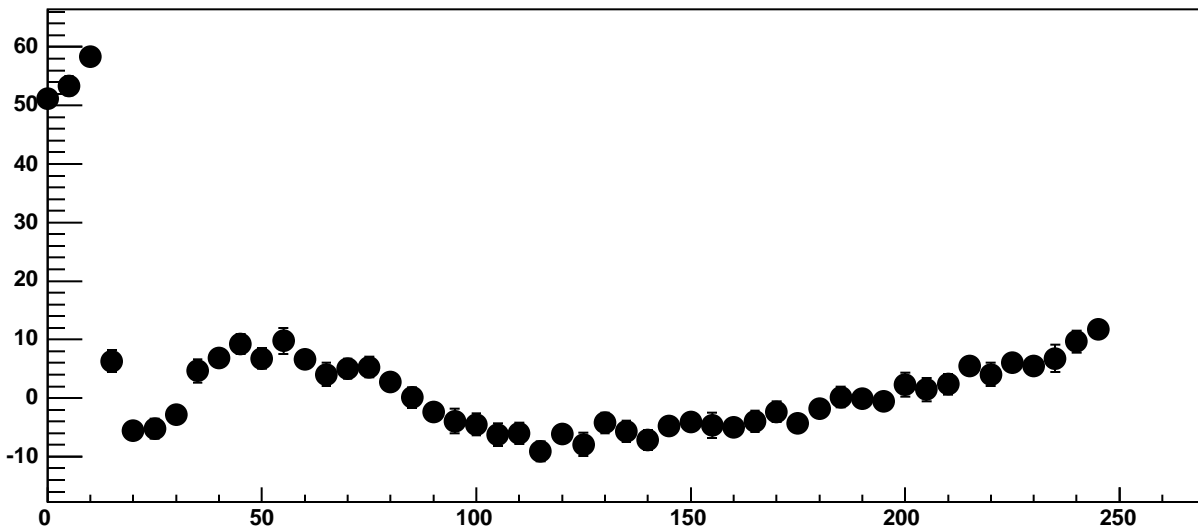


$\chi^2 / \text{ndf}$	461.1 / 41
p0	$390.6 \pm 3.2$
p1	$8.729 \pm 0.3105$
p2	$190.1 \pm 3.245$
p3	$9.029 \pm 0.253$
p4	$-0.292 \pm 0.004615$

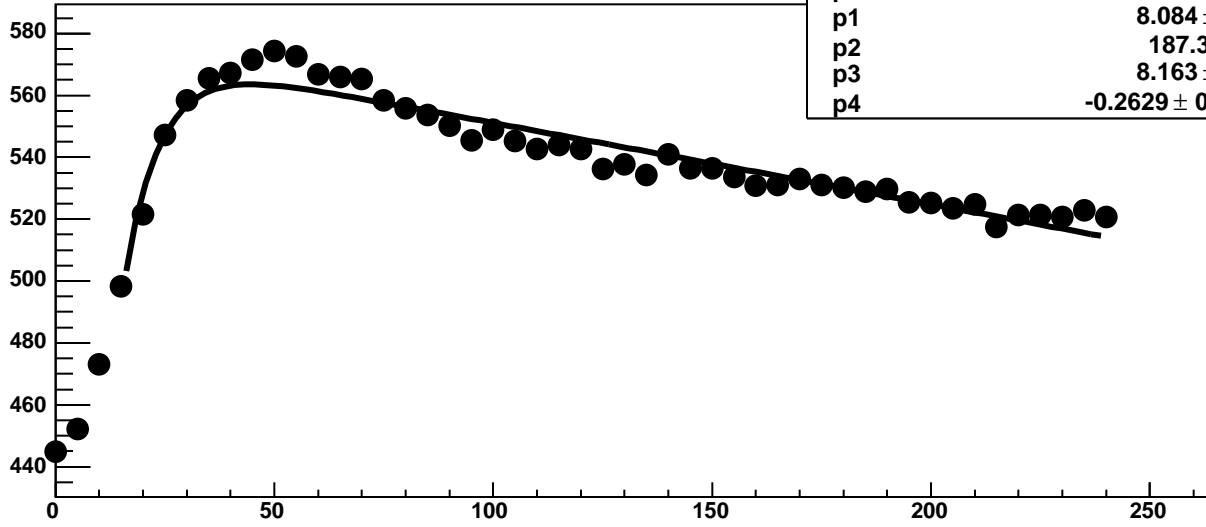
Chip 0, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

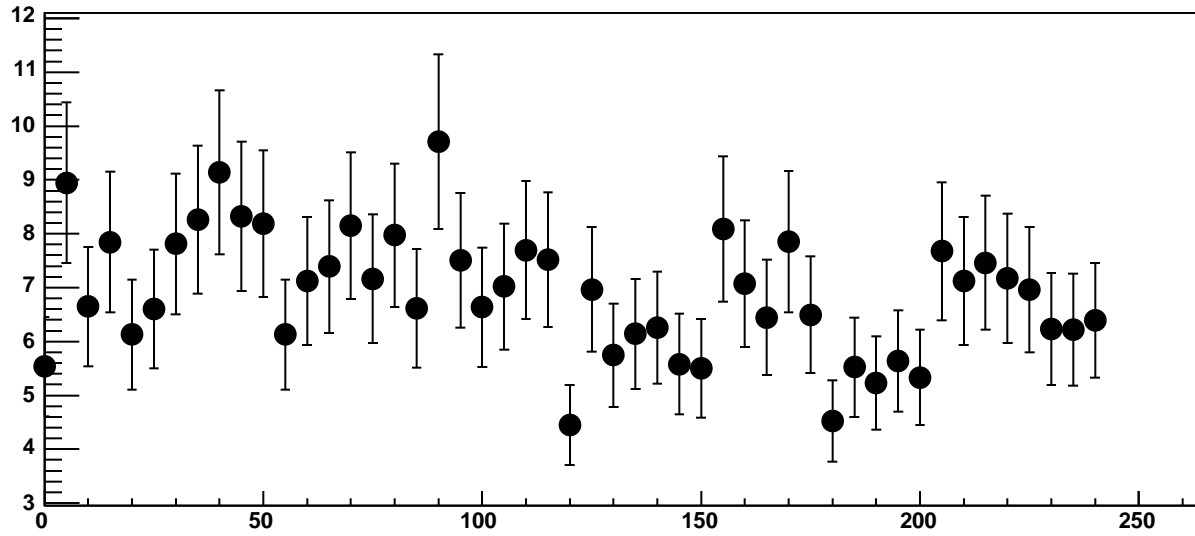


Chip 0, Channel 8, Enable 5, DAC=4000, ADC Mean vs Hold

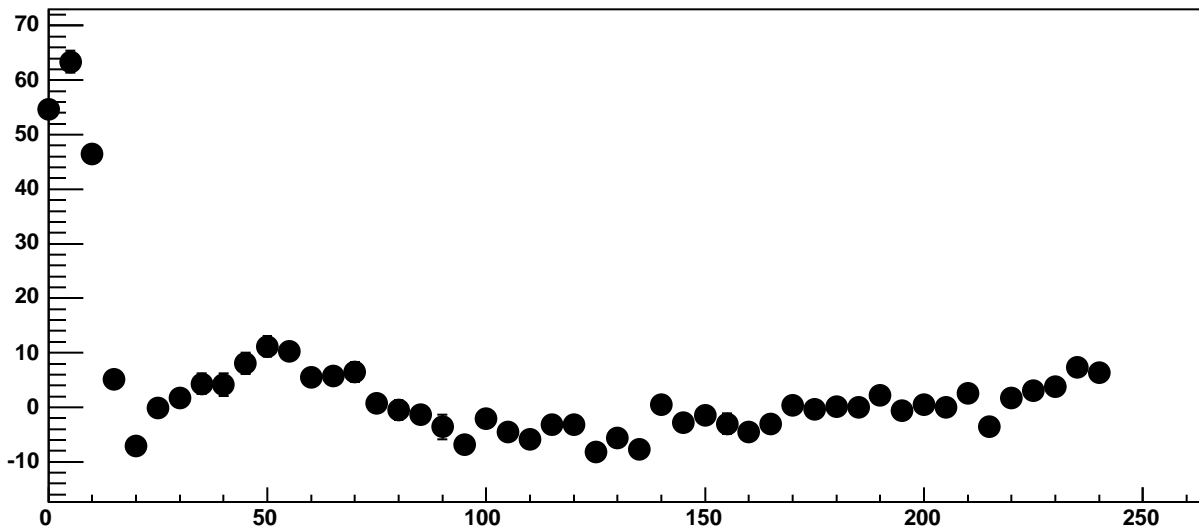


$\chi^2 / \text{ndf}$	401.6 / 41
p0	$388 \pm 2.93$
p1	$8.084 \pm 0.3573$
p2	$187.3 \pm 2.963$
p3	$8.163 \pm 0.2788$
p4	$-0.2629 \pm 0.004204$

Chip 0, Channel 8, Enable 5, DAC=4000, ADC Noise vs Hold

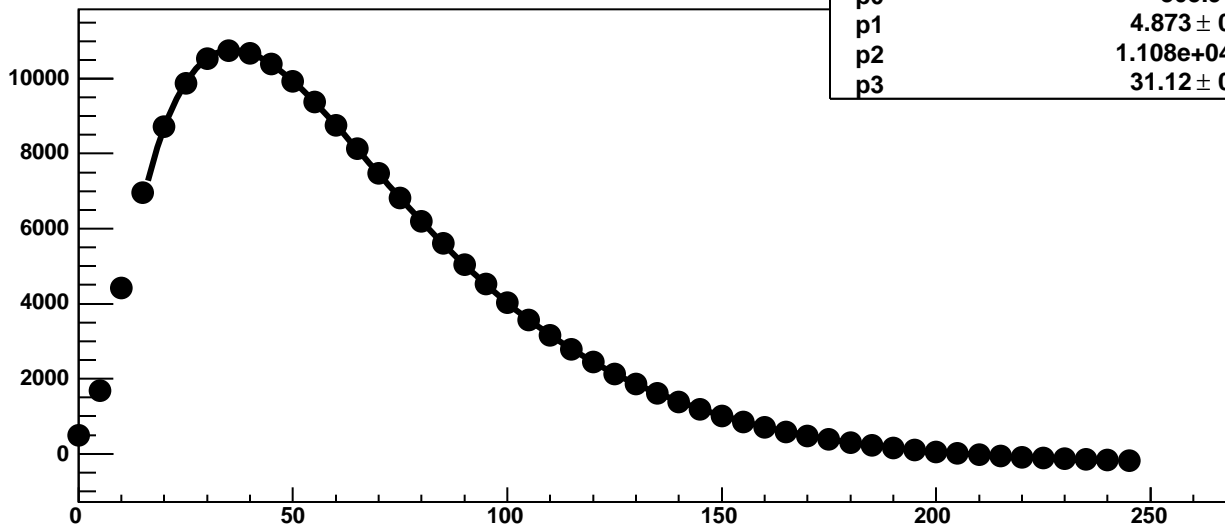


Chip 0, Channel 8, Enable 5, DAC=4000, ADC Residuals vs Hold



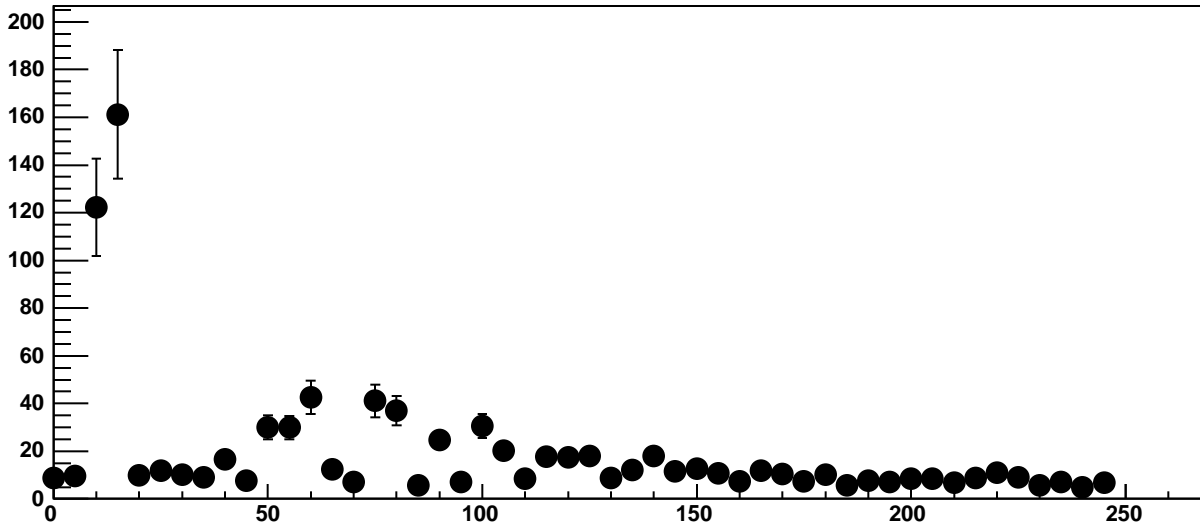


Chip 0, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold

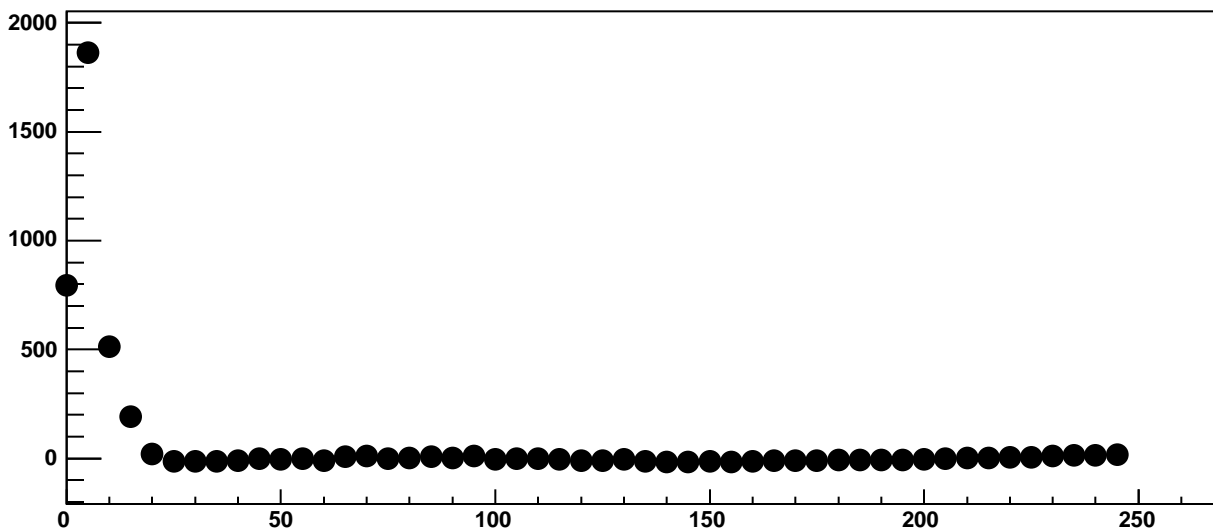


$\chi^2 / \text{ndf}$	990.8 / 42
p0	-305.9 ± 0.5677
p1	4.873 ± 0.009157
p2	1.108e+04 ± 1.019
p3	31.12 ± 0.005947

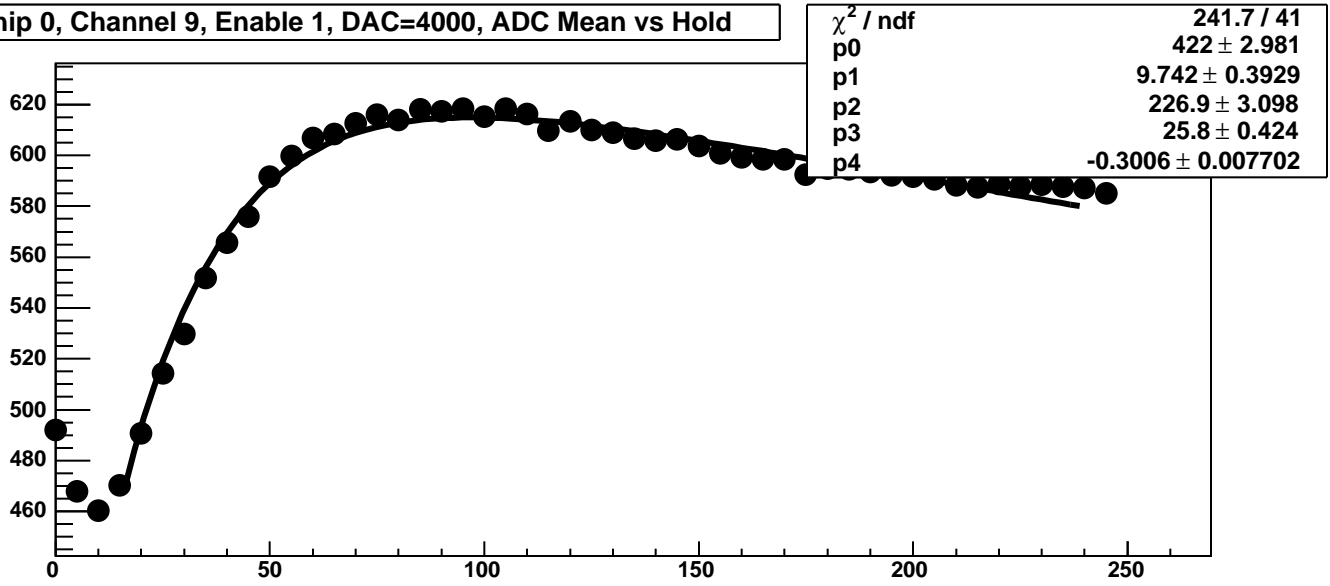
Chip 0, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold



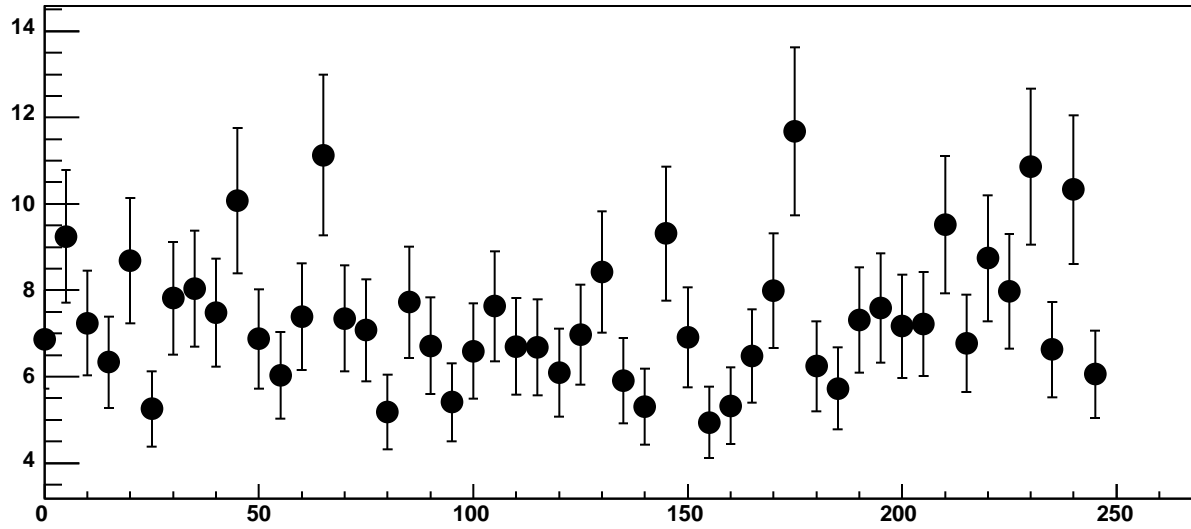
Chip 0, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold



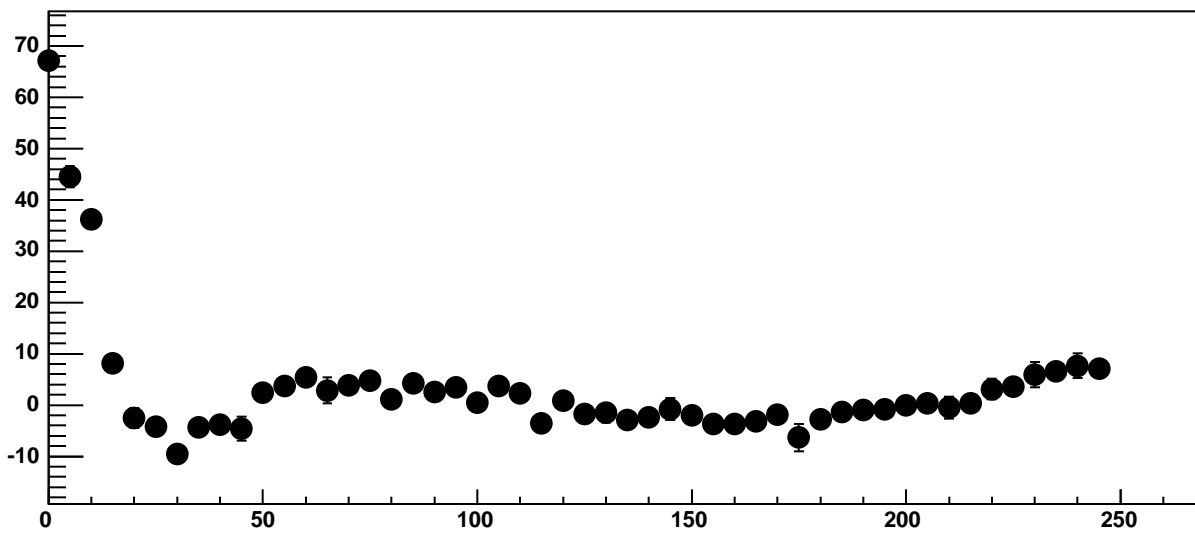
Chip 0, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold



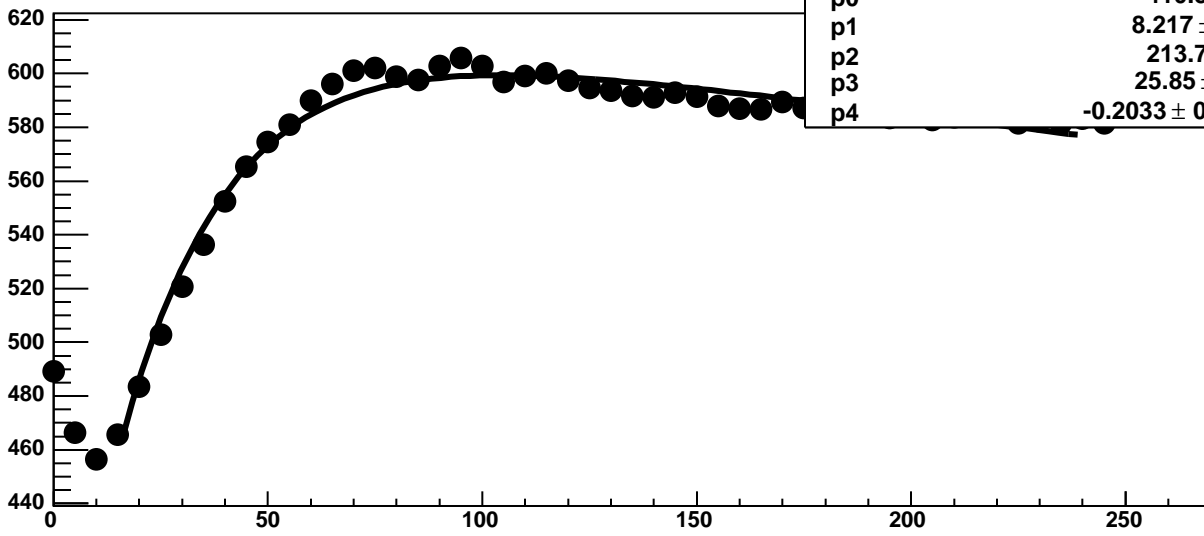
Chip 0, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

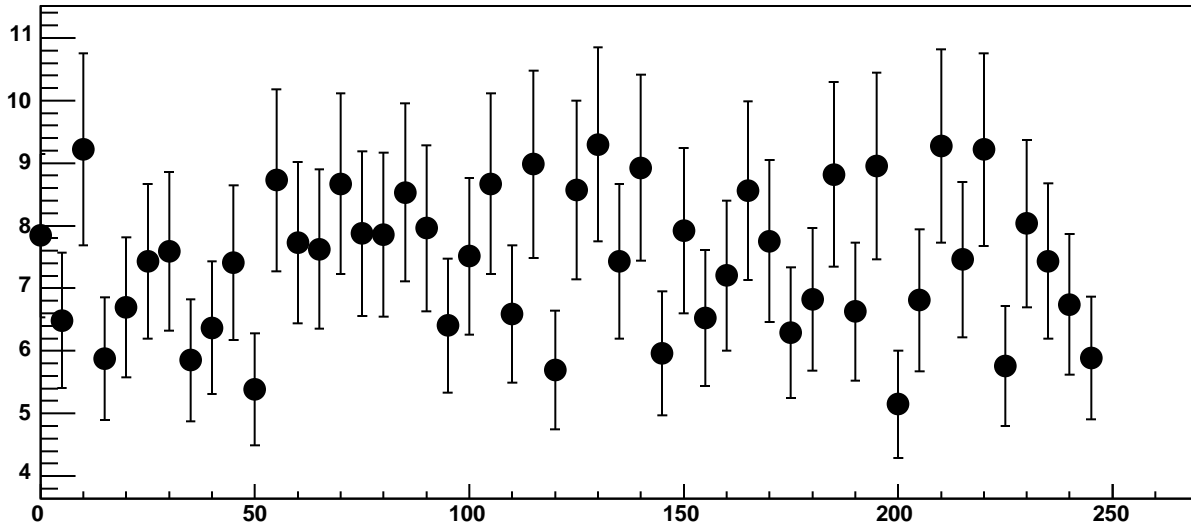


Chip 0, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

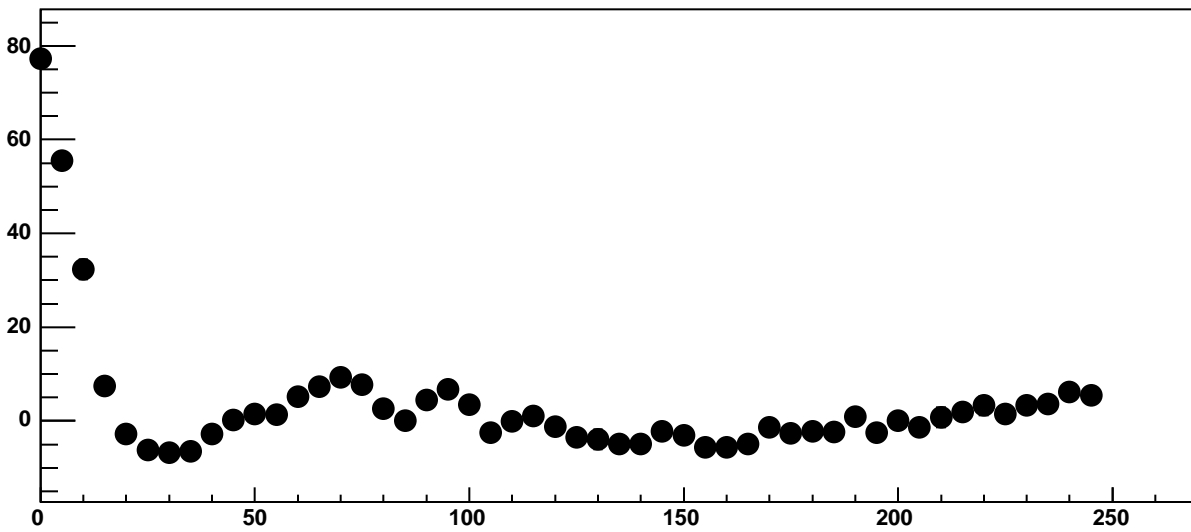


$\chi^2 / \text{ndf}$	289.3 / 41
p0	$410.3 \pm 3.098$
p1	$8.217 \pm 0.4336$
p2	$213.7 \pm 3.193$
p3	$25.85 \pm 0.4493$
p4	$-0.2033 \pm 0.007488$

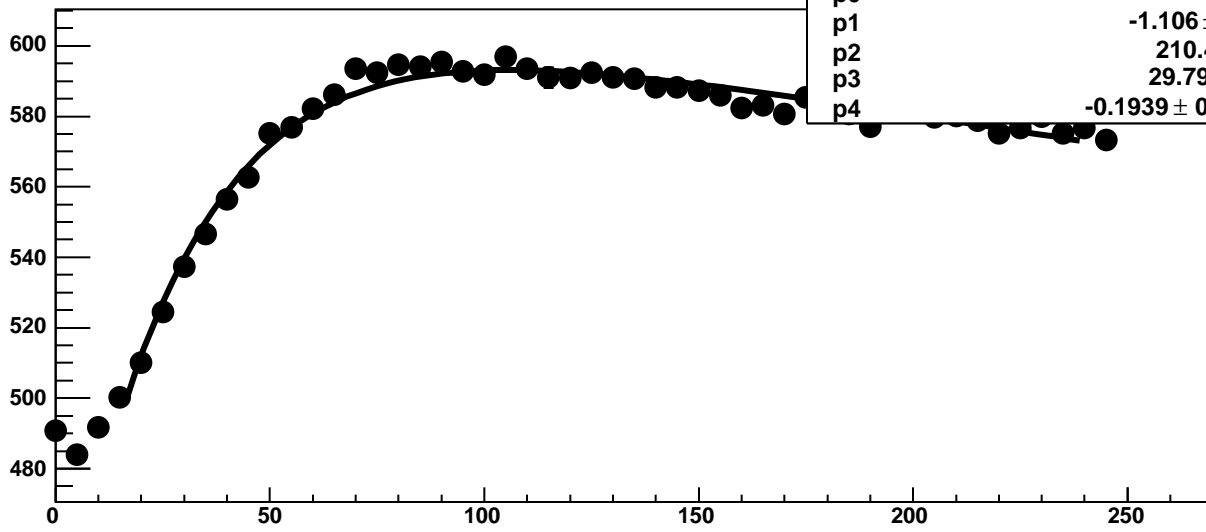
Chip 0, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold

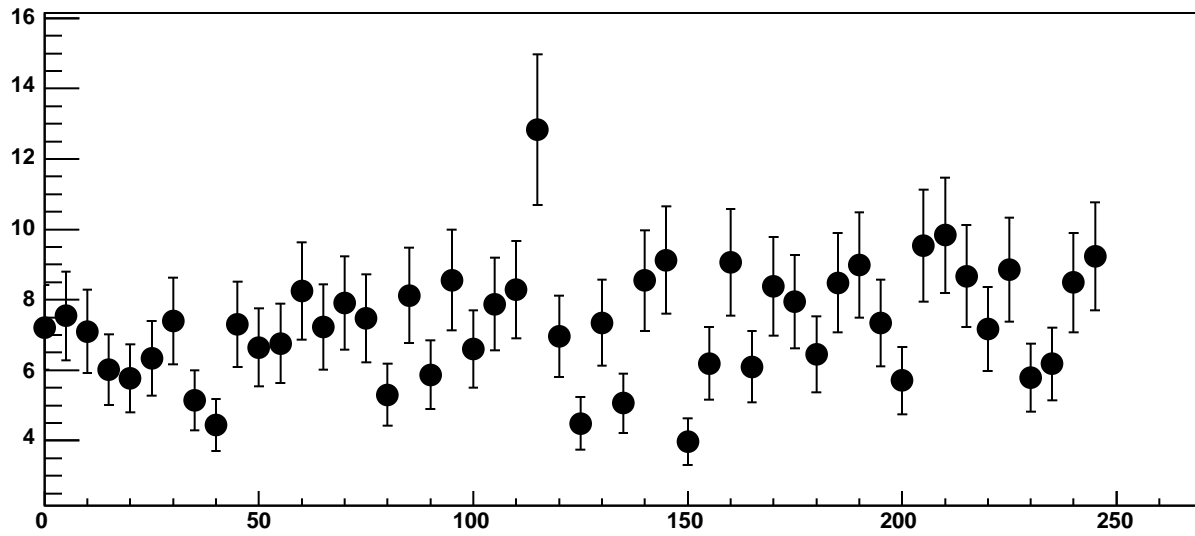


Chip 0, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold

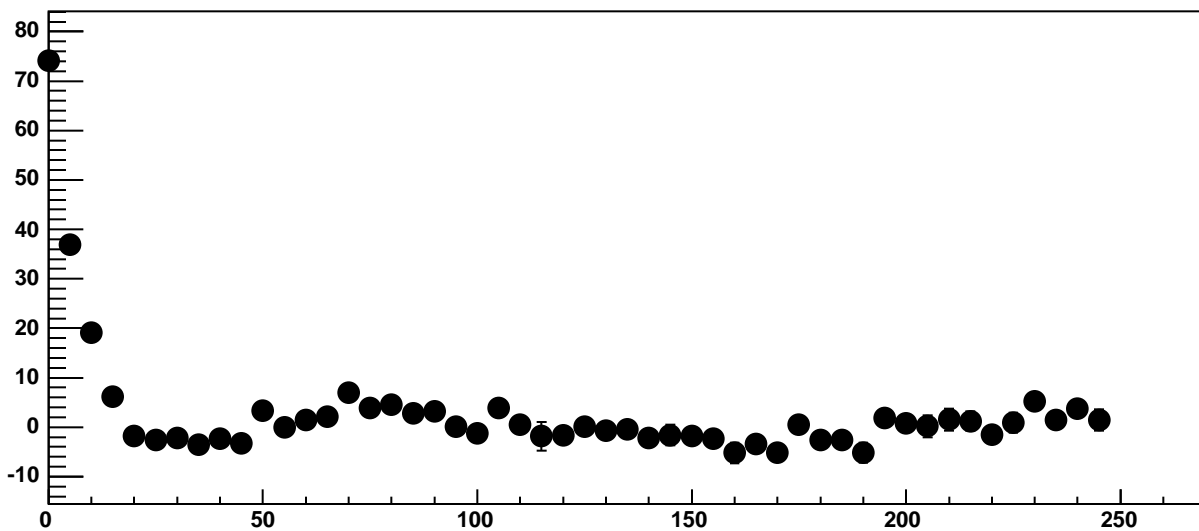


$\chi^2 / \text{ndf}$	162.1 / 41
p0	$409.3 \pm 2.874$
p1	$-1.106 \pm 0.5898$
p2	$210.4 \pm 2.97$
p3	$29.79 \pm 0.675$
p4	$-0.1939 \pm 0.007914$

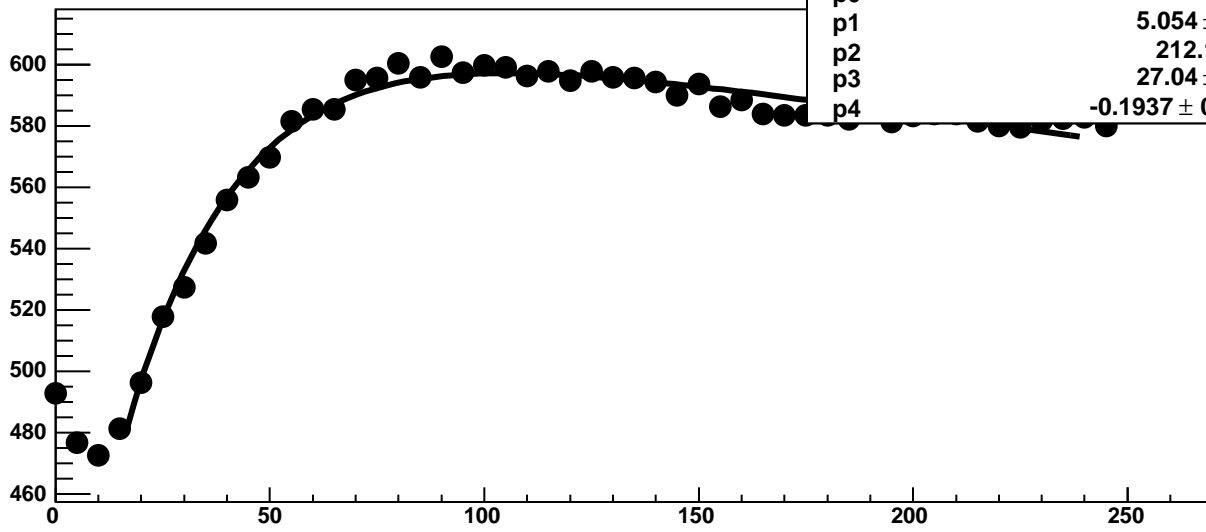
Chip 0, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold

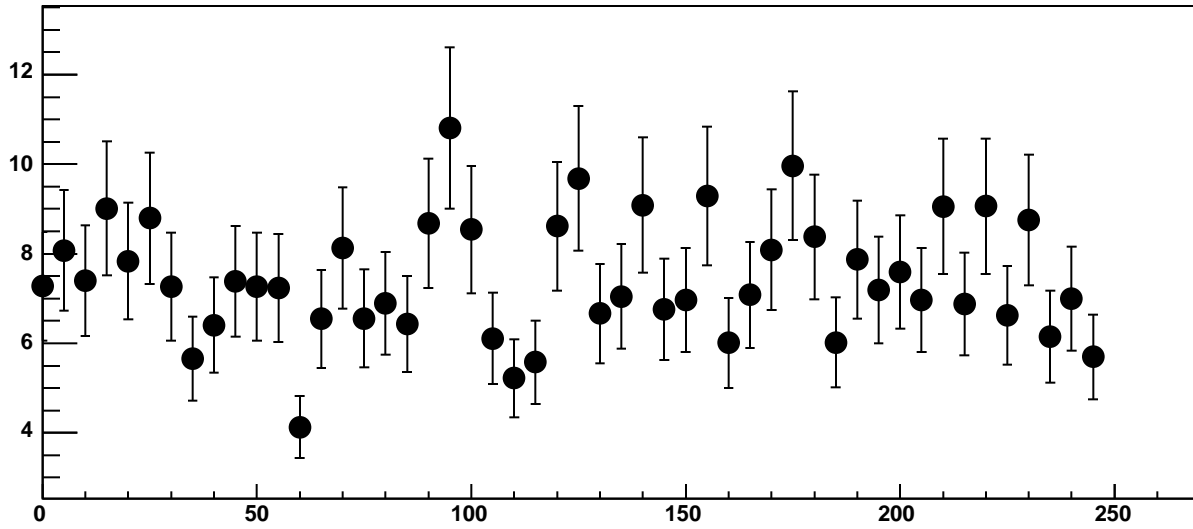


Chip 0, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

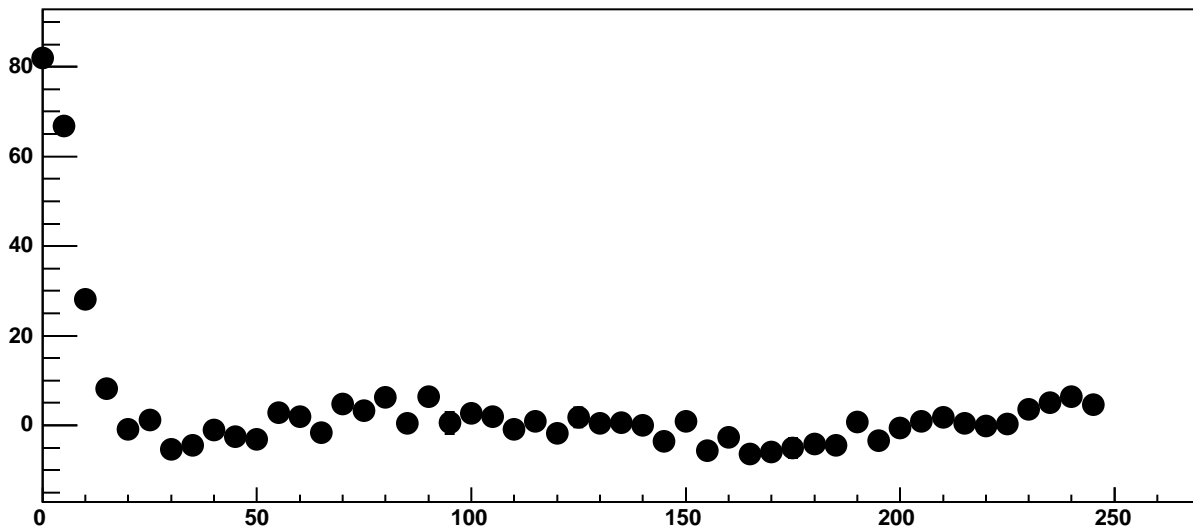


$\chi^2 / \text{ndf}$	197.1 / 41
p0	$409.9 \pm 3.022$
p1	$5.054 \pm 0.5247$
p2	$212.1 \pm 3.111$
p3	$27.04 \pm 0.5709$
p4	$-0.1937 \pm 0.007731$

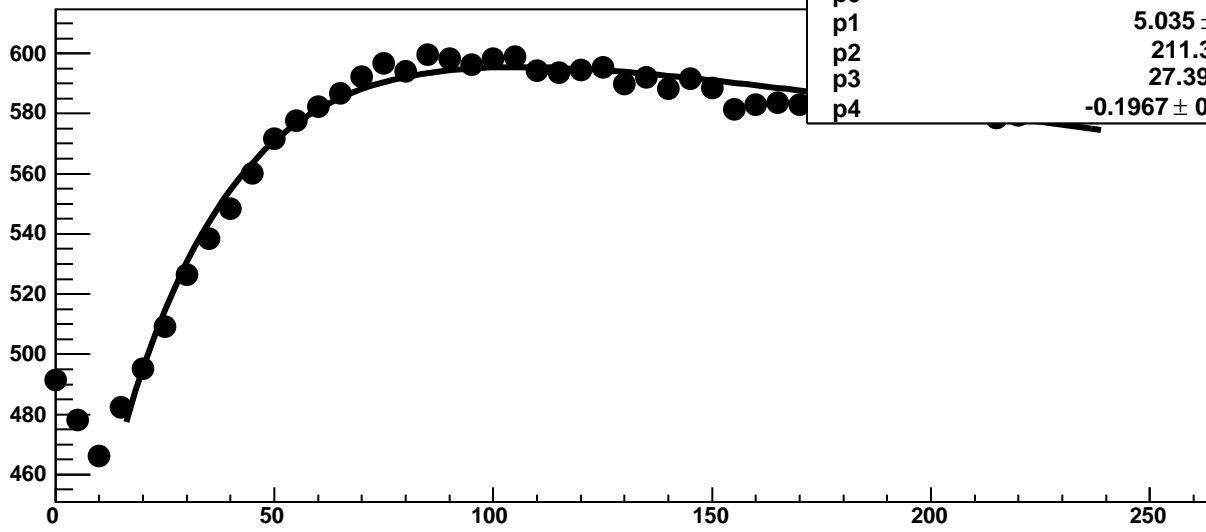
Chip 0, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

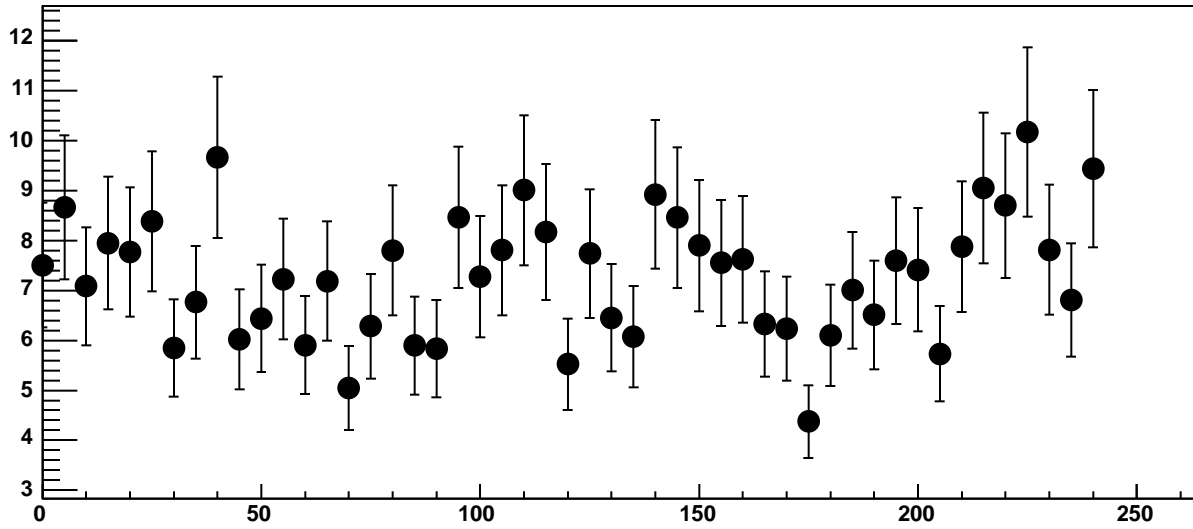


Chip 0, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold

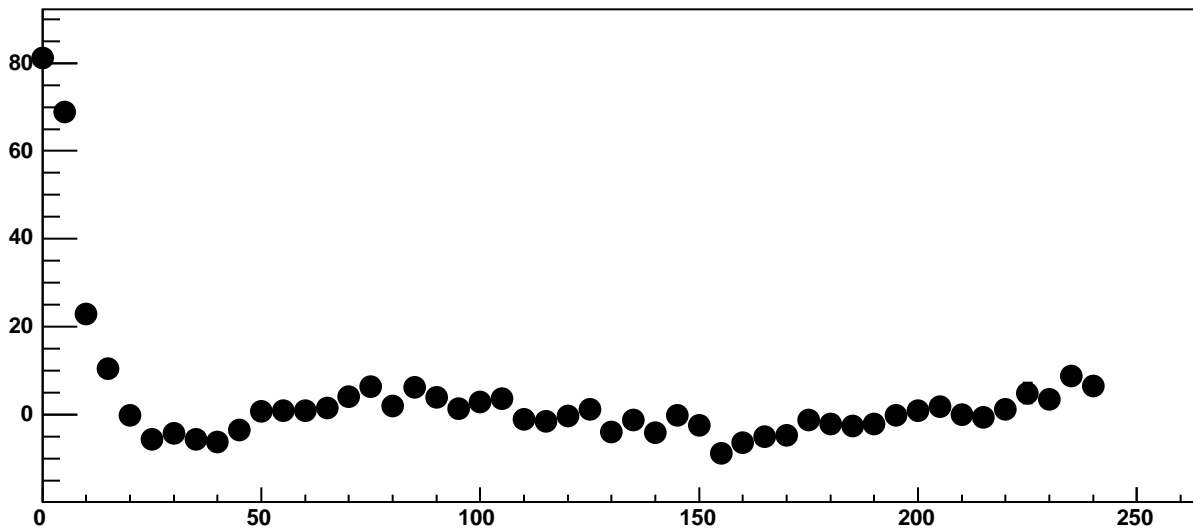


$\chi^2 / \text{ndf}$	288.8 / 41
p0	$409.3 \pm 2.964$
p1	$5.035 \pm 0.5066$
p2	$211.3 \pm 3.055$
p3	$27.39 \pm 0.564$
p4	$-0.1967 \pm 0.007986$

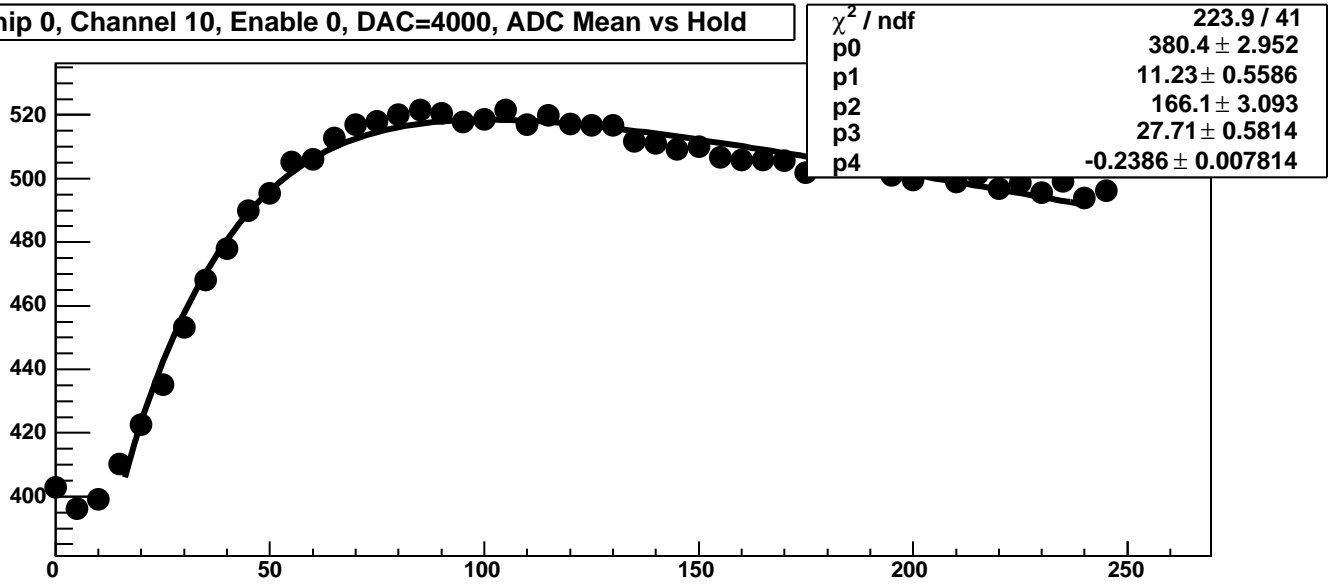
Chip 0, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



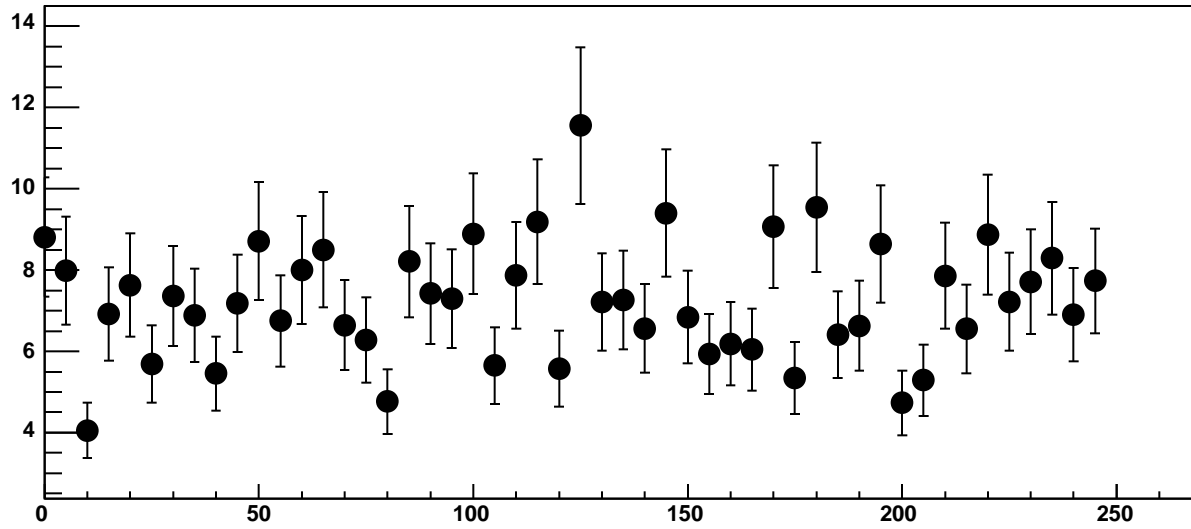
Chip 0, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold



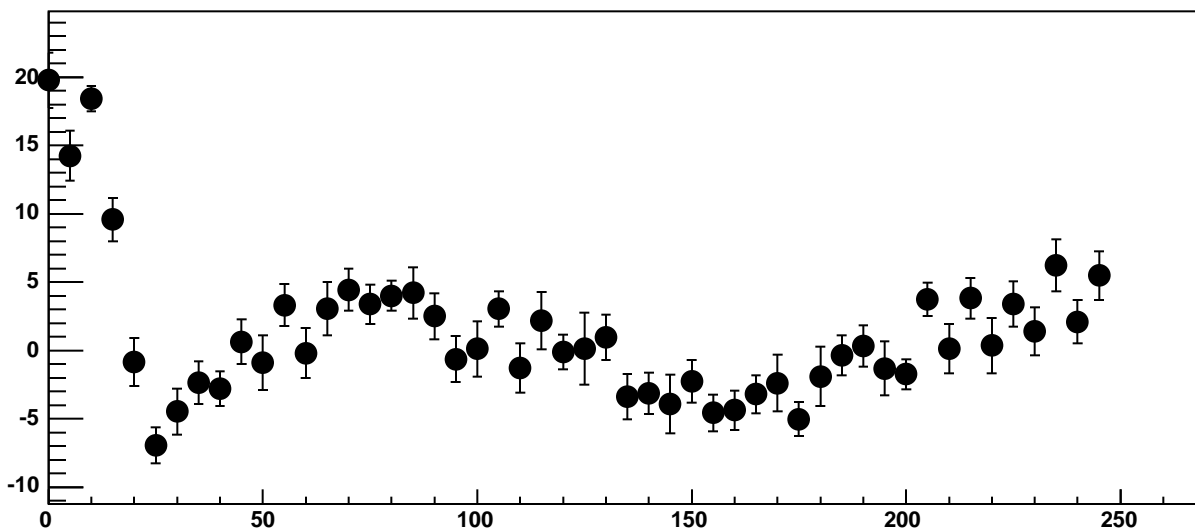
Chip 0, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold



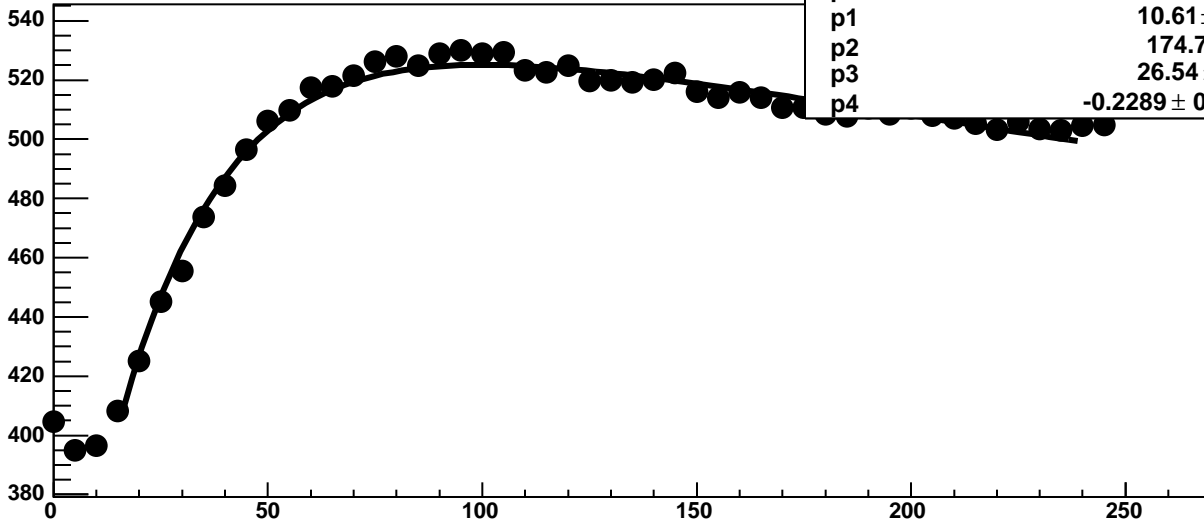
Chip 0, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold

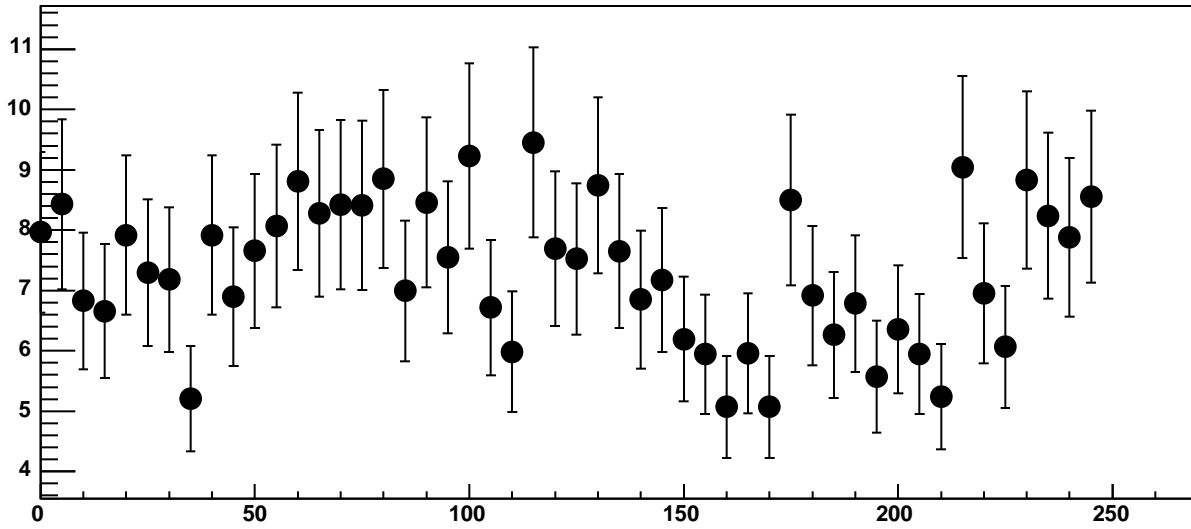


Chip 0, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

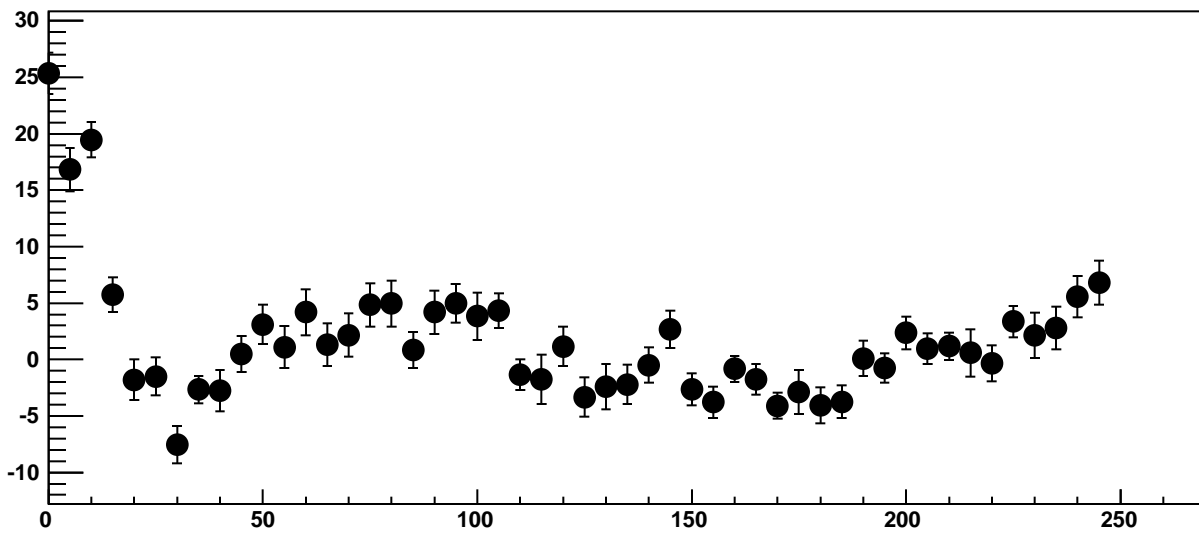


$\chi^2 / \text{ndf}$	166.1 / 41
p0	$376.9 \pm 3.016$
p1	$10.61 \pm 0.5267$
p2	$174.7 \pm 3.139$
p3	$26.54 \pm 0.5501$
p4	$-0.2289 \pm 0.007773$

Chip 0, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold

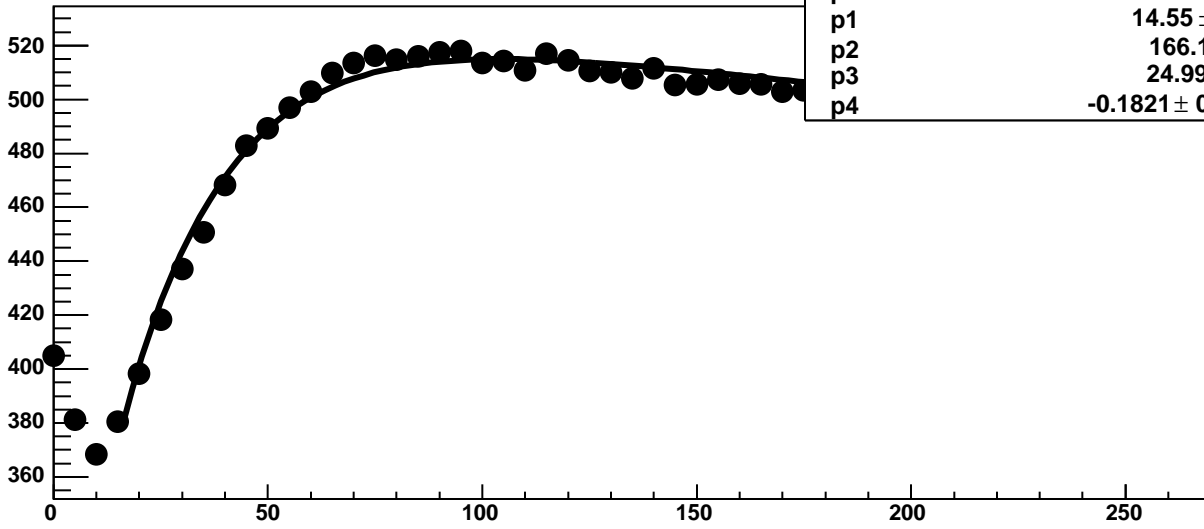


Chip 0, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold



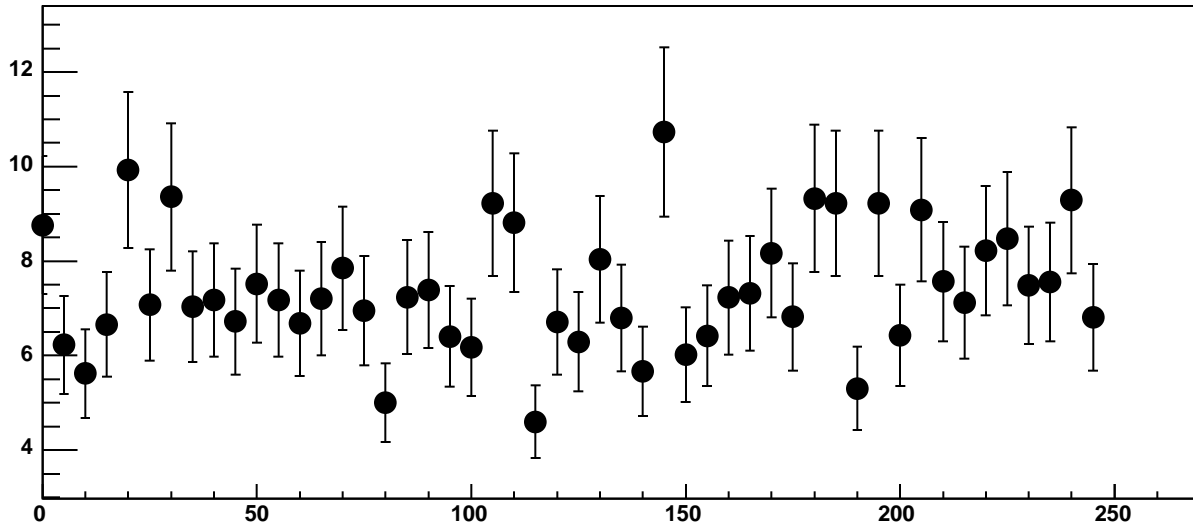


Chip 0, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

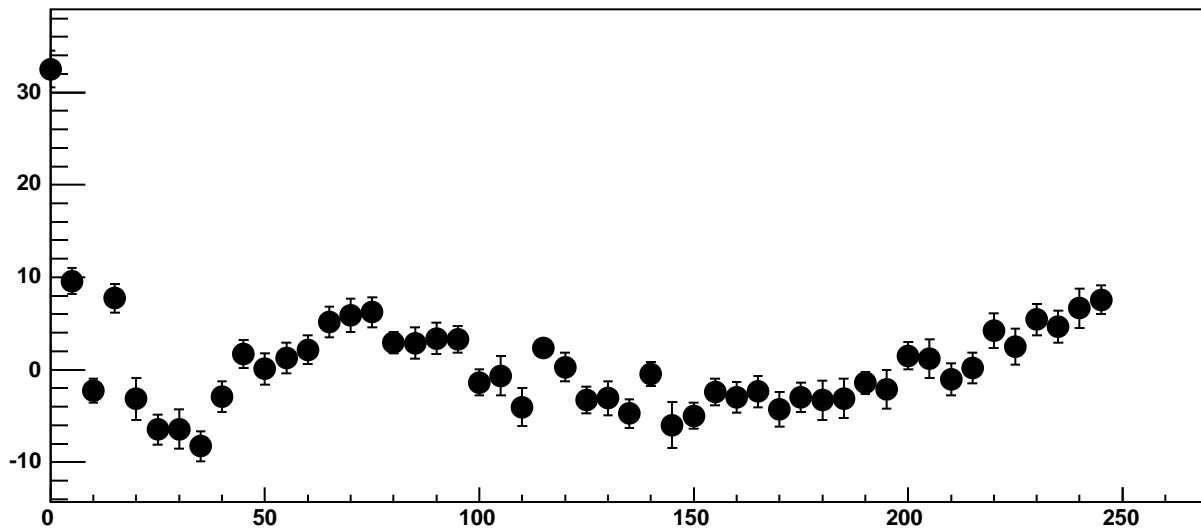


$\chi^2 / \text{ndf}$	244.3 / 41
p0	$369.8 \pm 0.771$
p1	$14.55 \pm 0.2015$
p2	$166.1 \pm 0.544$
p3	$24.99 \pm 0.477$
p4	$-0.1821 \pm 0.007862$

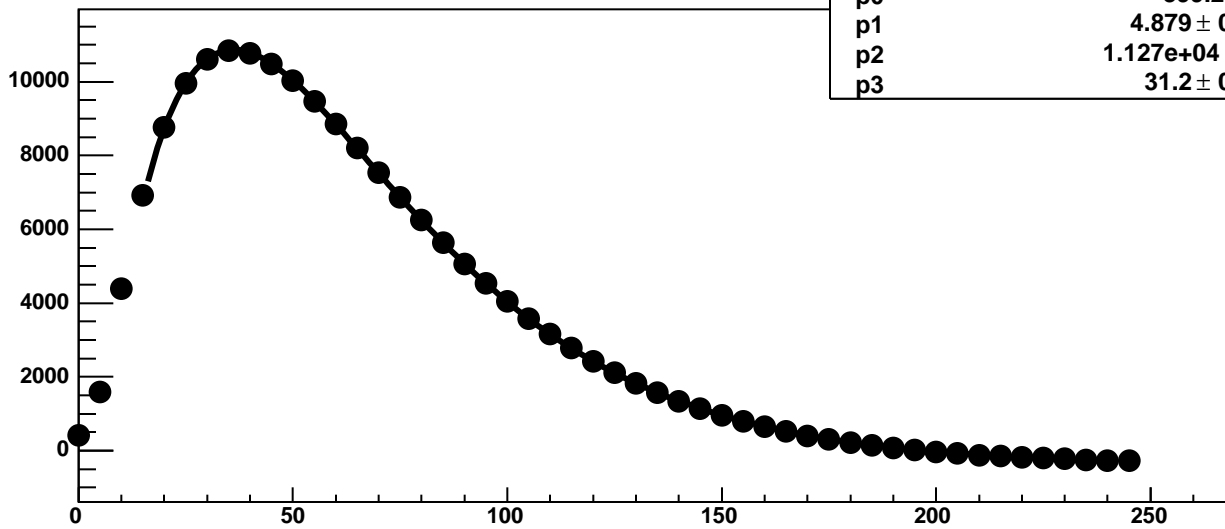
Chip 0, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

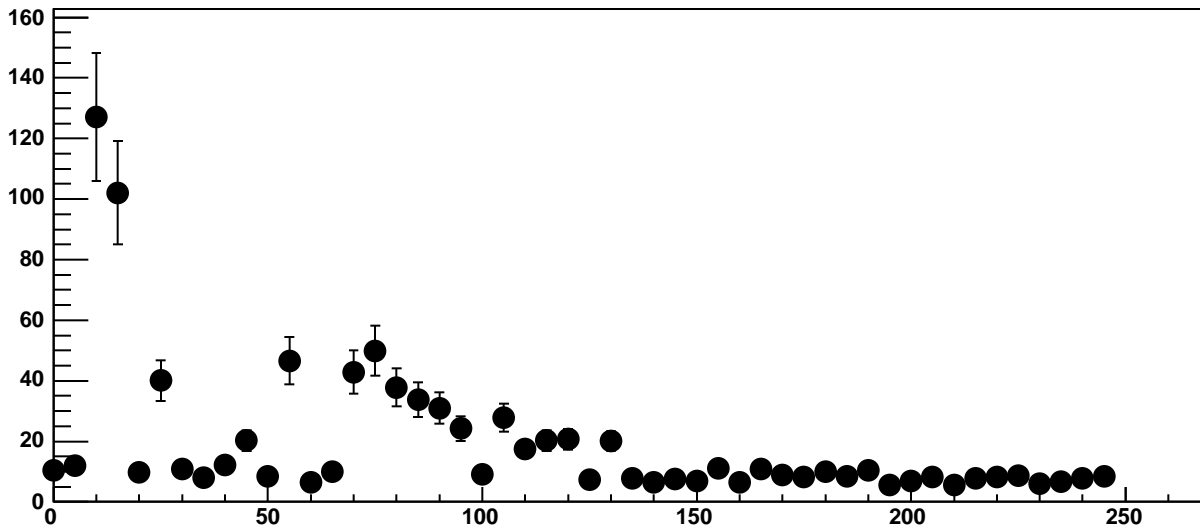


Chip 0, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold

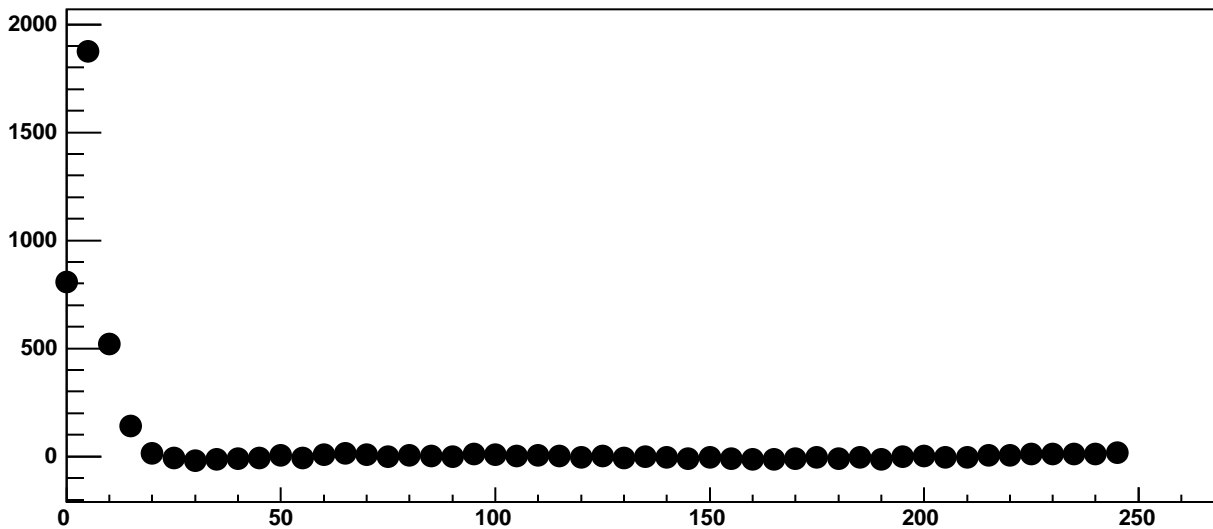


$\chi^2 / \text{ndf}$	788.5 / 42
p0	-399.2 ± 0.6121
p1	4.879 ± 0.008745
p2	1.127e+04 ± 0.9913
p3	31.2 ± 0.006107

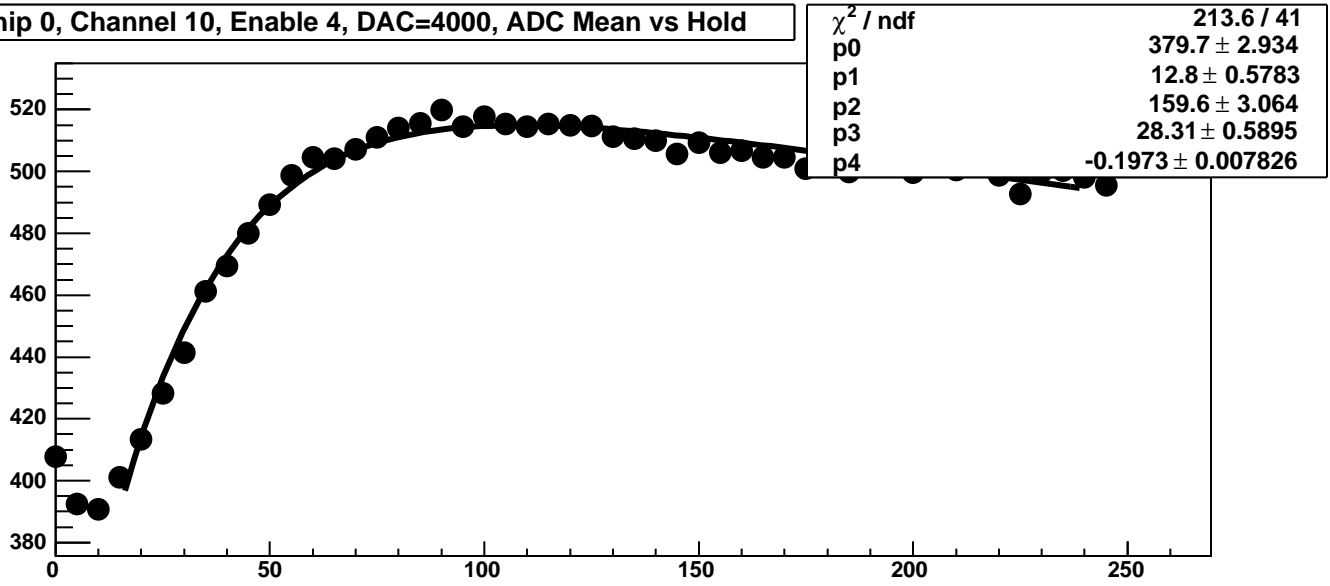
Chip 0, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold



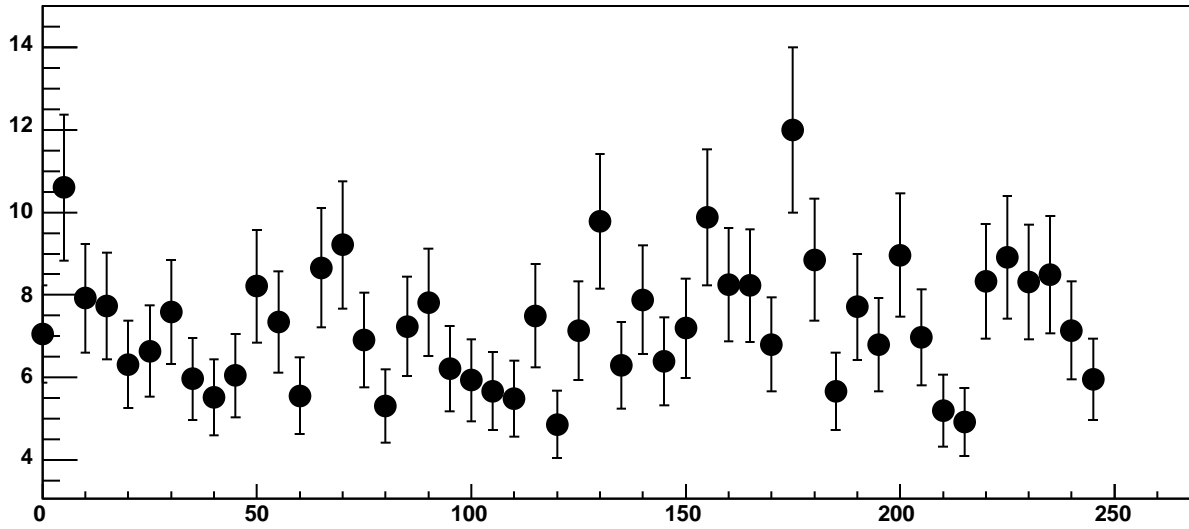
Chip 0, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold



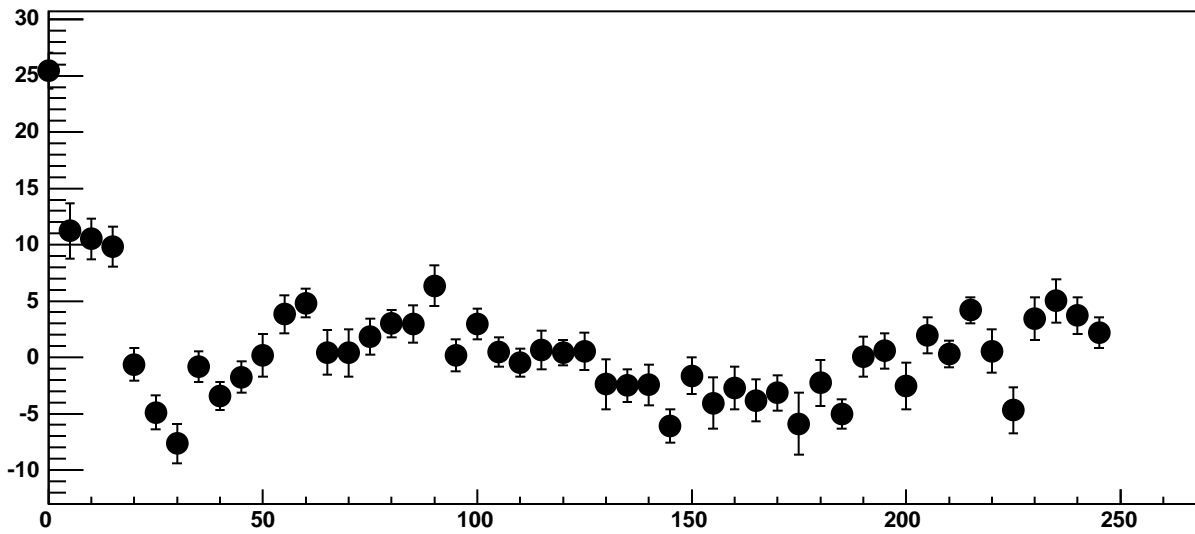
Chip 0, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold



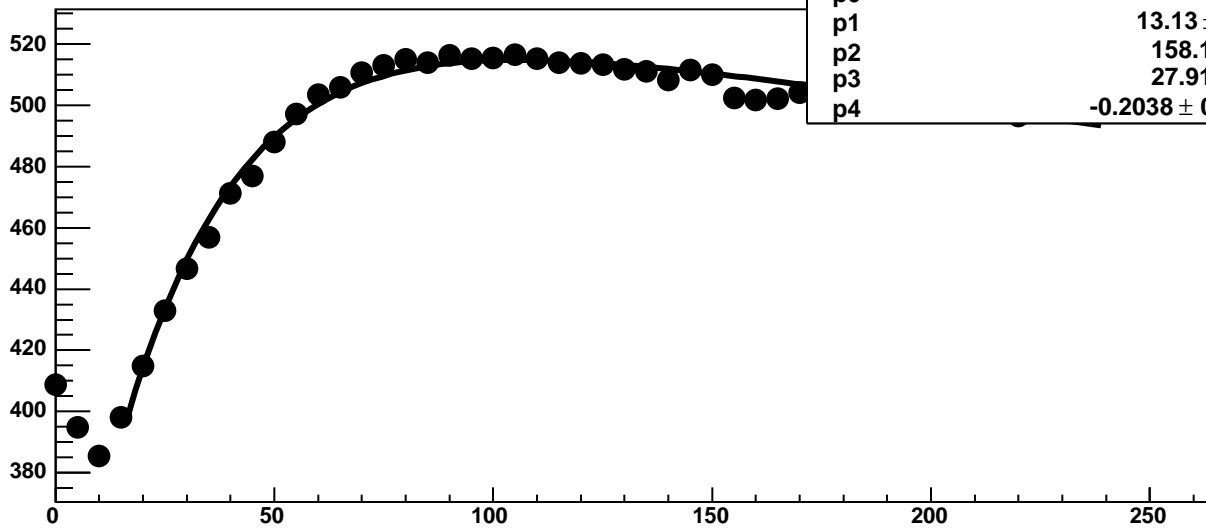
Chip 0, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

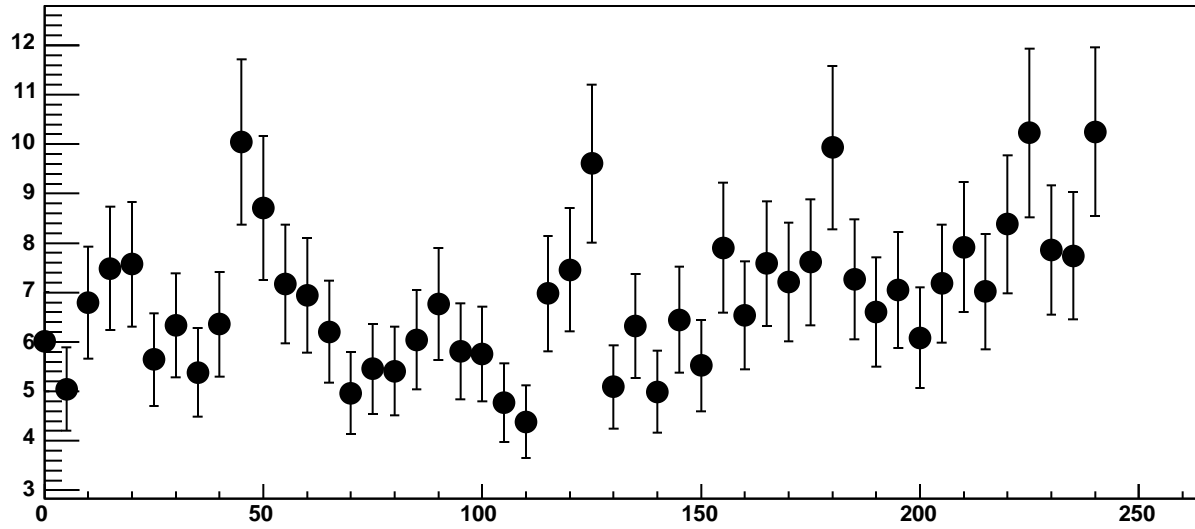


Chip 0, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold

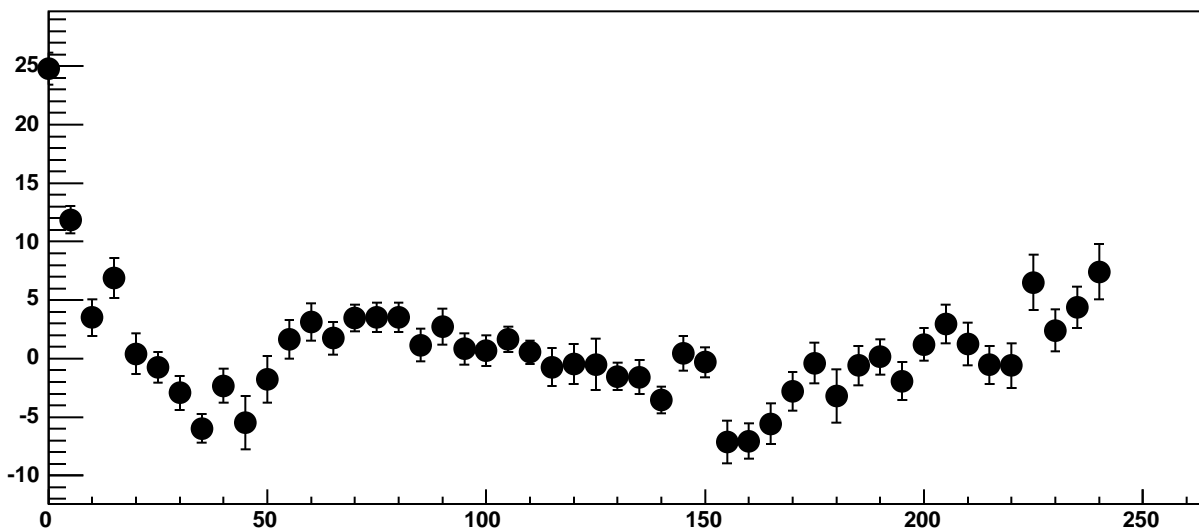


$\chi^2 / \text{ndf}$	188.6 / 41
p0	$381.3 \pm 2.789$
p1	$13.13 \pm 0.5489$
p2	$158.1 \pm 2.914$
p3	$27.91 \pm 0.578$
p4	$-0.2038 \pm 0.007901$

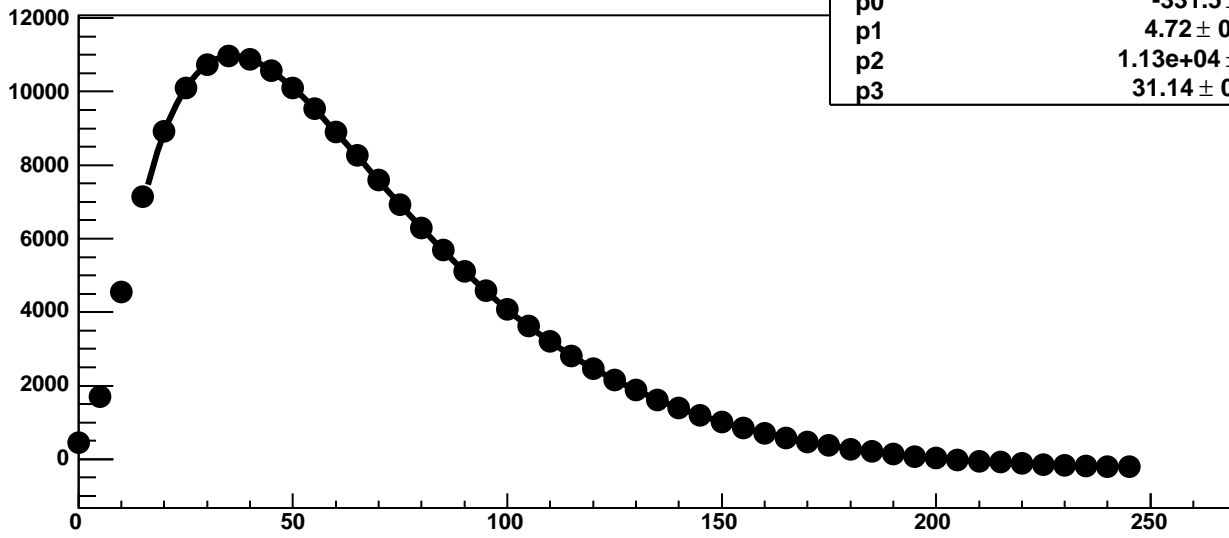
Chip 0, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

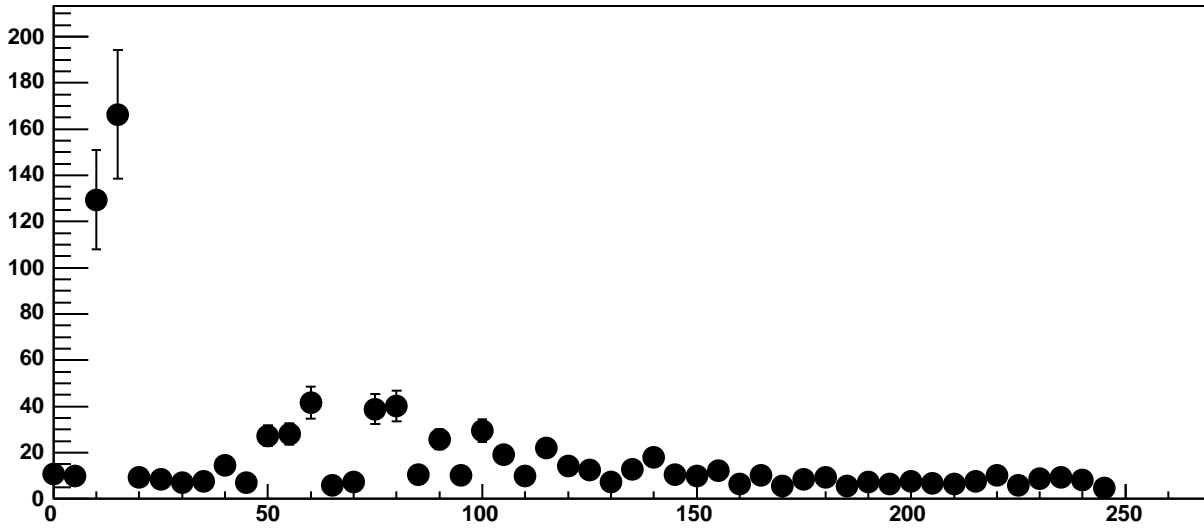


Chip 0, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold

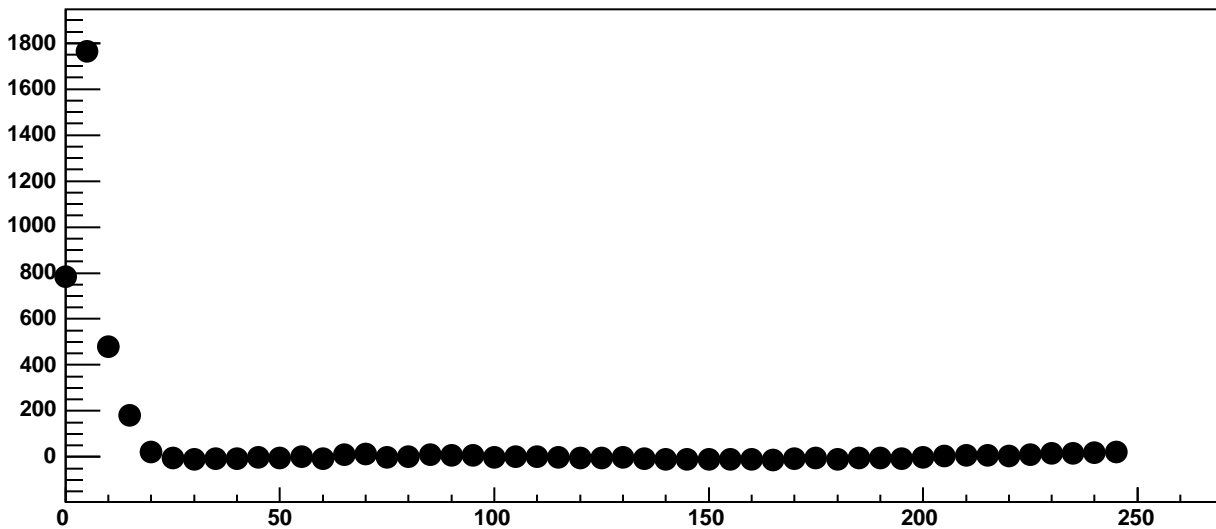


$\chi^2 / \text{ndf}$	859.9 / 42
p0	-331.5 ± 0.5969
p1	4.72 ± 0.008409
p2	1.13e+04 ± 0.8643
p3	31.14 ± 0.005952

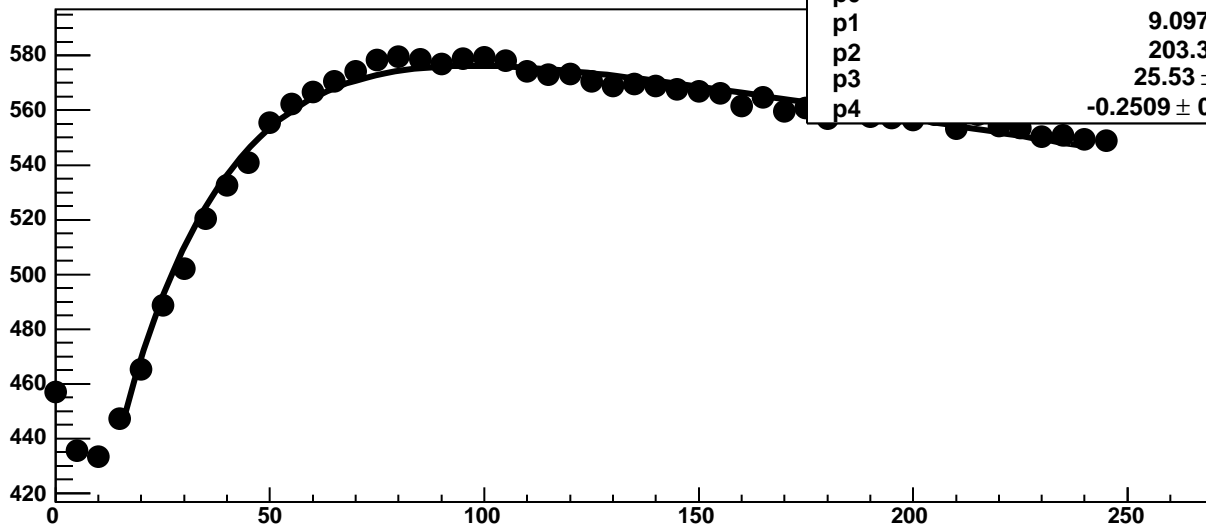
Chip 0, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold

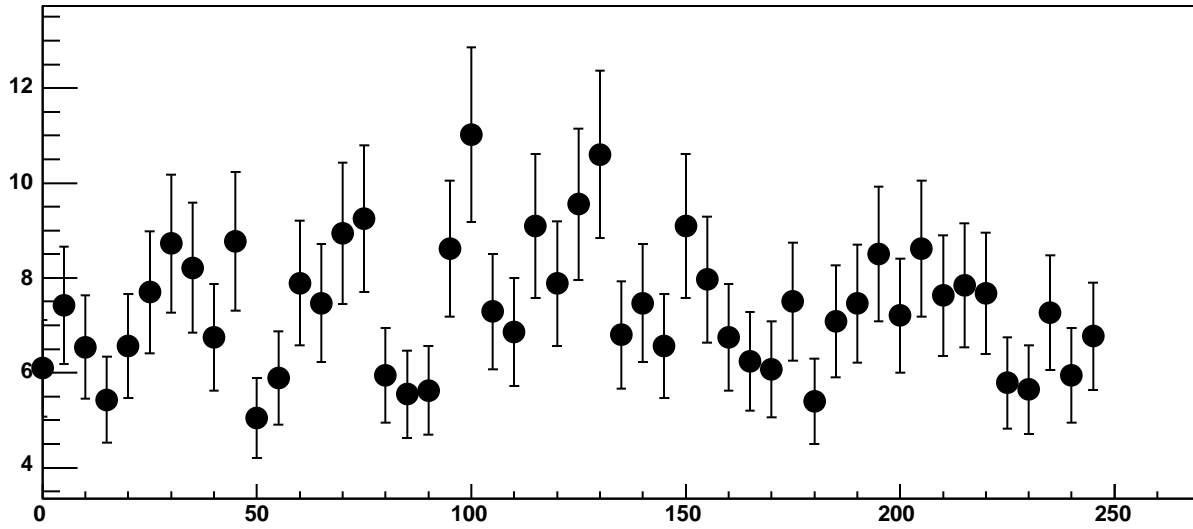


Chip 0, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

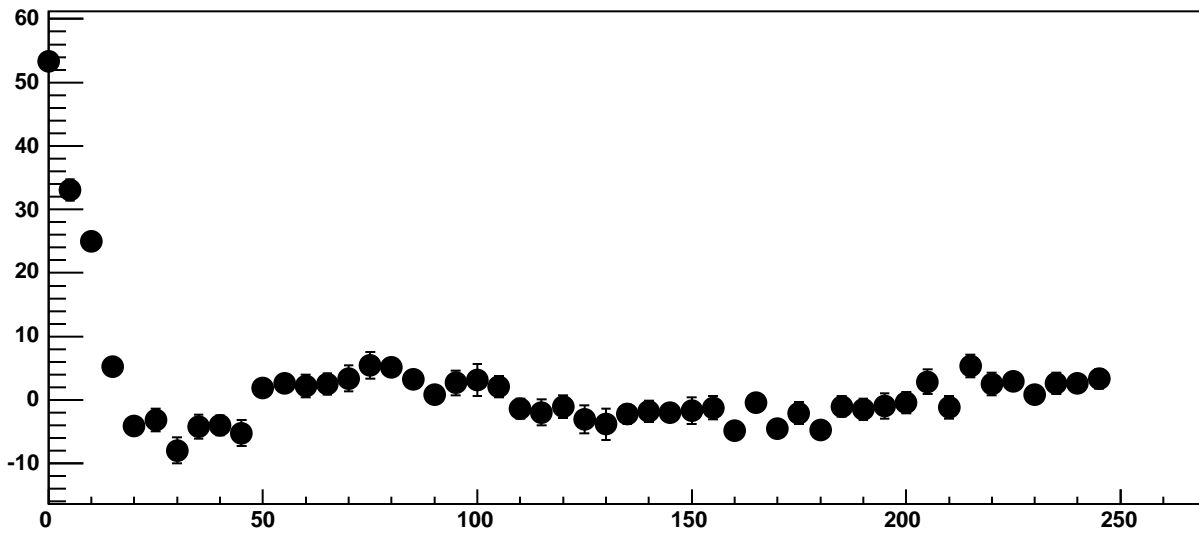


$\chi^2 / \text{ndf}$	182.7 / 41
p0	$401.5 \pm 3.044$
p1	$9.097 \pm 0.435$
p2	$203.3 \pm 3.157$
p3	$25.53 \pm 0.4615$
p4	$-0.2509 \pm 0.007272$

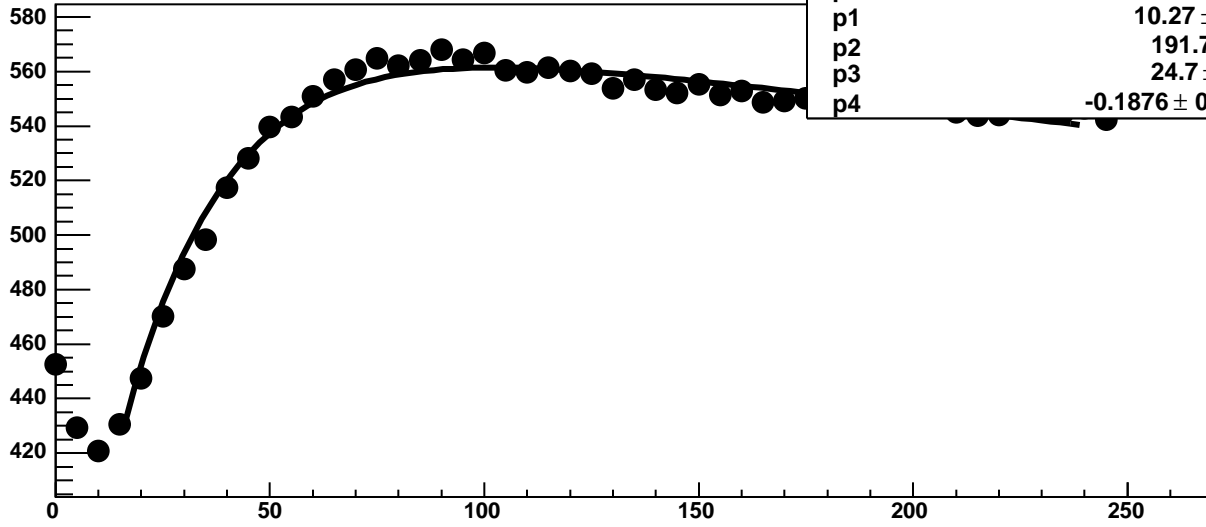
Chip 0, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold

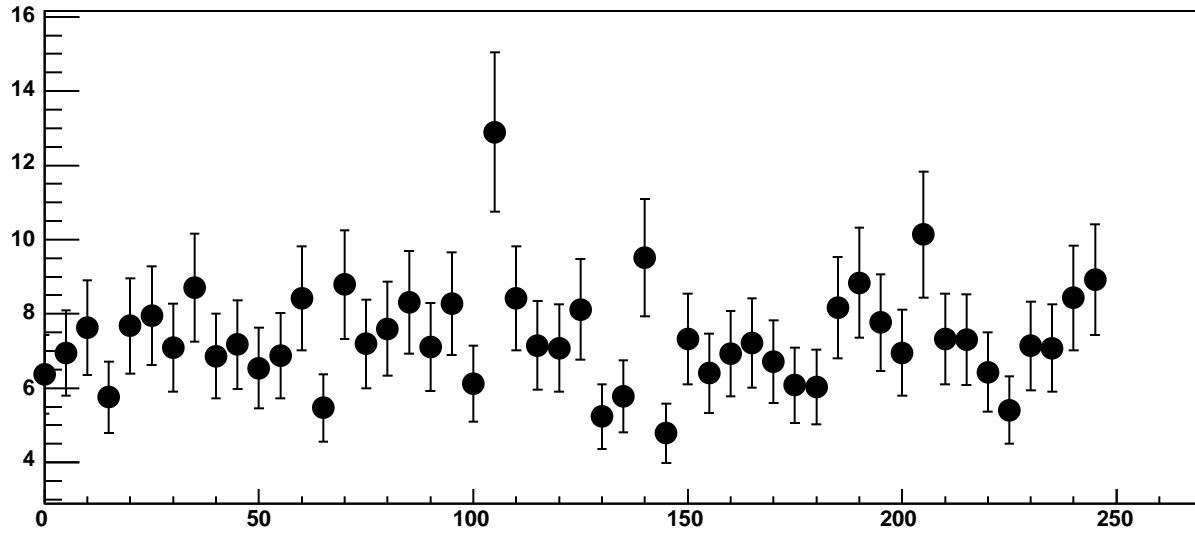


Chip 0, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

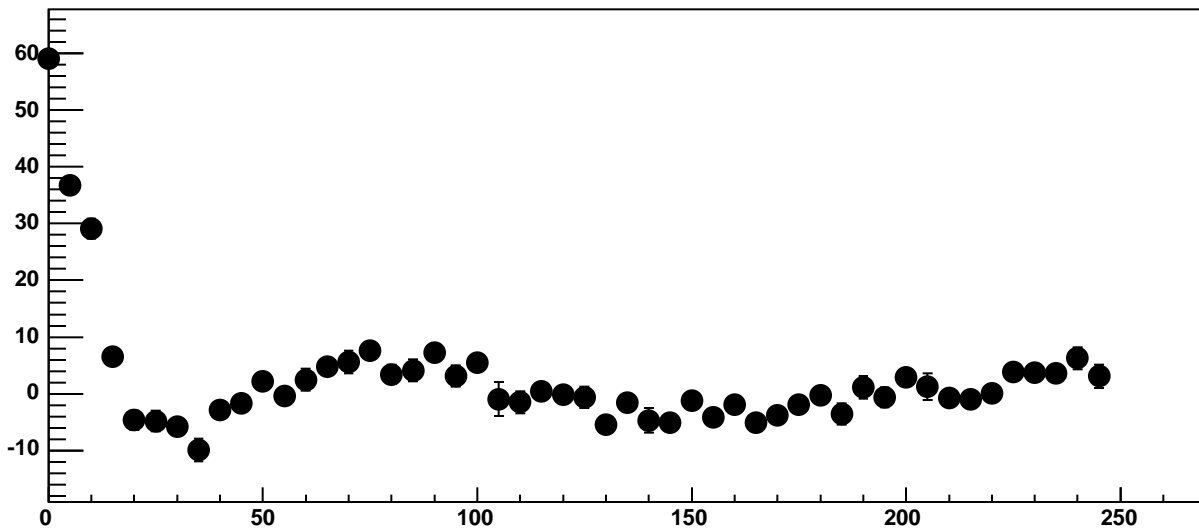


$\chi^2 / \text{ndf}$	287.5 / 41
p0	$391.6 \pm 3.031$
p1	$10.27 \pm 0.4416$
p2	$191.7 \pm 3.119$
p3	$24.7 \pm 0.4432$
p4	$-0.1876 \pm 0.007144$

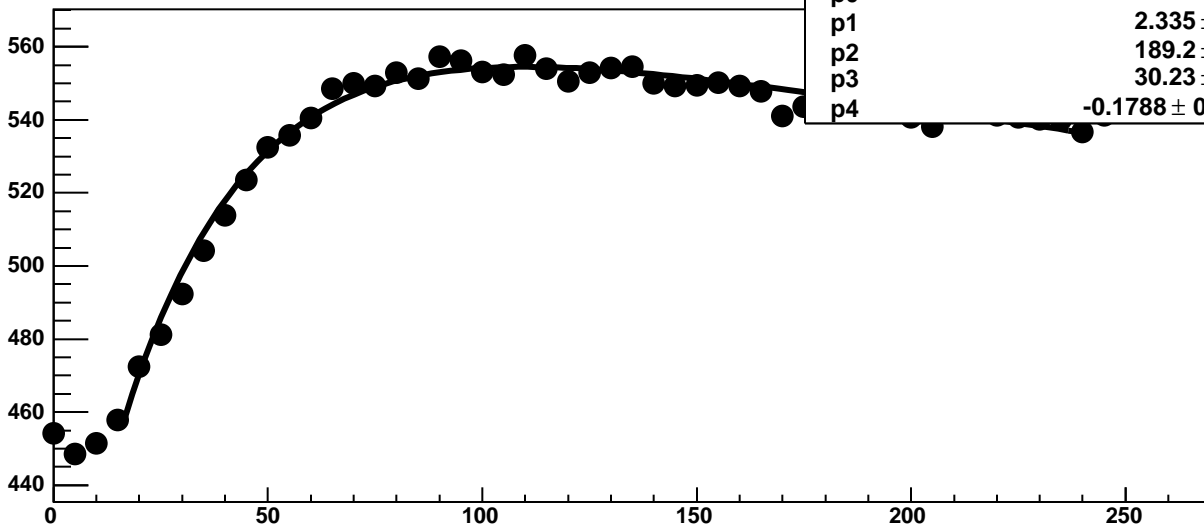
Chip 0, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

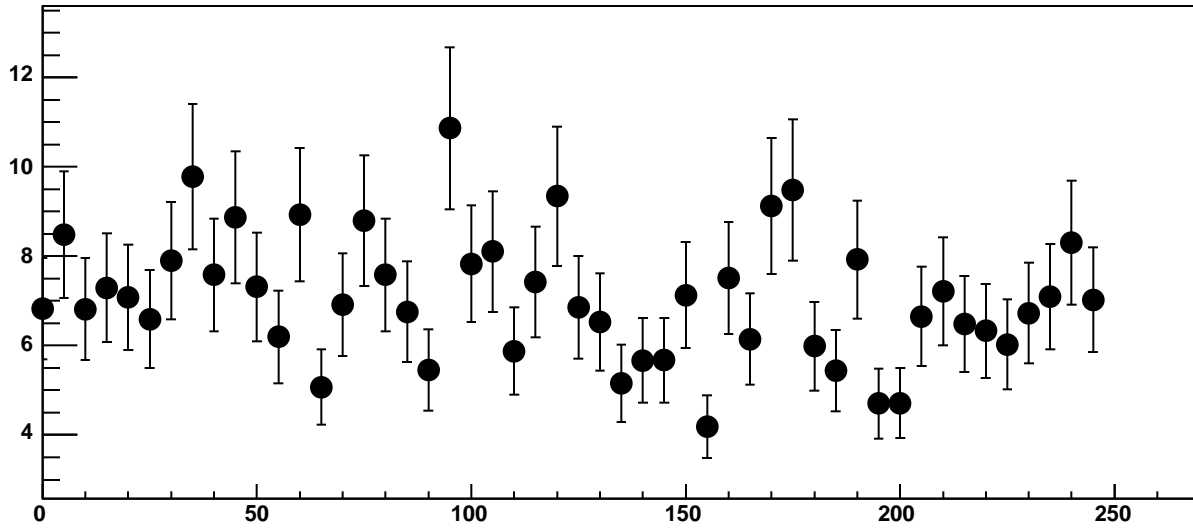


Chip 0, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold

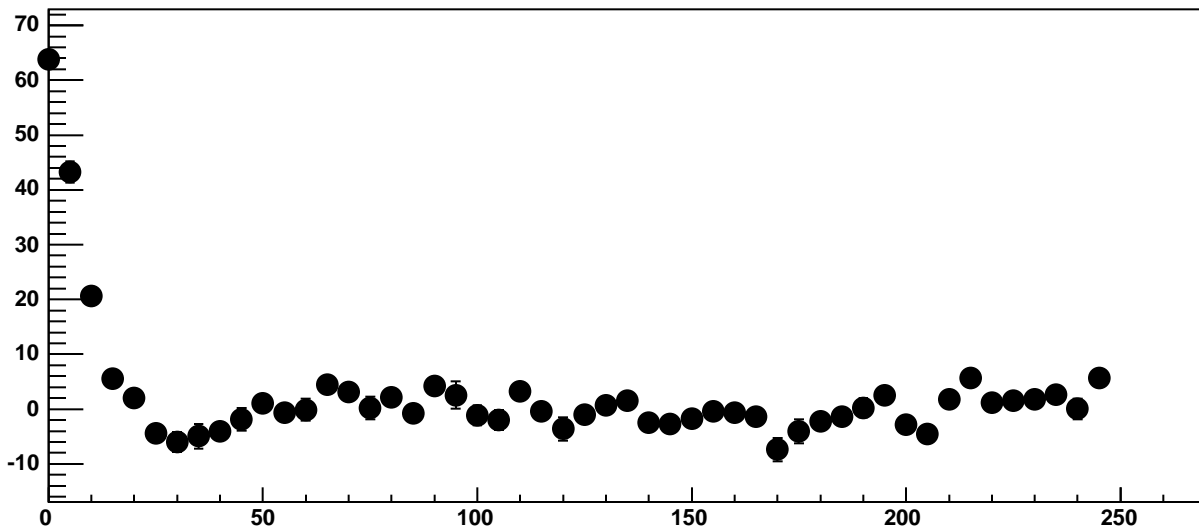


$\chi^2 / \text{ndf}$	162.6 / 41
p0	$389.9 \pm 0.8491$
p1	$2.335 \pm 0.4427$
p2	$189.2 \pm 0.9018$
p3	$30.23 \pm 0.8398$
p4	$-0.1788 \pm 0.009019$

Chip 0, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold

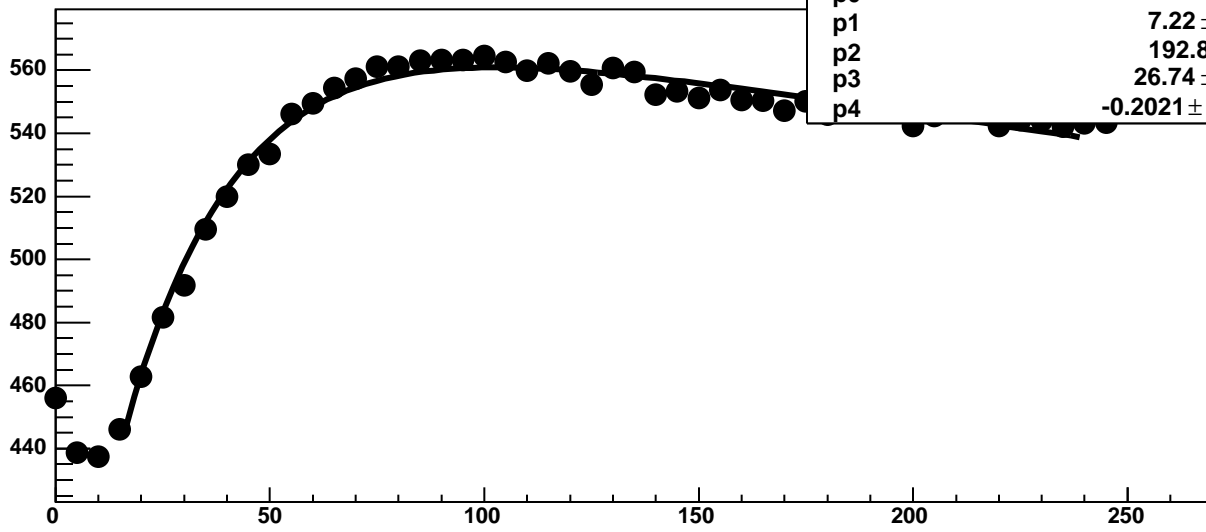


Chip 0, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold



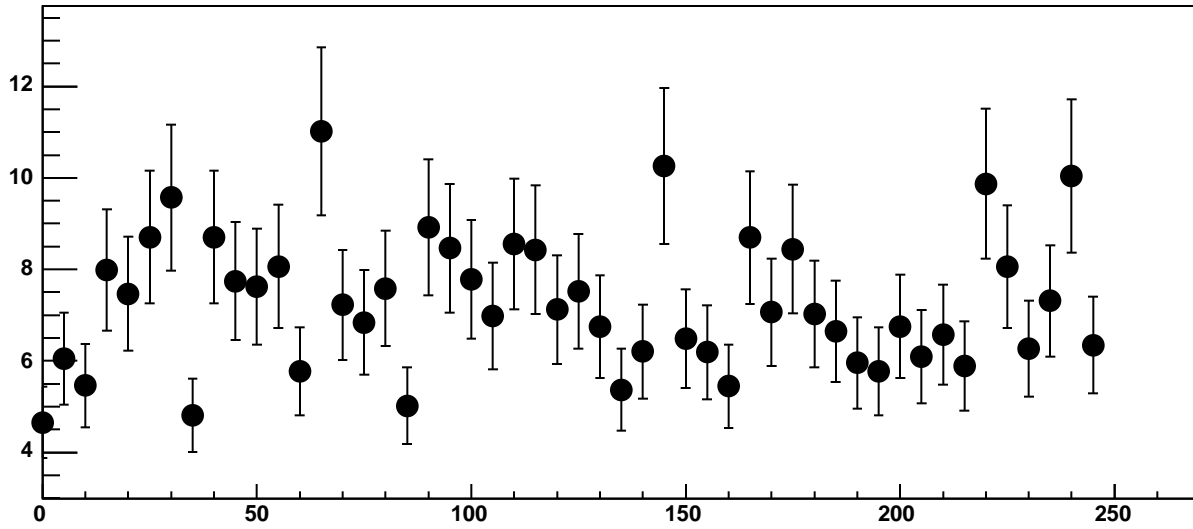


Chip 0, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

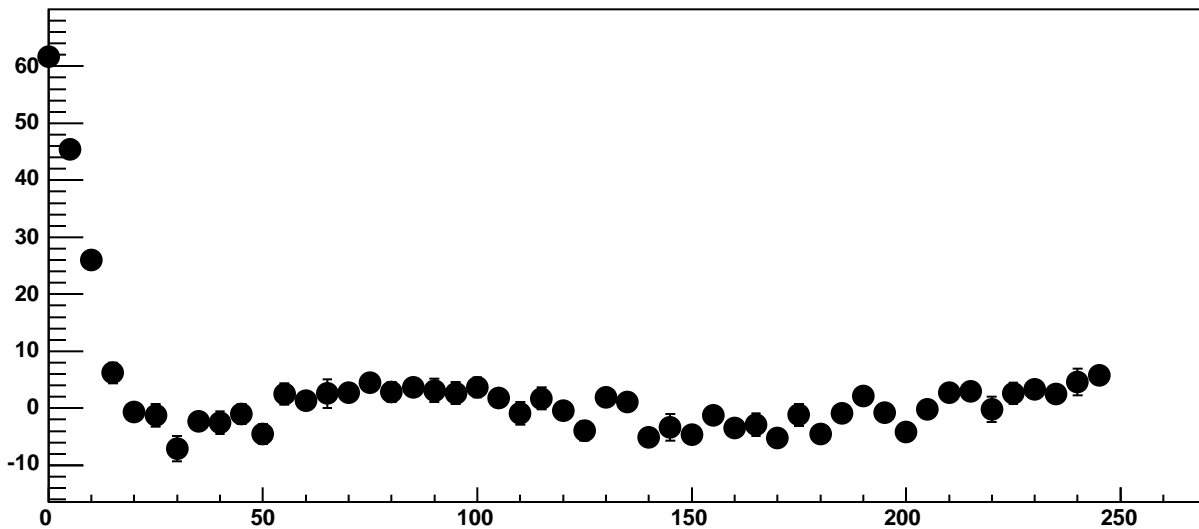


$\chi^2 / \text{ndf}$	167.8 / 41
p0	$392.9 \pm 3.025$
p1	$7.22 \pm 0.5285$
p2	$192.8 \pm 3.124$
p3	$26.74 \pm 0.5854$
p4	$-0.2021 \pm 0.00779$

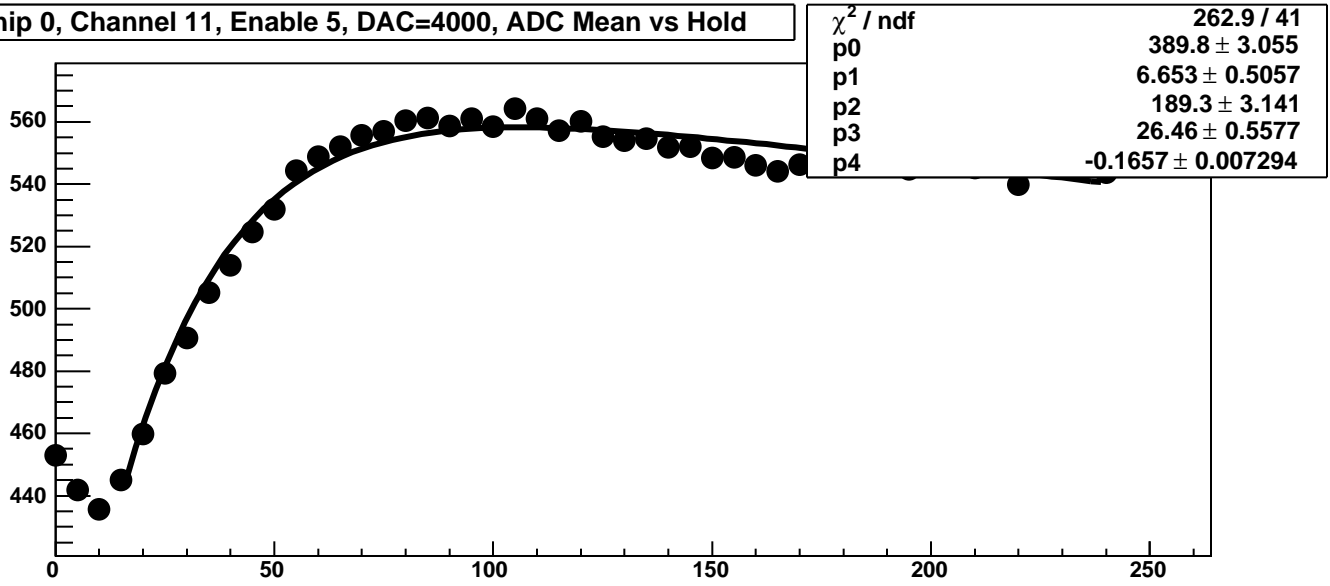
Chip 0, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



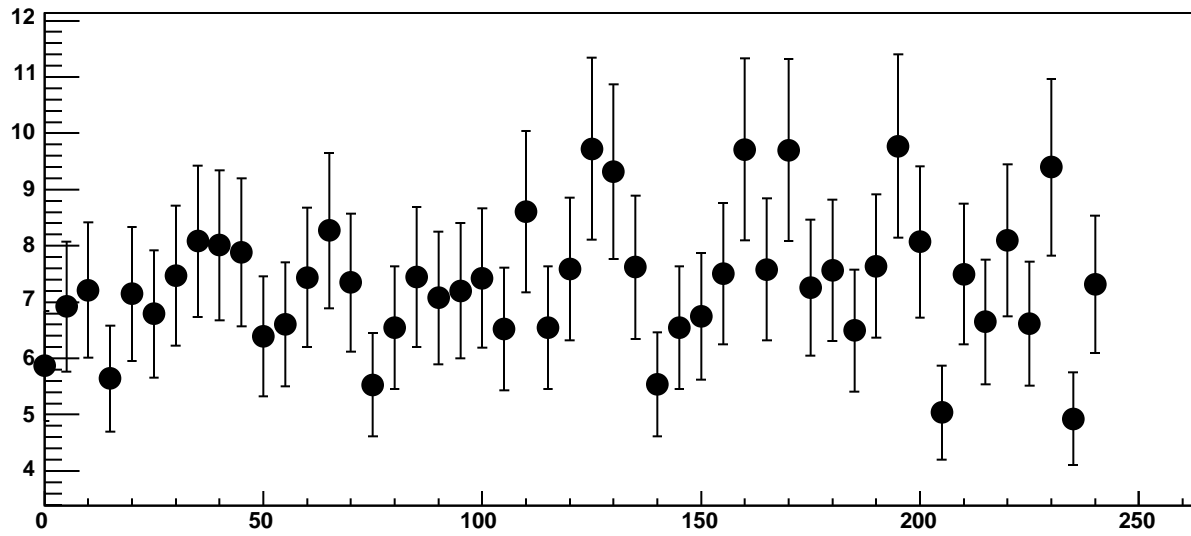
Chip 0, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold



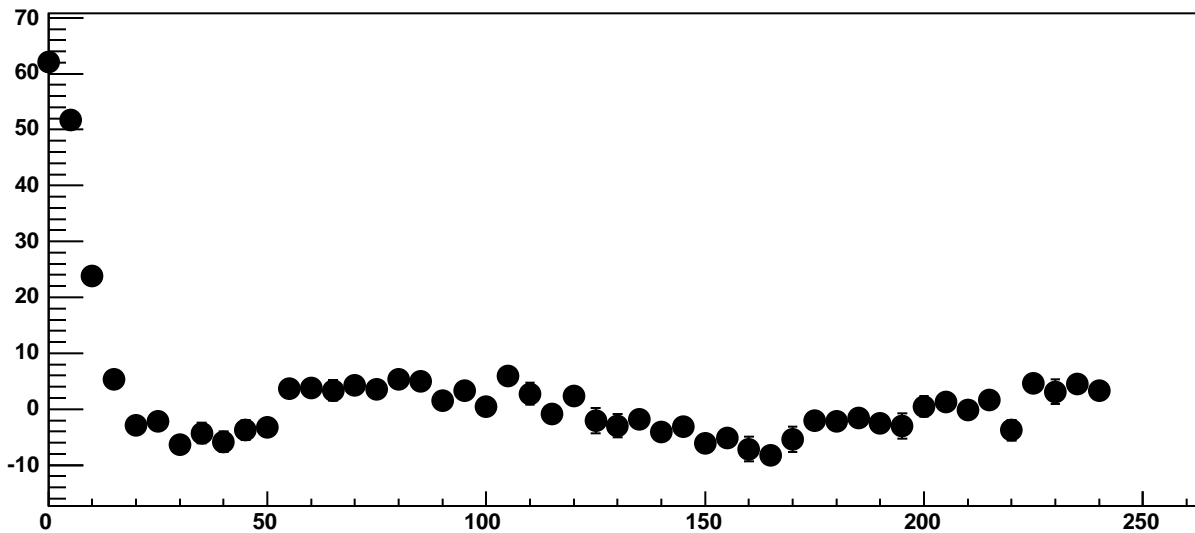
Chip 0, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold



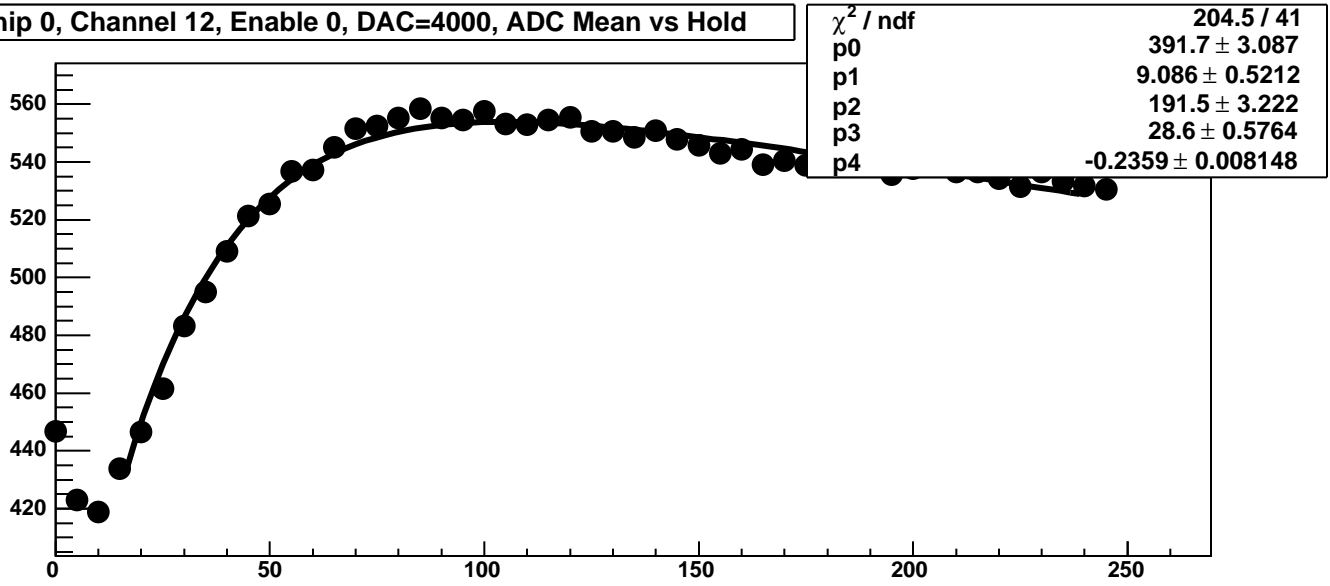
Chip 0, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



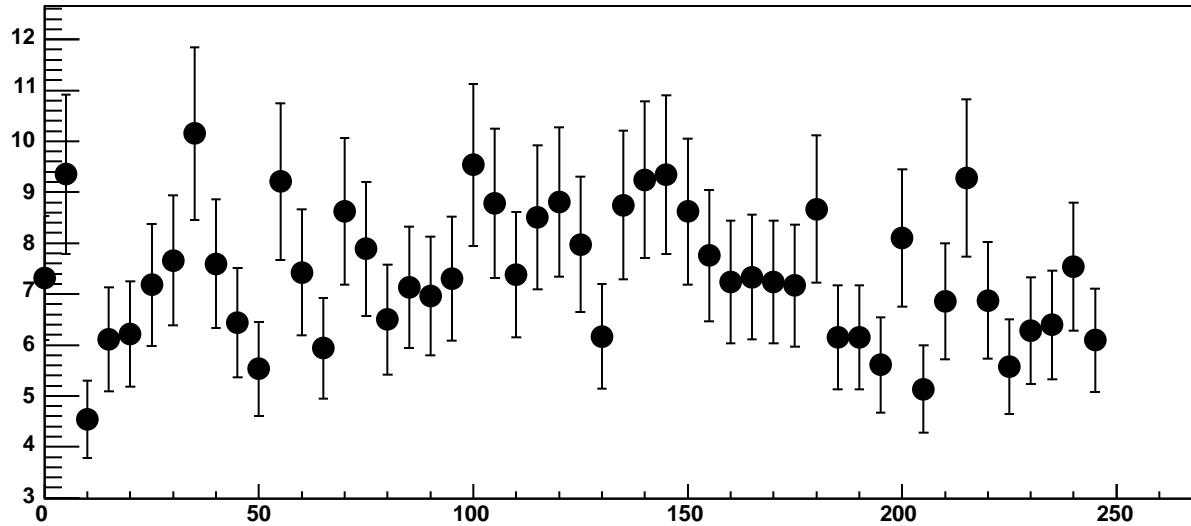
Chip 0, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold



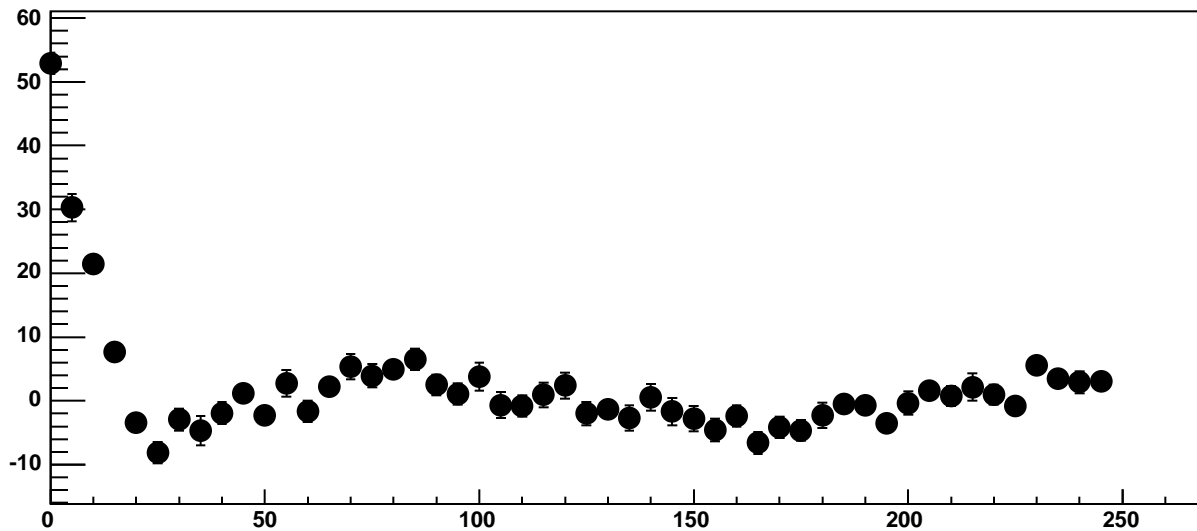
Chip 0, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold



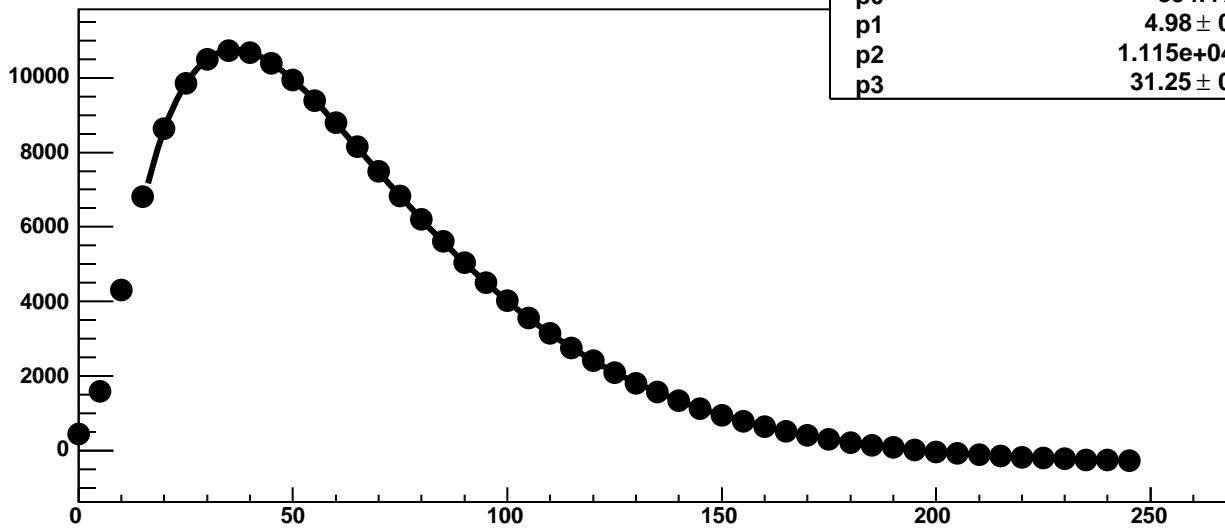
Chip 0, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

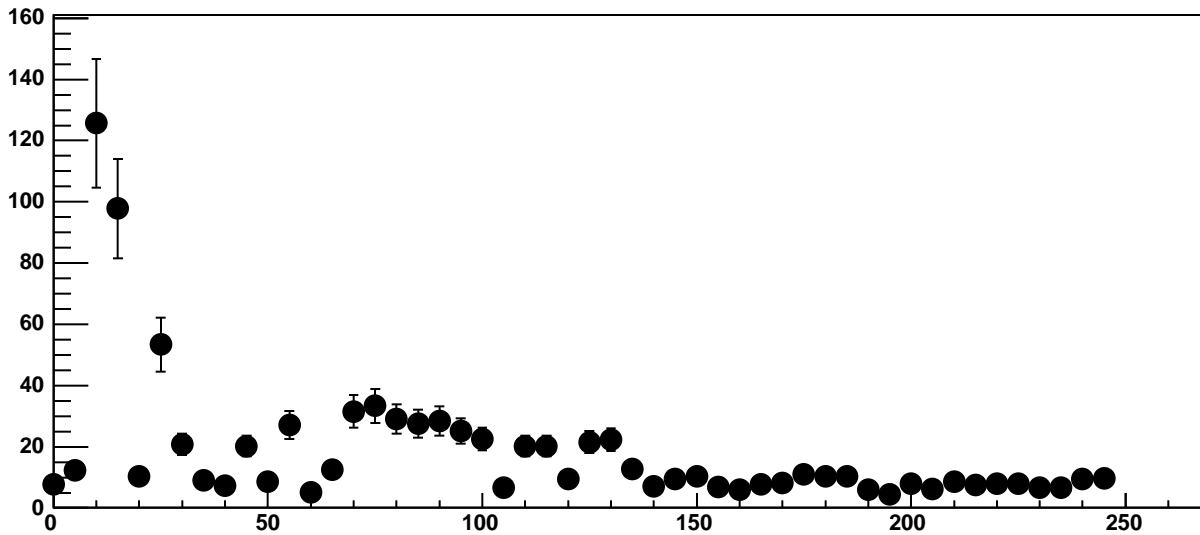


Chip 0, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold

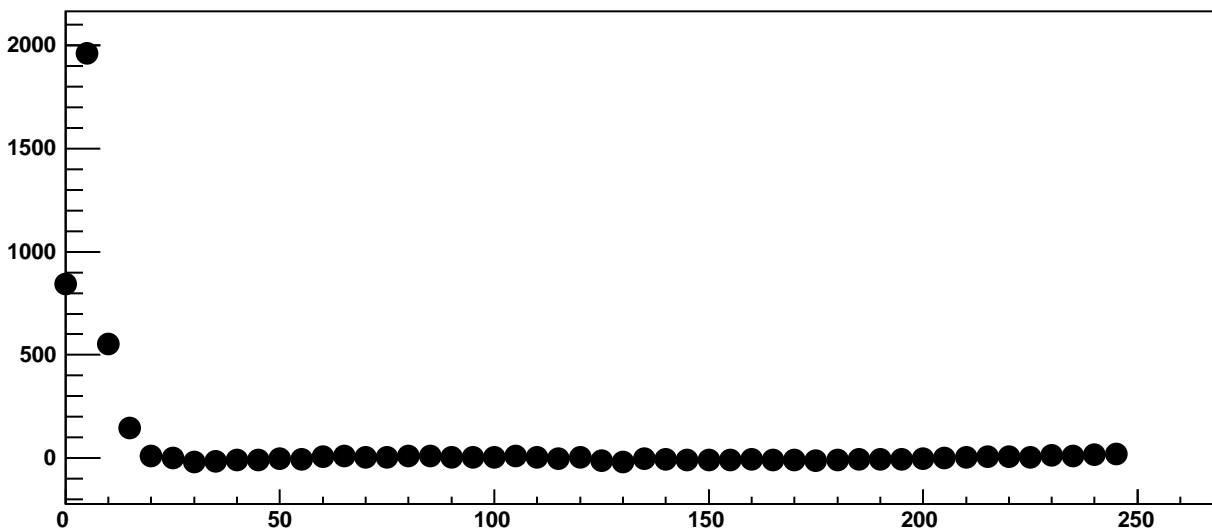


$\chi^2 / \text{ndf}$	776.6 / 42
p0	-394.4 ± 0.6044
p1	4.98 ± 0.008944
p2	1.115e+04 ± 0.962
p3	31.25 ± 0.006148

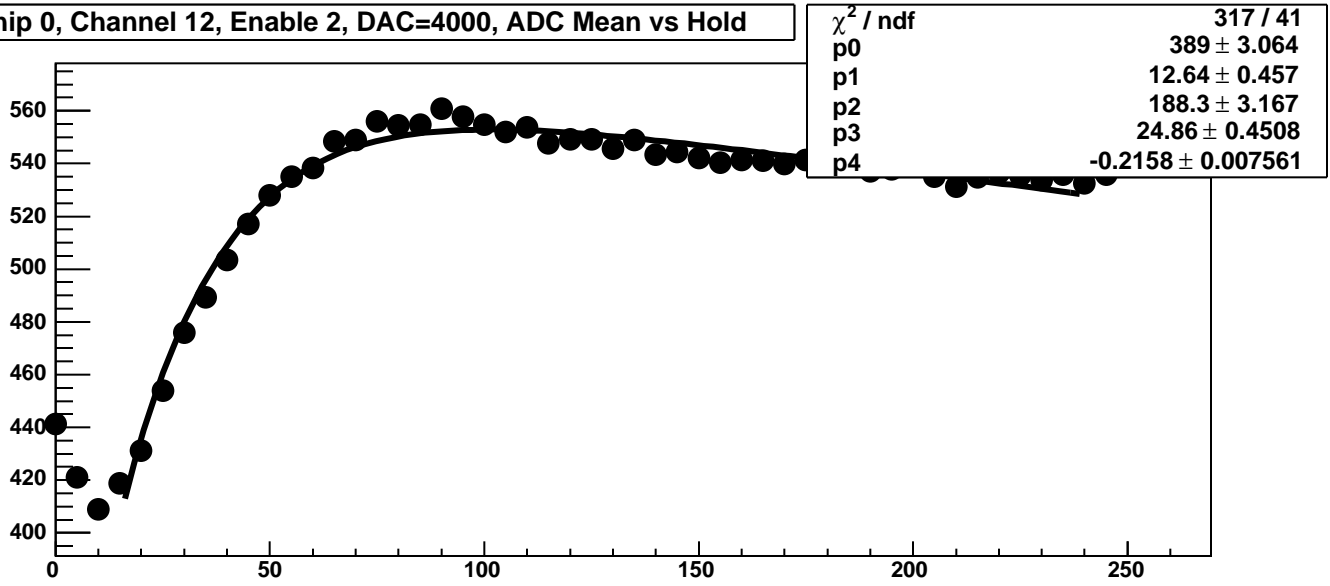
Chip 0, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold



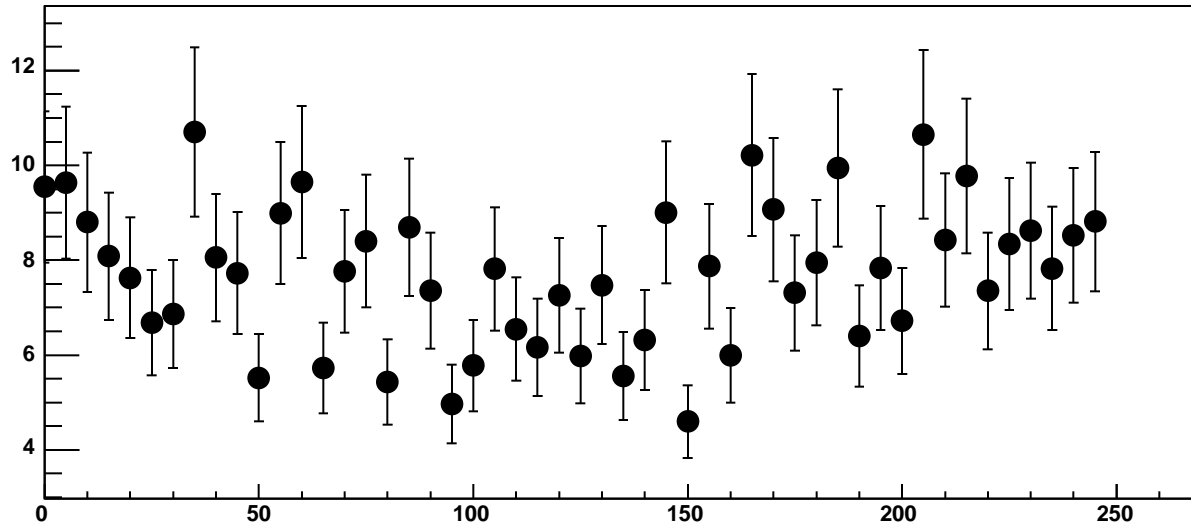
Chip 0, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold



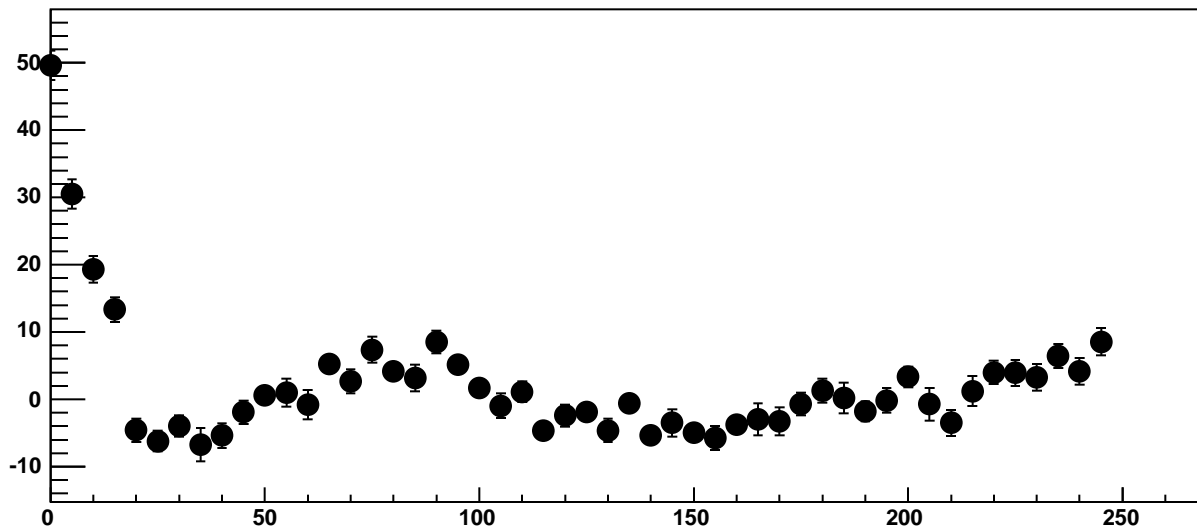
Chip 0, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold



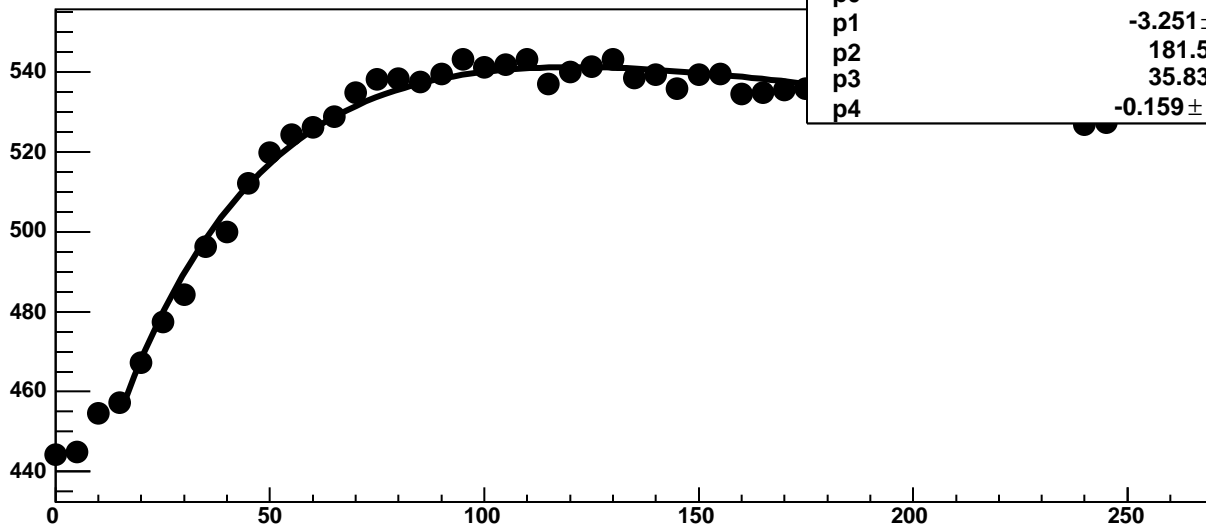
Chip 0, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

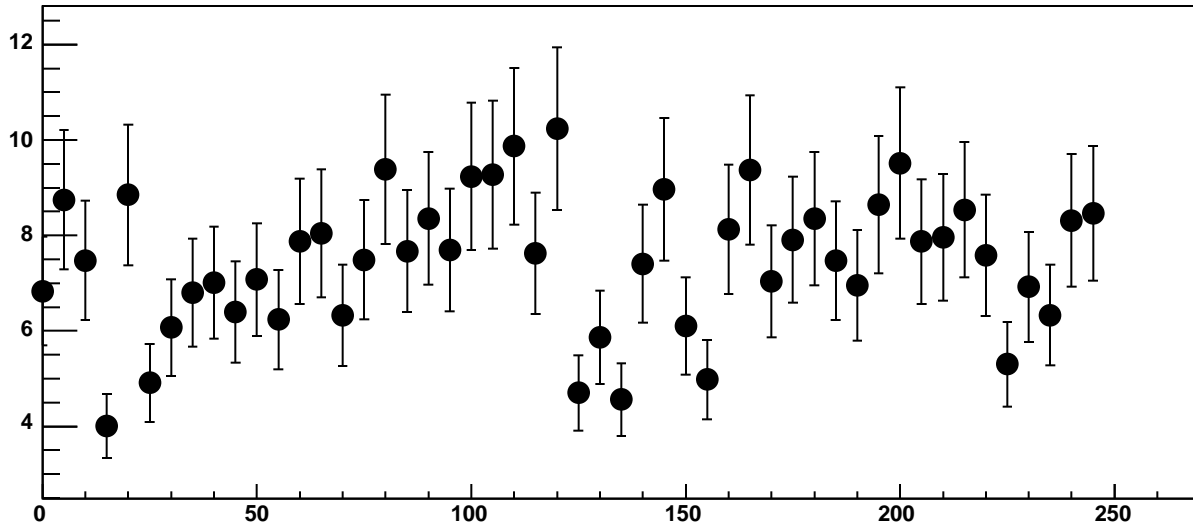


Chip 0, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

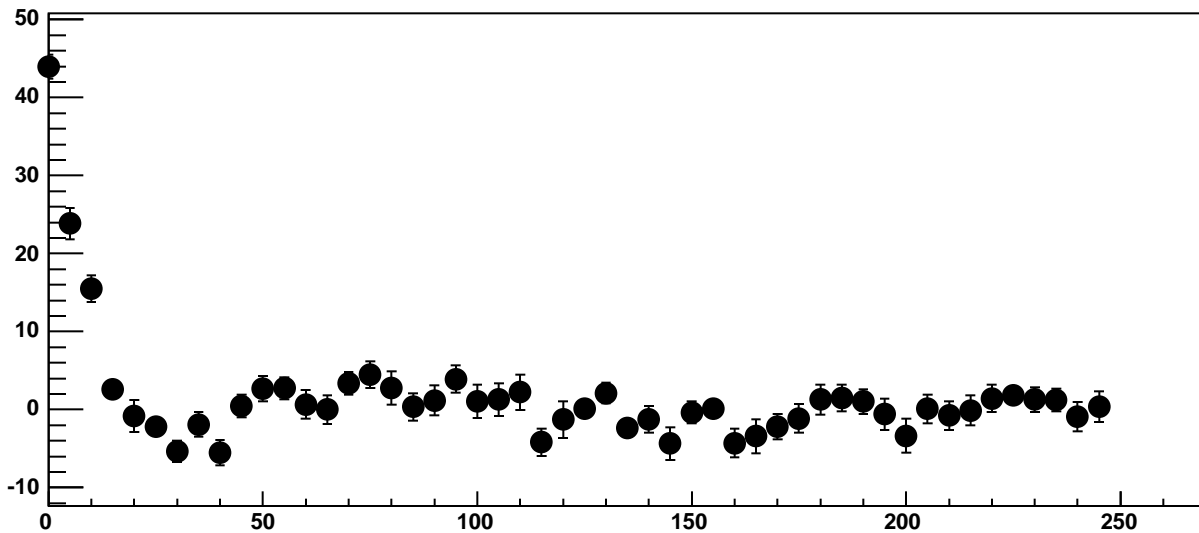


$\chi^2 / \text{ndf}$	104.9 / 41
p0	$385.1 \pm 1.051$
p1	$-3.251 \pm 0.4816$
p2	$181.5 \pm 1.183$
p3	$35.83 \pm 1.129$
p4	$-0.159 \pm 0.01084$

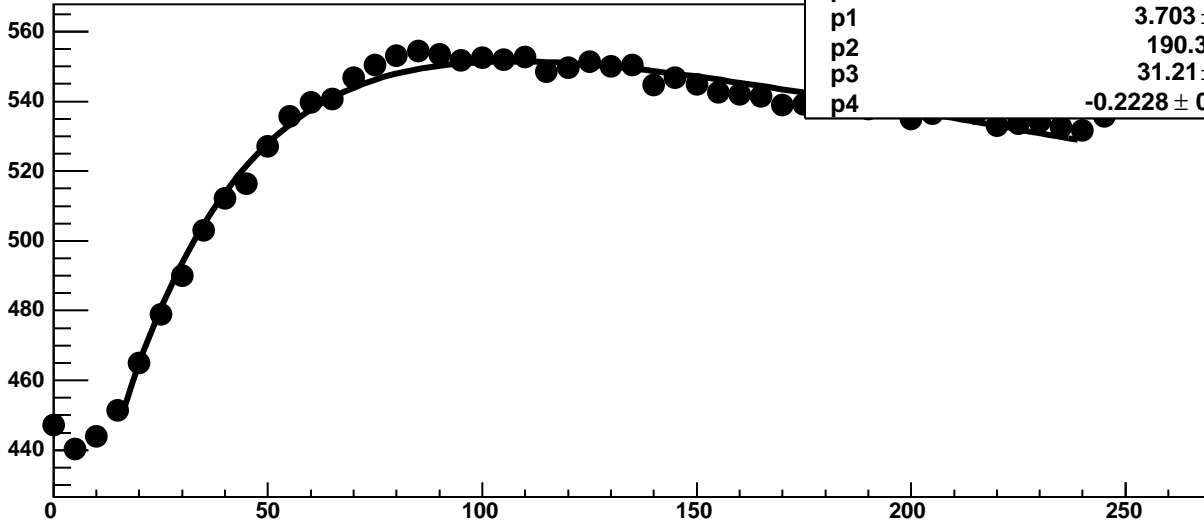
Chip 0, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

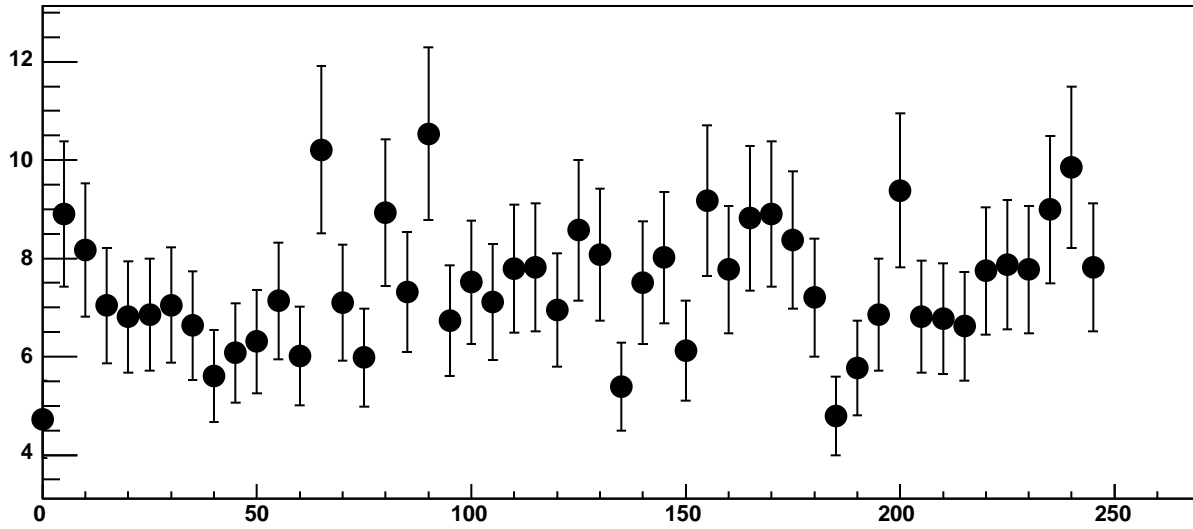


Chip 0, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold

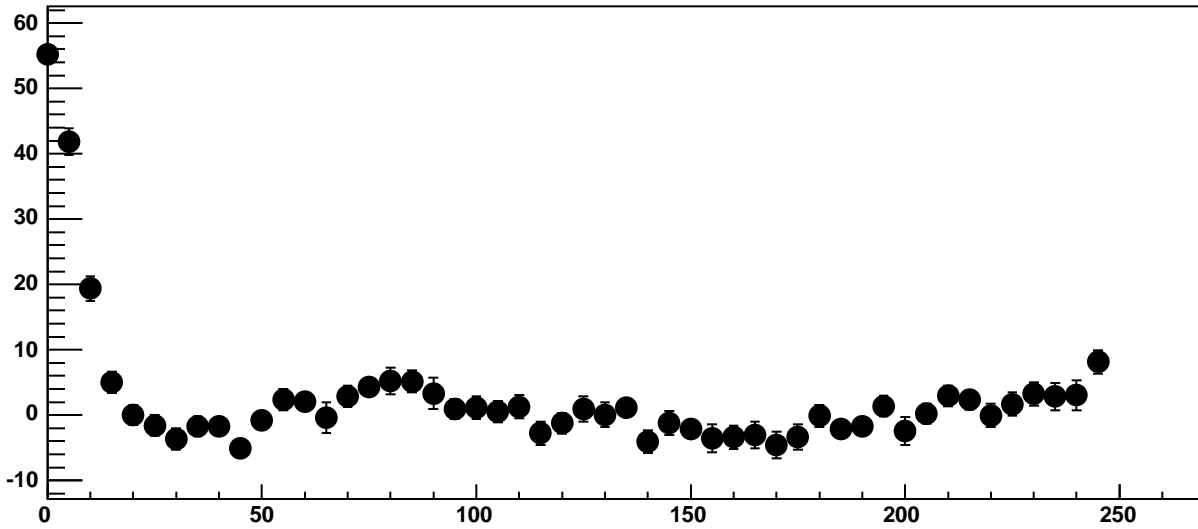


$\chi^2 / \text{ndf}$	119.5 / 41
p0	$391.2 \pm 3.074$
p1	$3.703 \pm 0.6322$
p2	$190.3 \pm 3.216$
p3	$31.21 \pm 0.7569$
p4	$-0.2228 \pm 0.009192$

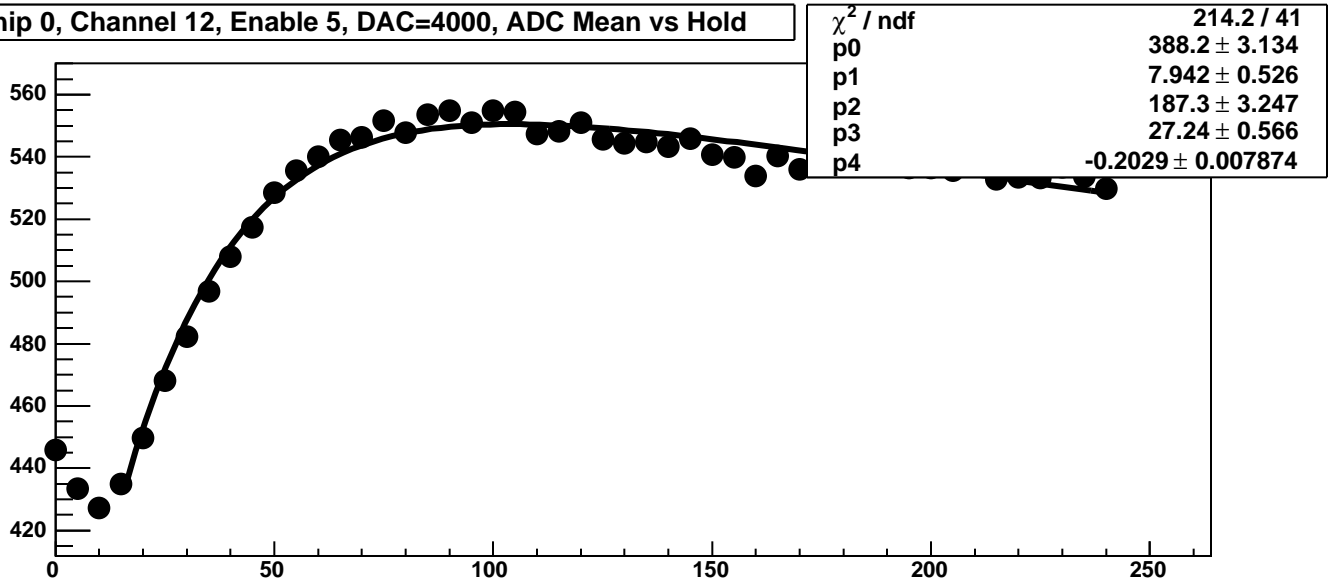
Chip 0, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold



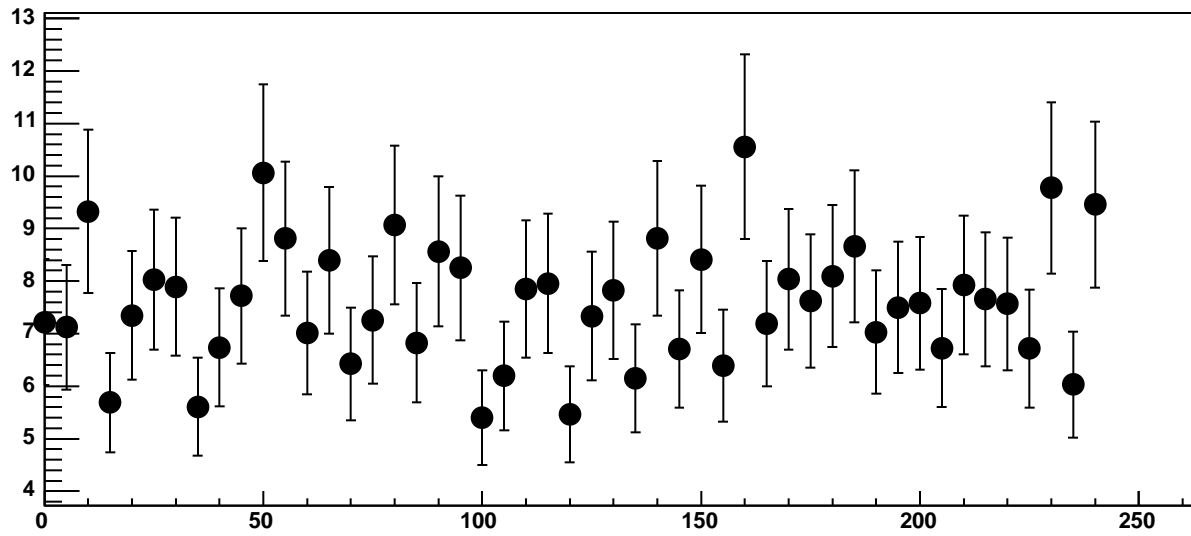
Chip 0, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold



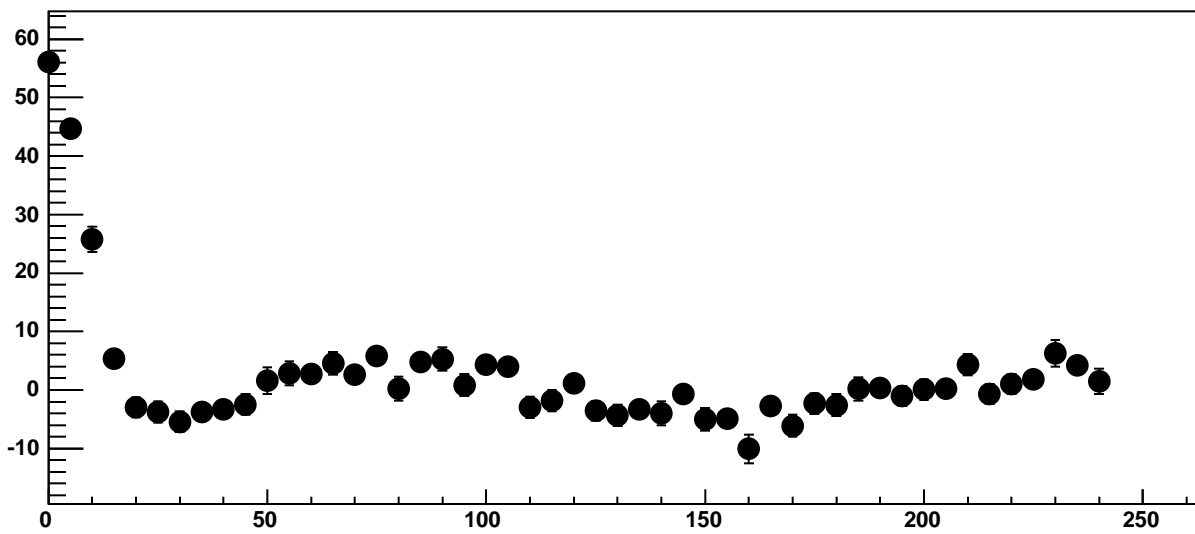
Chip 0, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold



Chip 0, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold

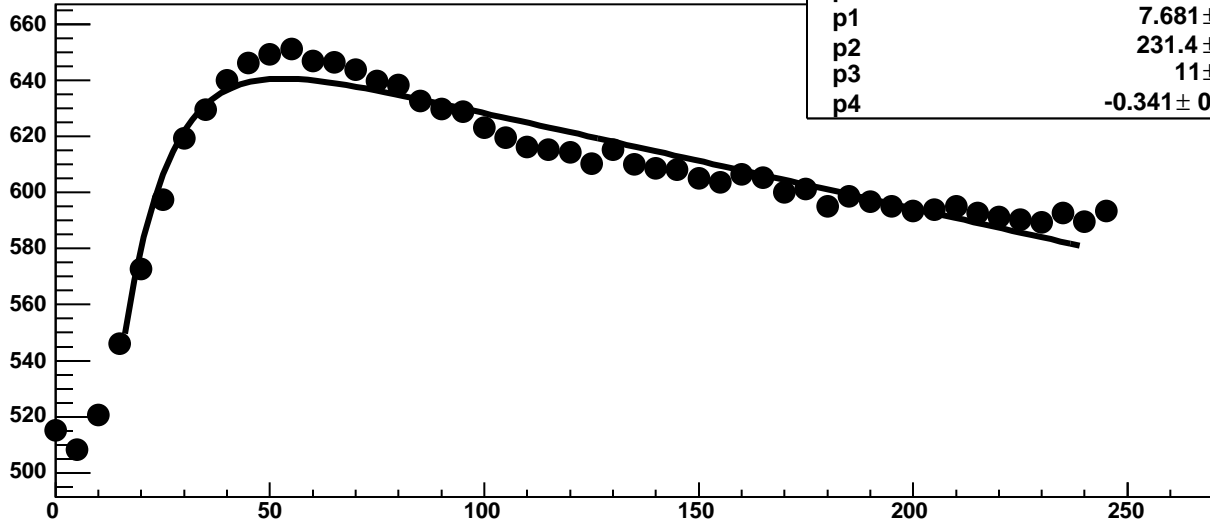


Chip 0, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold



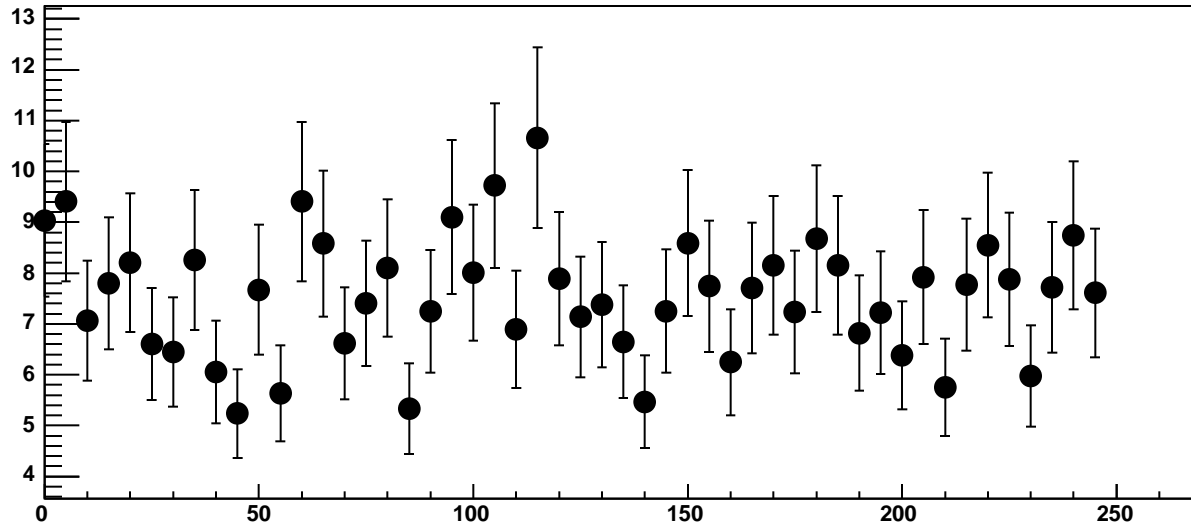


Chip 0, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold

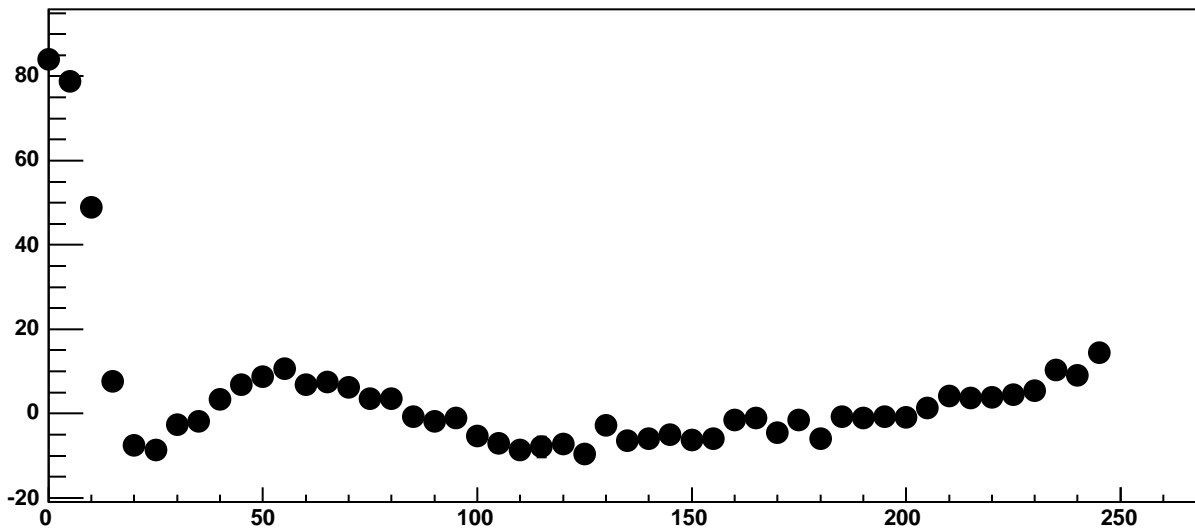


$\chi^2 / \text{ndf}$	548.8 / 41
p0	$428.4 \pm 0.3959$
p1	$7.681 \pm 0.2518$
p2	$231.4 \pm 0.4342$
p3	$11 \pm 0.2479$
p4	$-0.341 \pm 0.004842$

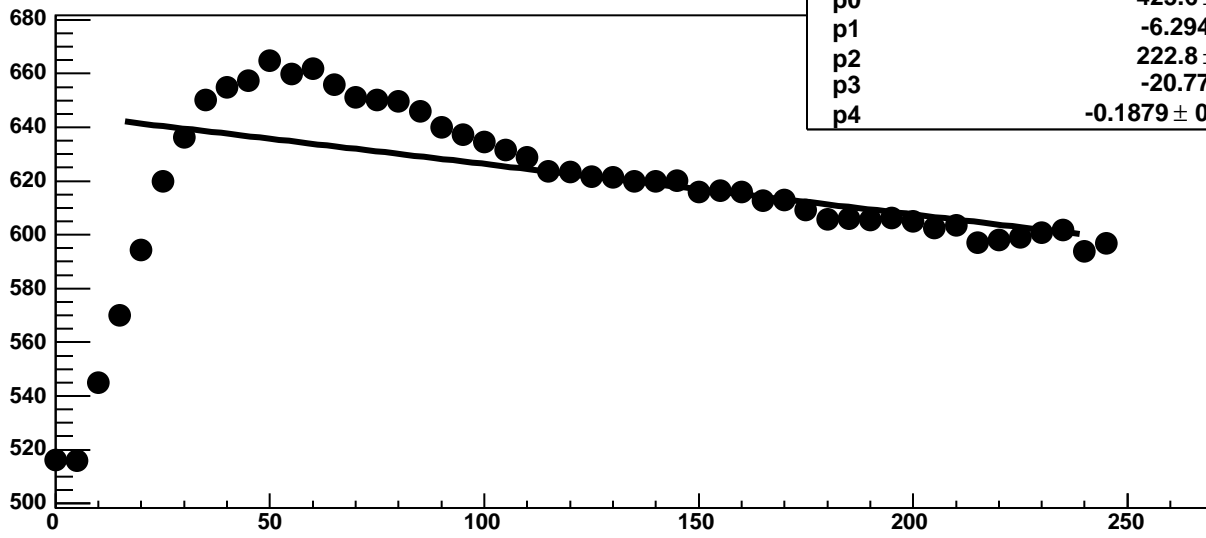
Chip 0, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold

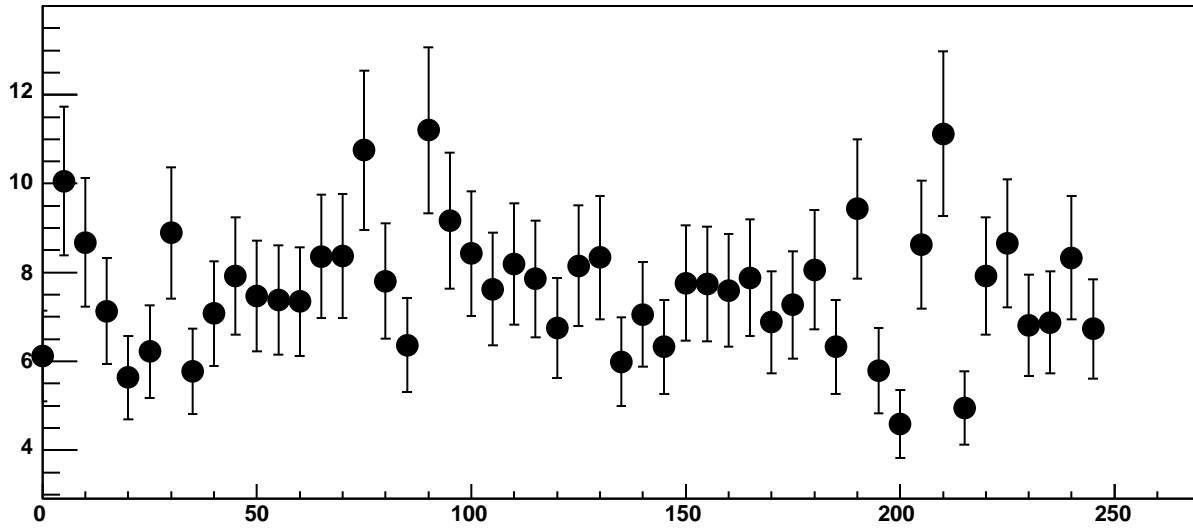


Chip 0, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold

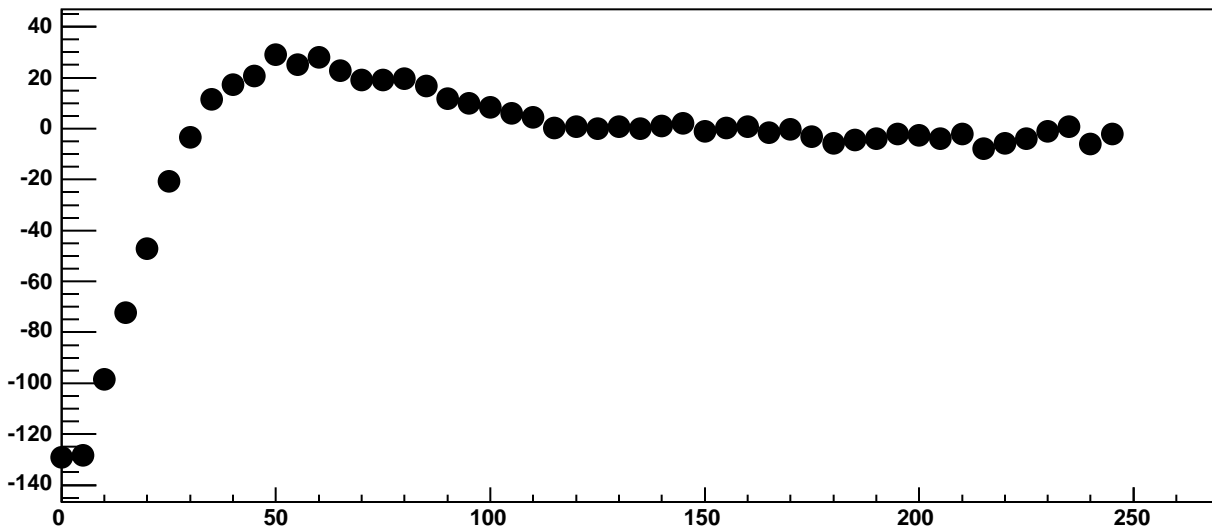


$\chi^2 / \text{ndf}$	5343 / 41
p0	$423.6 \pm 0.4969$
p1	$-6.294 \pm 10.39$
p2	$222.8 \pm 0.3648$
p3	$-20.77 \pm 3.789$
p4	$-0.1879 \pm 0.001975$

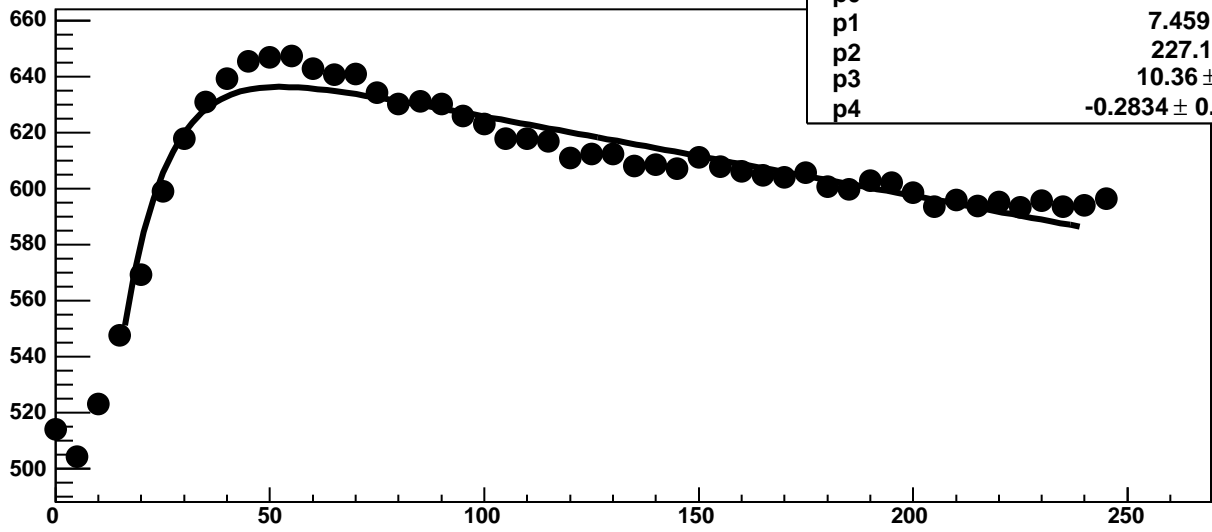
Chip 0, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold

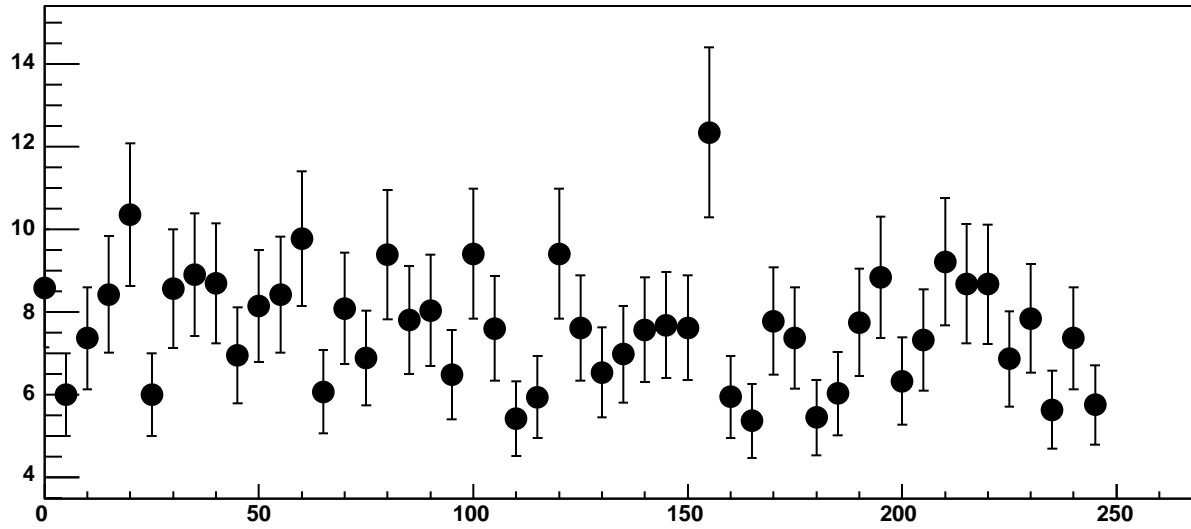


Chip 0, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

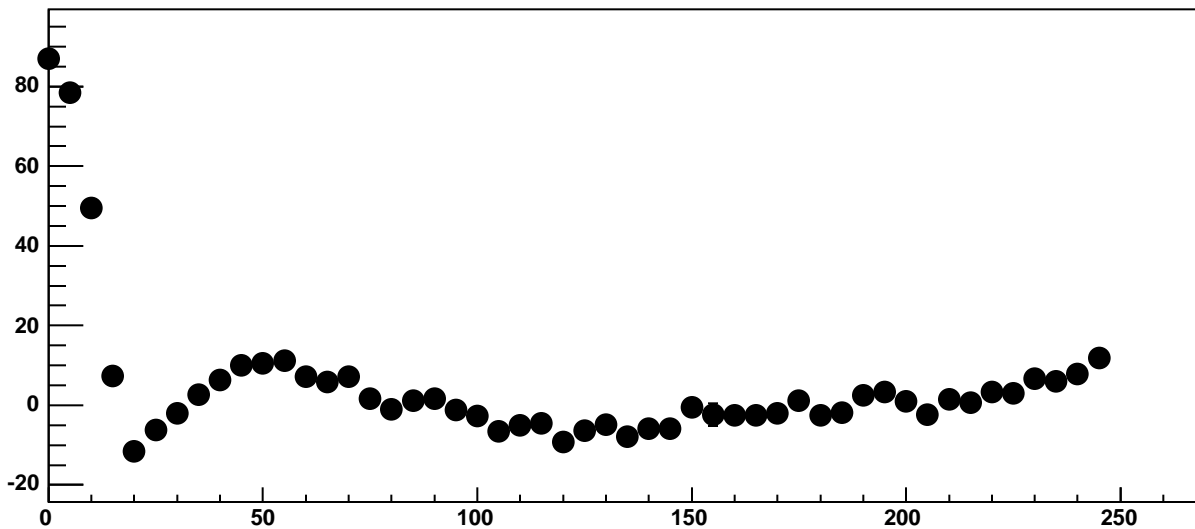


$\chi^2 / \text{ndf}$	443 / 41
p0	$424.9 \pm 3.232$
p1	$7.459 \pm 0.329$
p2	$227.1 \pm 3.274$
p3	$10.36 \pm 0.2672$
p4	$-0.2834 \pm 0.004763$

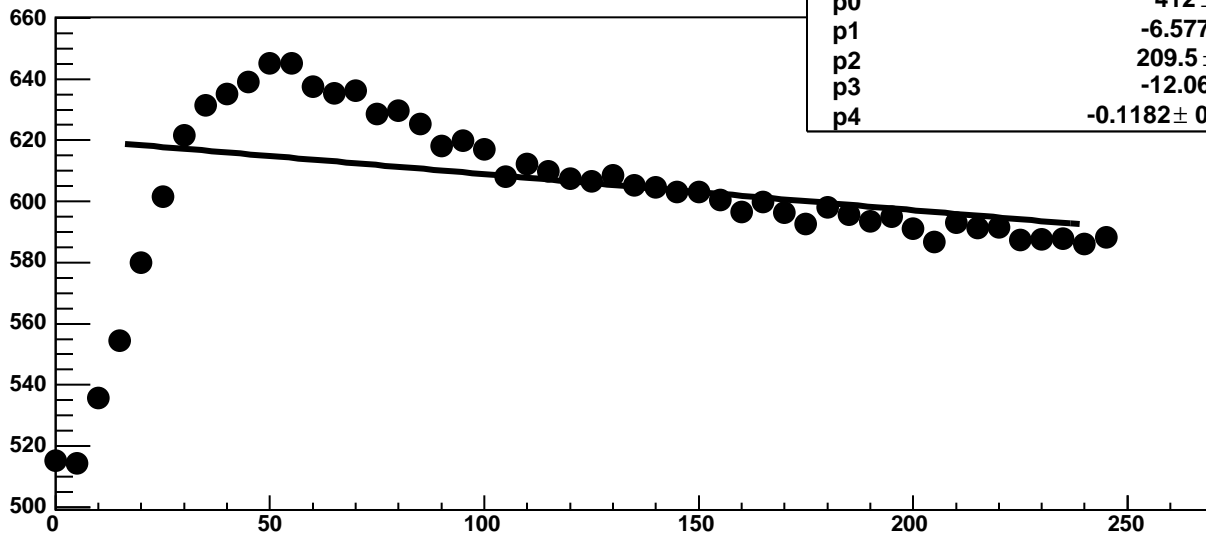
Chip 0, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

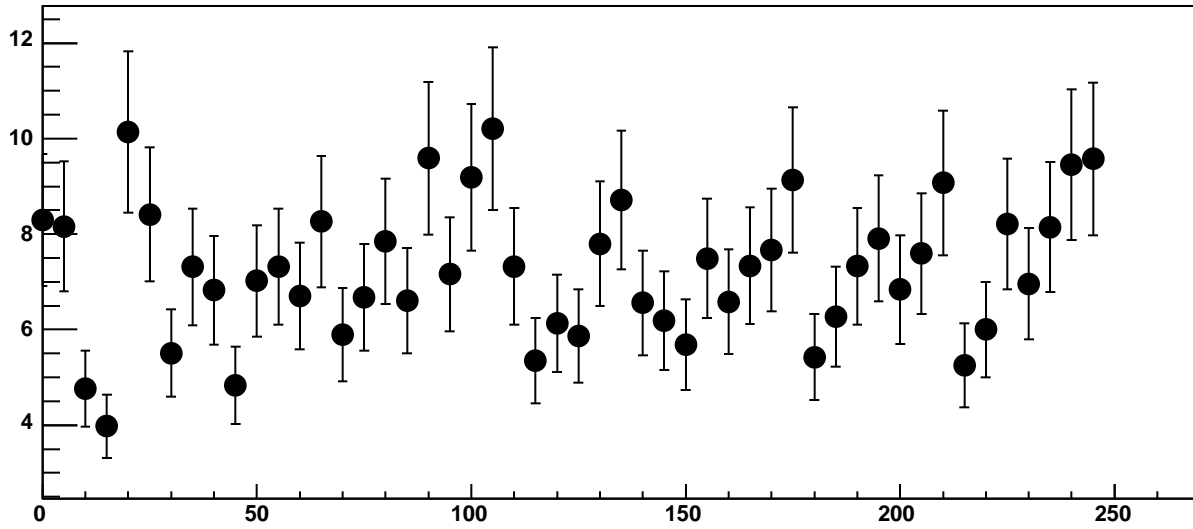


Chip 0, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

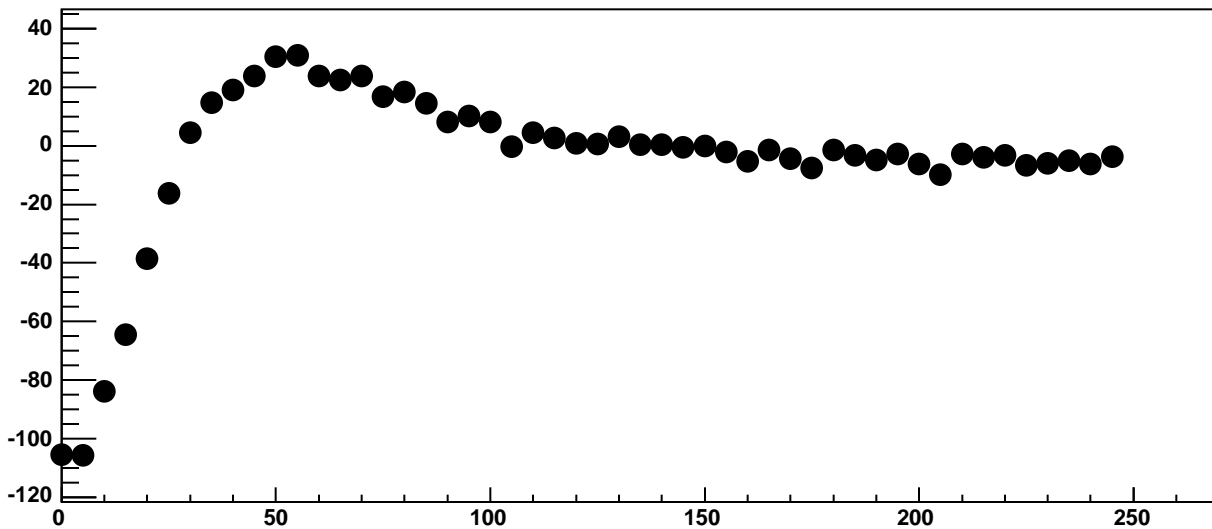


$\chi^2 / \text{ndf}$	7979 / 41
p0	$412 \pm 0.4835$
p1	$-6.577 \pm 12.36$
p2	$209.5 \pm 0.4613$
p3	$-12.06 \pm 16.48$
p4	$-0.1182 \pm 0.002895$

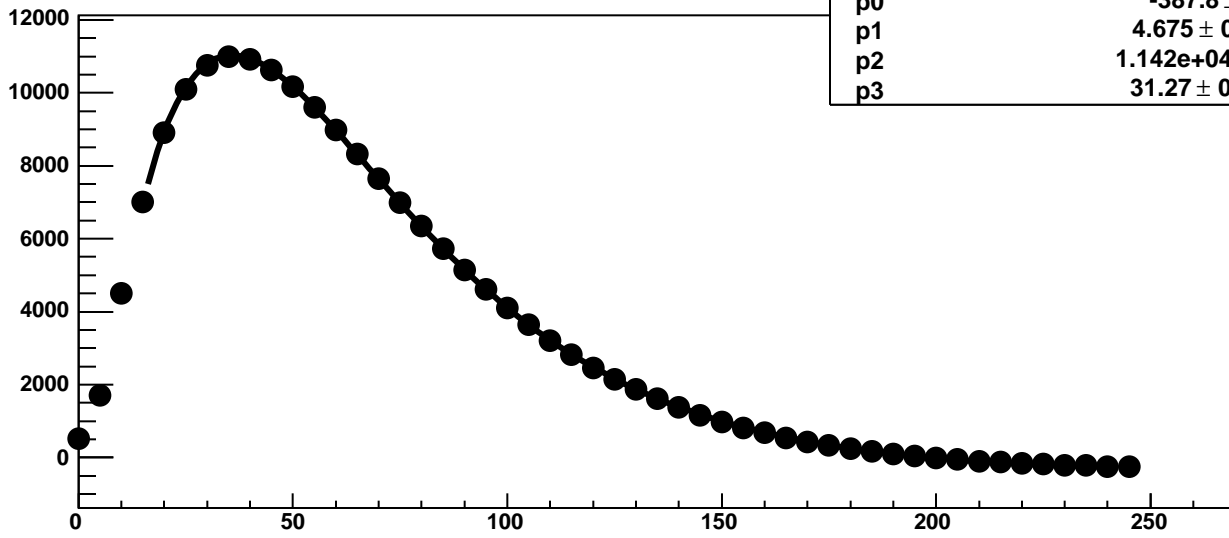
Chip 0, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

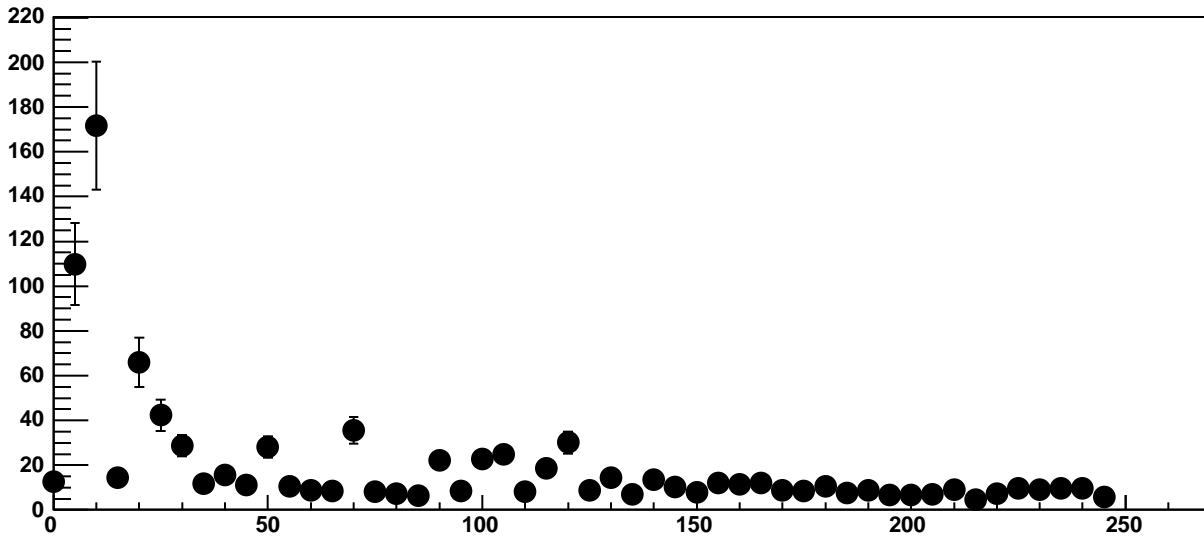


Chip 0, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold

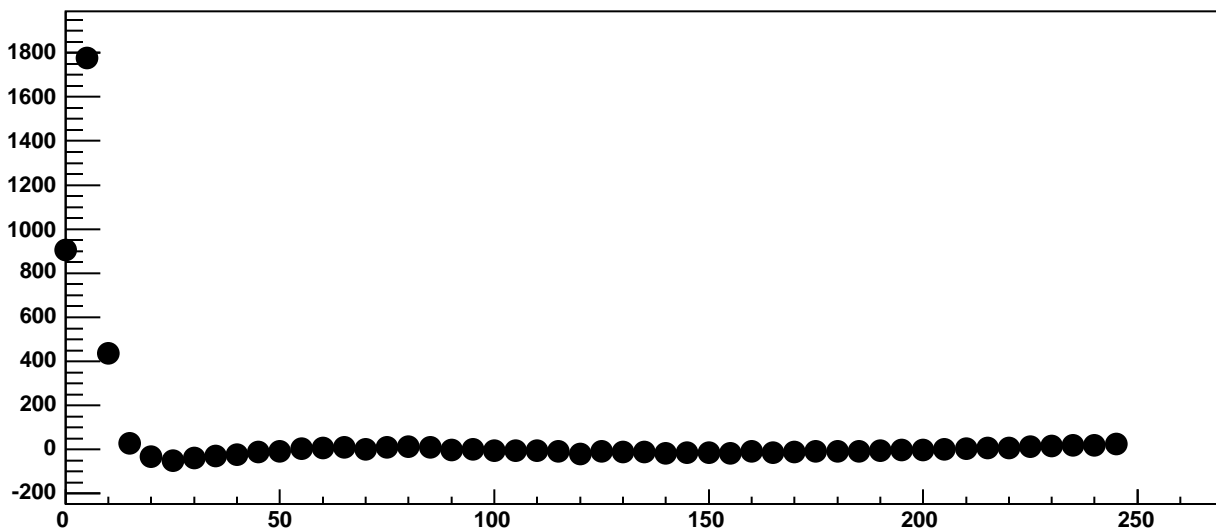


$\chi^2 / \text{ndf}$	1207 / 42
p0	-387.8 ± 0.6006
p1	4.675 ± 0.007792
p2	1.142e+04 ± 1.247
p3	31.27 ± 0.005514

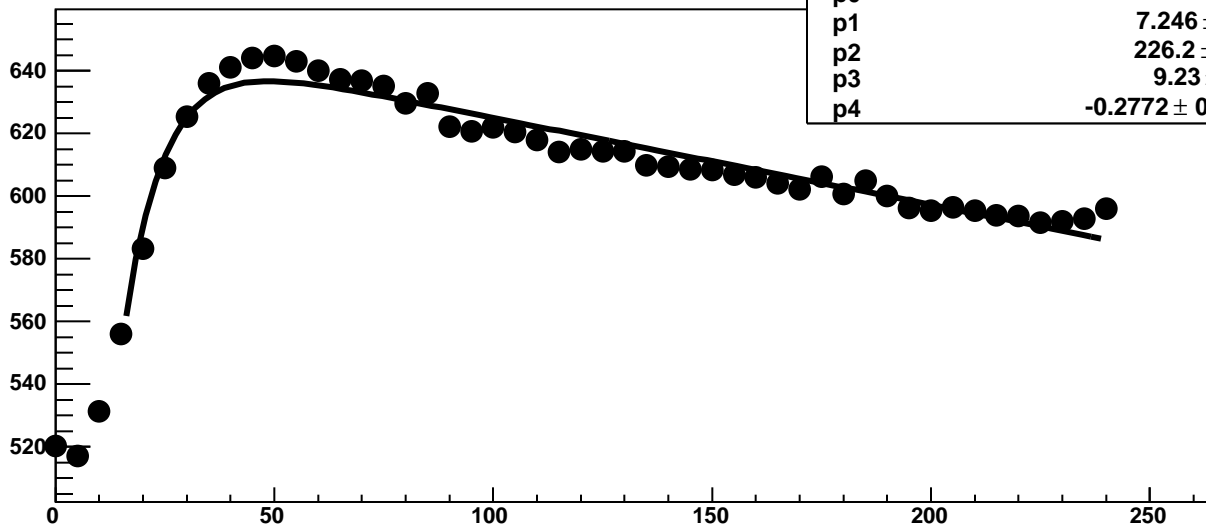
Chip 0, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold

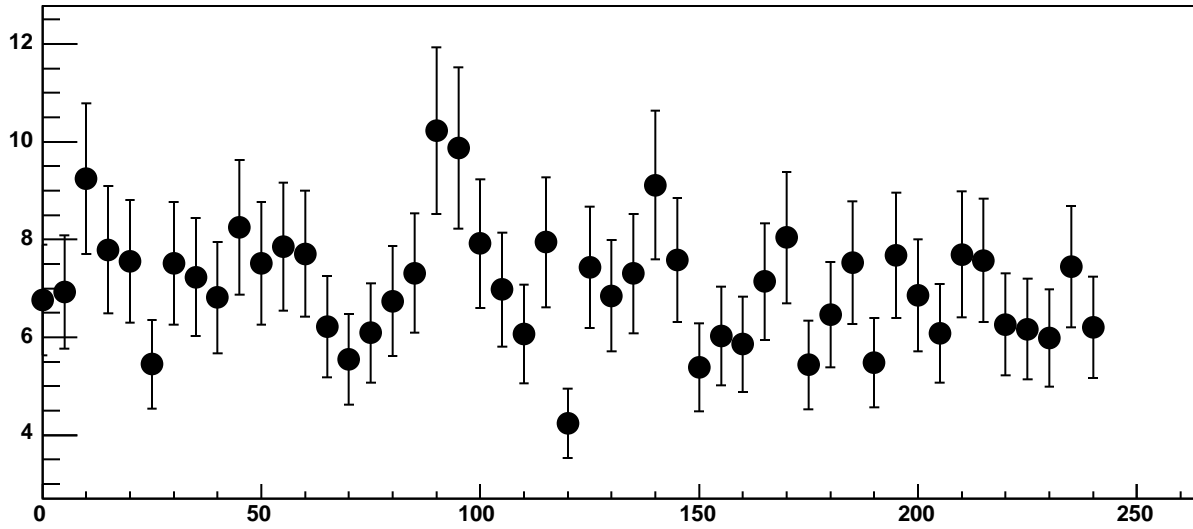


Chip 0, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

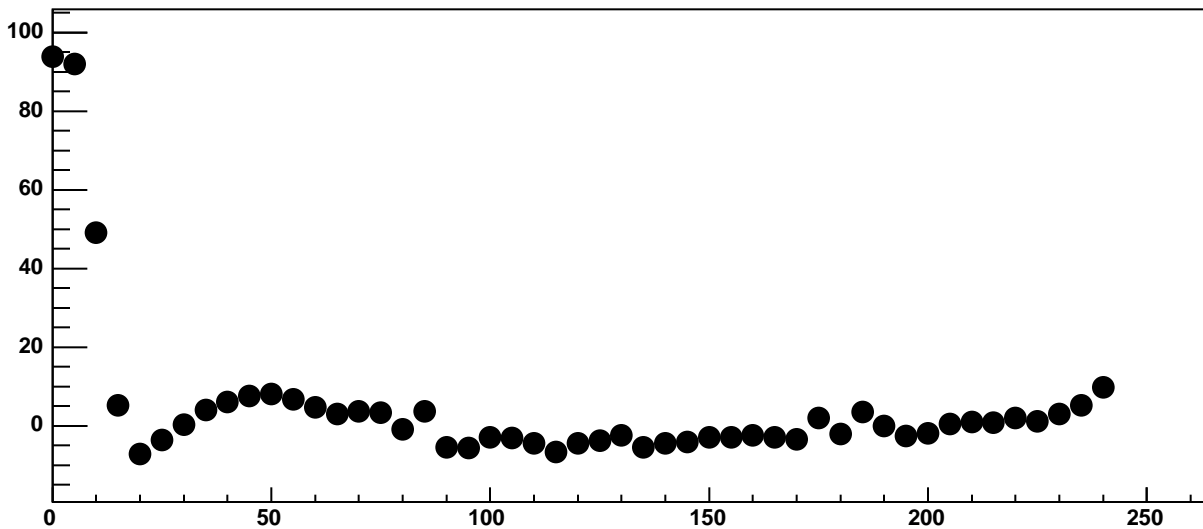


$\chi^2 / \text{ndf}$	325.4 / 41
p0	$424.4 \pm 0.3626$
p1	$7.246 \pm 0.3069$
p2	$226.2 \pm 0.3926$
p3	$9.23 \pm 0.2571$
p4	$-0.2772 \pm 0.004343$

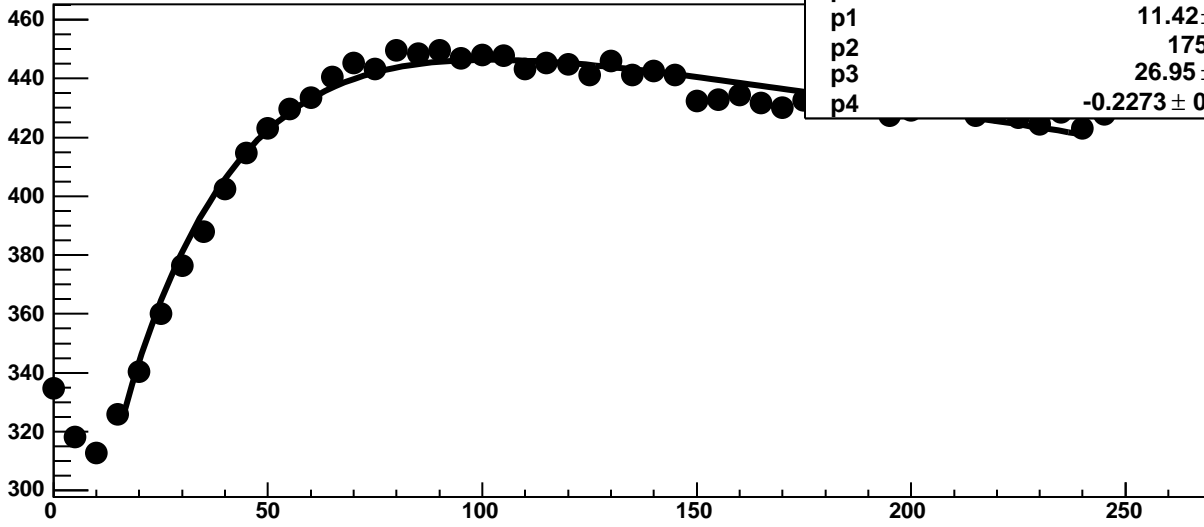
Chip 0, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold

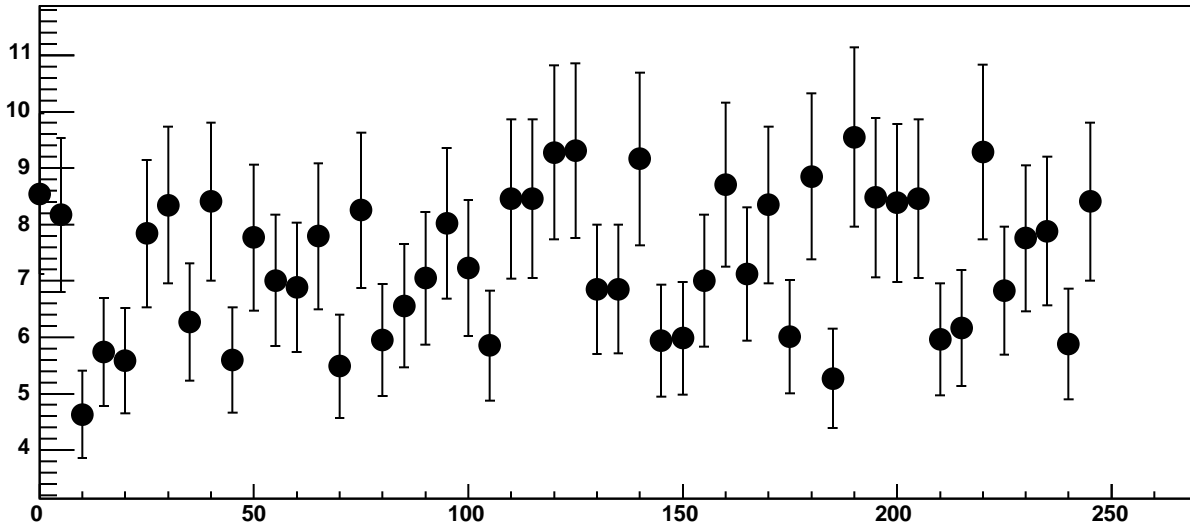


Chip 0, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

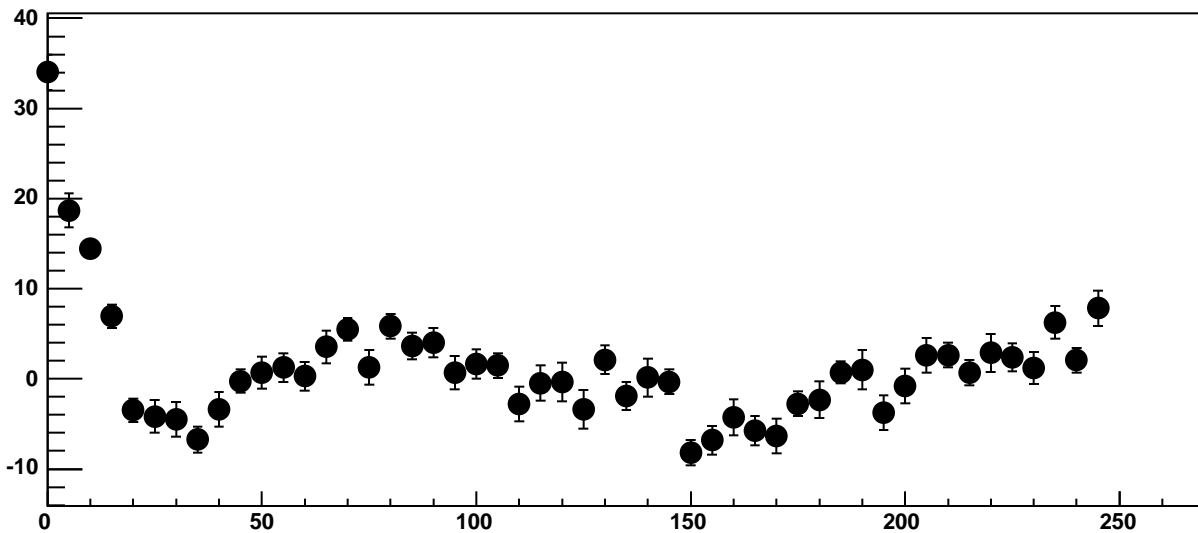


$\chi^2 / \text{ndf}$	251.3 / 41
p0	$298.1 \pm 3.03$
p1	$11.42 \pm 0.5137$
p2	$175 \pm 3.162$
p3	$26.95 \pm 0.5169$
p4	$-0.2273 \pm 0.007666$

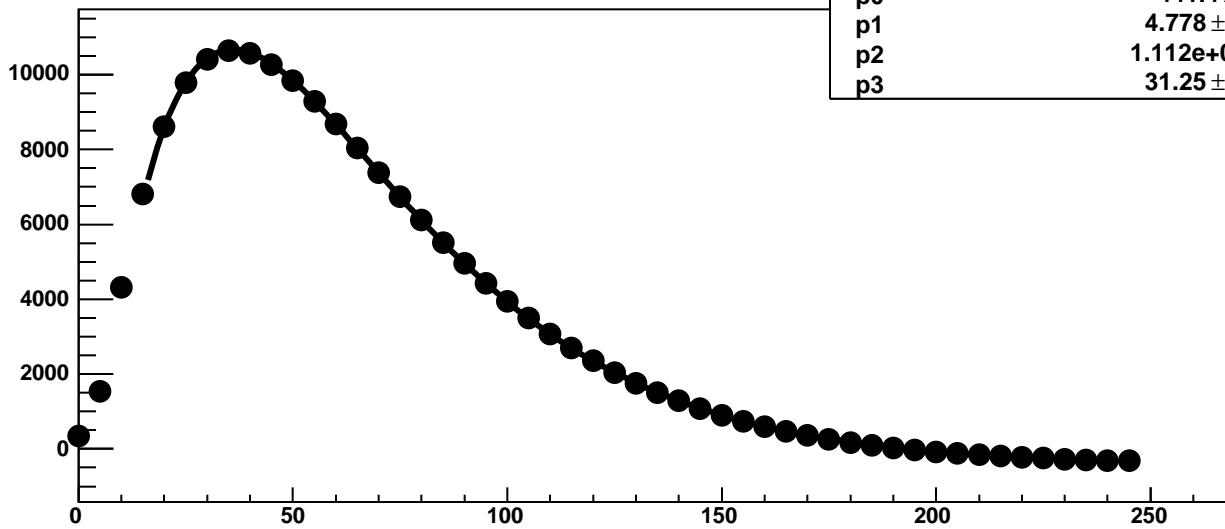
Chip 0, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

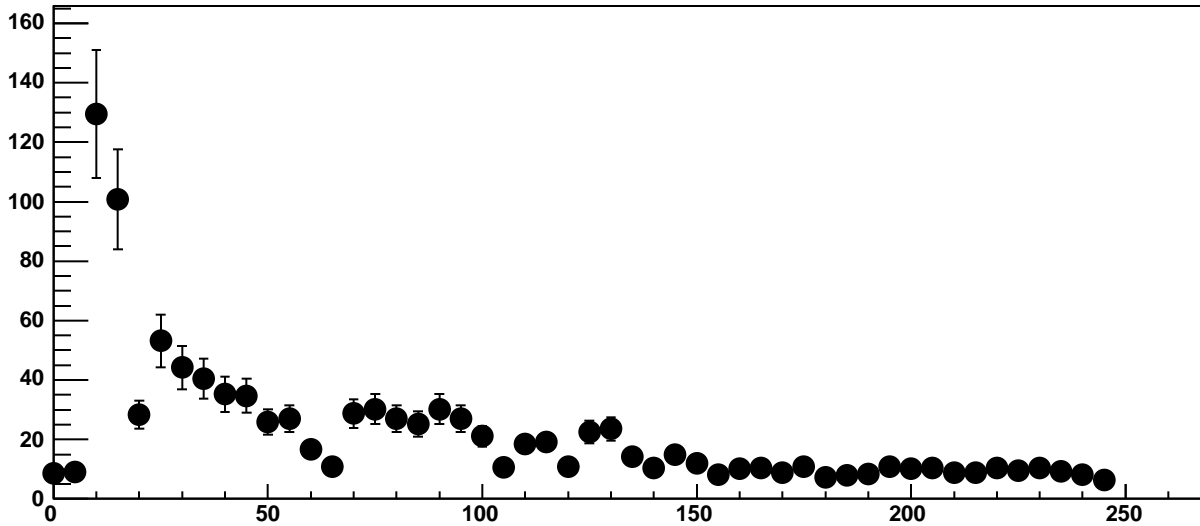


Chip 0, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold

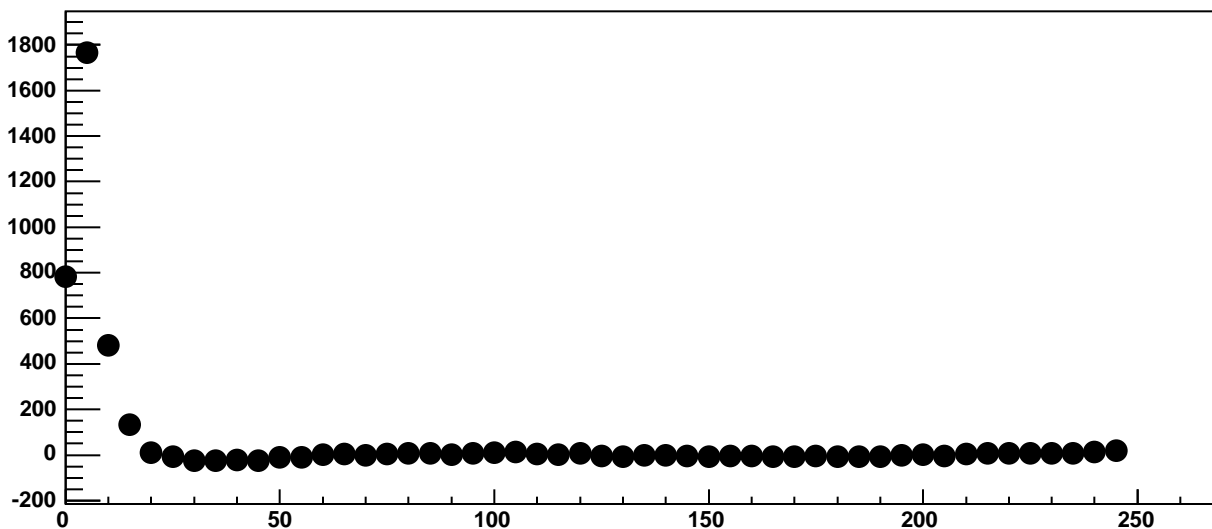


$\chi^2 / \text{ndf}$	325.4 / 42
p0	-441.4 ± 0.8226
p1	4.778 ± 0.01904
p2	1.112e+04 ± 2.4
p3	31.25 ± 0.01006

Chip 0, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold

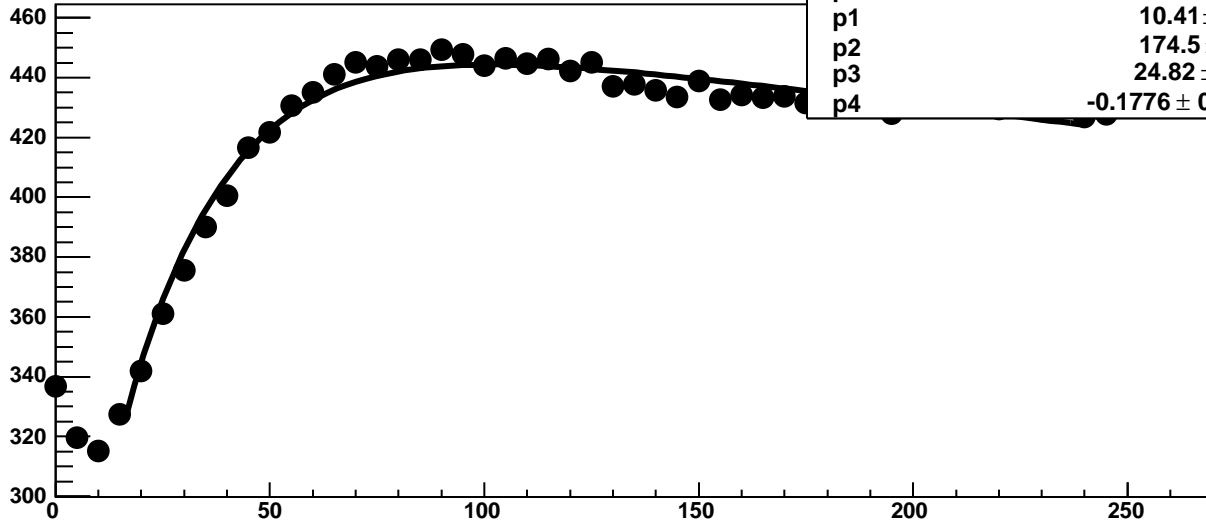


Chip 0, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold



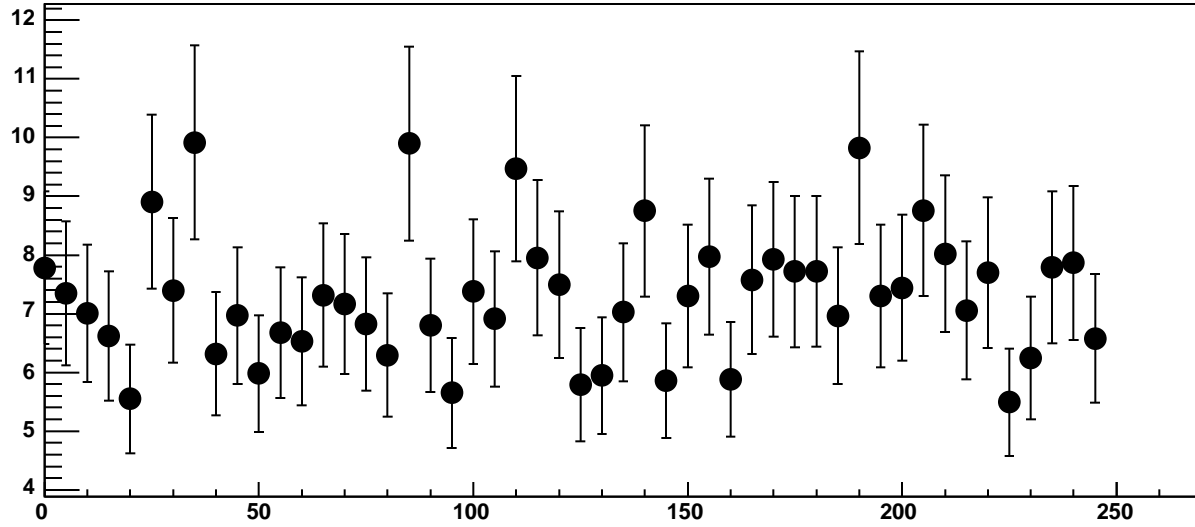


Chip 0, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

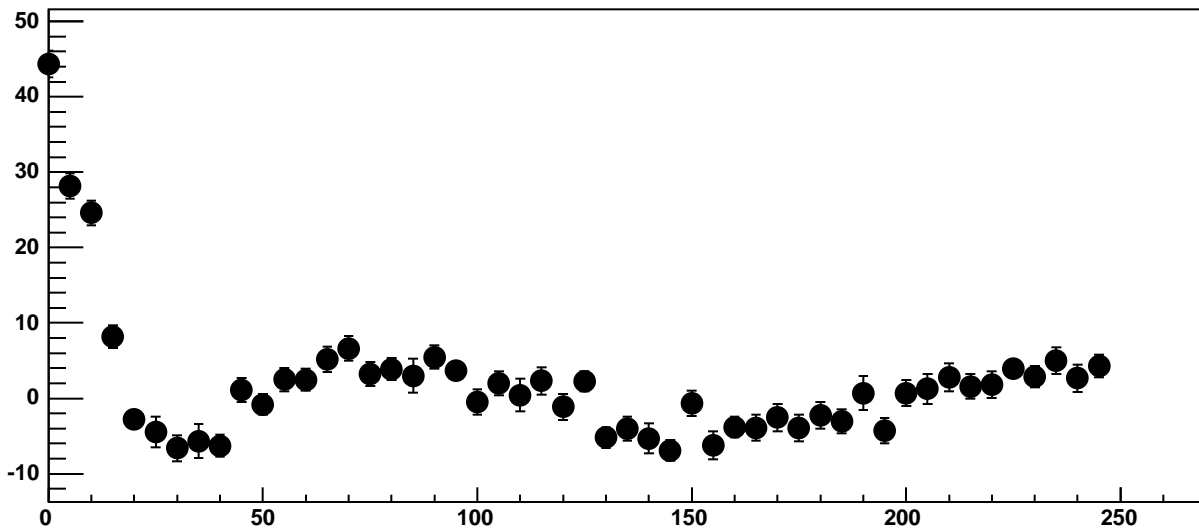


$\chi^2 / \text{ndf}$	276.8 / 41
p0	$290.5 \pm 1.519$
p1	$10.41 \pm 0.2543$
p2	$174.5 \pm 0.4161$
p3	$24.82 \pm 0.5176$
p4	$-0.1776 \pm 0.007492$

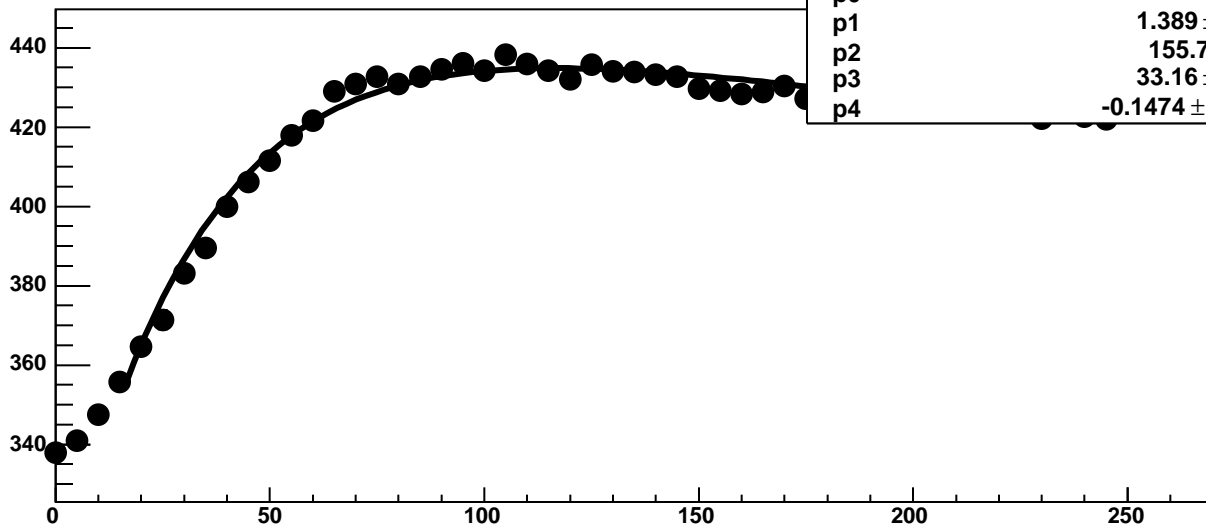
Chip 0, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

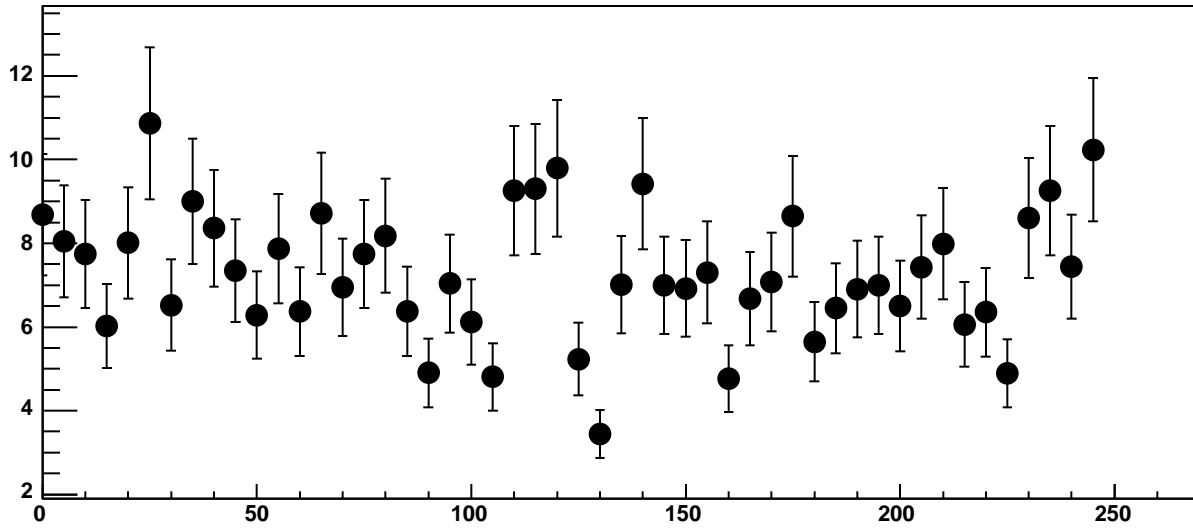


Chip 0, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

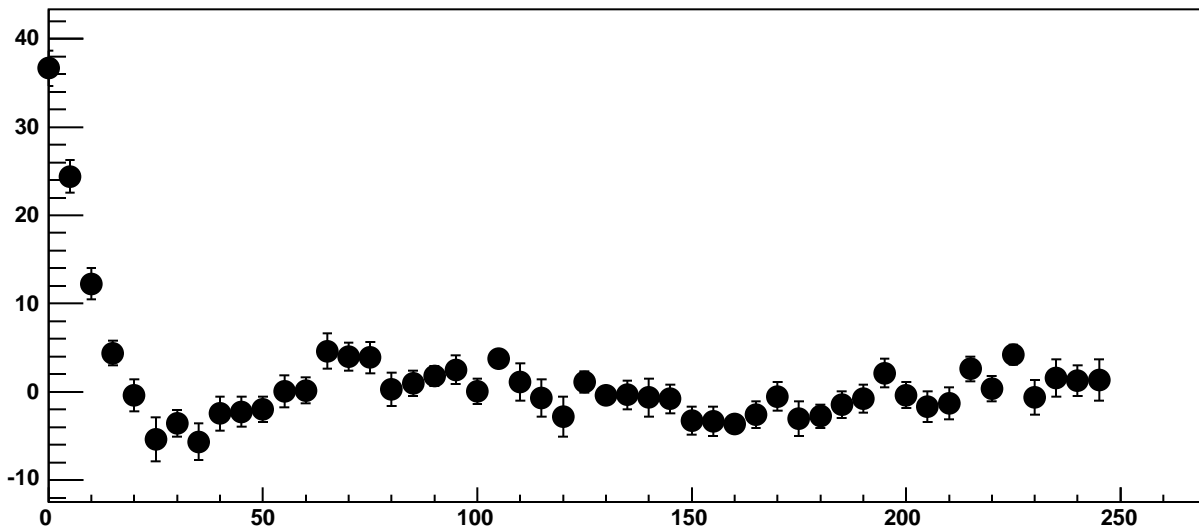


$\chi^2 / \text{ndf}$	122.4 / 41
p0	$301 \pm 2.844$
p1	$1.389 \pm 0.7928$
p2	$155.7 \pm 2.953$
p3	$33.16 \pm 0.9983$
p4	$-0.1474 \pm 0.00851$

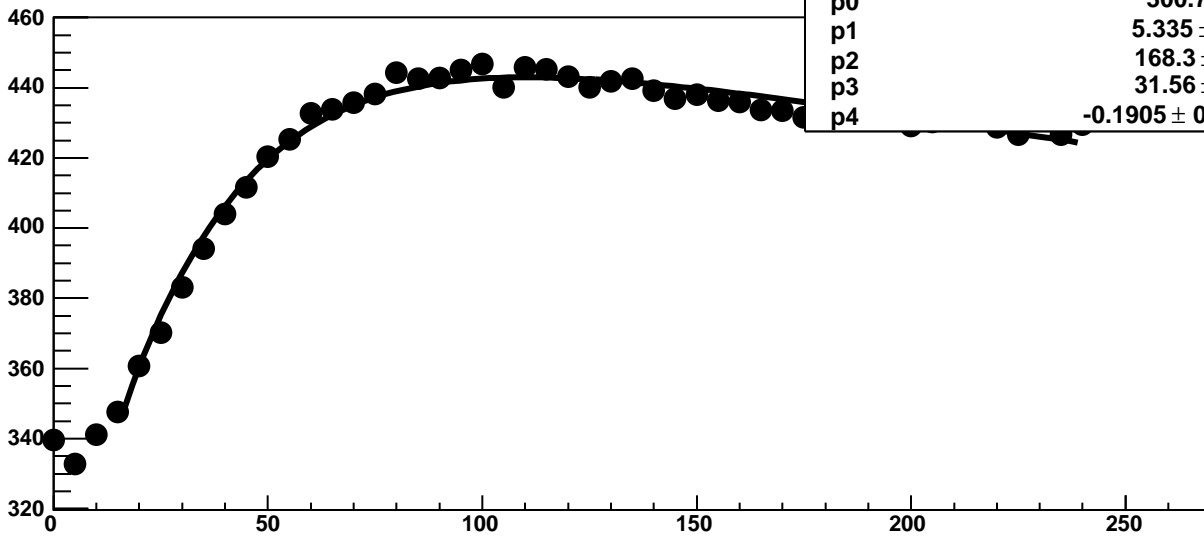
Chip 0, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

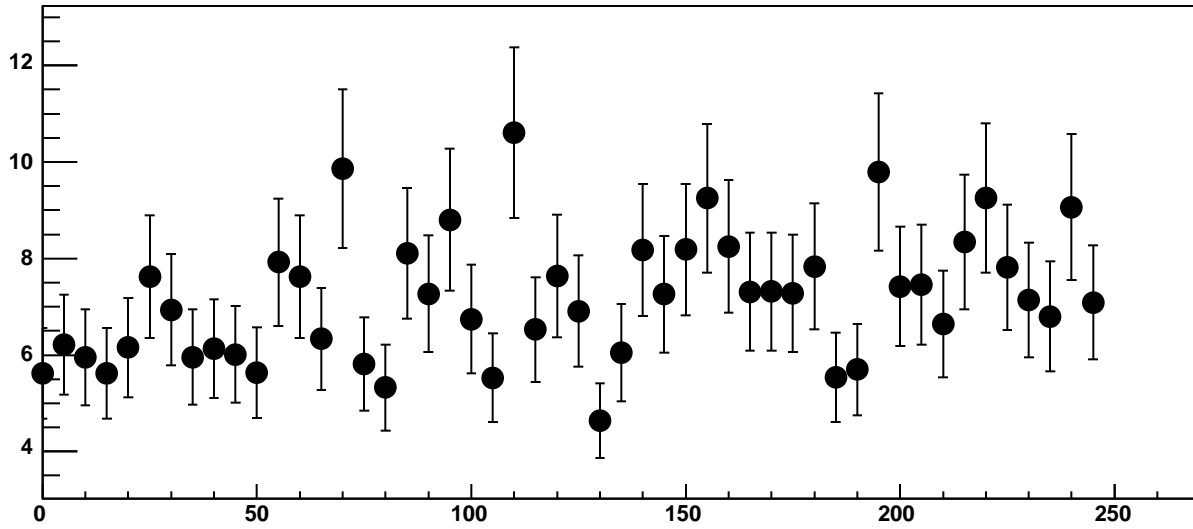


Chip 0, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold

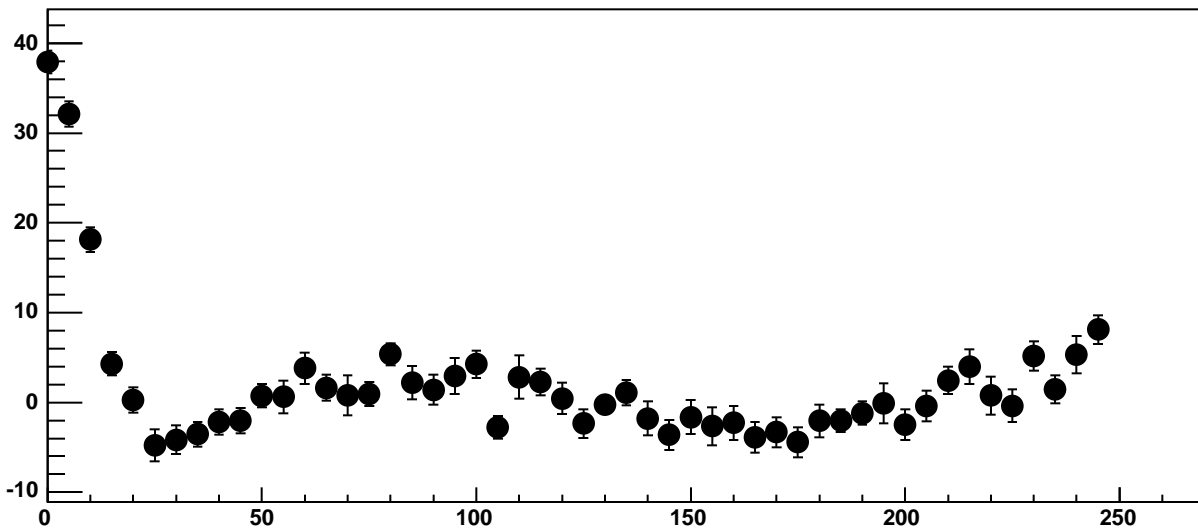


$\chi^2 / \text{ndf}$	143.2 / 41
p0	$300.7 \pm 2.104$
p1	$5.335 \pm 0.3735$
p2	$168.3 \pm 0.5008$
p3	$31.56 \pm 0.8302$
p4	$-0.1905 \pm 0.009648$

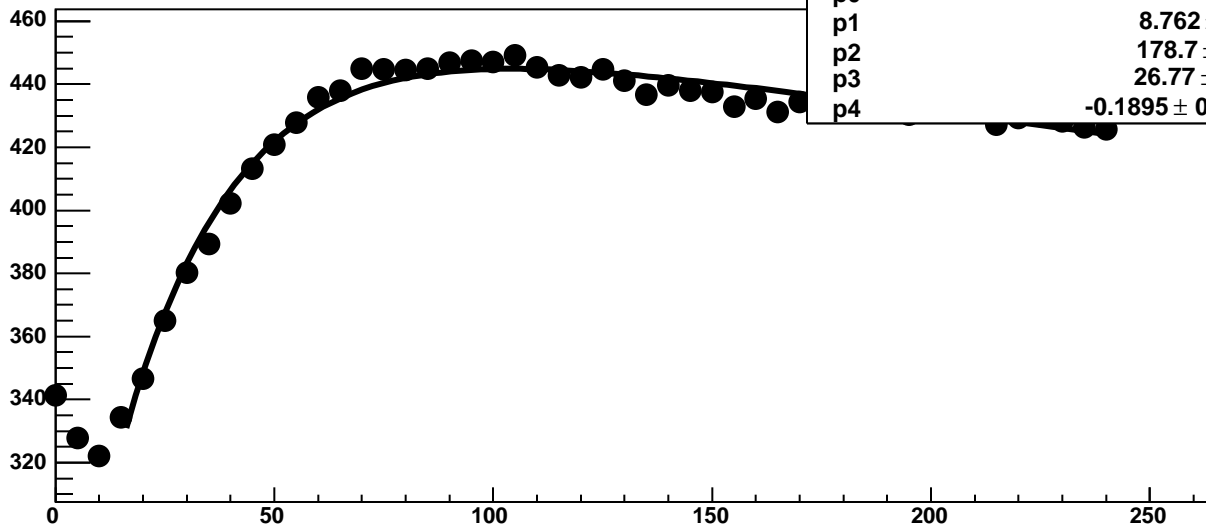
Chip 0, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold

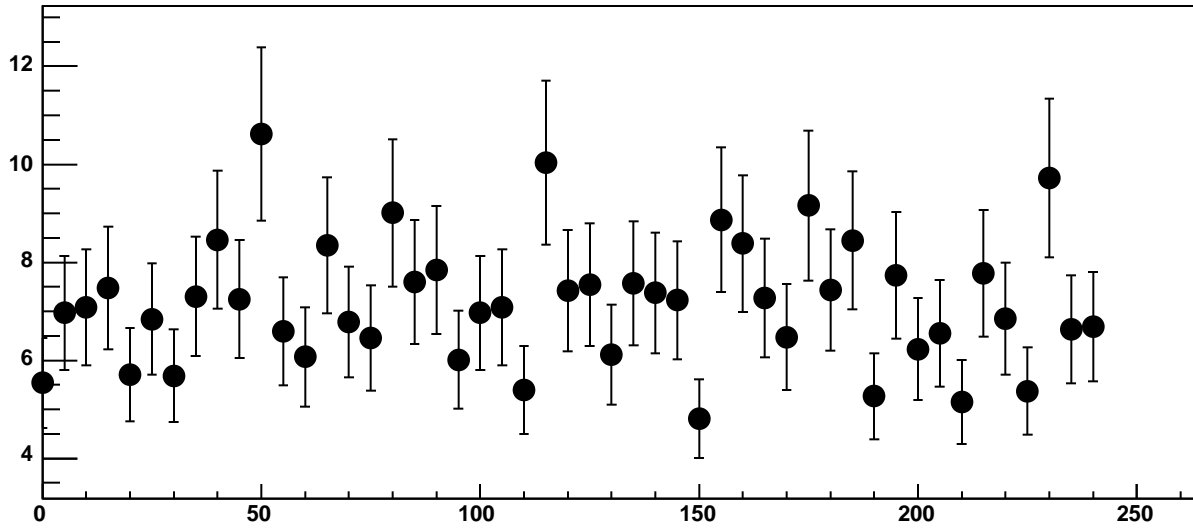


Chip 0, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

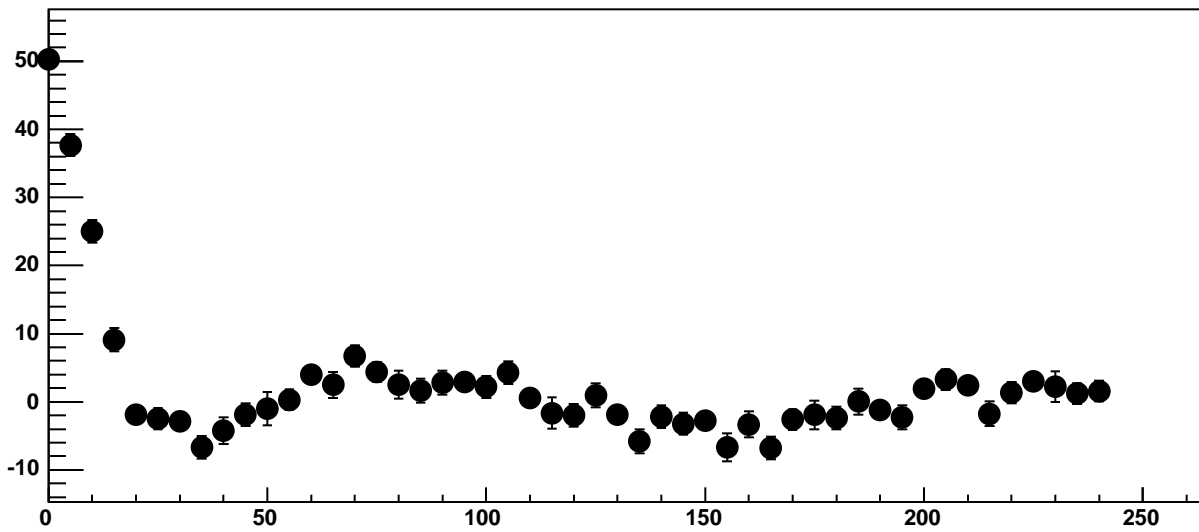


$\chi^2 / \text{ndf}$	201.6 / 41
p0	$289.3 \pm 1.626$
p1	$8.762 \pm 0.2881$
p2	$178.7 \pm 0.3813$
p3	$26.77 \pm 0.6166$
p4	$-0.1895 \pm 0.007937$

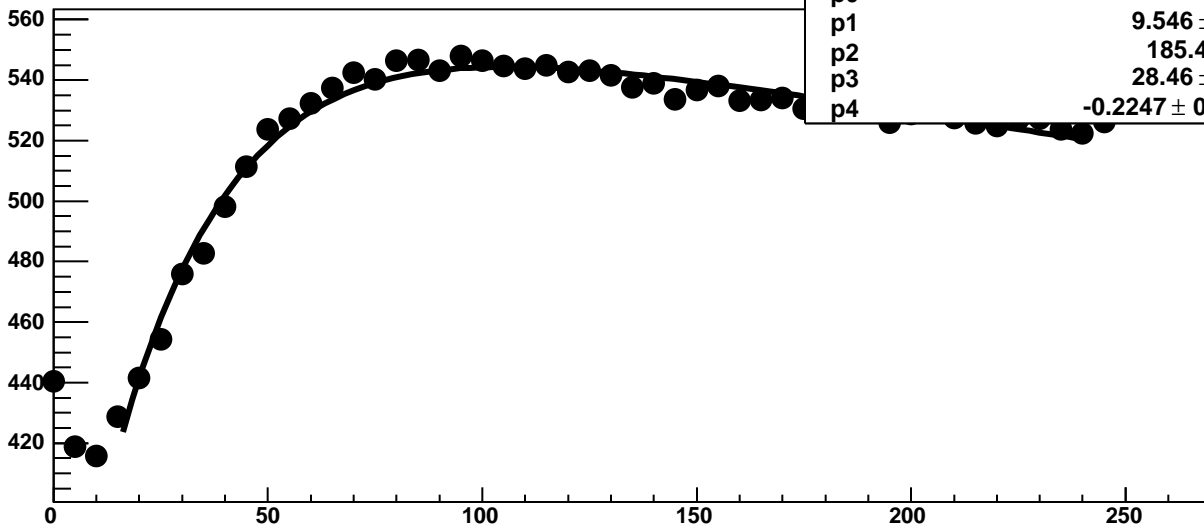
Chip 0, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

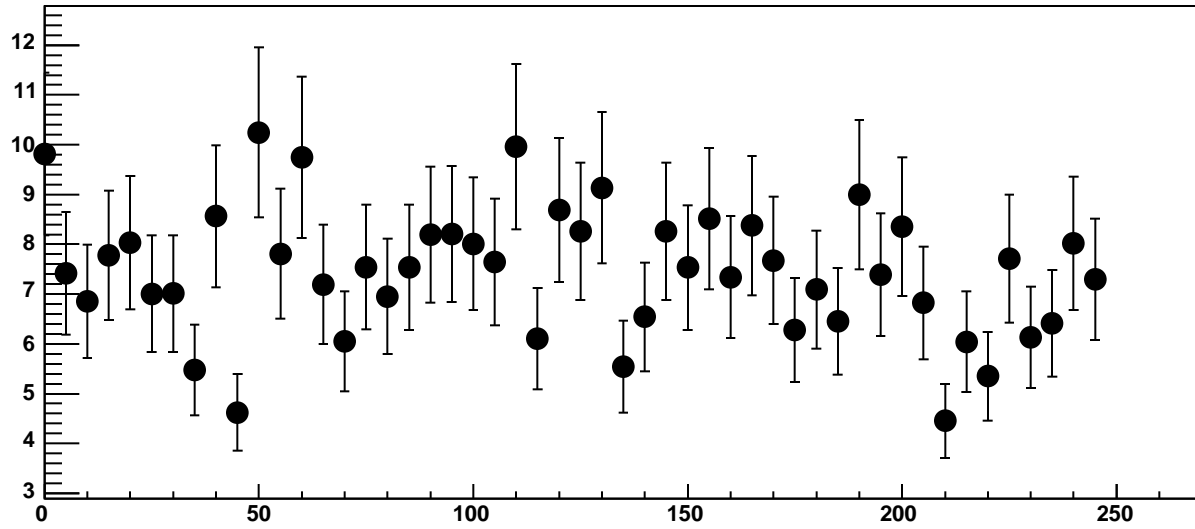


Chip 0, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

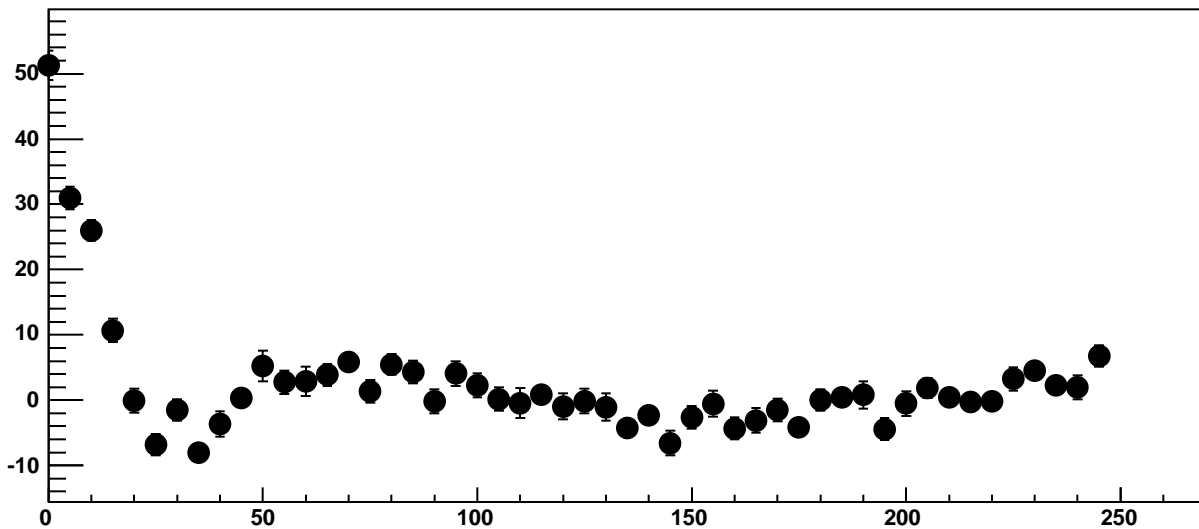


$\chi^2 / \text{ndf}$	228.9 / 41
p0	$386.9 \pm 3.039$
p1	$9.546 \pm 0.5466$
p2	$185.4 \pm 3.165$
p3	$28.46 \pm 0.5835$
p4	$-0.2247 \pm 0.007869$

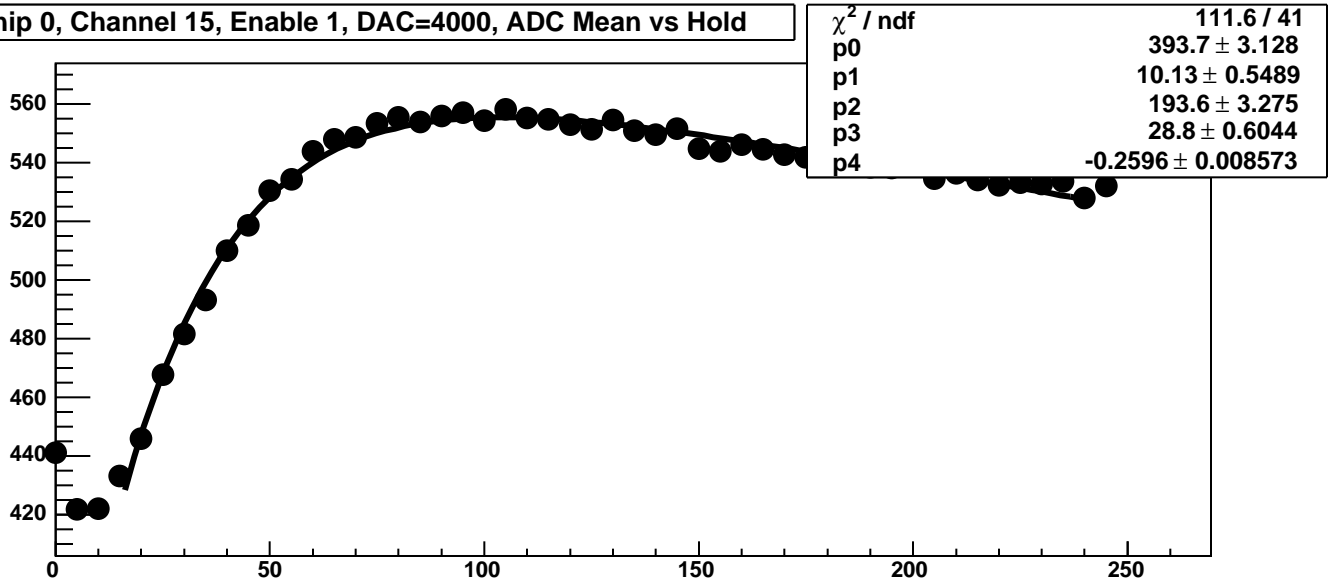
Chip 0, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



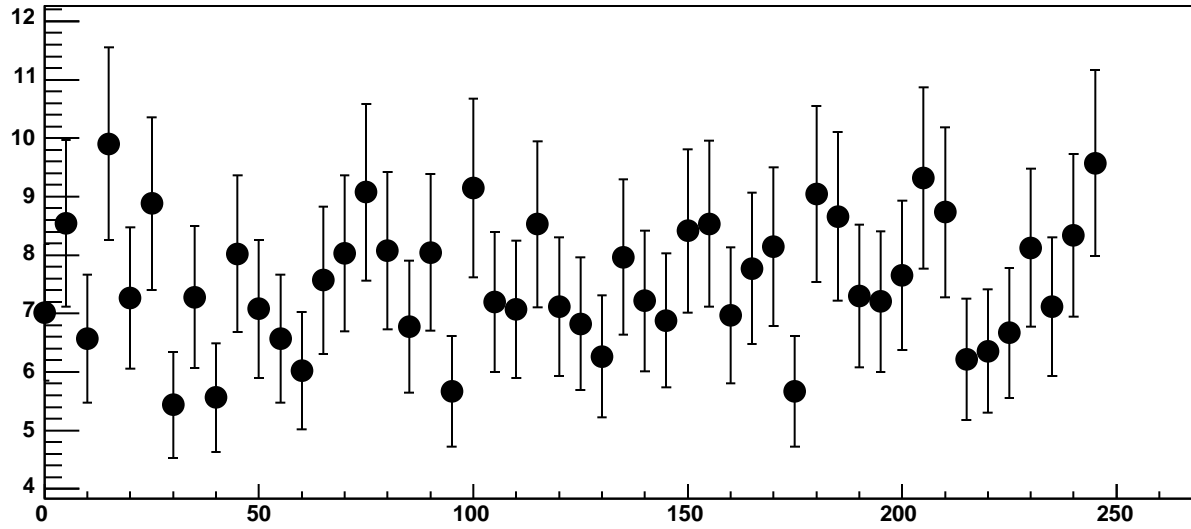
Chip 0, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold



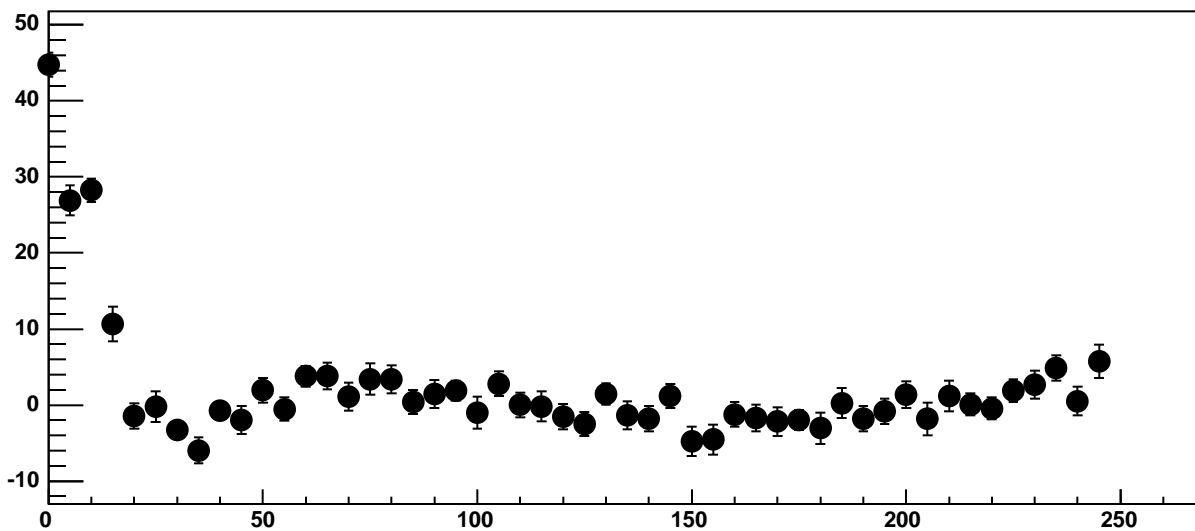
Chip 0, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold



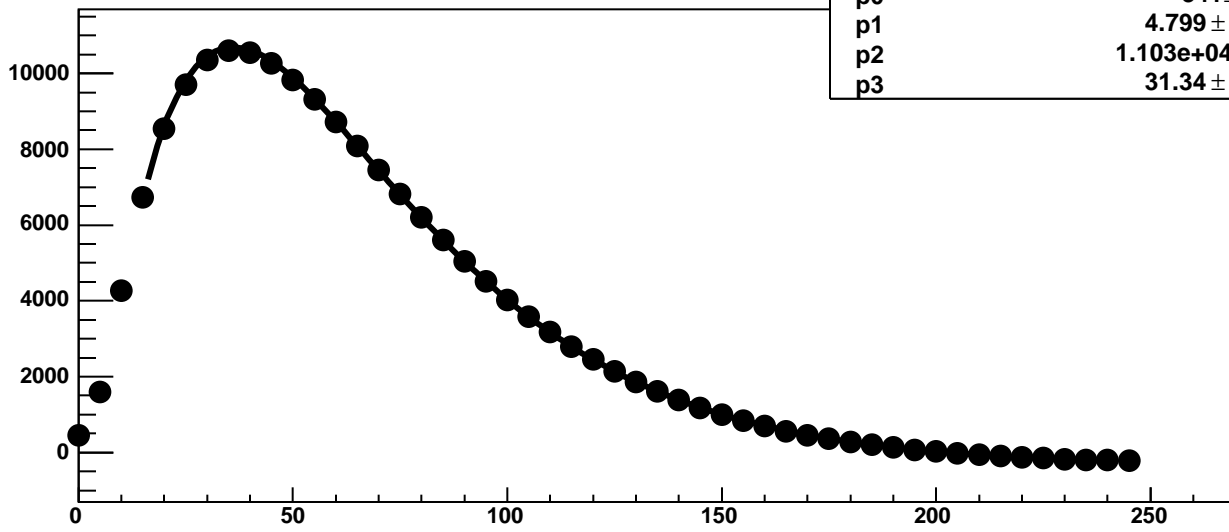
Chip 0, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

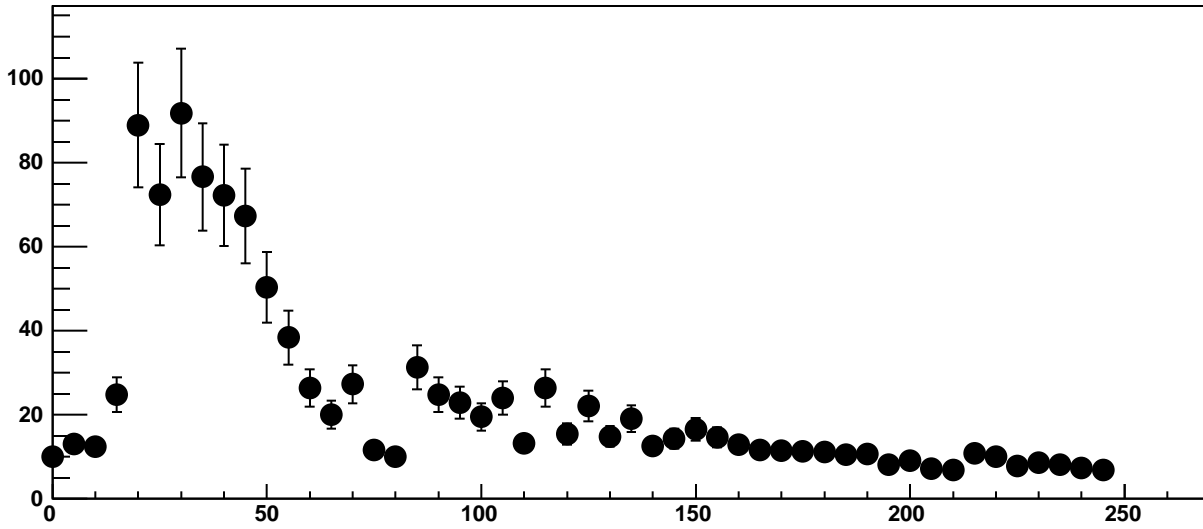


Chip 0, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold

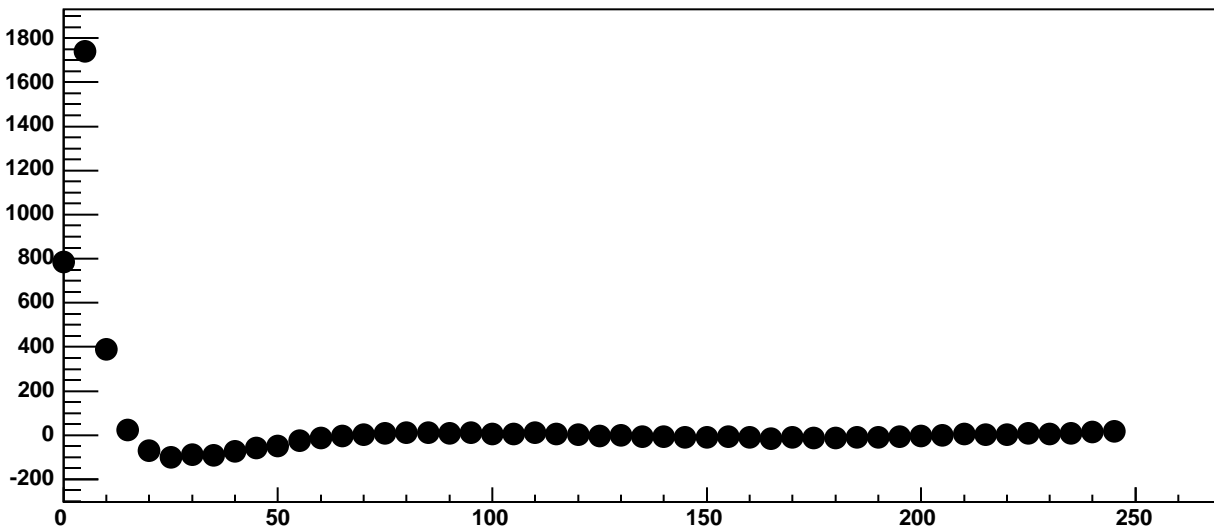


$\chi^2 / \text{ndf}$	536.1 / 42
p0	-341 ± 0.7954
p1	4.799 ± 0.01426
p2	1.103e+04 ± 3.364
p3	31.34 ± 0.01126

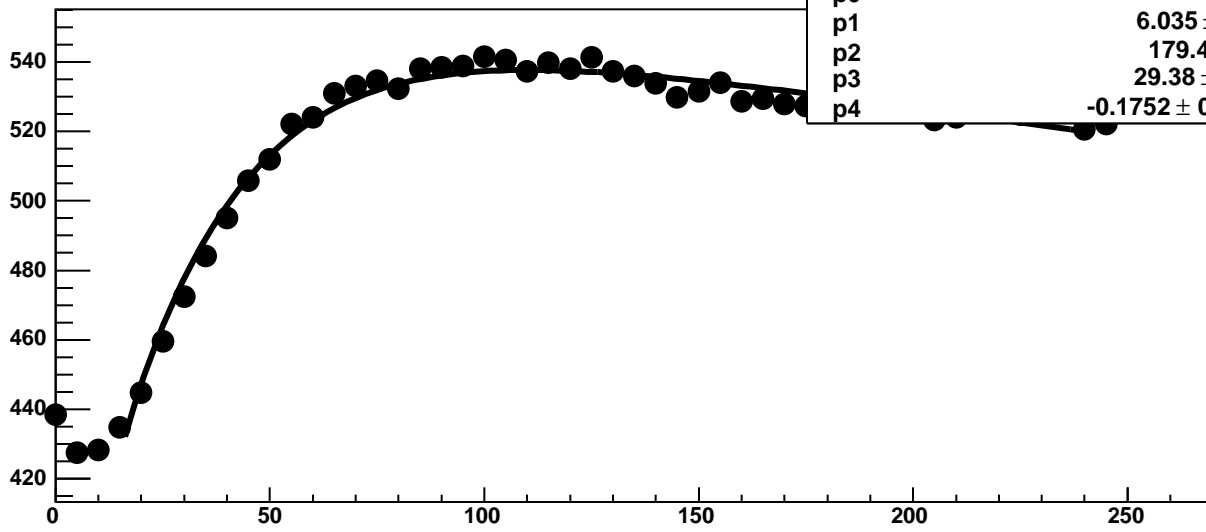
Chip 0, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold

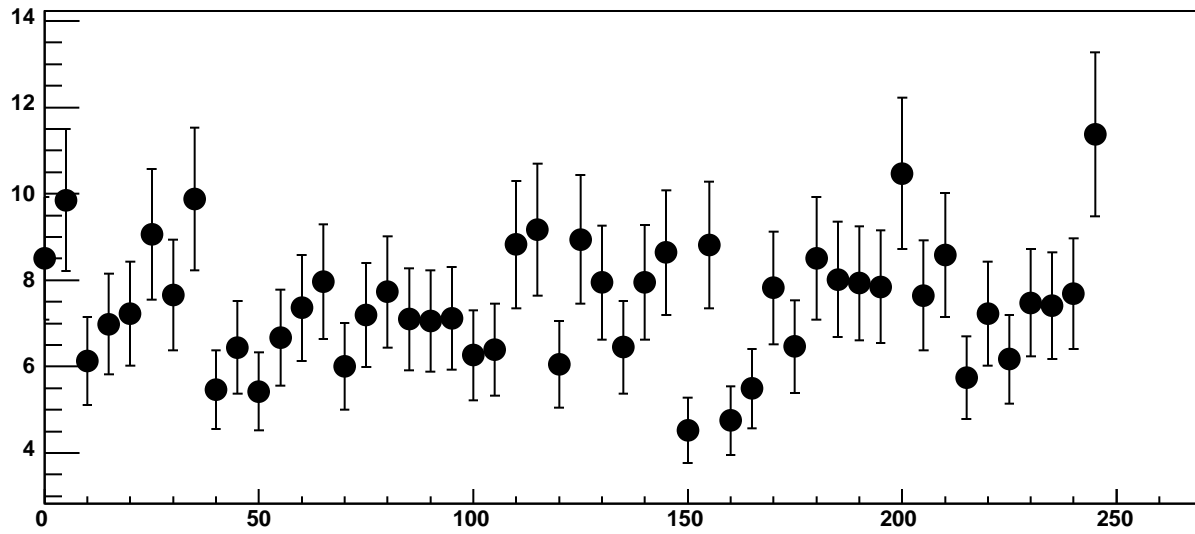


Chip 0, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

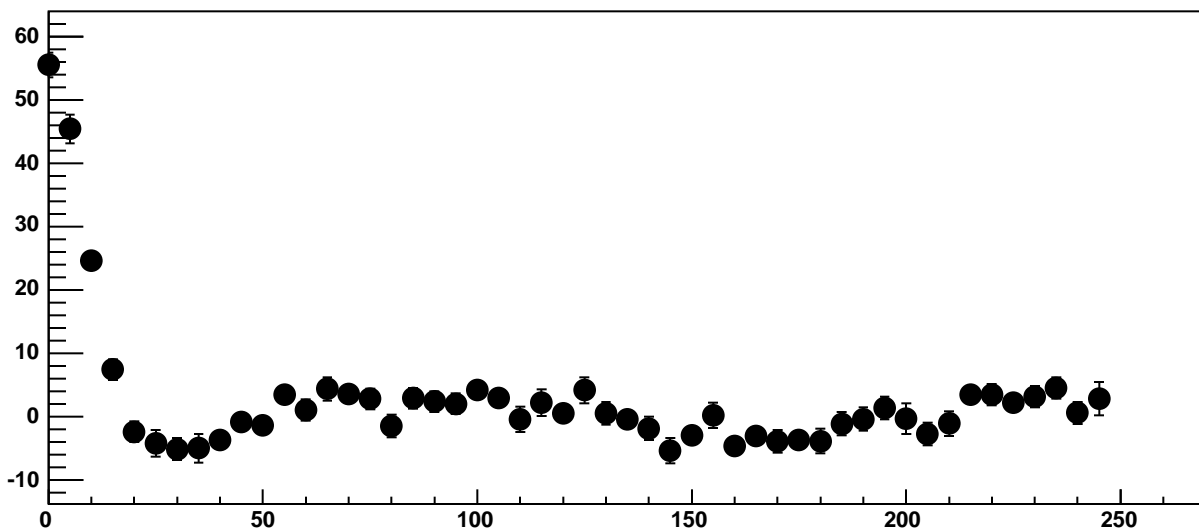


$\chi^2 / \text{ndf}$	180.8 / 41
p0	$381.8 \pm 2.992$
p1	$6.035 \pm 0.5999$
p2	$179.4 \pm 3.099$
p3	$29.38 \pm 0.6705$
p4	$-0.1752 \pm 0.008212$

Chip 0, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold

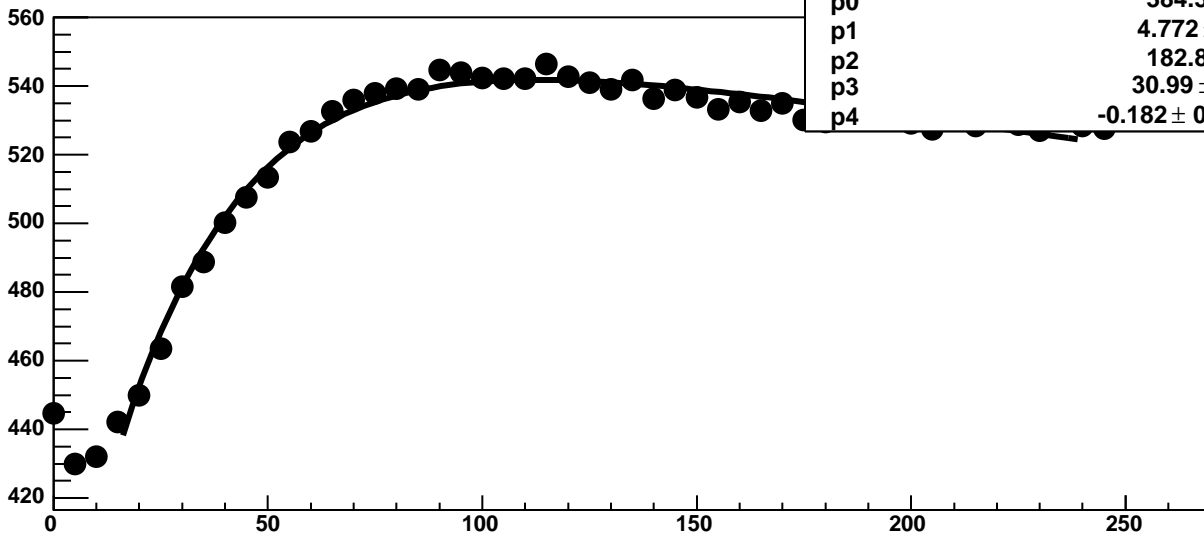


Chip 0, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold



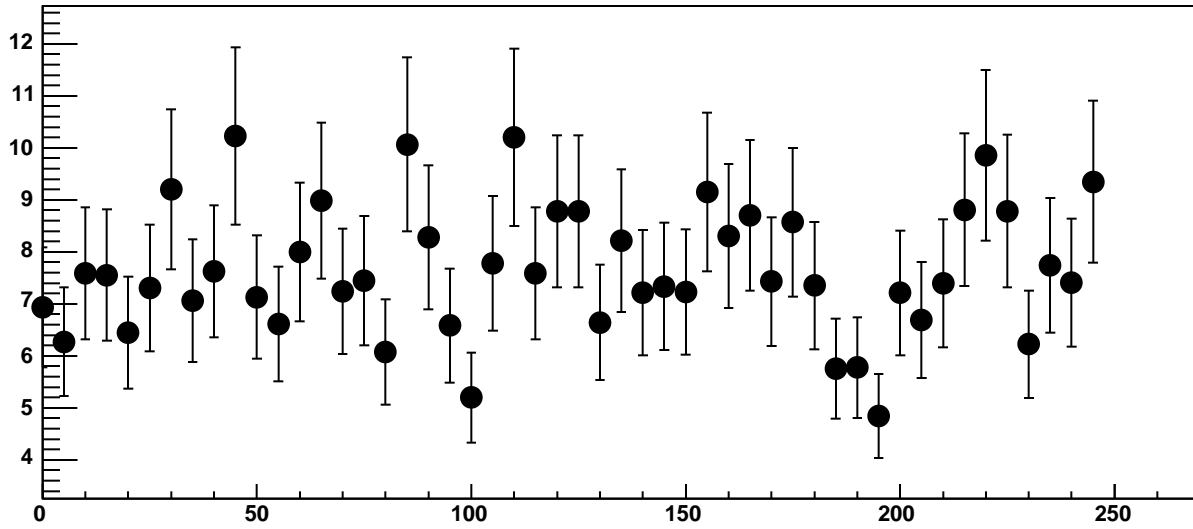


Chip 0, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

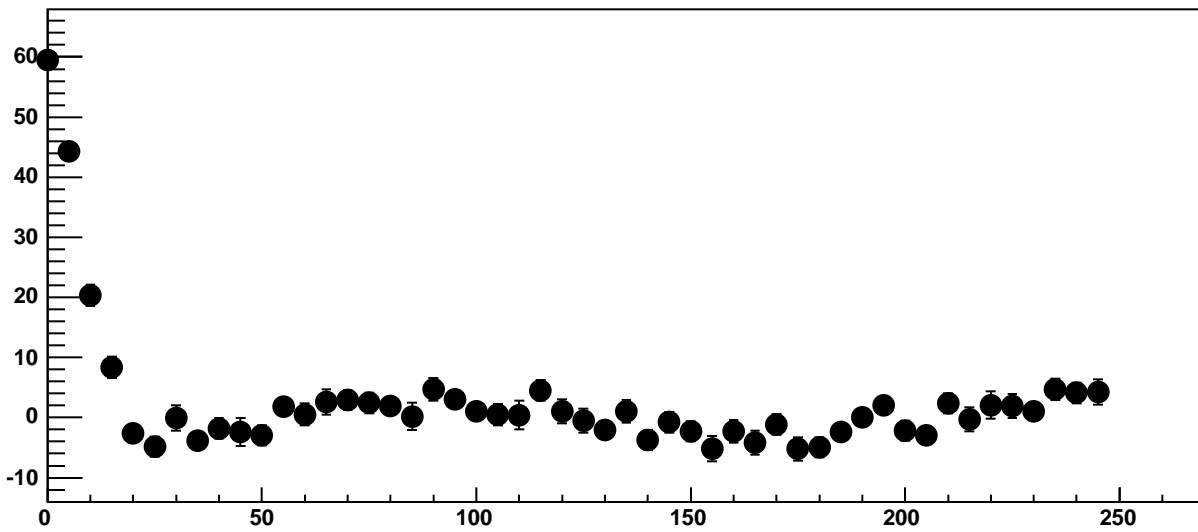


$\chi^2 / \text{ndf}$	141.1 / 41
p0	$384.3 \pm 3.165$
p1	$4.772 \pm 0.6571$
p2	$182.8 \pm 3.288$
p3	$30.99 \pm 0.8104$
p4	$-0.182 \pm 0.009098$

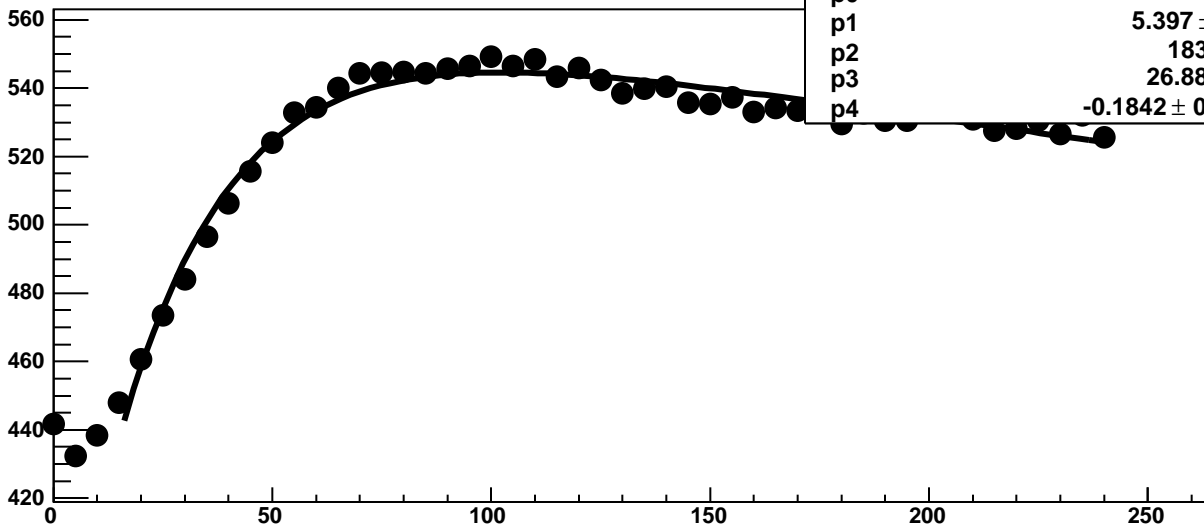
Chip 0, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

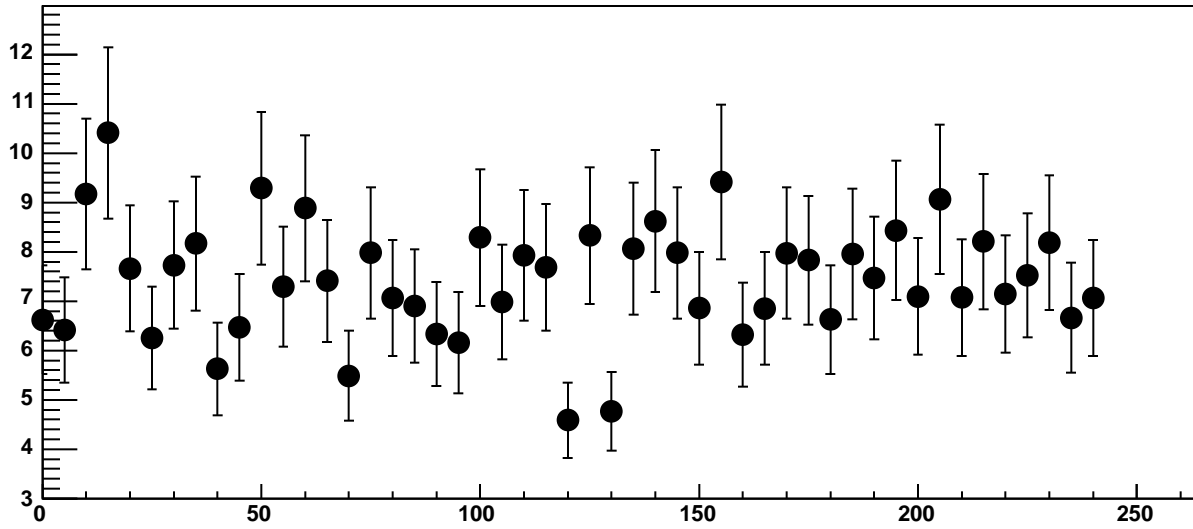


Chip 0, Channel 15, Enable 5, DAC=4000, ADC Mean vs Hold

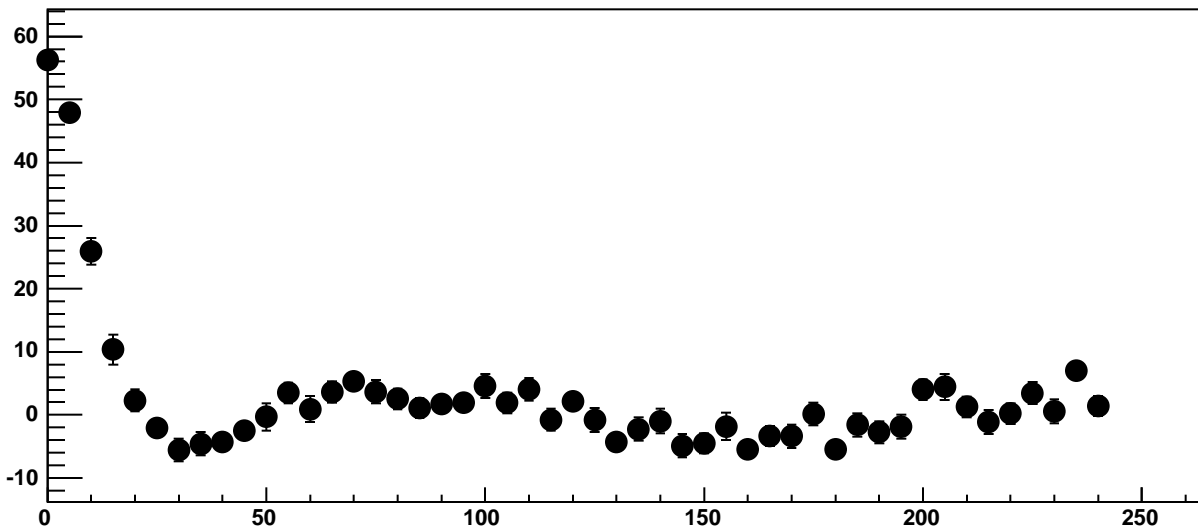


$\chi^2 / \text{ndf}$	216.5 / 41
p0	$384.4 \pm 3.054$
p1	$5.397 \pm 0.6145$
p2	$183 \pm 3.148$
p3	$26.88 \pm 0.669$
p4	$-0.1842 \pm 0.007636$

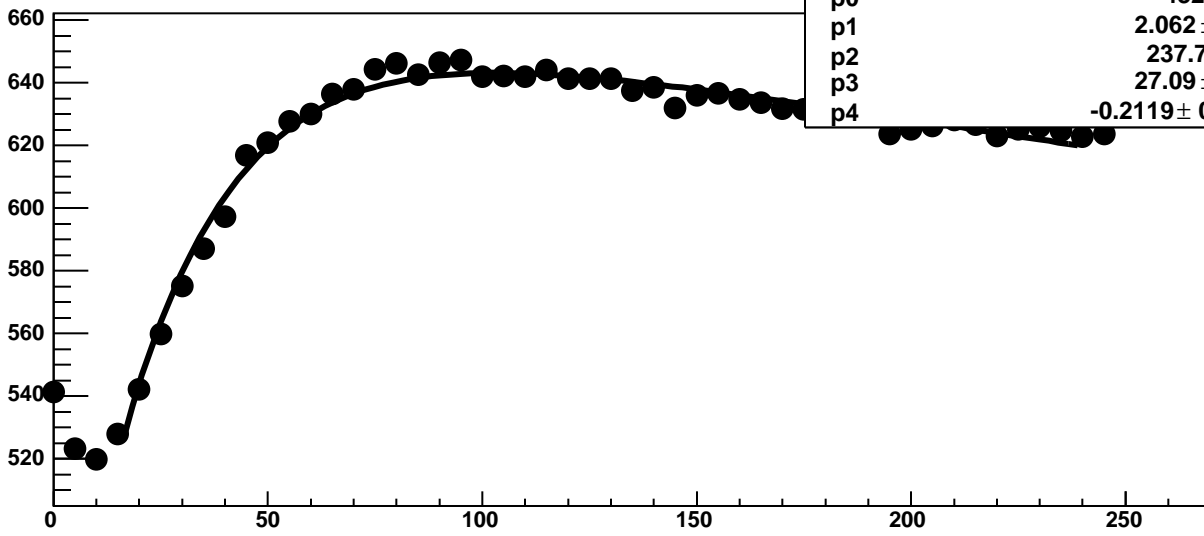
Chip 0, Channel 15, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 15, Enable 5, DAC=4000, ADC Residuals vs Hold

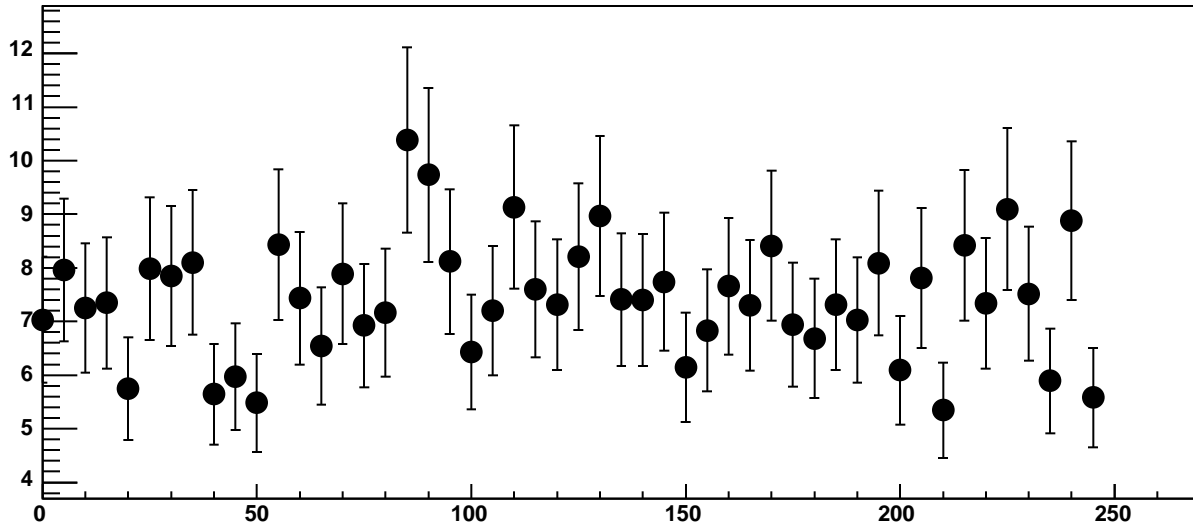


Chip 0, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

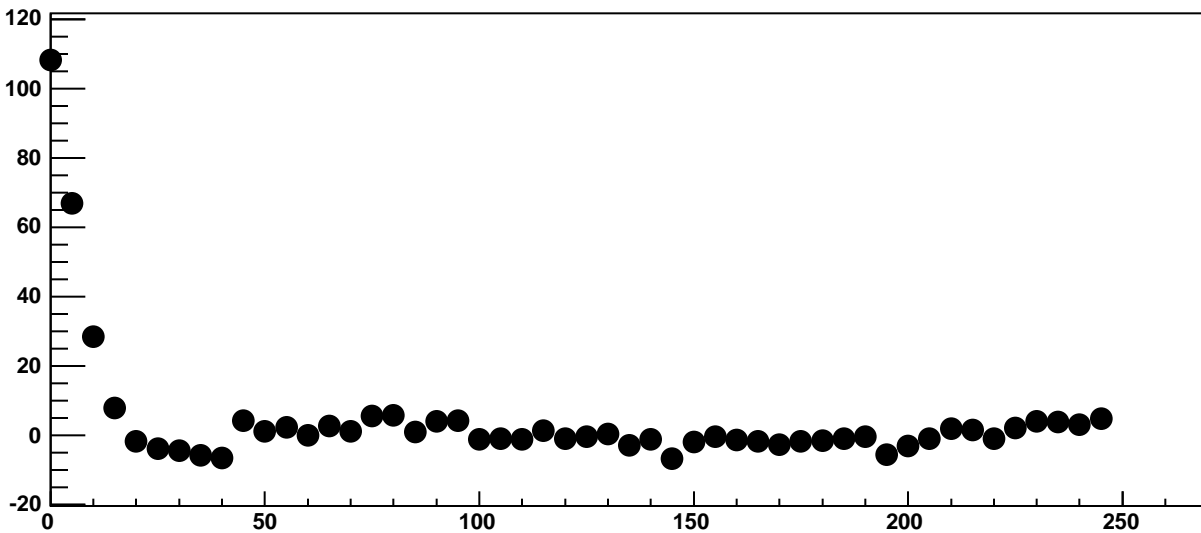


$\chi^2 / \text{ndf}$	179.1 / 41
p0	$432.6 \pm 3.1$
p1	$2.062 \pm 0.4929$
p2	$237.7 \pm 3.189$
p3	$27.09 \pm 0.5572$
p4	$-0.2119 \pm 0.007971$

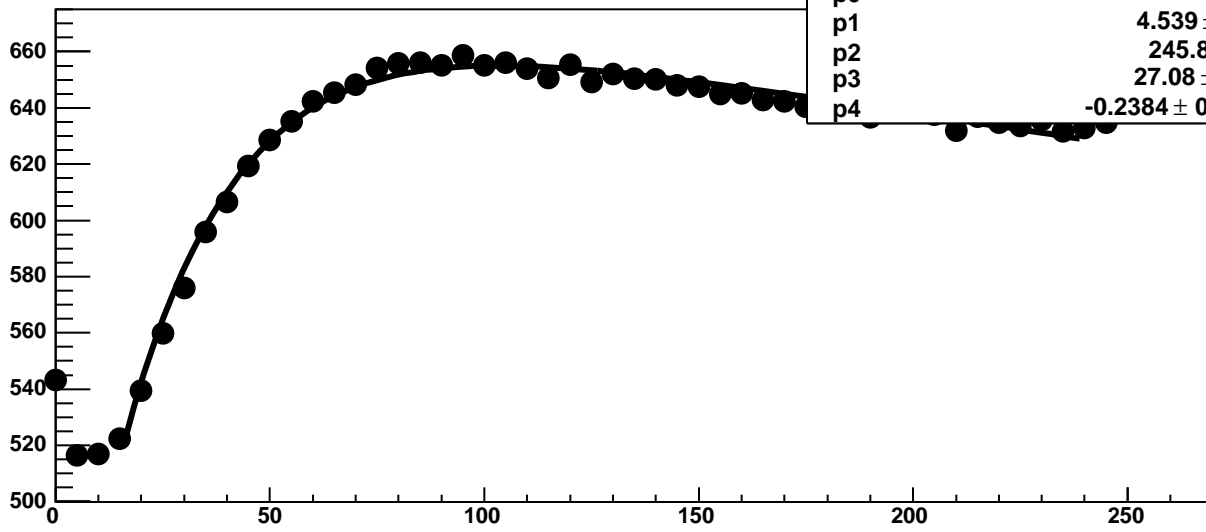
Chip 0, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

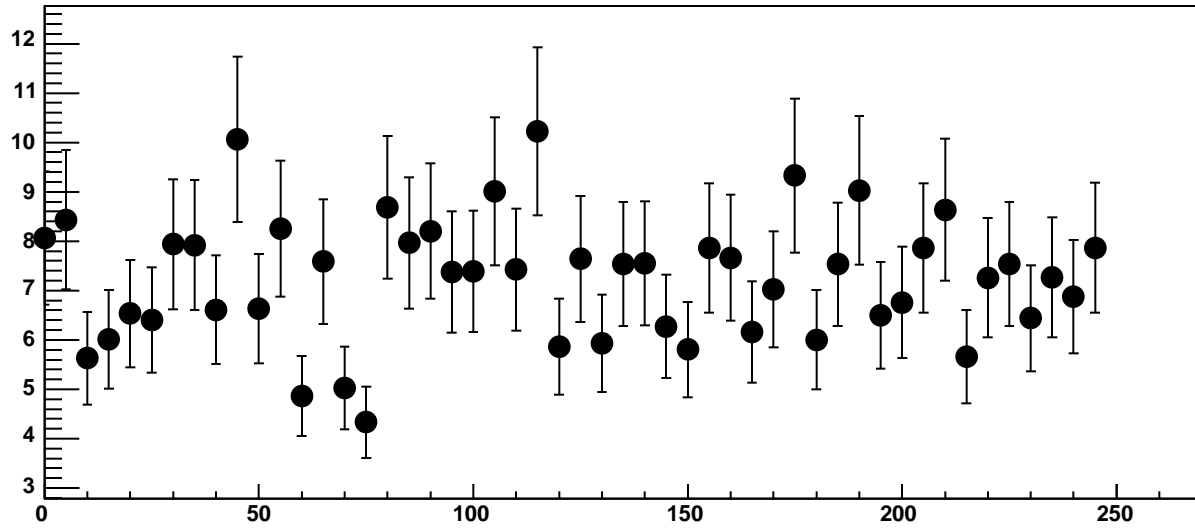


Chip 0, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold

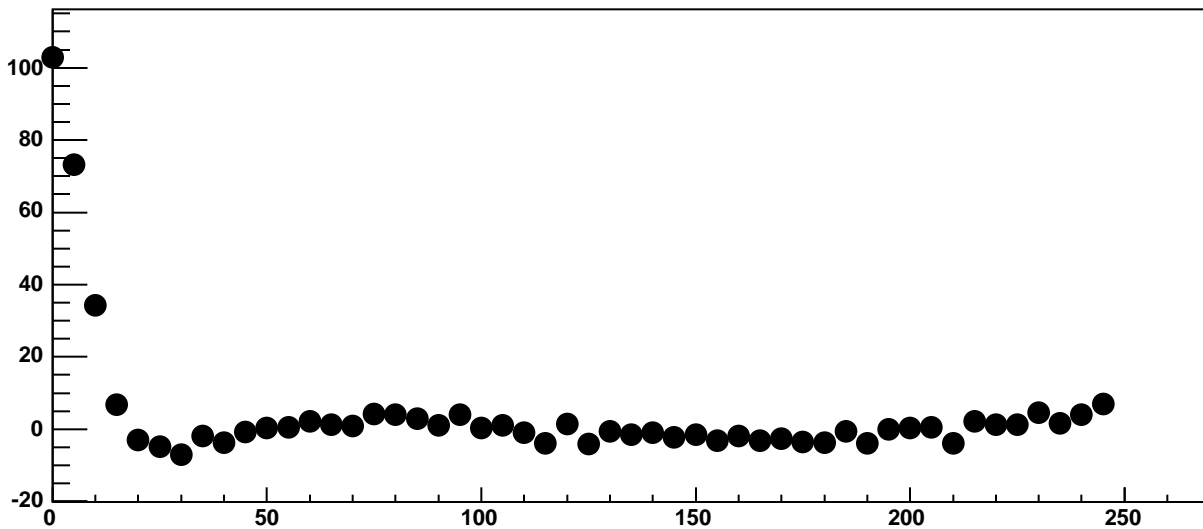


$\chi^2 / \text{ndf}$	162.2 / 41
p0	$439.1 \pm 2.955$
p1	$4.539 \pm 0.4107$
p2	$245.8 \pm 3.047$
p3	$27.08 \pm 0.4836$
p4	$-0.2384 \pm 0.007644$

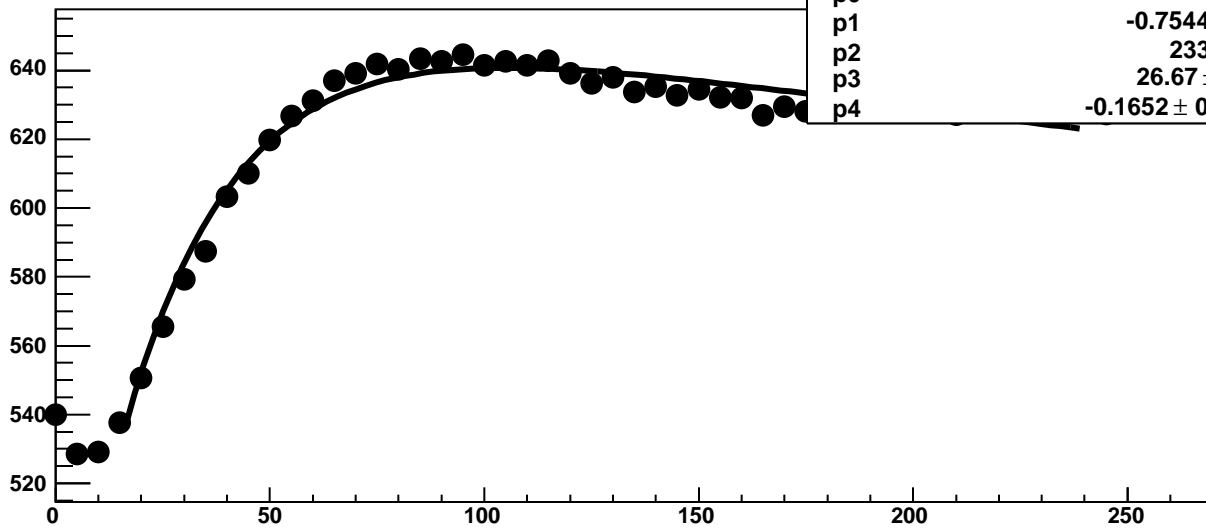
Chip 0, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold

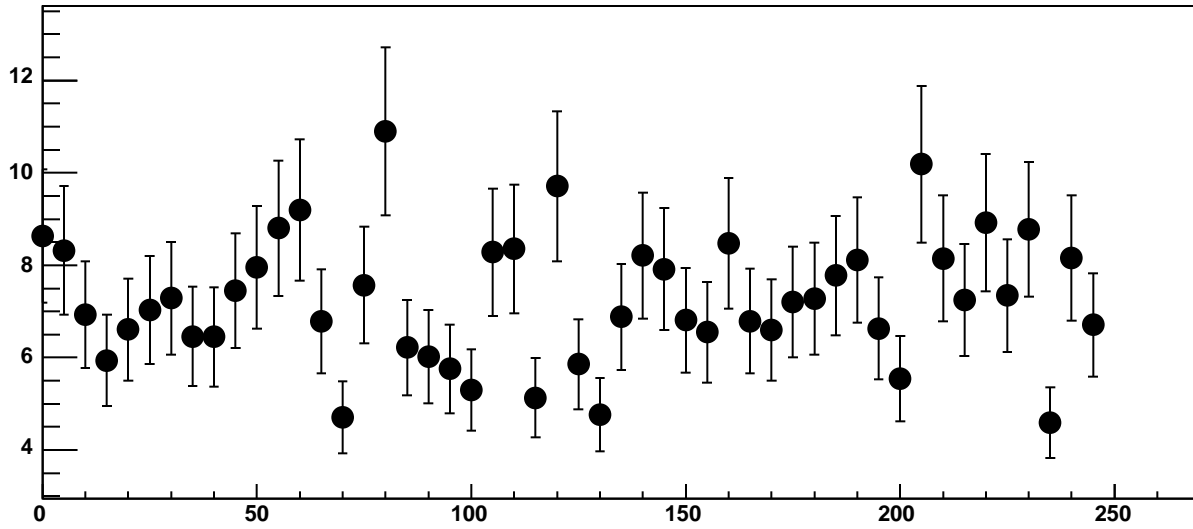


Chip 0, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold

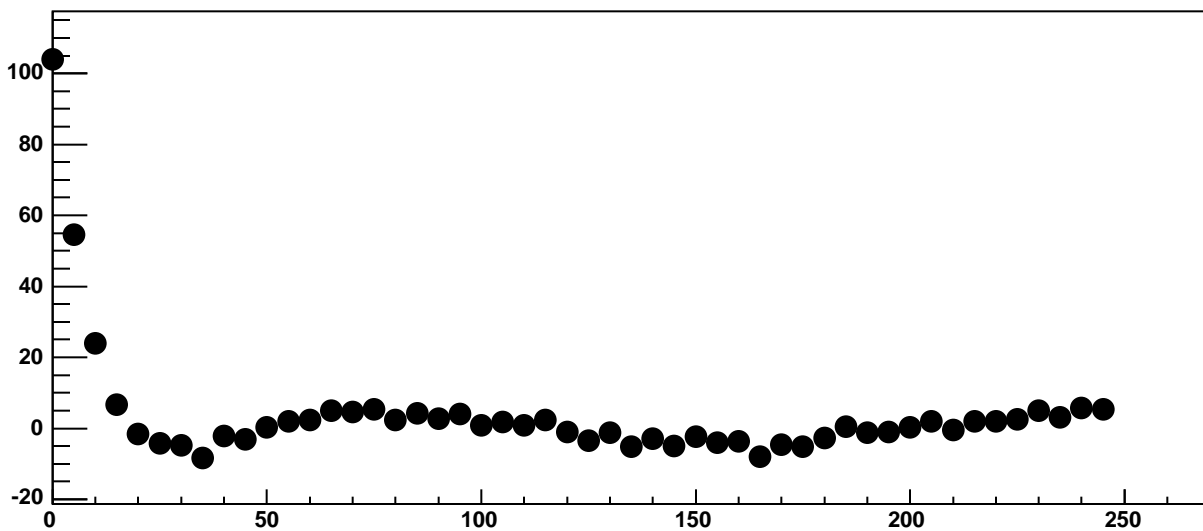


$\chi^2 / \text{ndf}$	264.6 / 41
p0	$429.7 \pm 2.913$
p1	$-0.7544 \pm 0.519$
p2	$233 \pm 2.976$
p3	$26.67 \pm 0.5687$
p4	$-0.1652 \pm 0.006973$

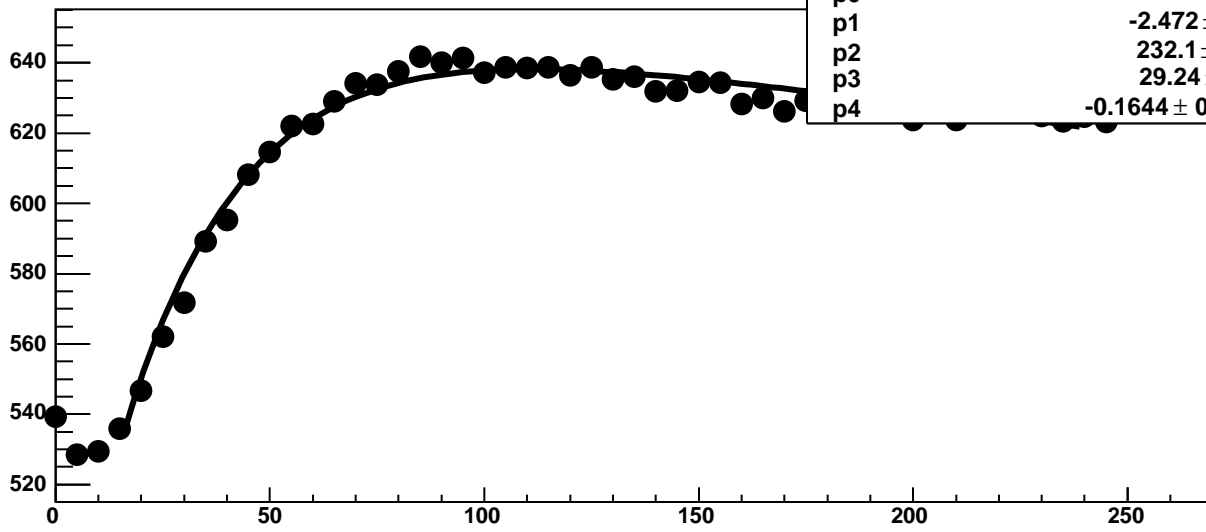
Chip 0, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold

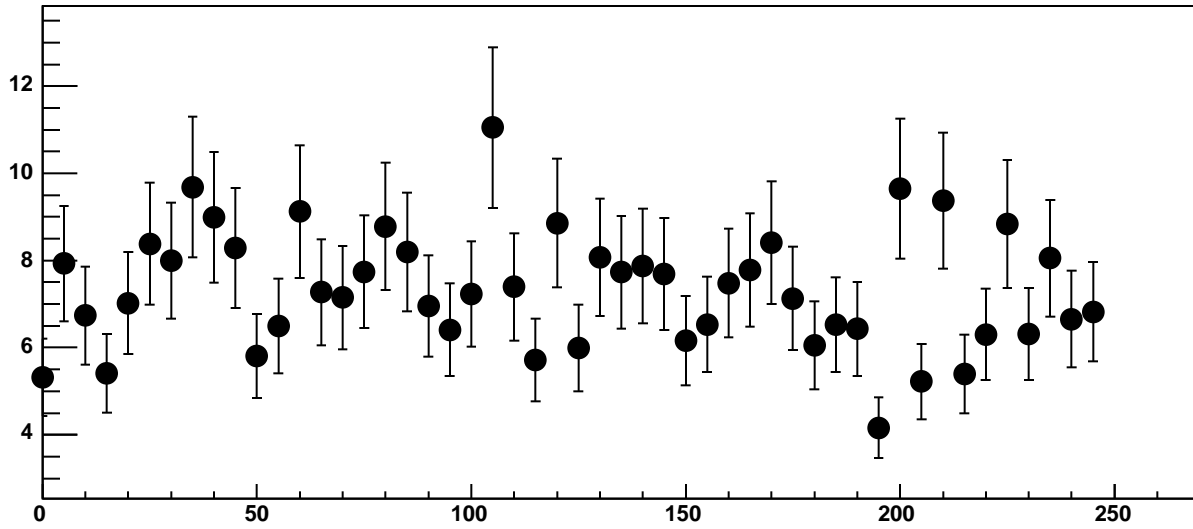


Chip 0, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

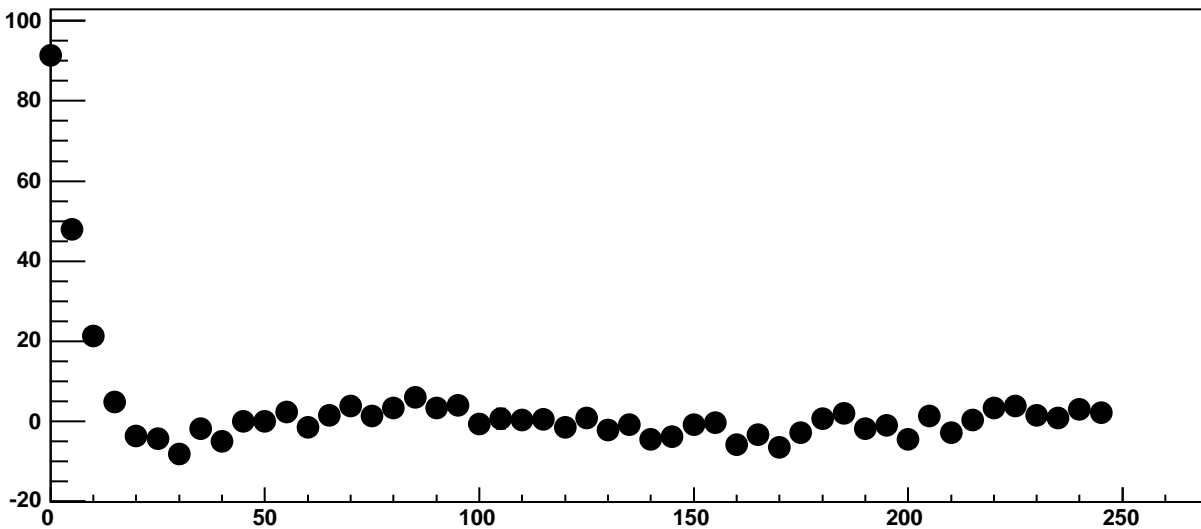


$\chi^2 / \text{ndf}$	154.7 / 41
p0	$429.5 \pm 0.8554$
p1	$-2.472 \pm 0.4528$
p2	$232.1 \pm 0.9266$
p3	$29.24 \pm 0.7631$
p4	$-0.1644 \pm 0.008883$

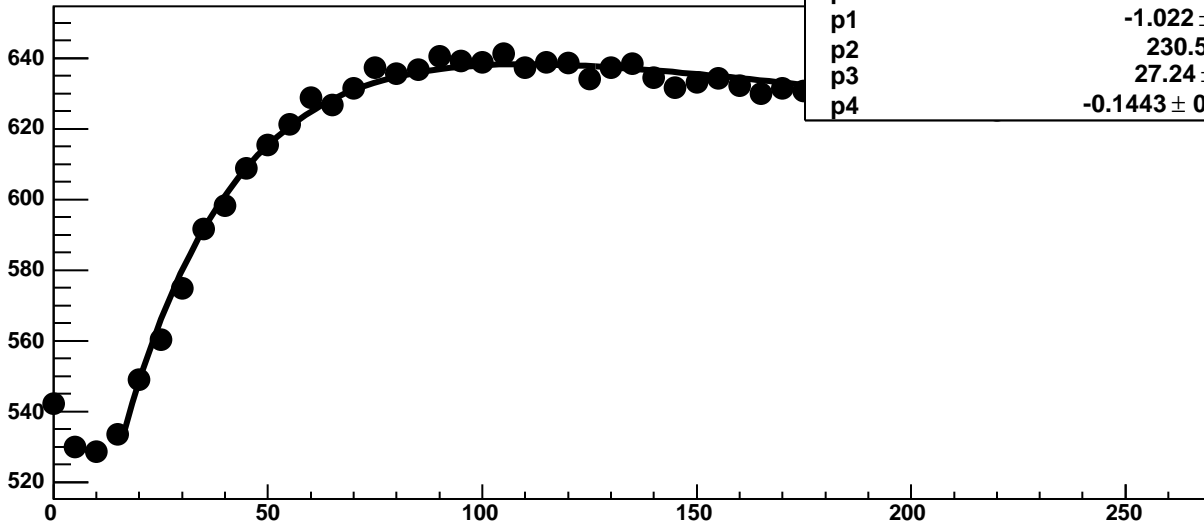
Chip 0, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

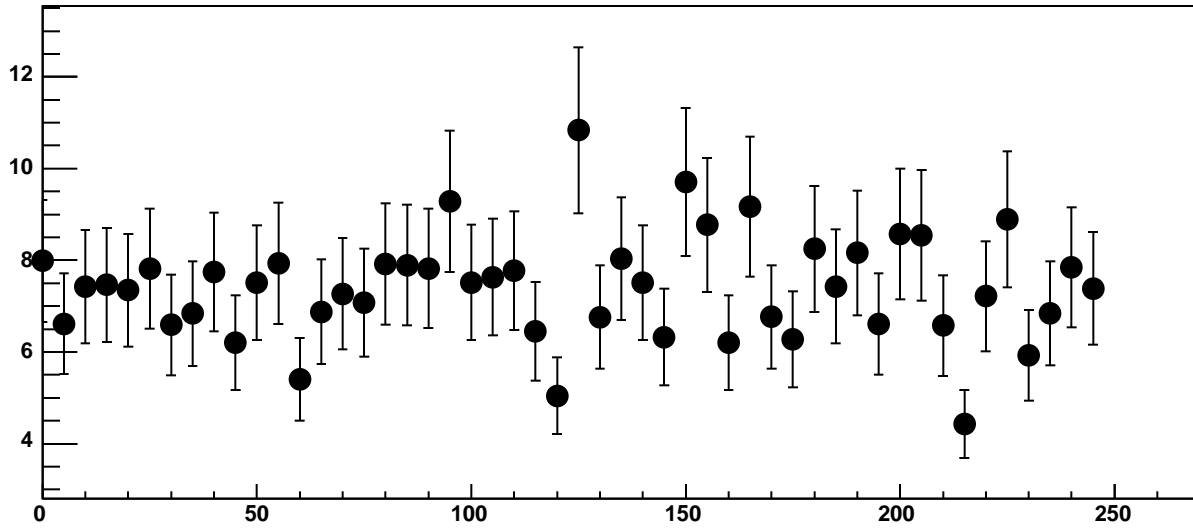


Chip 0, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

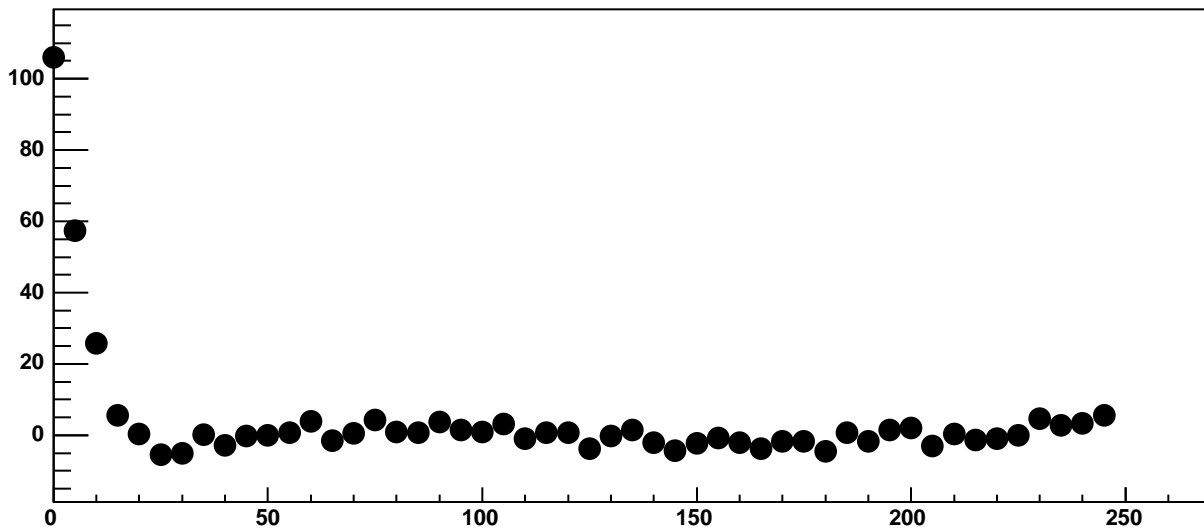


$\chi^2 / \text{ndf}$	116.4 / 41
p0	$427.7 \pm 3.046$
p1	$-1.022 \pm 0.5936$
p2	$230.5 \pm 3.108$
p3	$27.24 \pm 0.6548$
p4	$-0.1443 \pm 0.007637$

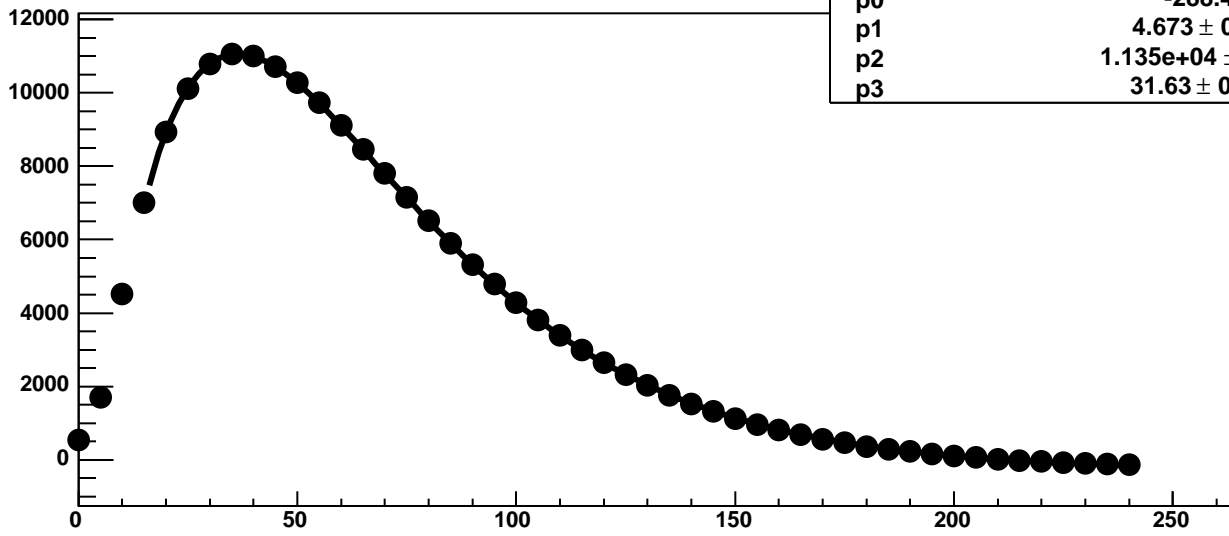
Chip 0, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

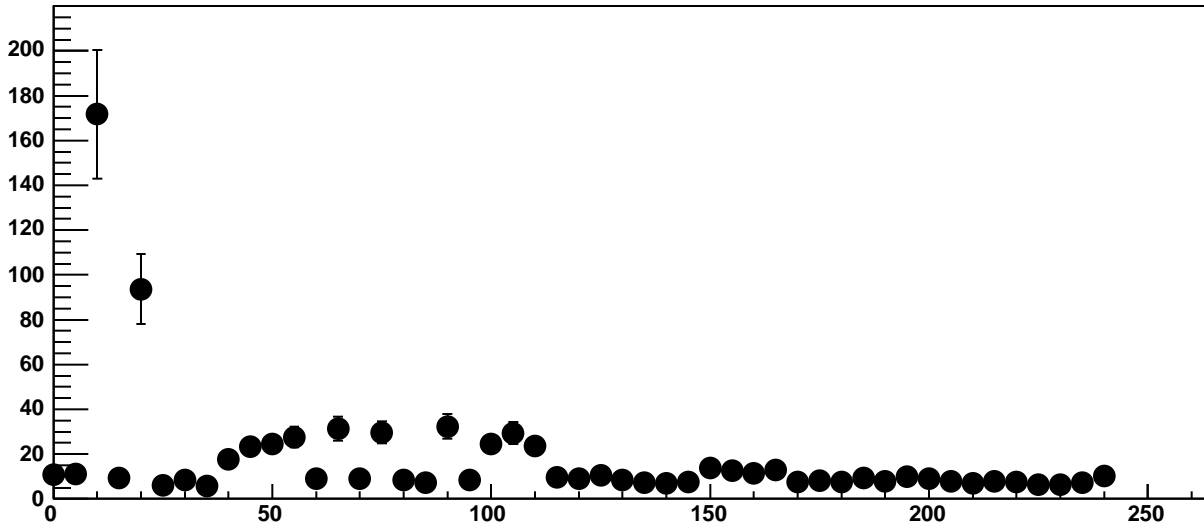


Chip 0, Channel 16, Enable 5!, DAC=4000, ADC Mean vs Hold

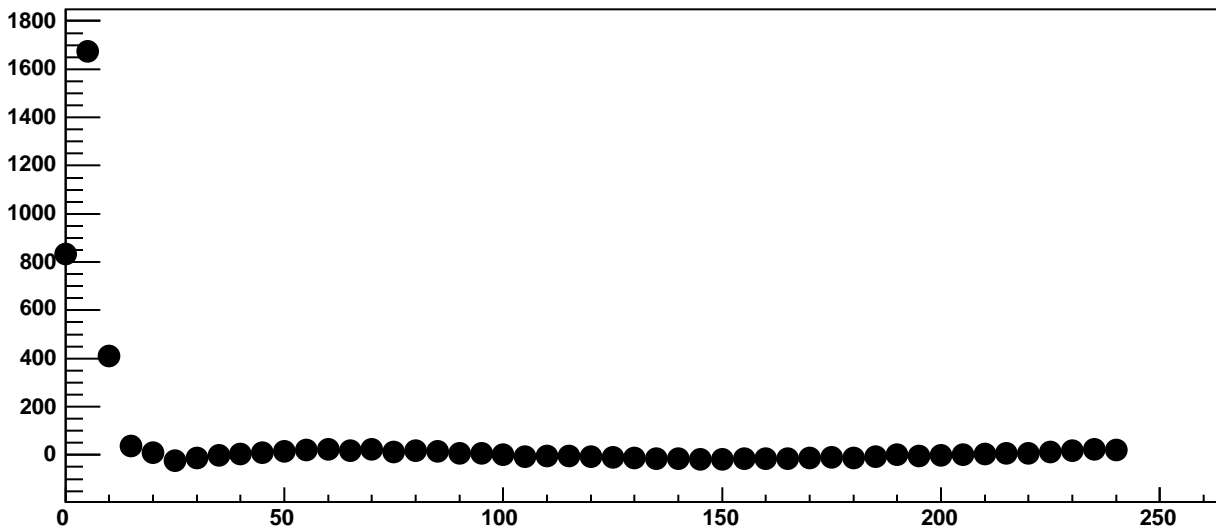


$\chi^2 / \text{ndf}$	2285 / 42
p0	-288.4 ± 0.586
p1	4.673 ± 0.005672
p2	1.135e+04 ± 0.9354
p3	31.63 ± 0.004605

Chip 0, Channel 16, Enable 5!, DAC=4000, ADC Noise vs Hold

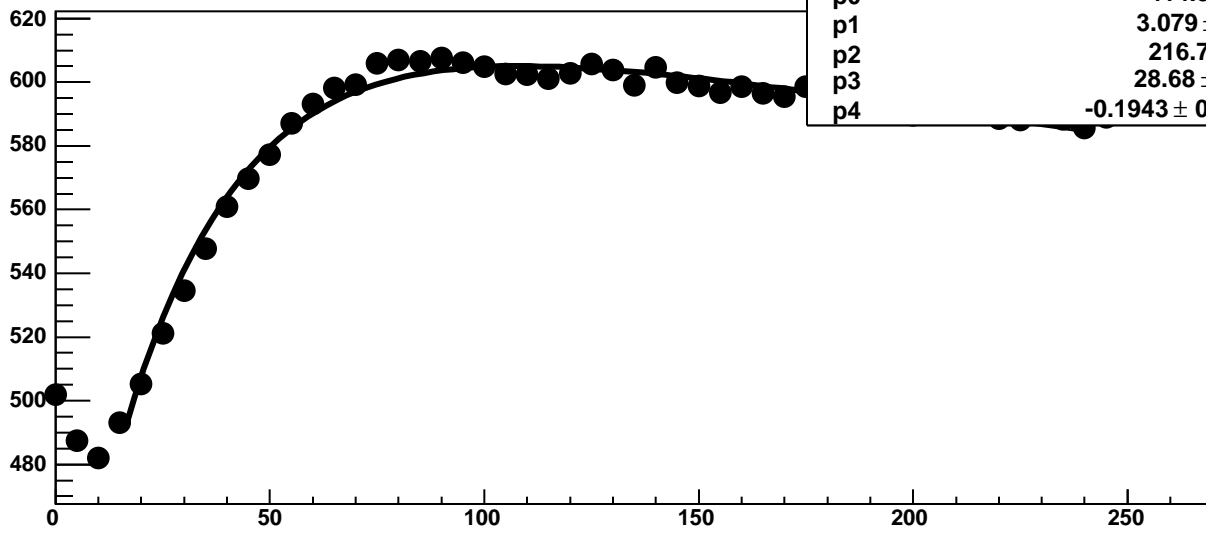


Chip 0, Channel 16, Enable 5!, DAC=4000, ADC Residuals vs Hold



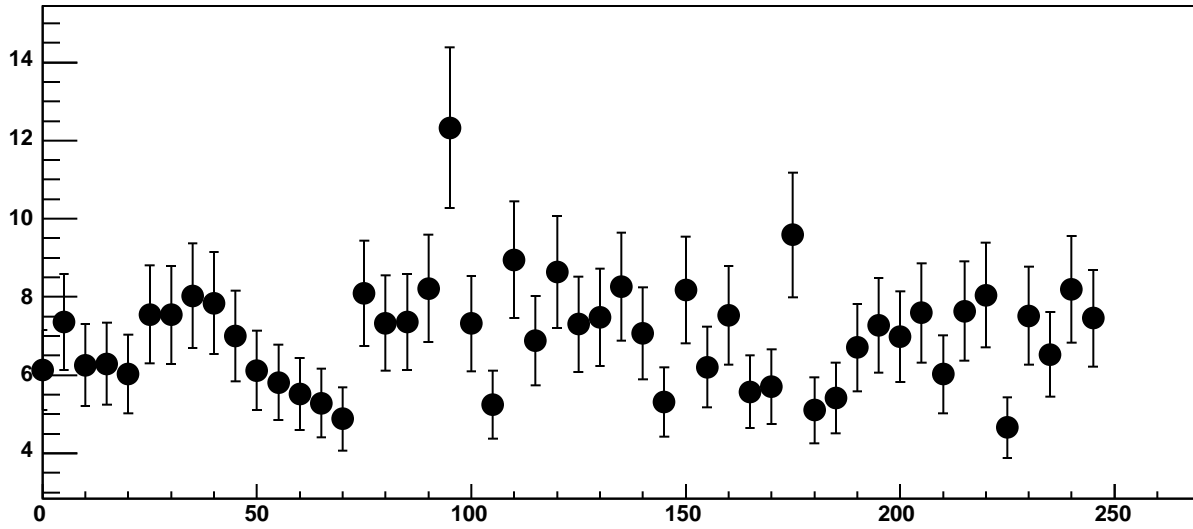


Chip 0, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

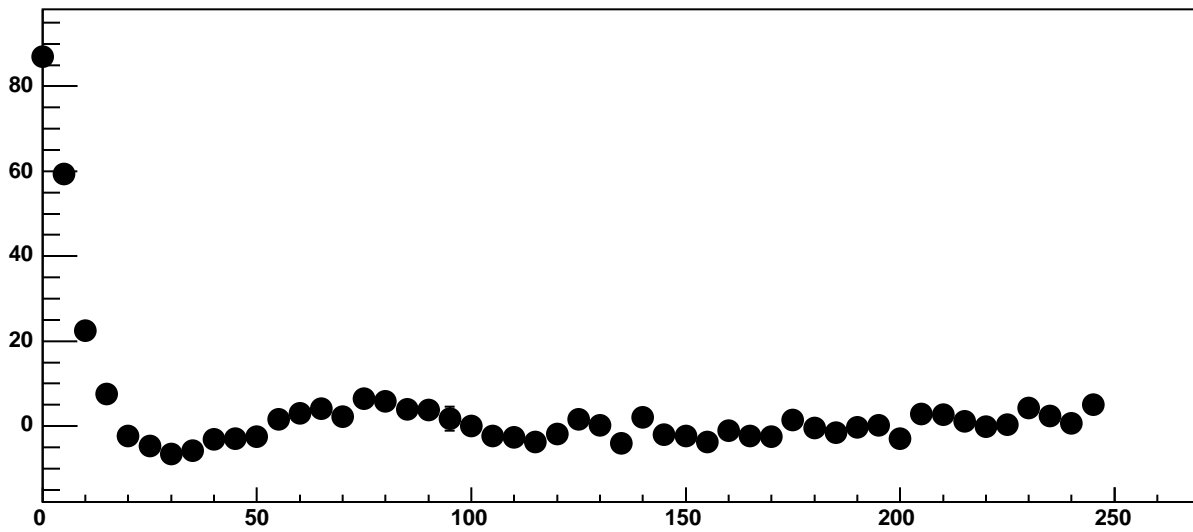


$\chi^2 / \text{ndf}$	186.8 / 41
p0	$414.3 \pm 2.868$
p1	$3.079 \pm 0.4927$
p2	$216.7 \pm 2.958$
p3	$28.68 \pm 0.5875$
p4	$-0.1943 \pm 0.007826$

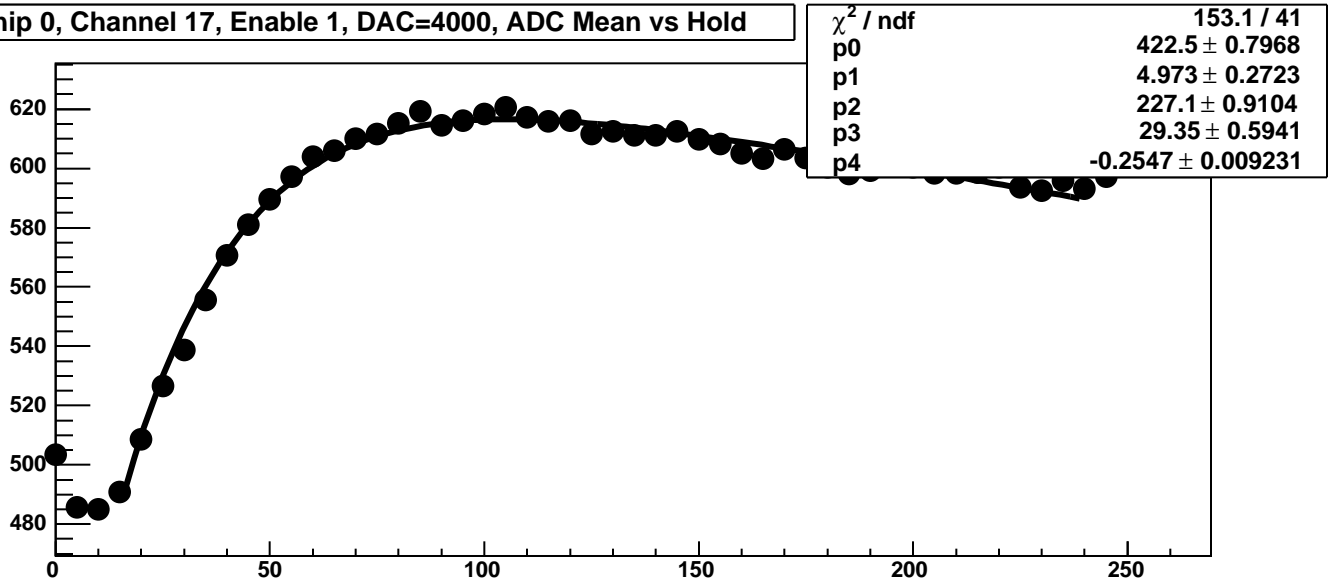
Chip 0, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



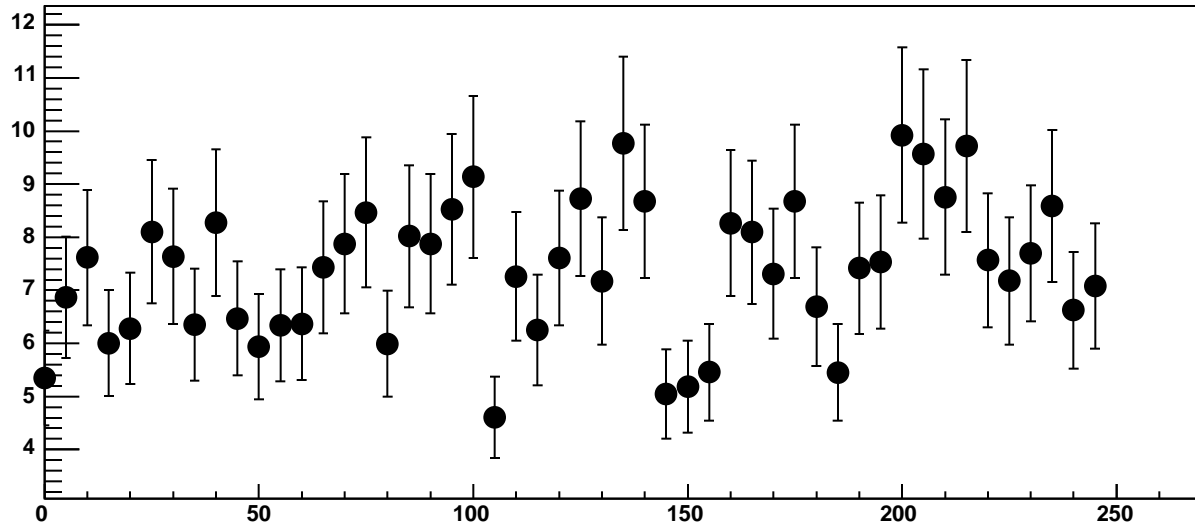
Chip 0, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold



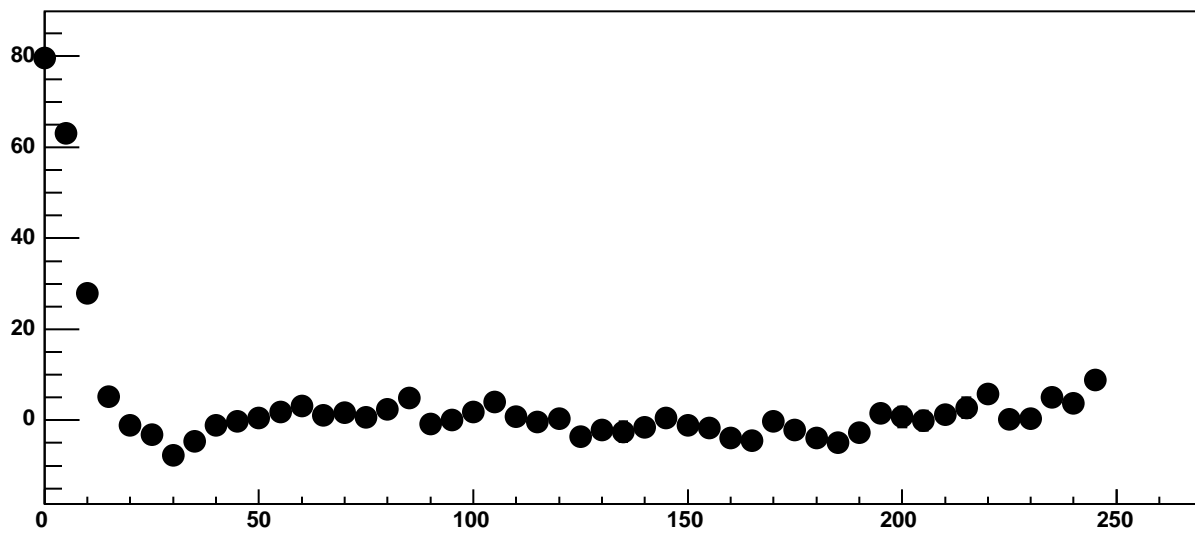
Chip 0, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold



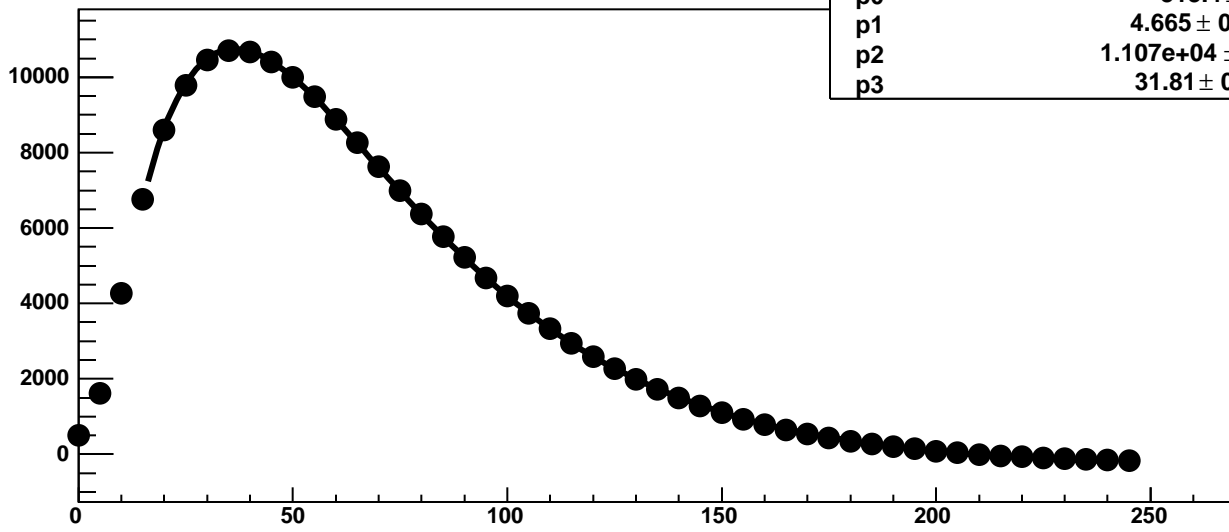
Chip 0, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

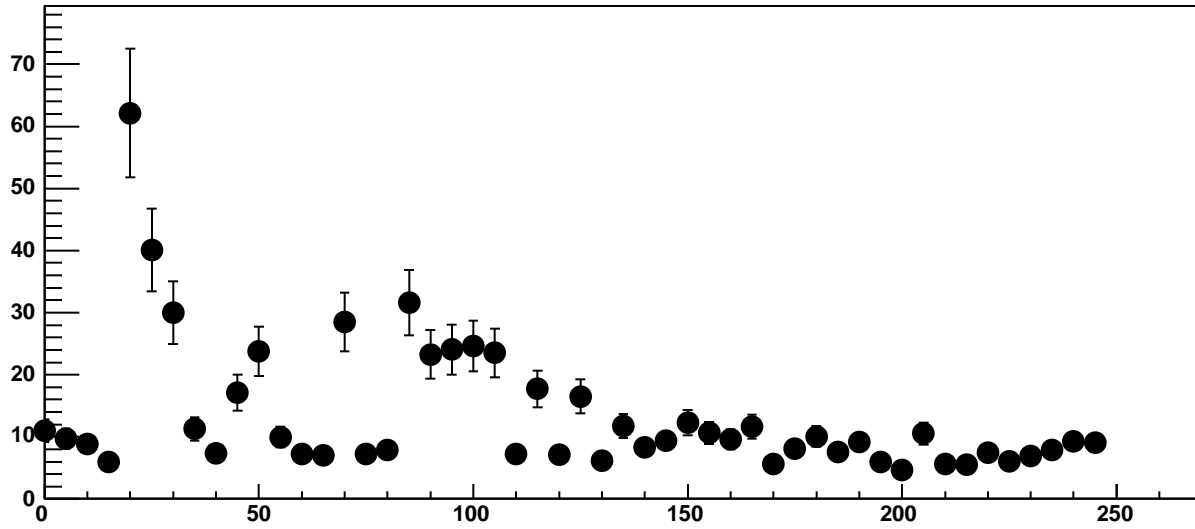


Chip 0, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold

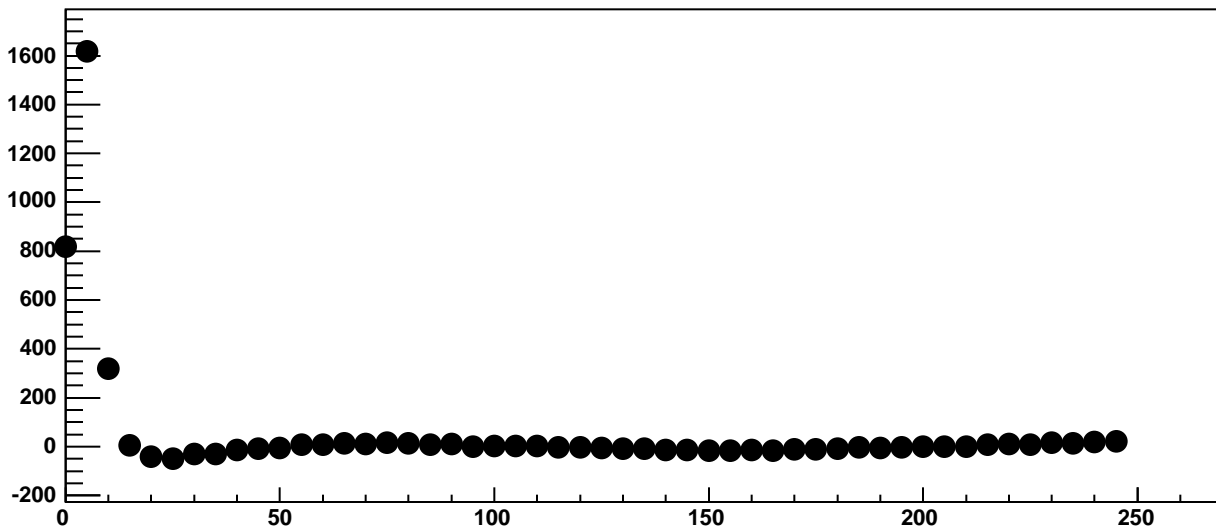


$\chi^2 / \text{ndf}$	1309 / 42
p0	$-313.4 \pm 0.5529$
p1	$4.665 \pm 0.004423$
p2	$1.107\text{e}+04 \pm 0.9965$
p3	$31.81 \pm 0.004942$

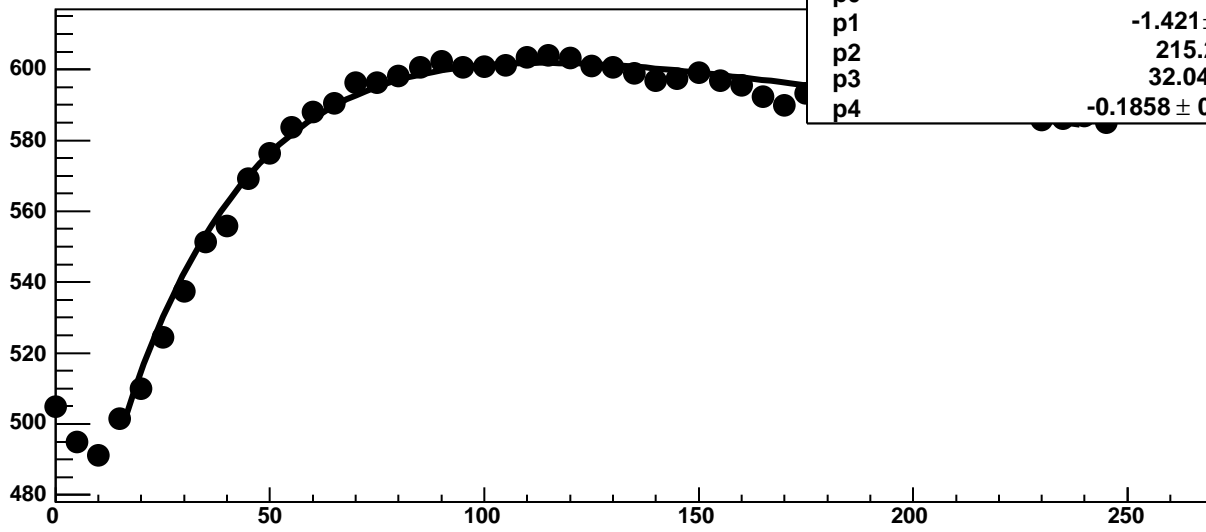
Chip 0, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold



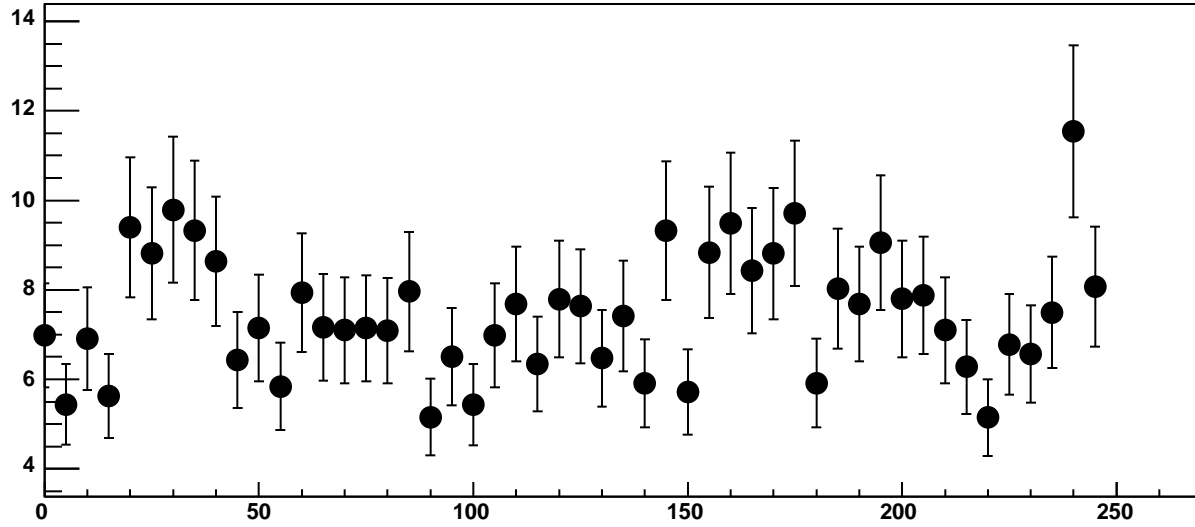
Chip 0, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold



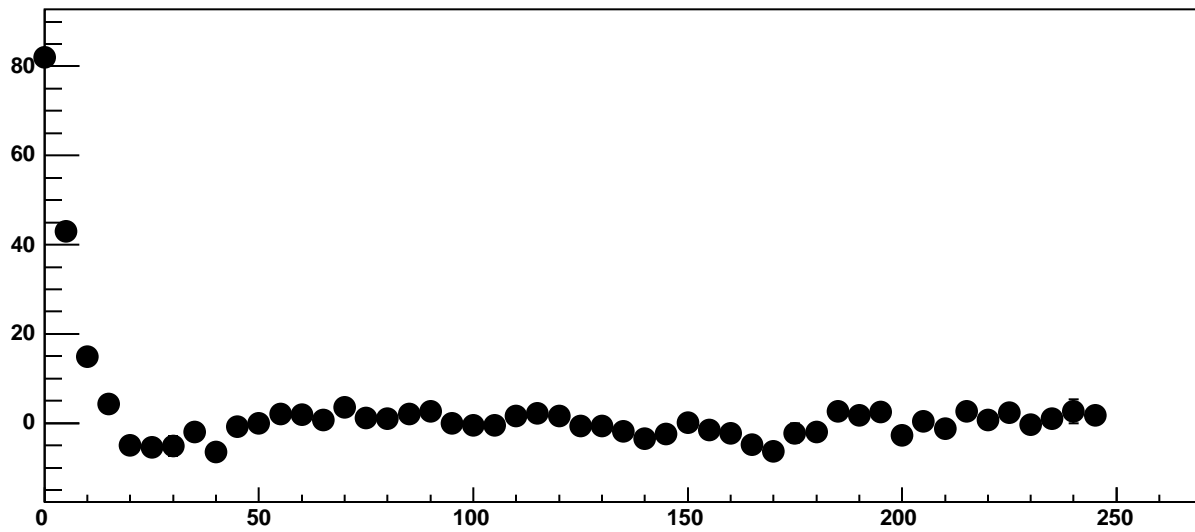
Chip 0, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



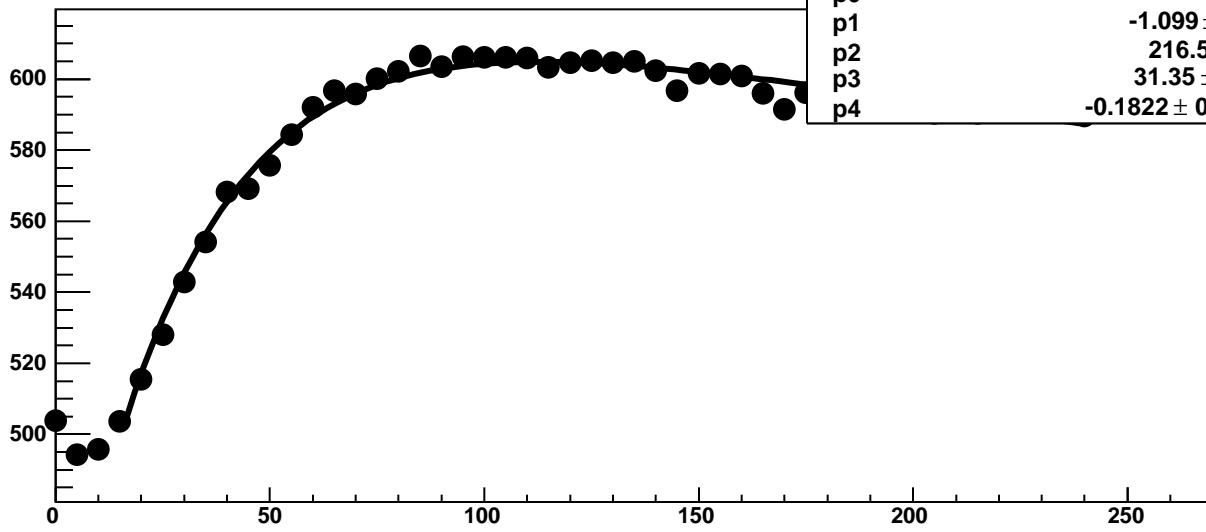
Chip 0, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold

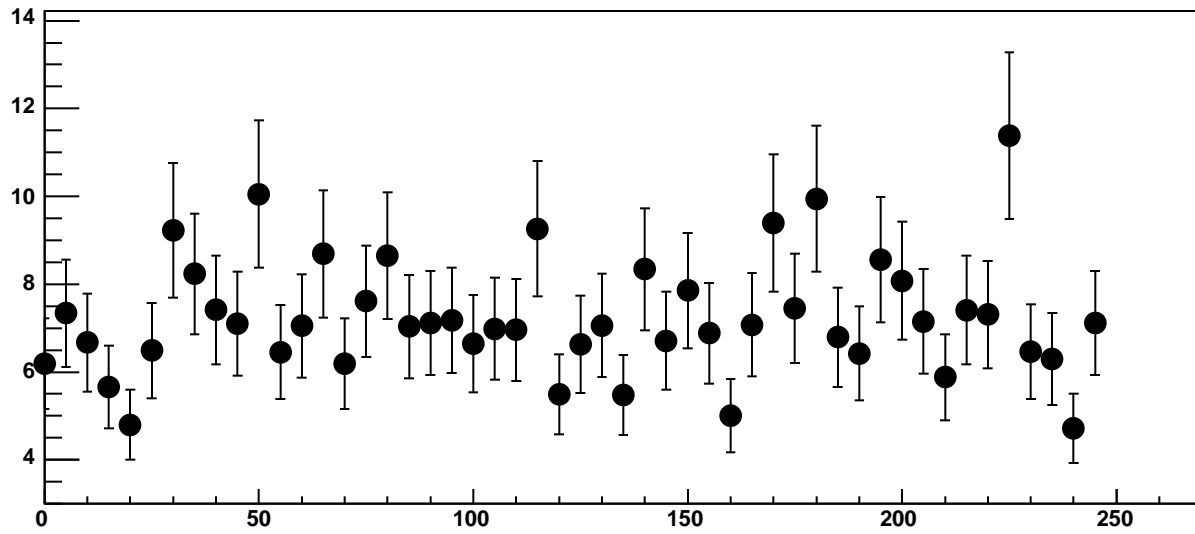


Chip 0, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold

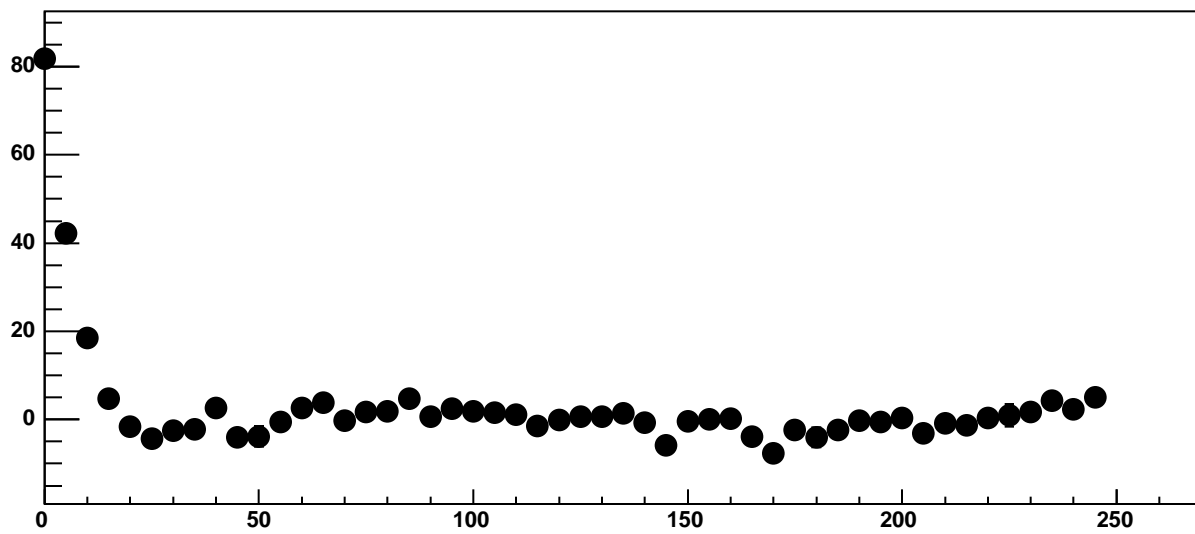


$\chi^2 / \text{ndf}$	123 / 41
p0	$414.8 \pm 2.97$
p1	$-1.099 \pm 0.5868$
p2	$216.5 \pm 3.066$
p3	$31.35 \pm 0.7476$
p4	$-0.1822 \pm 0.008107$

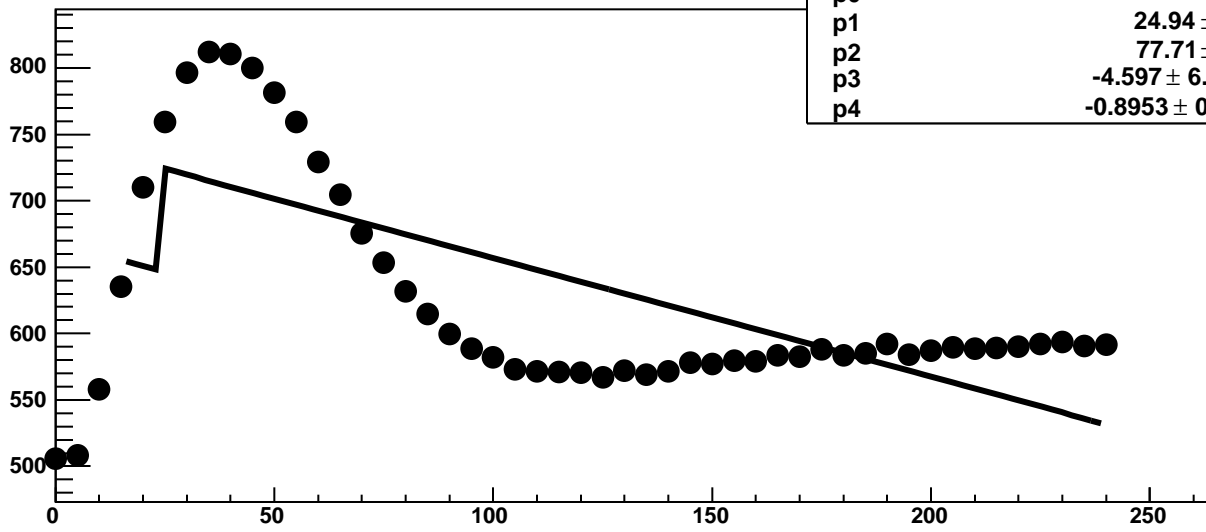
Chip 0, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

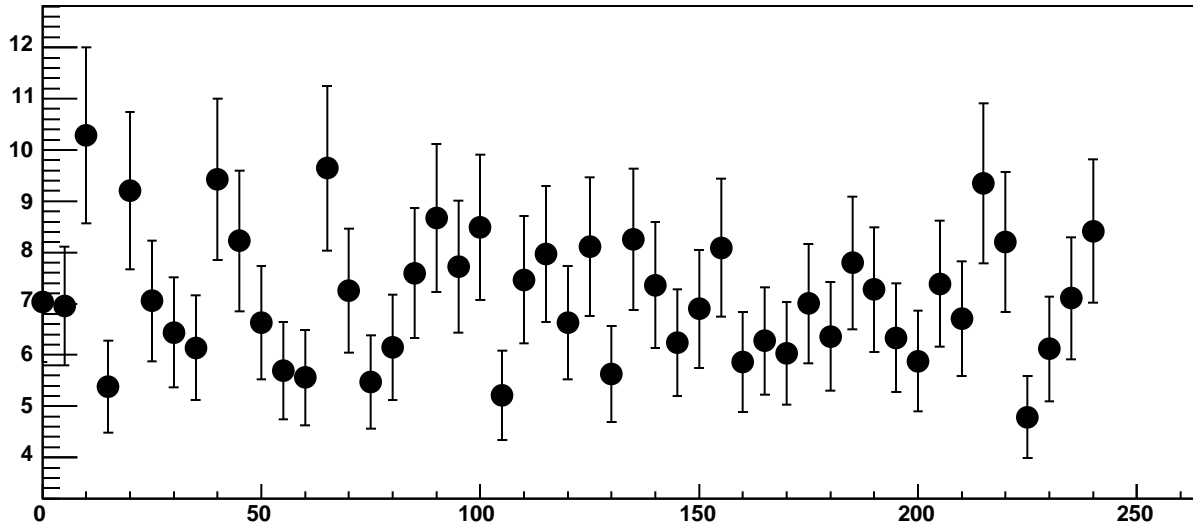


Chip 0, Channel 17, Enable 5, DAC=4000, ADC Mean vs Hold

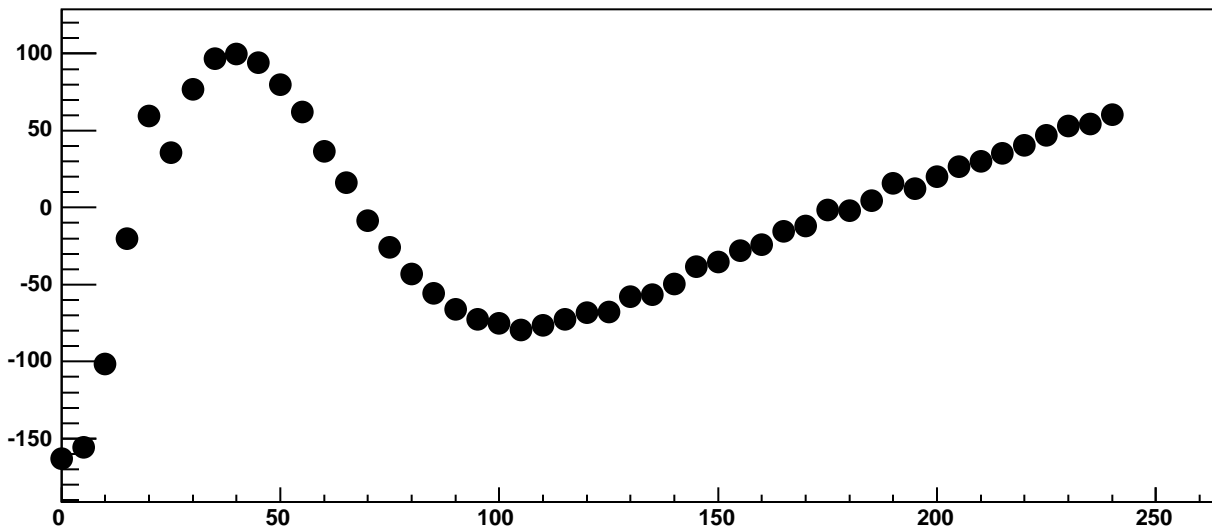


$\chi^2 / \text{ndf}$	5.027e+04 / 41
p0	646.4 ± 0.1463
p1	24.94 ± 0.1634
p2	77.71 ± 0.1566
p3	-4.597 ± 6.756e-06
p4	-0.8953 ± 0.005507

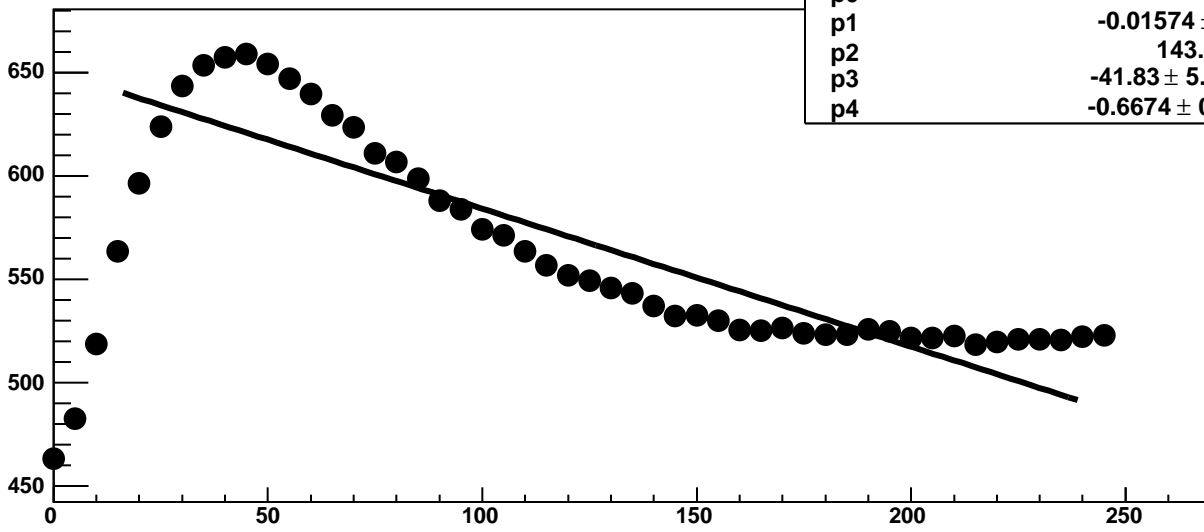
Chip 0, Channel 17, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 17, Enable 5, DAC=4000, ADC Residuals vs Hold

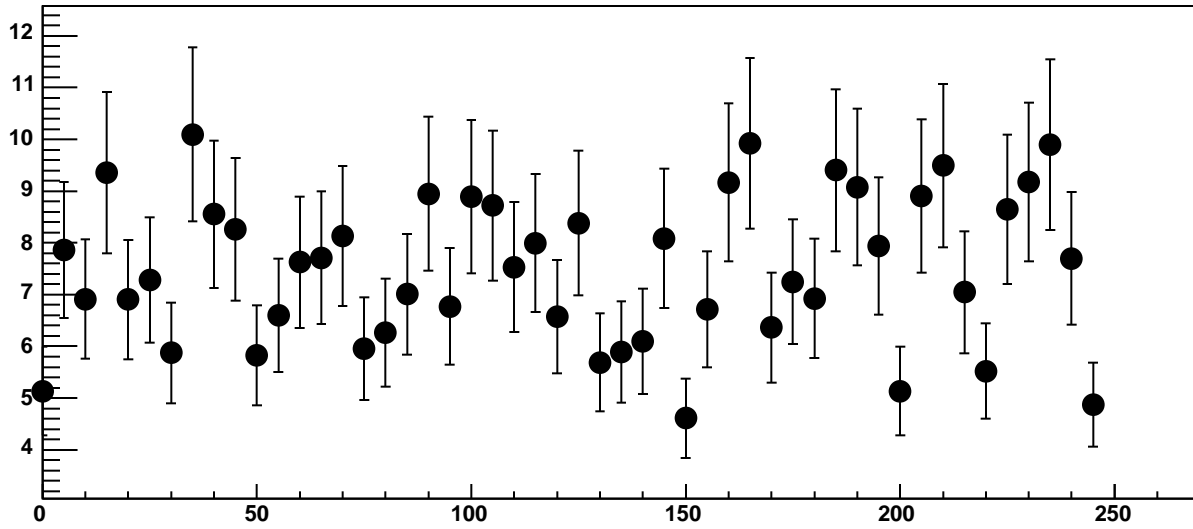


Chip 1, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold

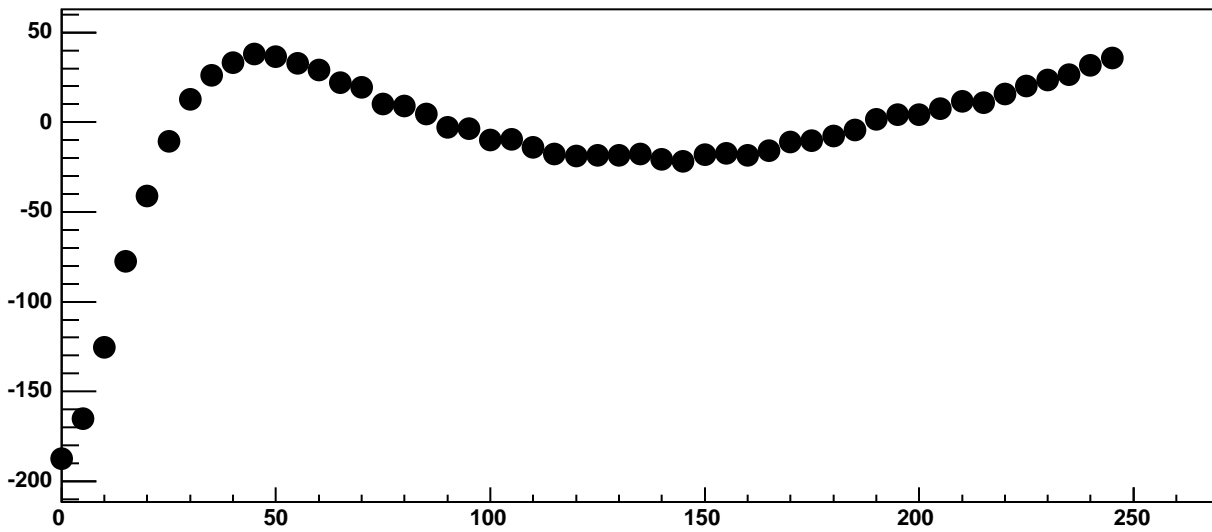


$\chi^2 / \text{ndf}$	7583 / 41
p0	$507.8 \pm 1.806$
p1	$-0.01574 \pm 0.9518$
p2	$143.1 \pm 1.77$
p3	$-41.83 \pm 5.569e-07$
p4	$-0.6674 \pm 0.003971$

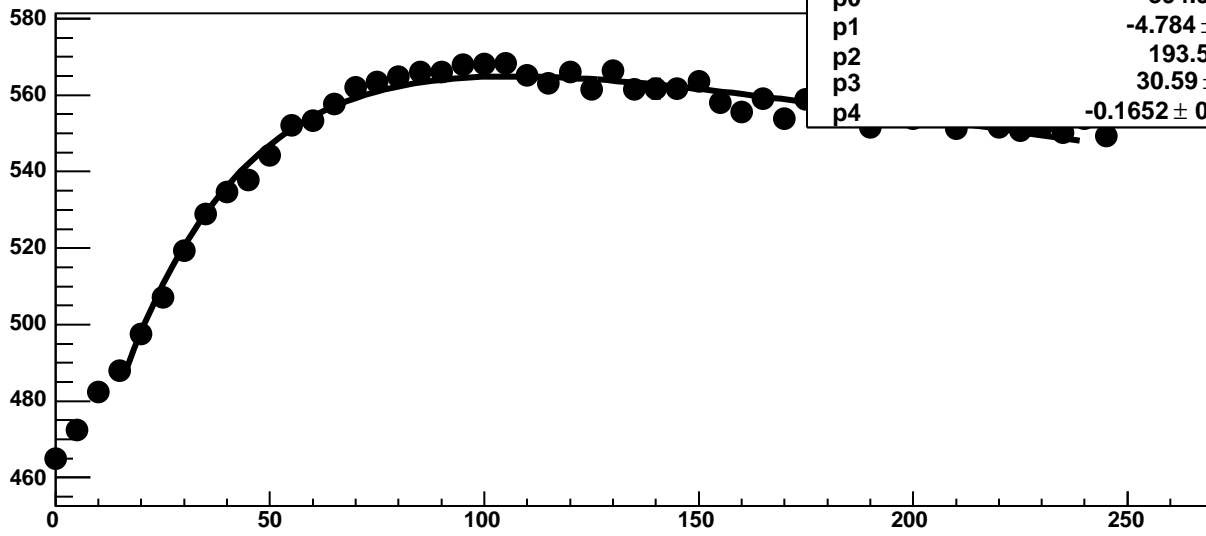
Chip 1, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold

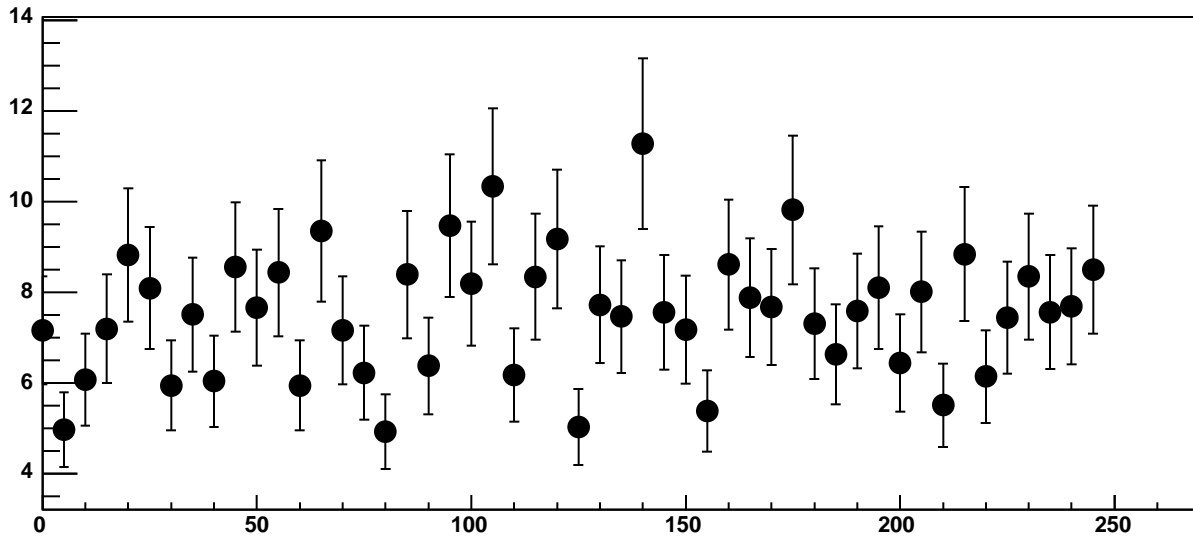


Chip 1, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

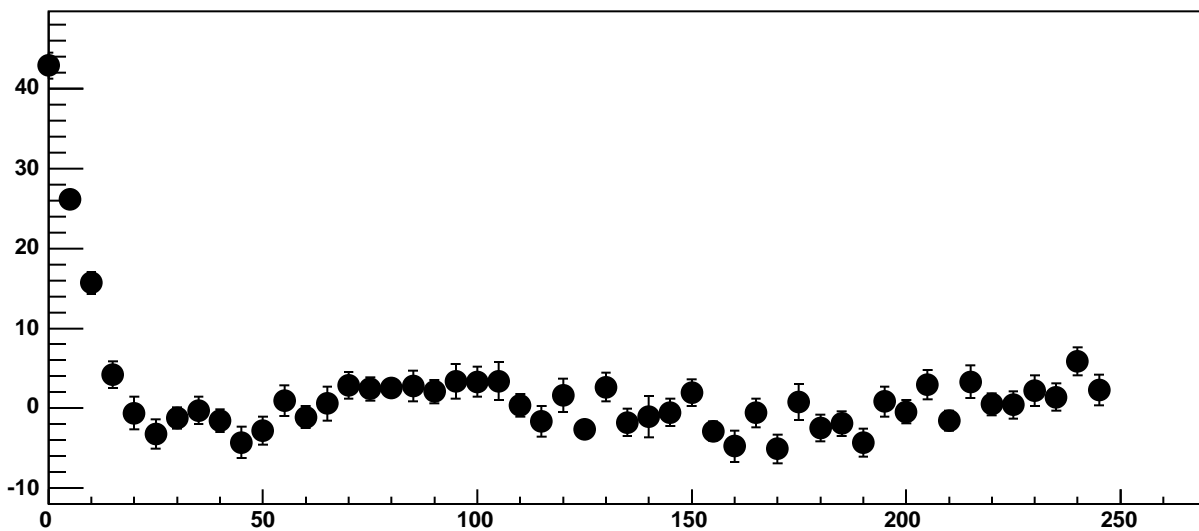


$\chi^2 / \text{ndf}$	104.2 / 41
p0	$394.9 \pm 3.099$
p1	$-4.784 \pm 0.8566$
p2	$193.5 \pm 3.192$
p3	$30.59 \pm 0.9678$
p4	$-0.1652 \pm 0.008586$

Chip 1, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold

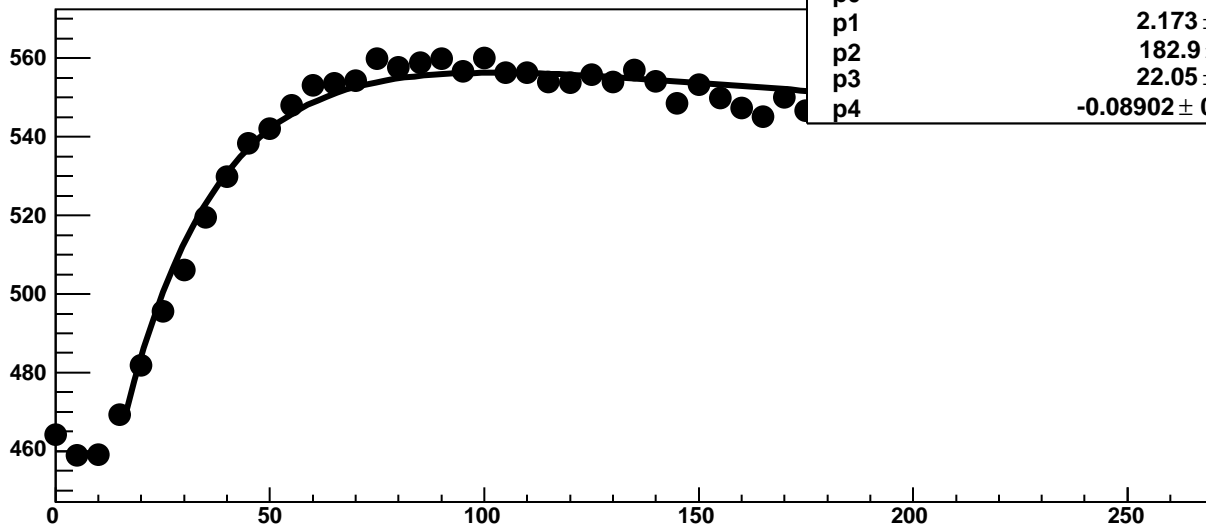


Chip 1, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold



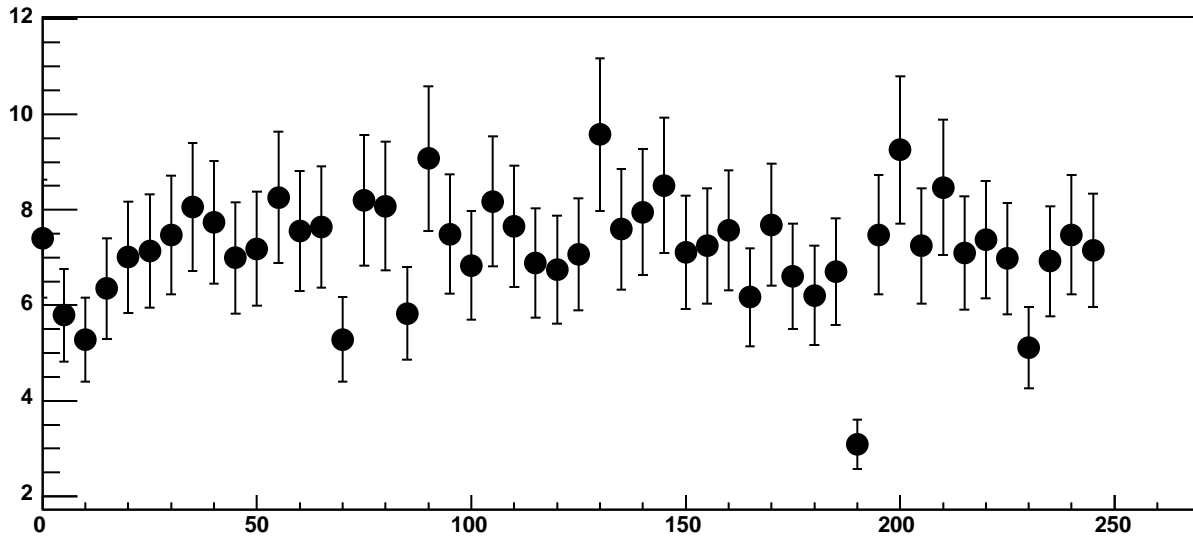


Chip 1, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

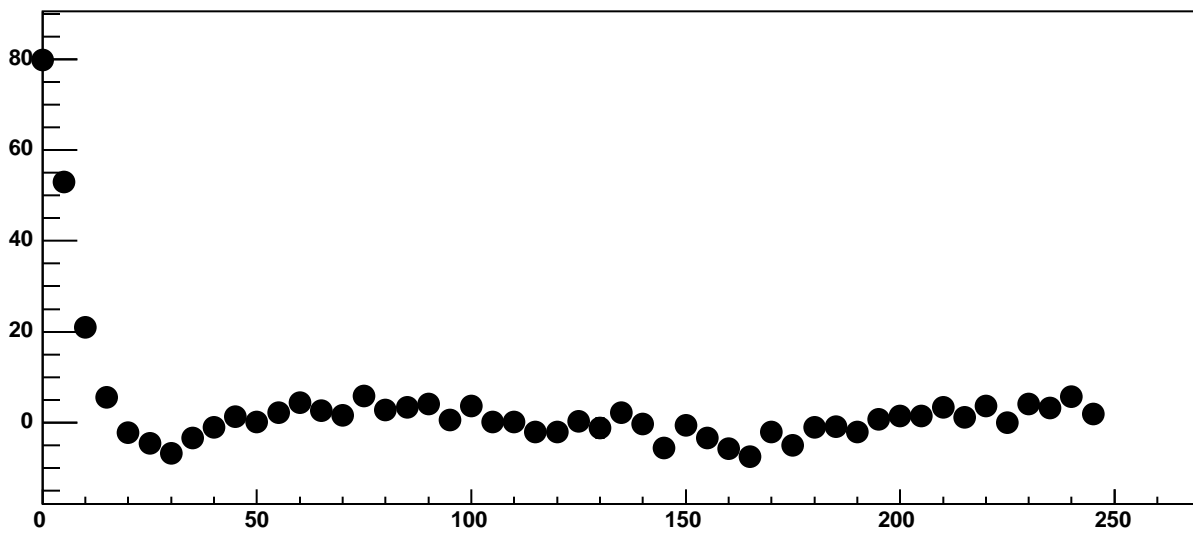


$\chi^2 / \text{ndf}$	199.5 / 41
p0	$384.2 \pm 0.6023$
p1	$2.173 \pm 0.4583$
p2	$182.9 \pm 0.6361$
p3	$22.05 \pm 0.6298$
p4	$-0.08902 \pm 0.006641$

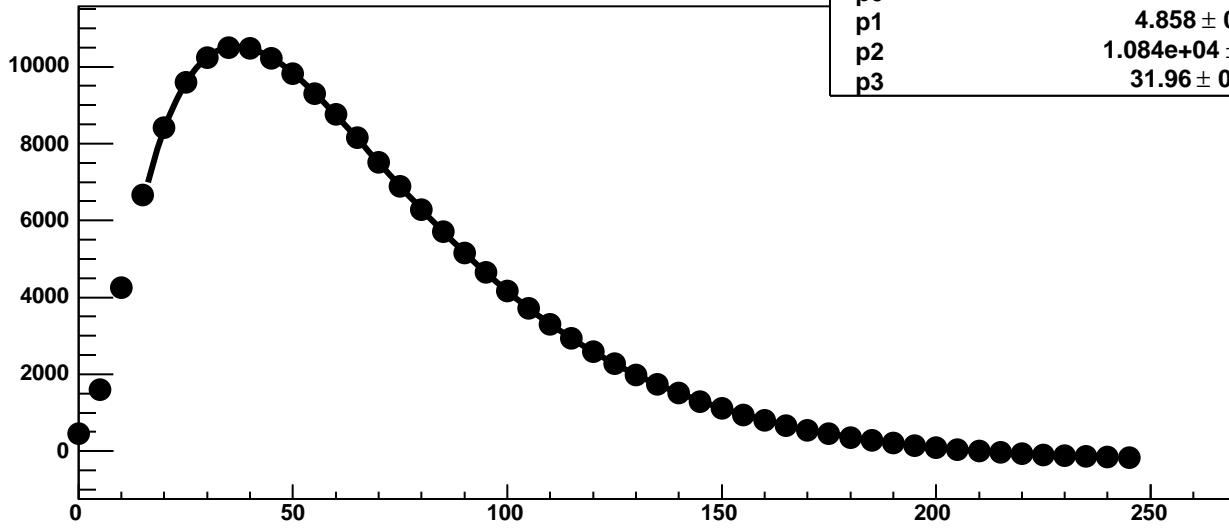
Chip 1, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

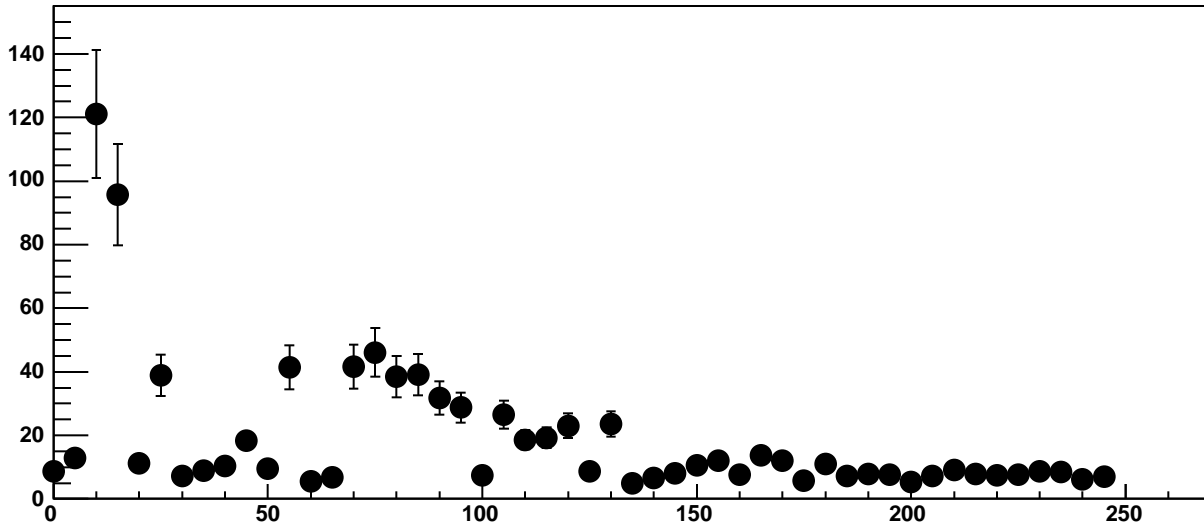


Chip 1, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold

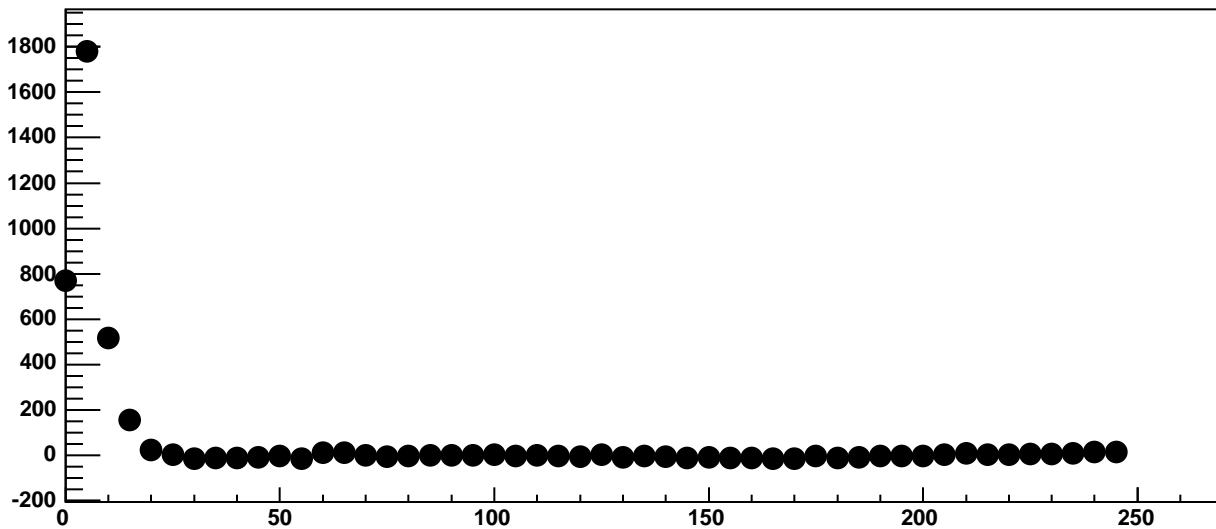


$\chi^2 / \text{ndf}$	813.9 / 42
p0	-305.8 ± 0.6347
p1	4.858 ± 0.009461
p2	1.084e+04 ± 0.9168
p3	31.96 ± 0.006353

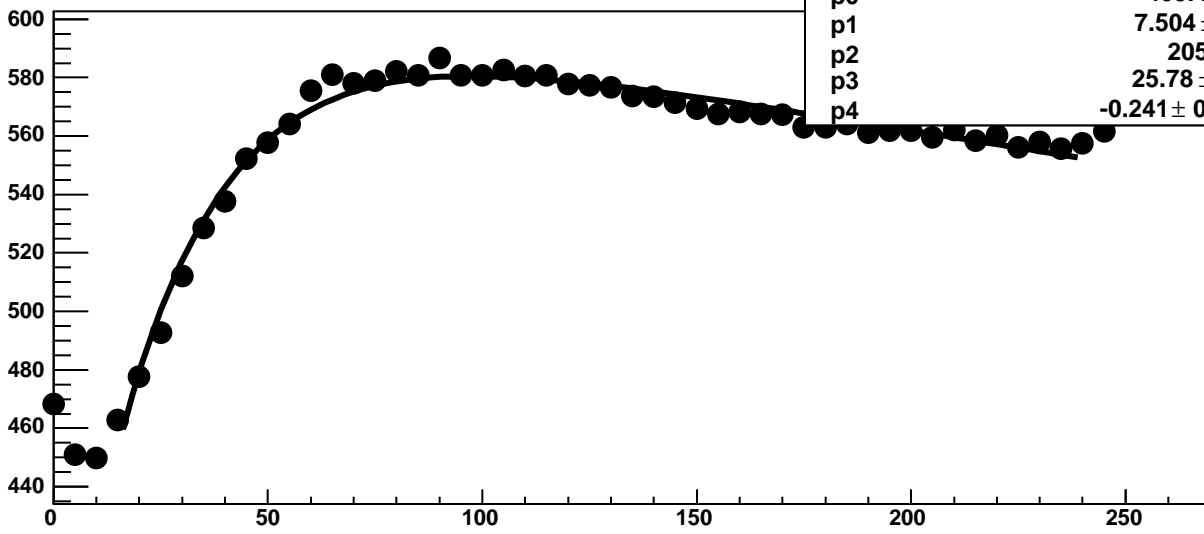
Chip 1, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold

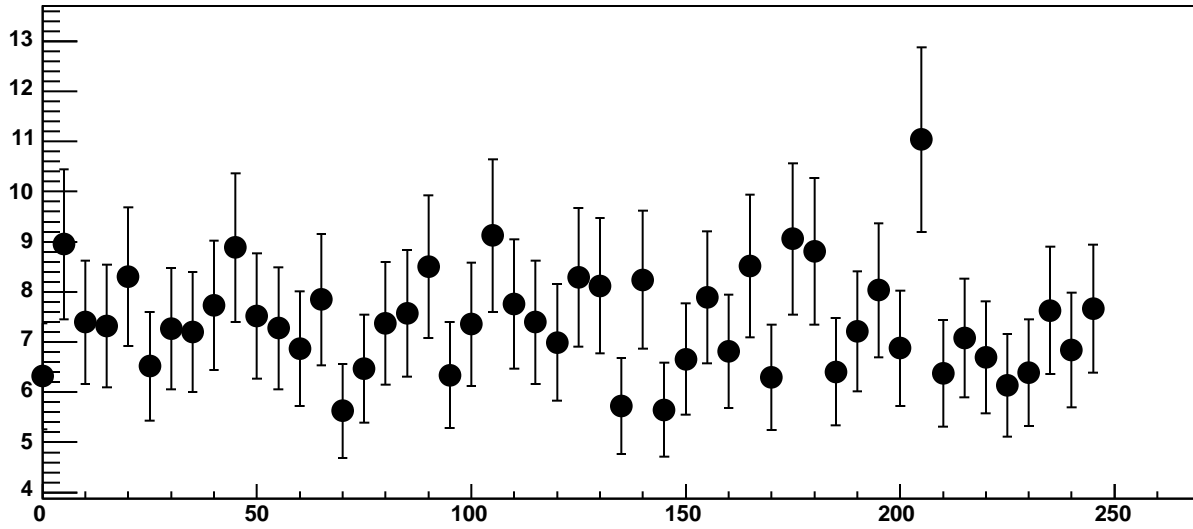


Chip 1, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

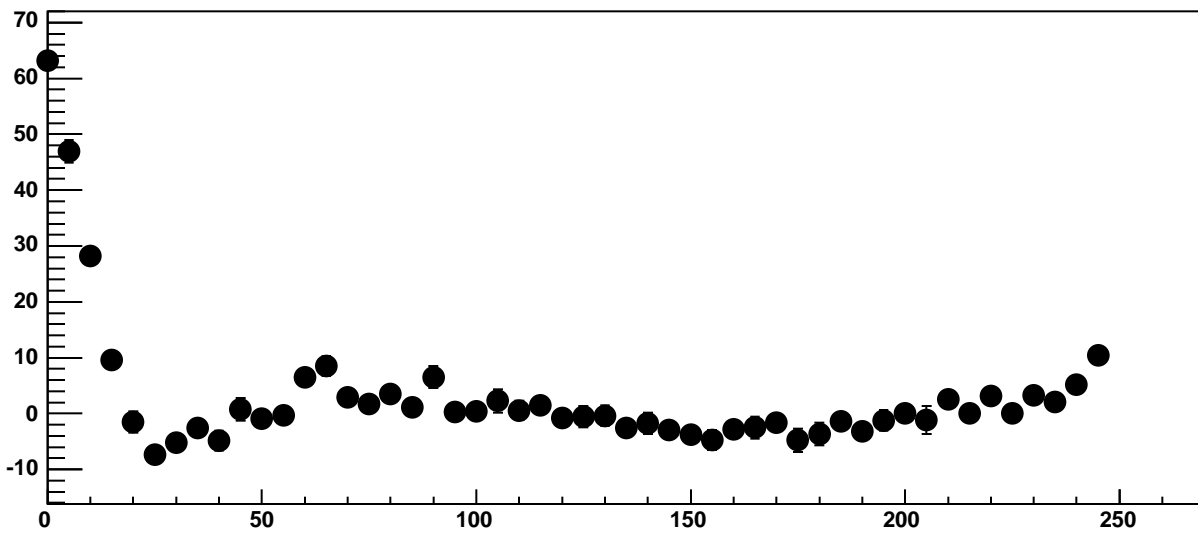


$\chi^2 / \text{ndf}$	209.2 / 41
p0	$403.4 \pm 3.097$
p1	$7.504 \pm 0.4812$
p2	$205 \pm 3.203$
p3	$25.78 \pm 0.5206$
p4	$-0.241 \pm 0.007393$

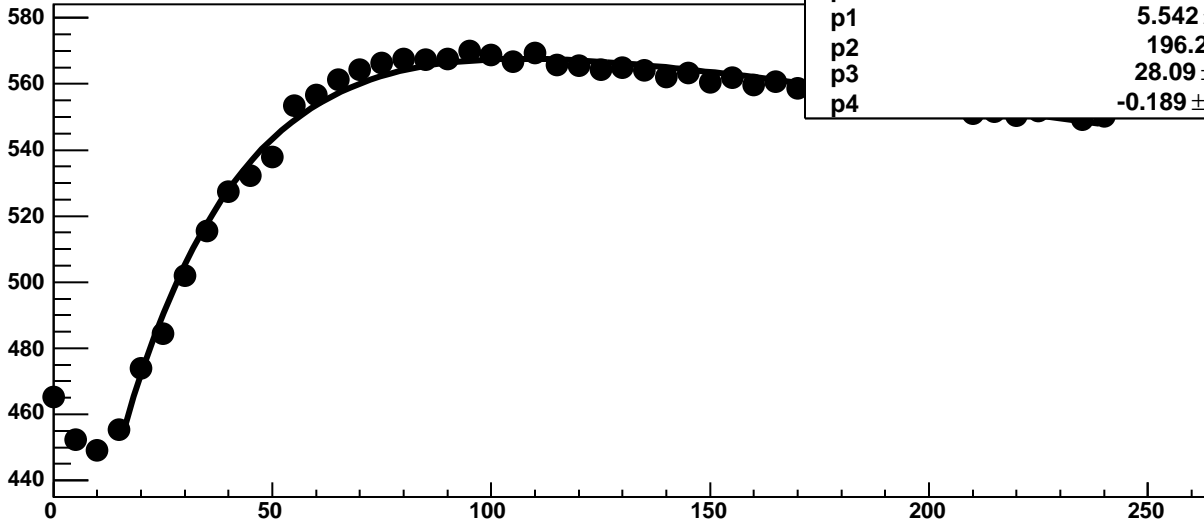
Chip 1, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

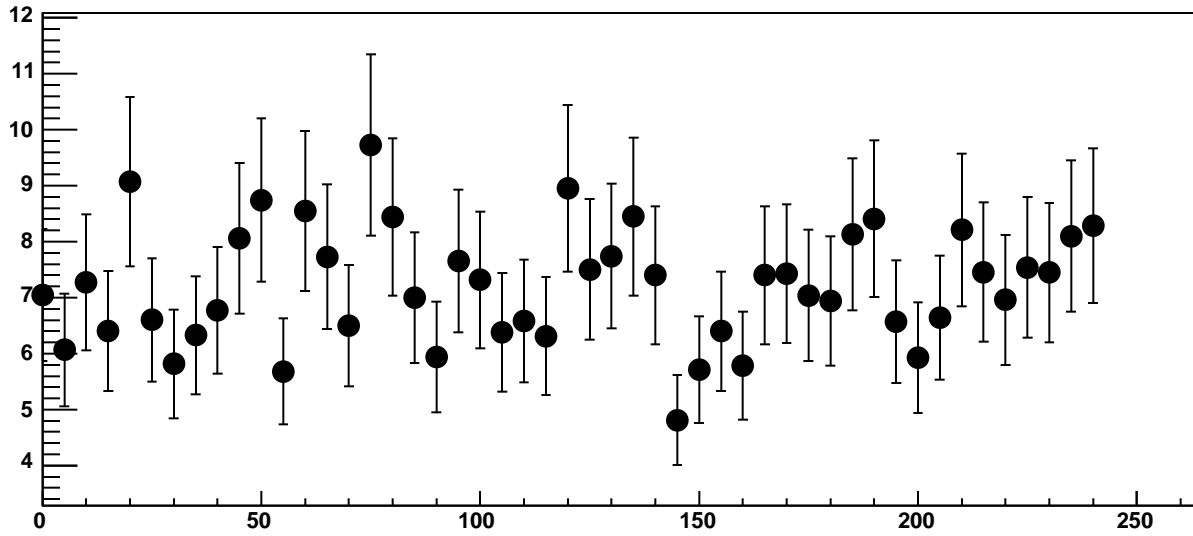


Chip 1, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold

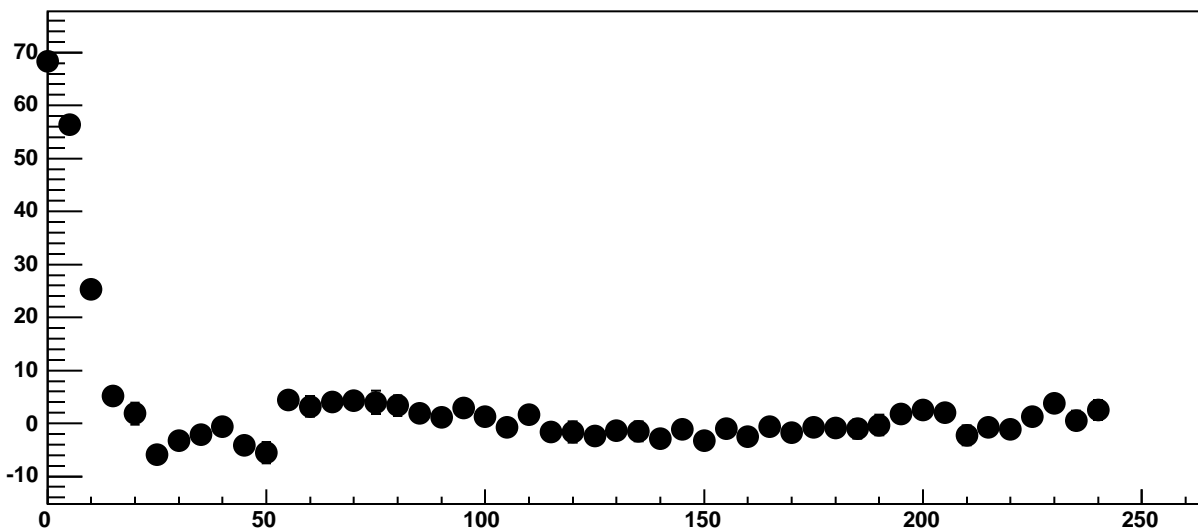


$\chi^2 / \text{ndf}$	126.9 / 41
p0	$395.9 \pm 3.01$
p1	$5.542 \pm 0.5381$
p2	$196.2 \pm 3.109$
p3	$28.09 \pm 0.6188$
p4	$-0.189 \pm 0.00811$

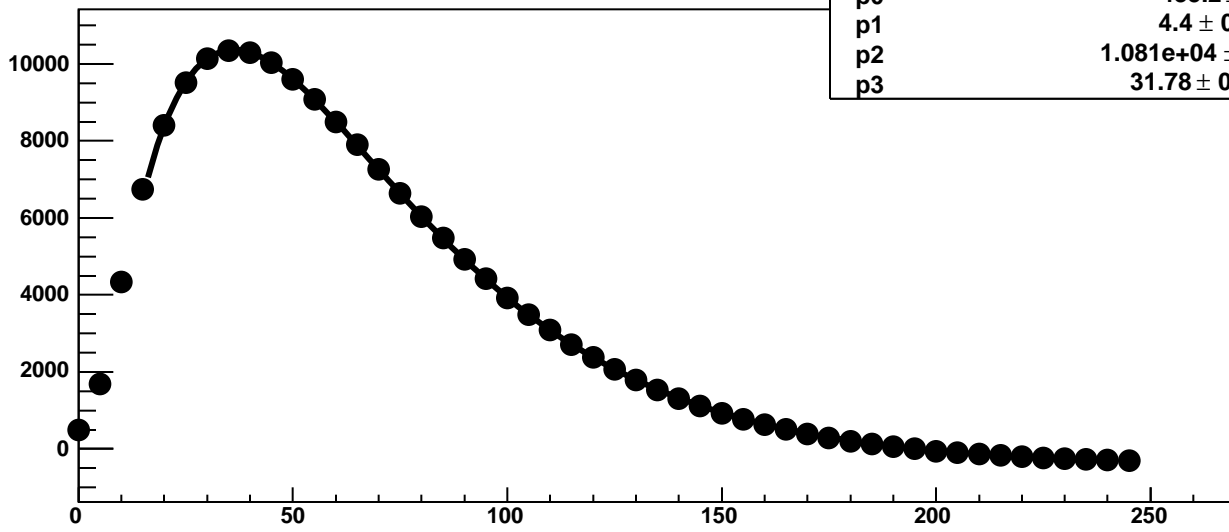
Chip 1, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold

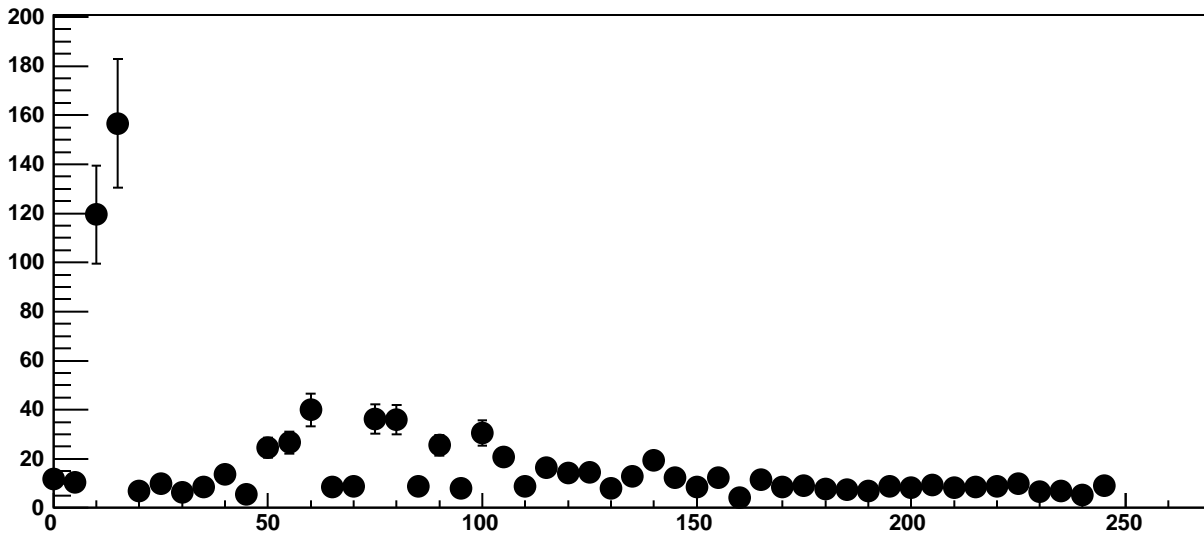


Chip 1, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold

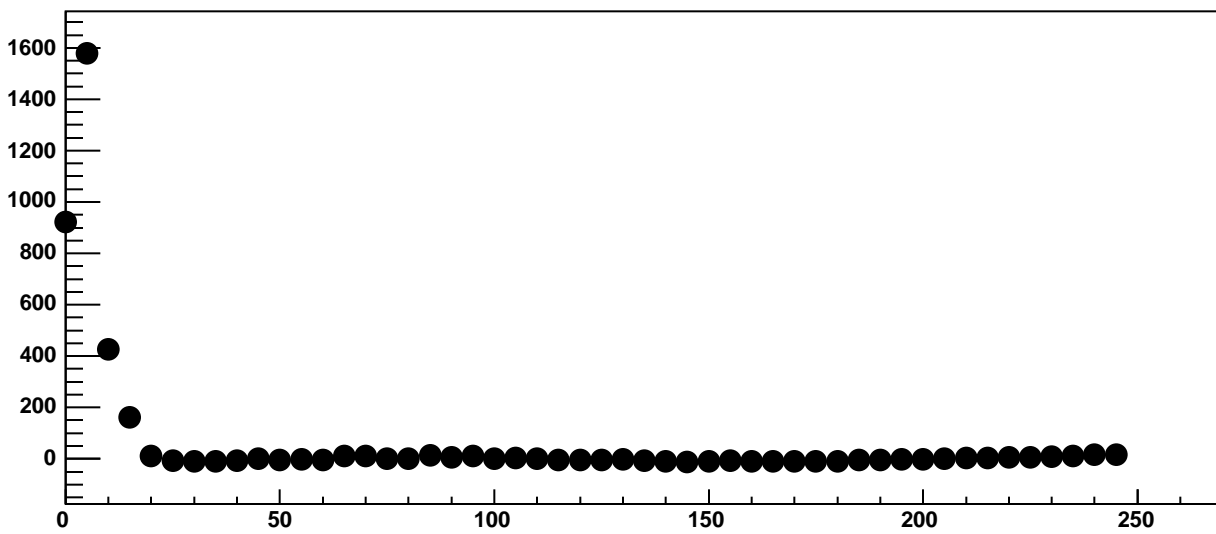


$\chi^2 / \text{ndf}$	842.2 / 42
p0	-435.2 ± 0.6068
p1	4.4 ± 0.007662
p2	1.081e+04 ± 0.8614
p3	31.78 ± 0.005778

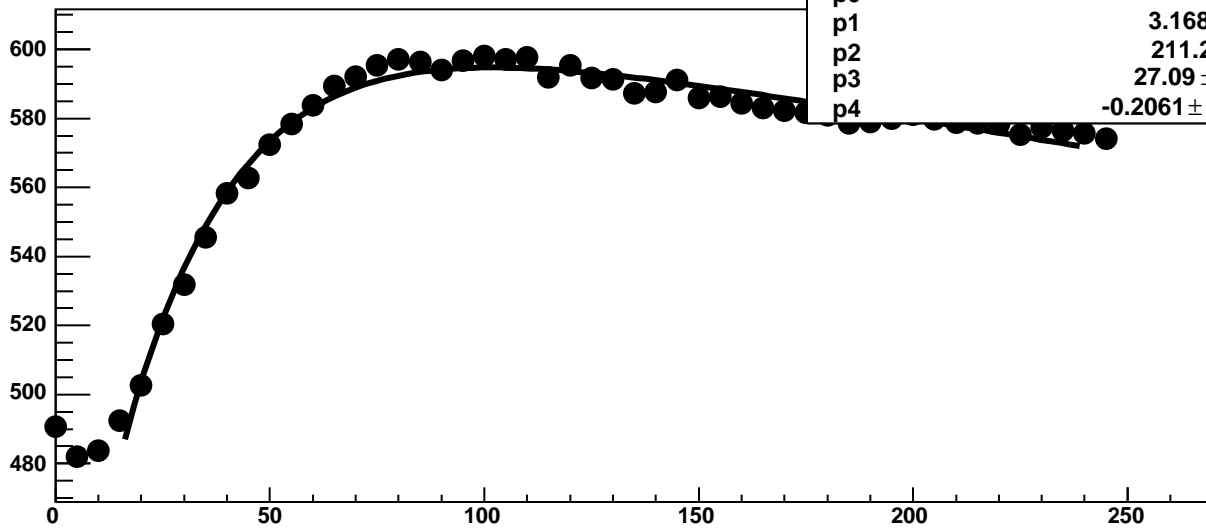
Chip 1, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold

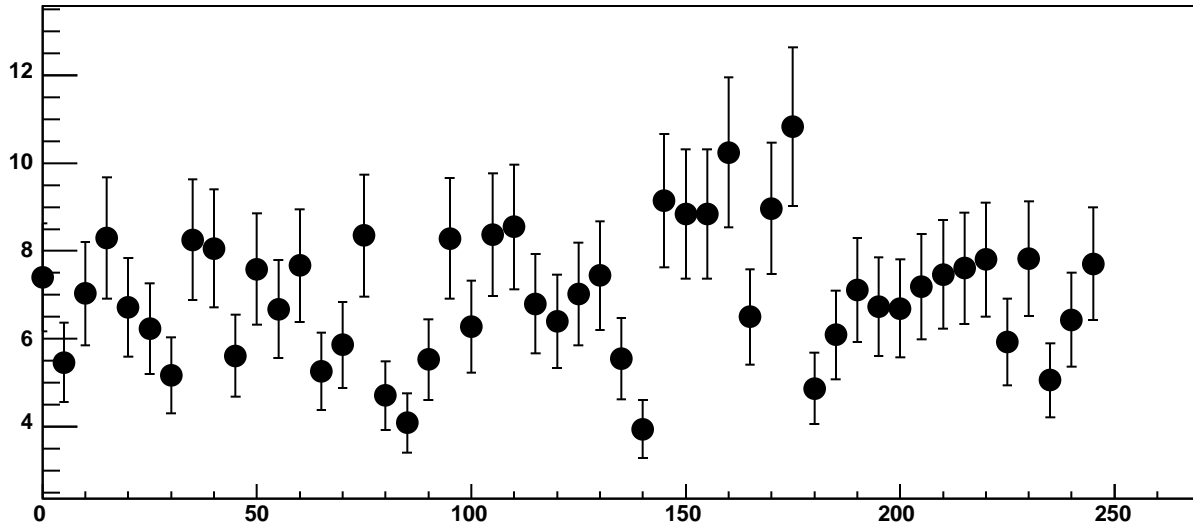


Chip 1, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

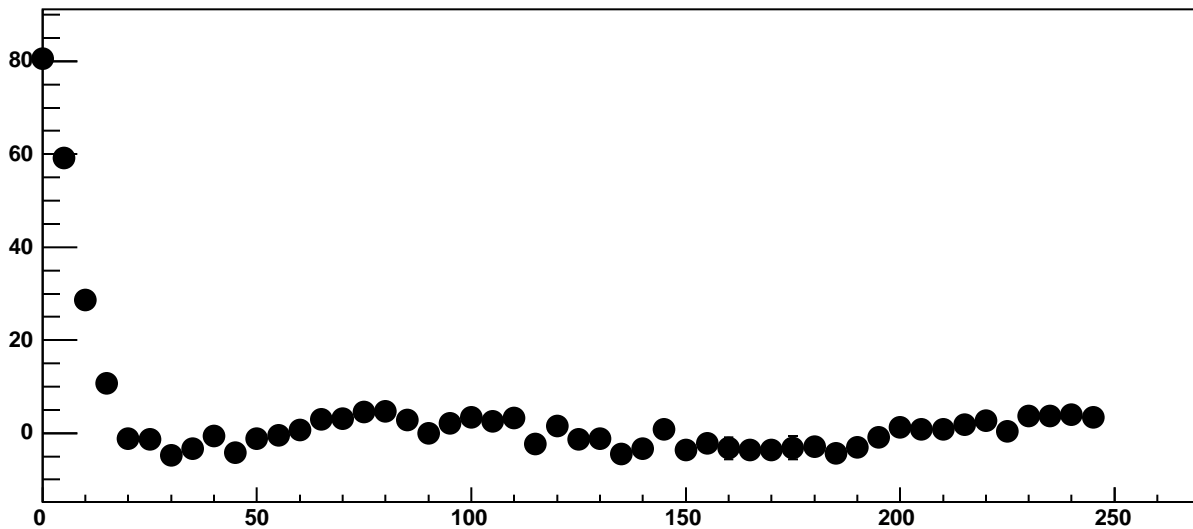


$\chi^2 / \text{ndf}$	215.1 / 41
p0	$409.4 \pm 2.78$
p1	$3.168 \pm 0.513$
p2	$211.2 \pm 2.863$
p3	$27.09 \pm 0.5755$
p4	$-0.2061 \pm 0.00693$

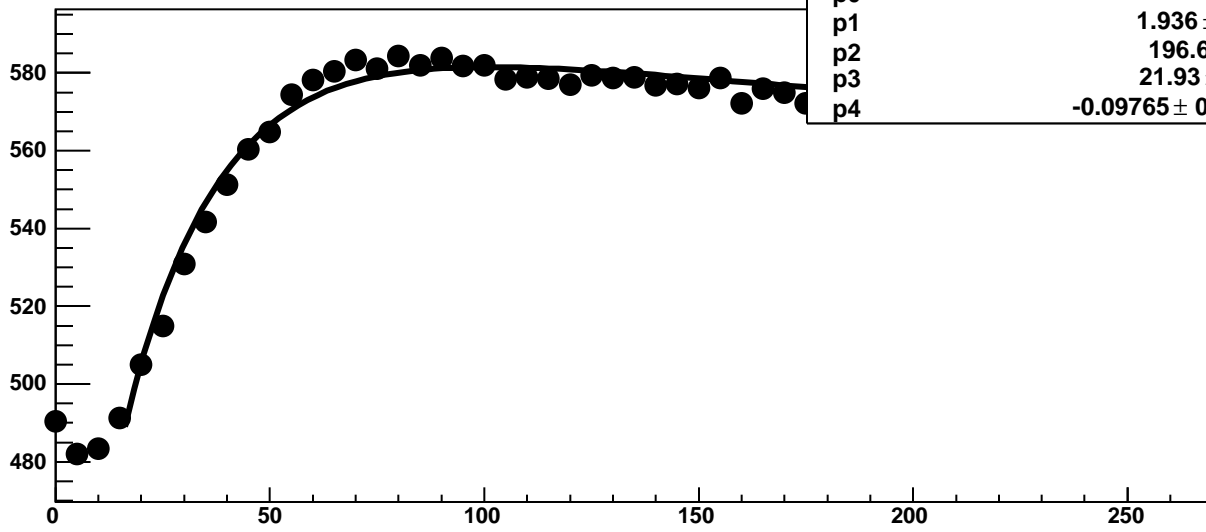
Chip 1, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

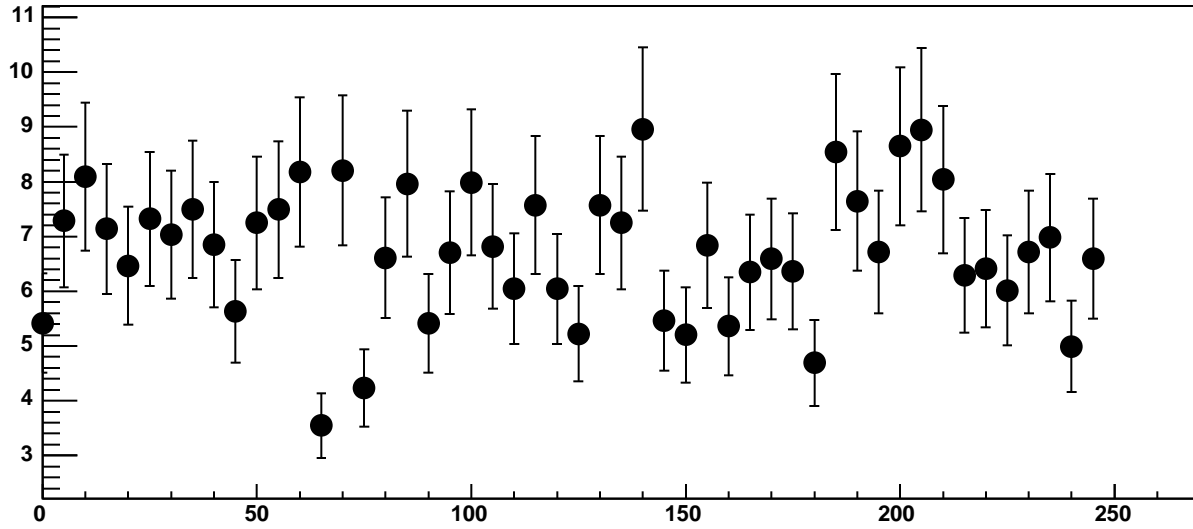


Chip 1, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

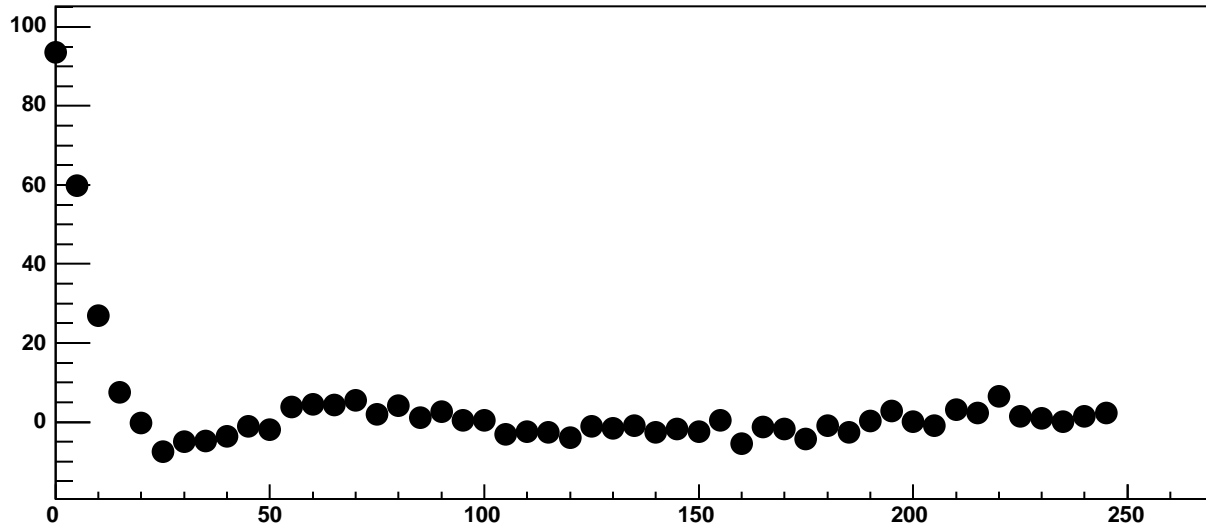


$\chi^2 / \text{ndf}$	221.3 / 41
p0	$396.7 \pm 2.711$
p1	$1.936 \pm 0.5202$
p2	$196.6 \pm 2.744$
p3	$21.93 \pm 0.5251$
p4	$-0.09765 \pm 0.005764$

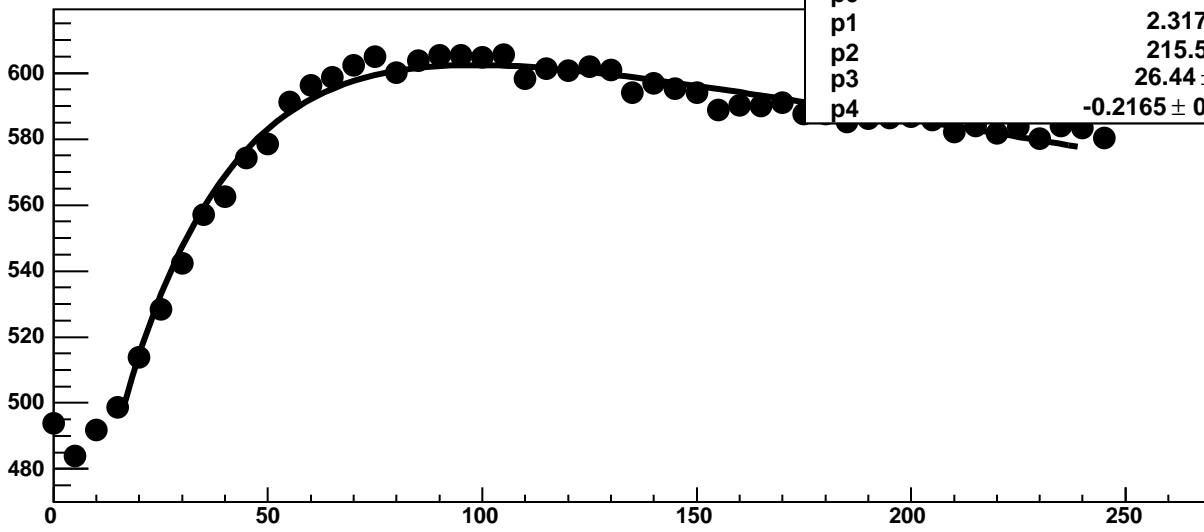
Chip 1, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

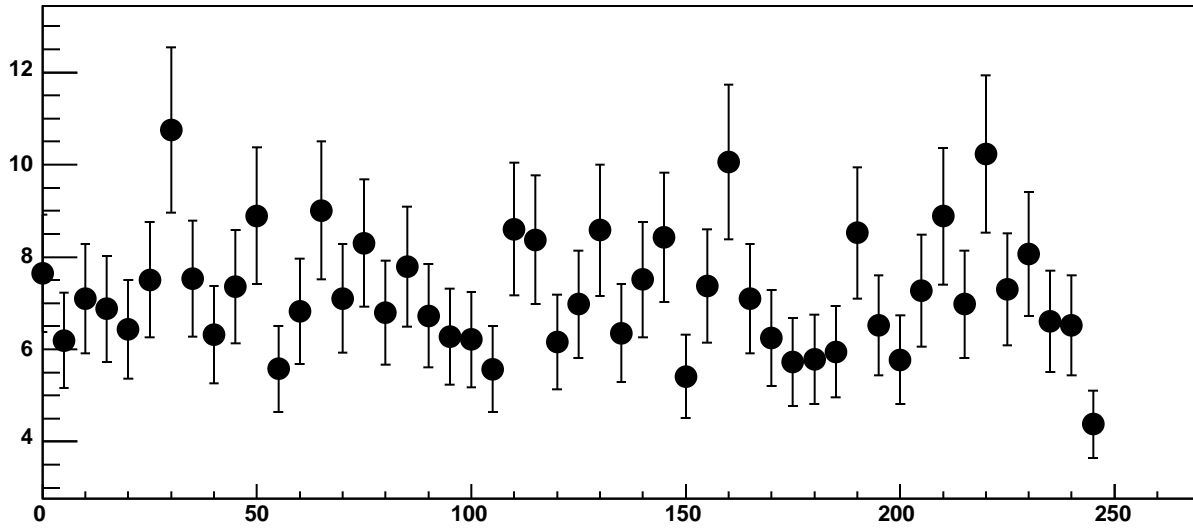


Chip 1, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold

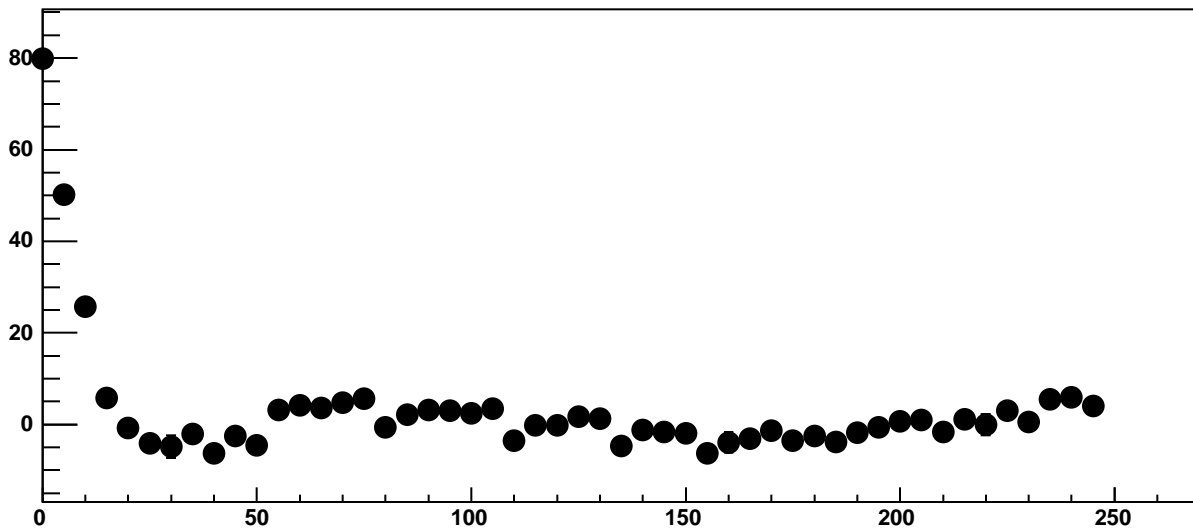


$\chi^2 / \text{ndf}$	199.9 / 41
p0	$413.4 \pm 3.009$
p1	$2.317 \pm 0.525$
p2	$215.5 \pm 3.097$
p3	$26.44 \pm 0.5902$
p4	$-0.2165 \pm 0.007459$

Chip 1, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold

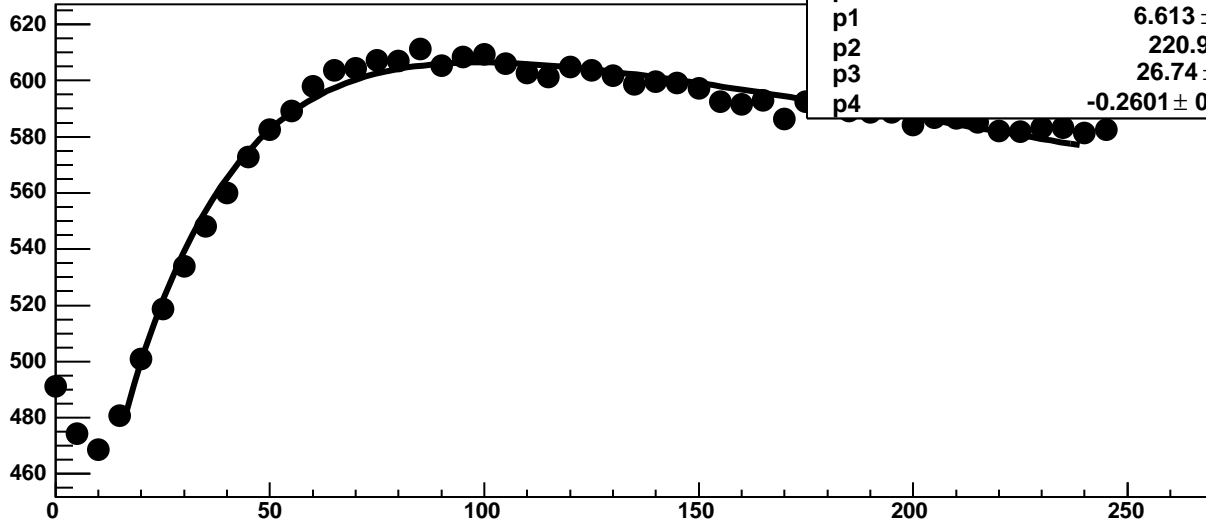


Chip 1, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold



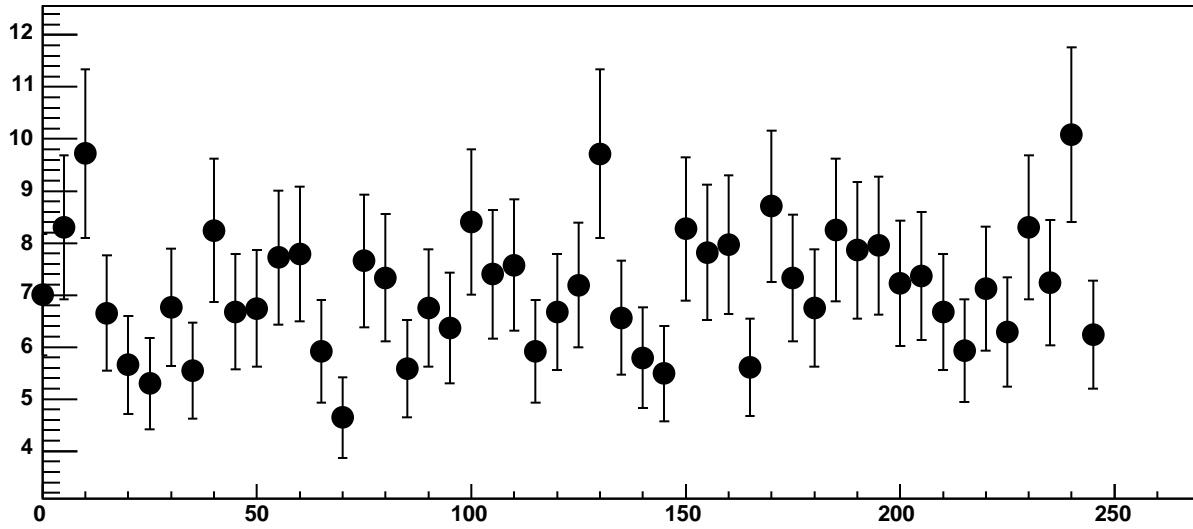


Chip 1, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

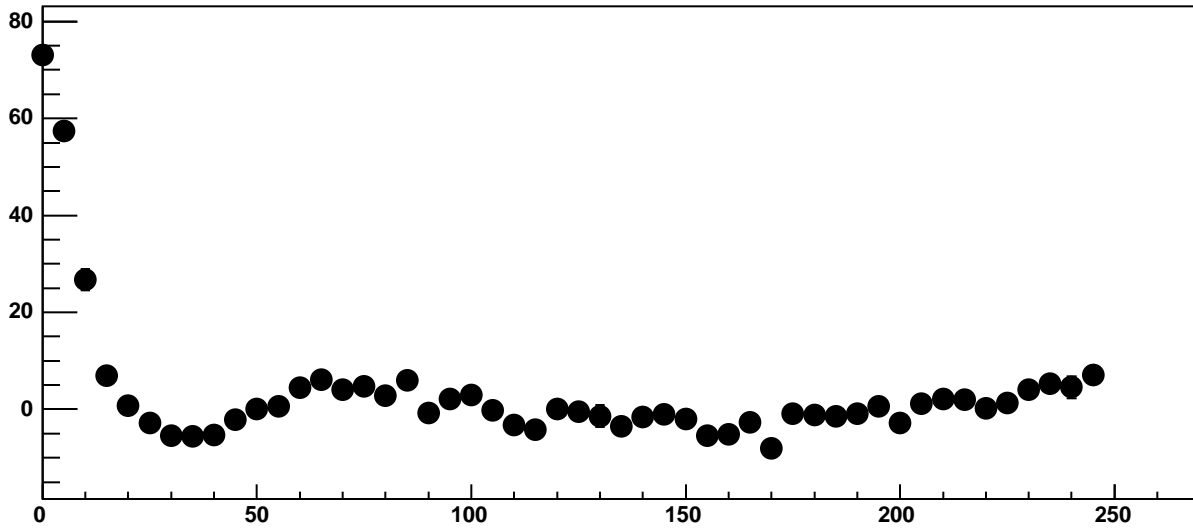


$\chi^2 / \text{ndf}$	234.2 / 41
p0	$416.5 \pm 2.916$
p1	$6.613 \pm 0.4285$
p2	$220.9 \pm 3.025$
p3	$26.74 \pm 0.4837$
p4	$-0.2601 \pm 0.007554$

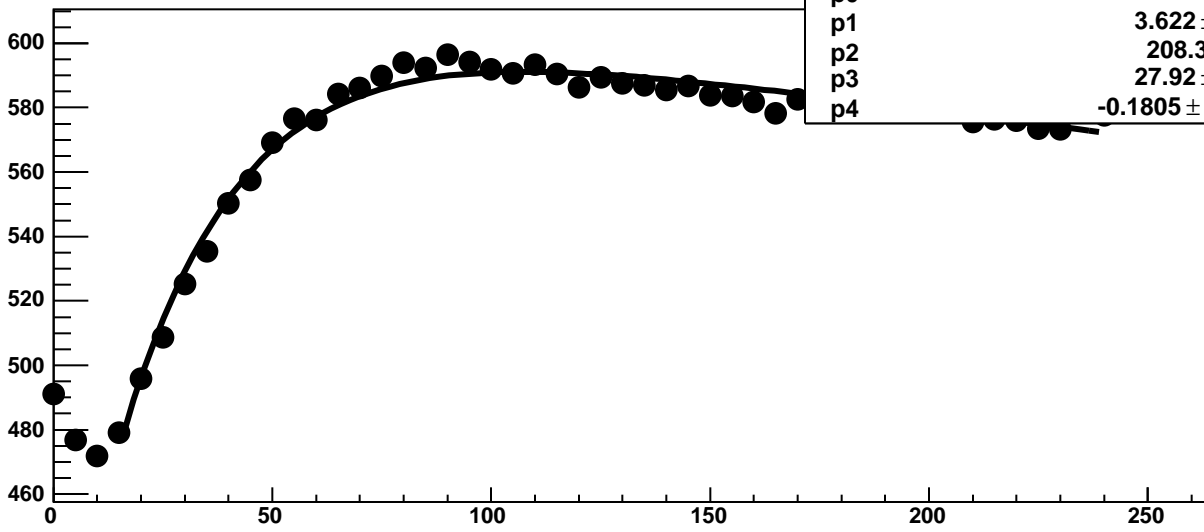
Chip 1, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

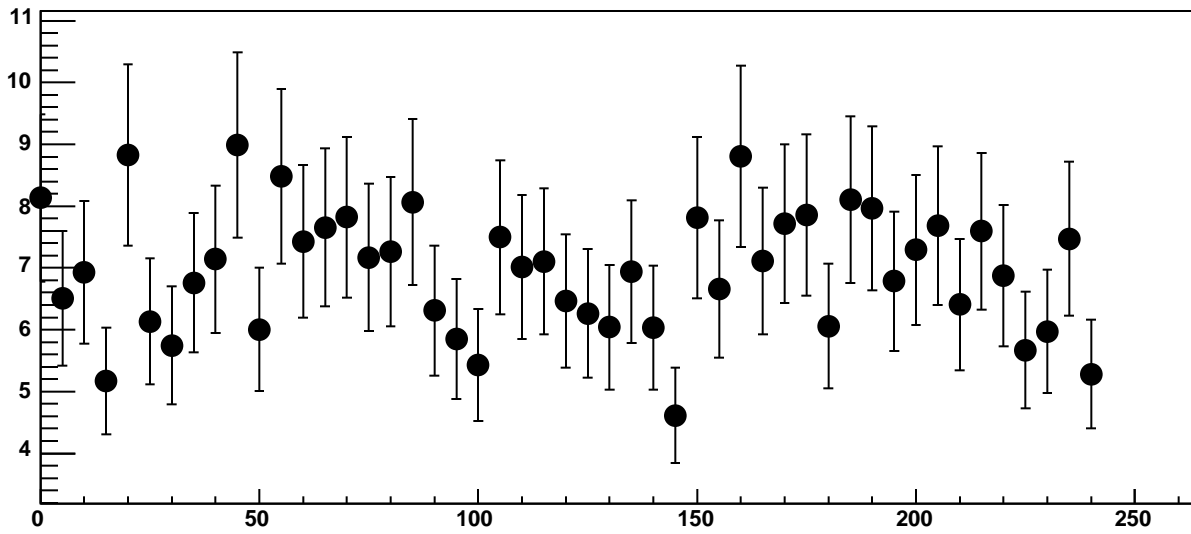


Chip 1, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

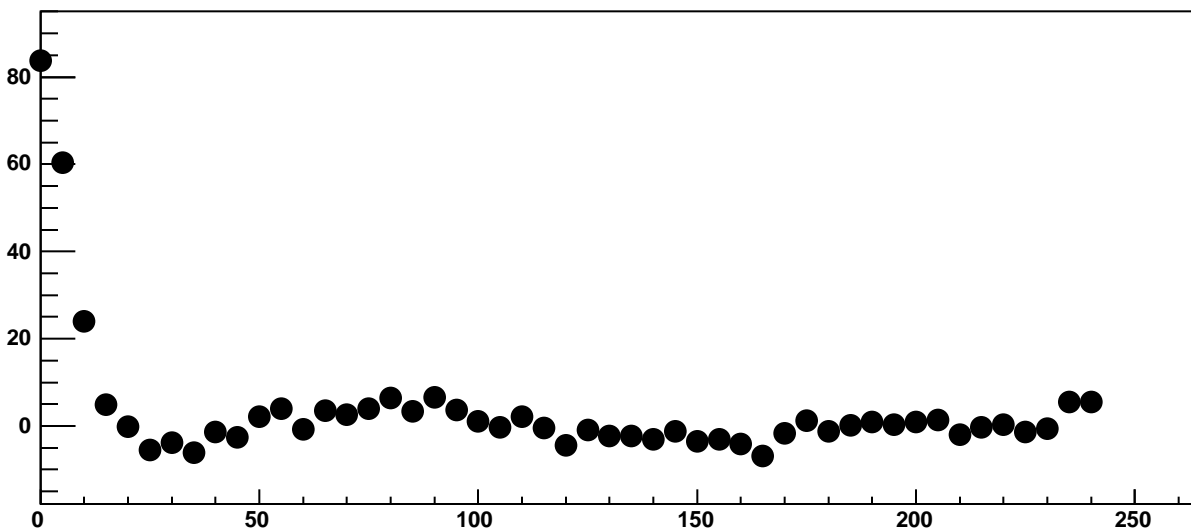


$\chi^2 / \text{ndf}$	211.8 / 41
p0	$406.6 \pm 2.881$
p1	$3.622 \pm 0.4918$
p2	$208.3 \pm 2.964$
p3	$27.92 \pm 0.5629$
p4	$-0.1805 \pm 0.00708$

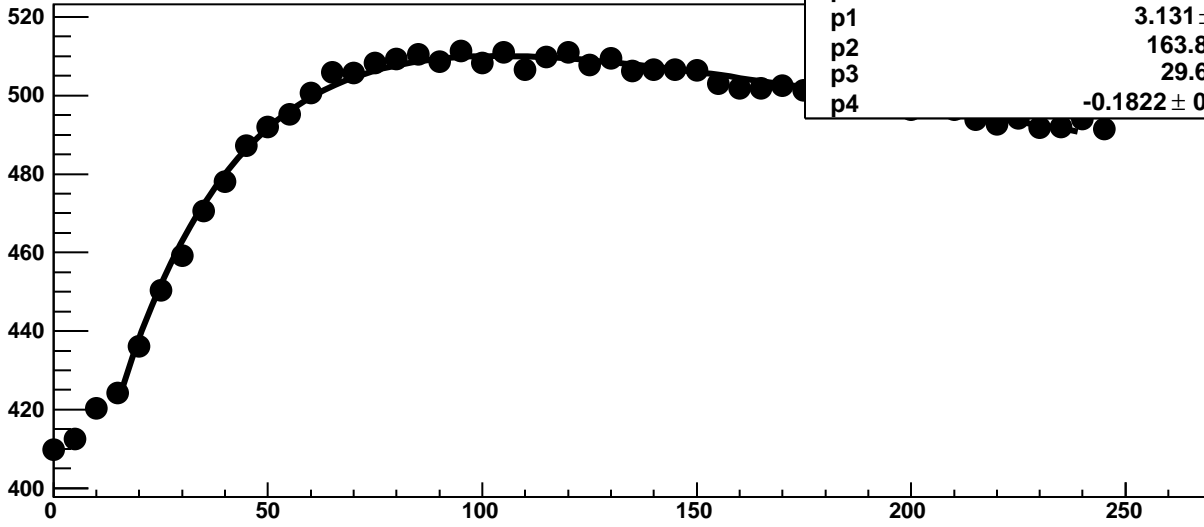
Chip 1, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold

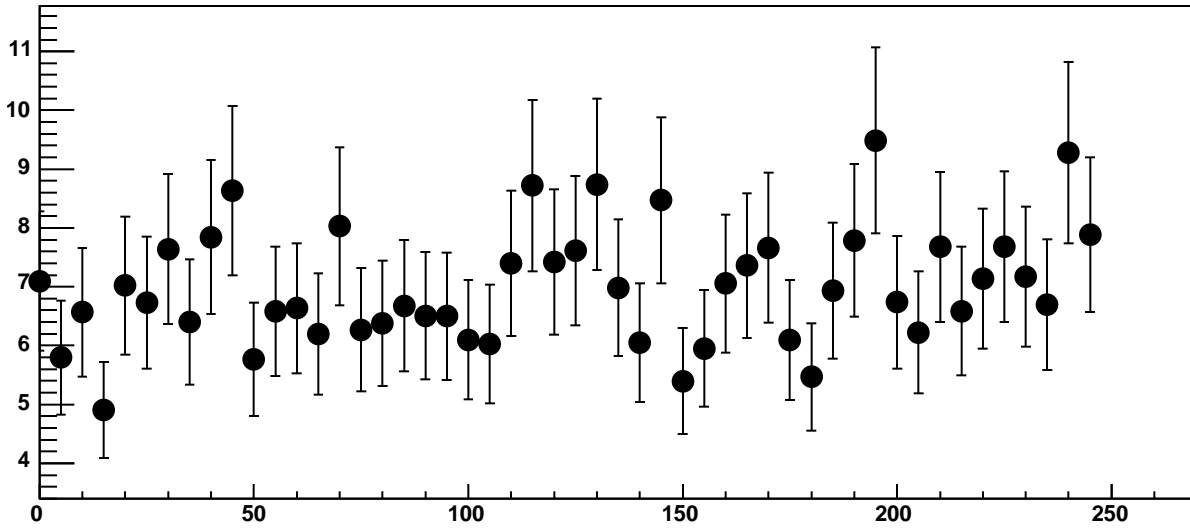


Chip 1, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold

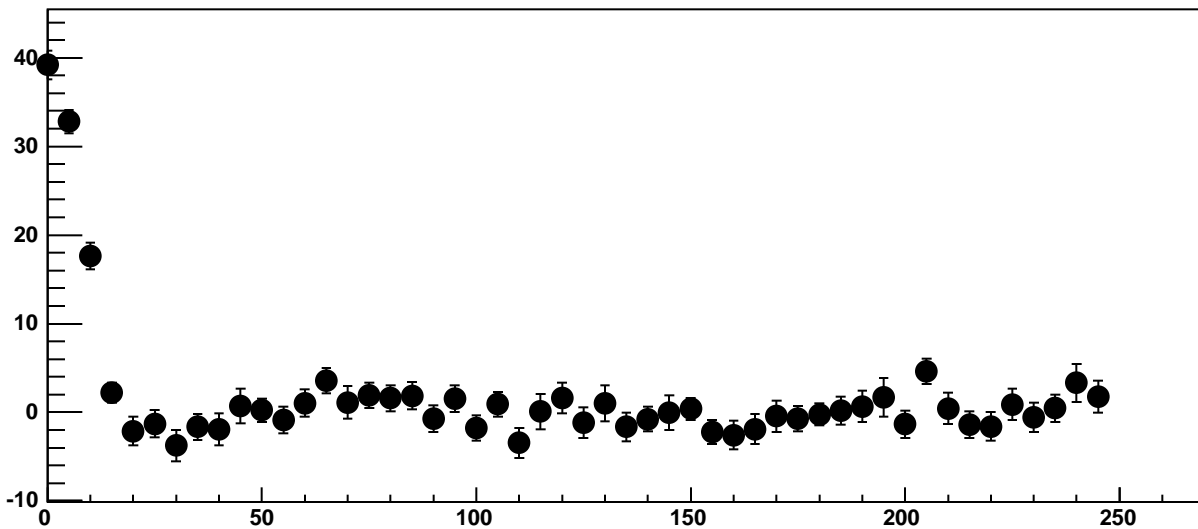


$\chi^2 / \text{ndf}$	59.41 / 41
p0	$370.1 \pm 2.93$
p1	$3.131 \pm 0.6536$
p2	$163.8 \pm 3.048$
p3	$29.6 \pm 0.818$
p4	$-0.1822 \pm 0.008378$

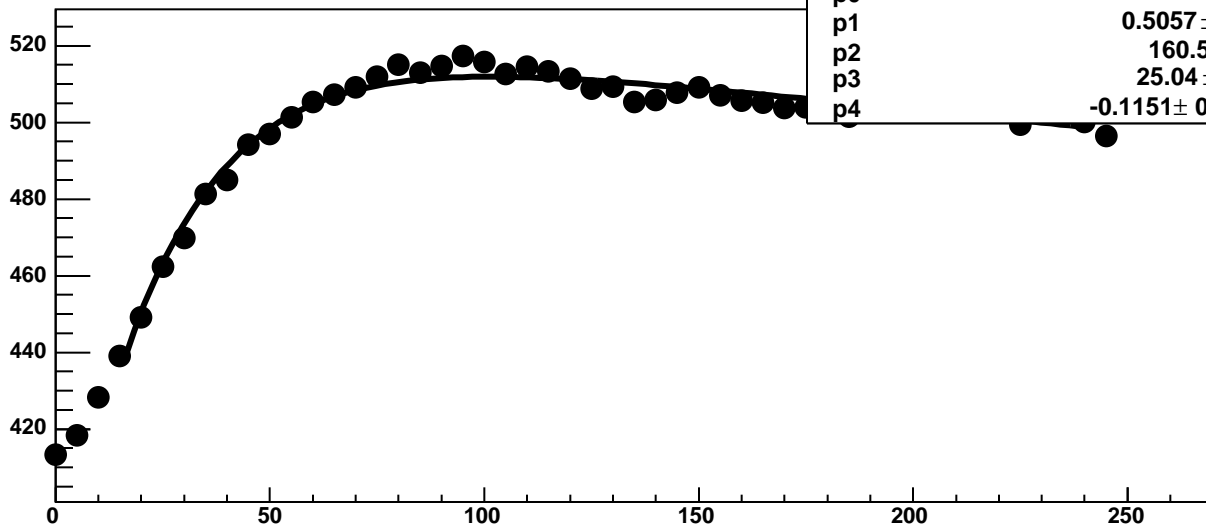
Chip 1, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold

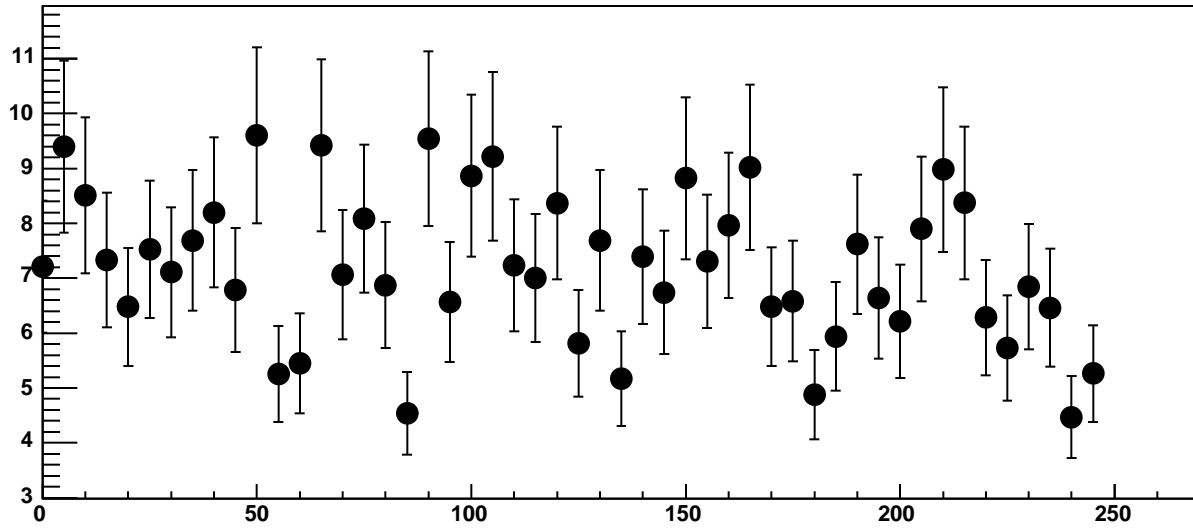


Chip 1, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

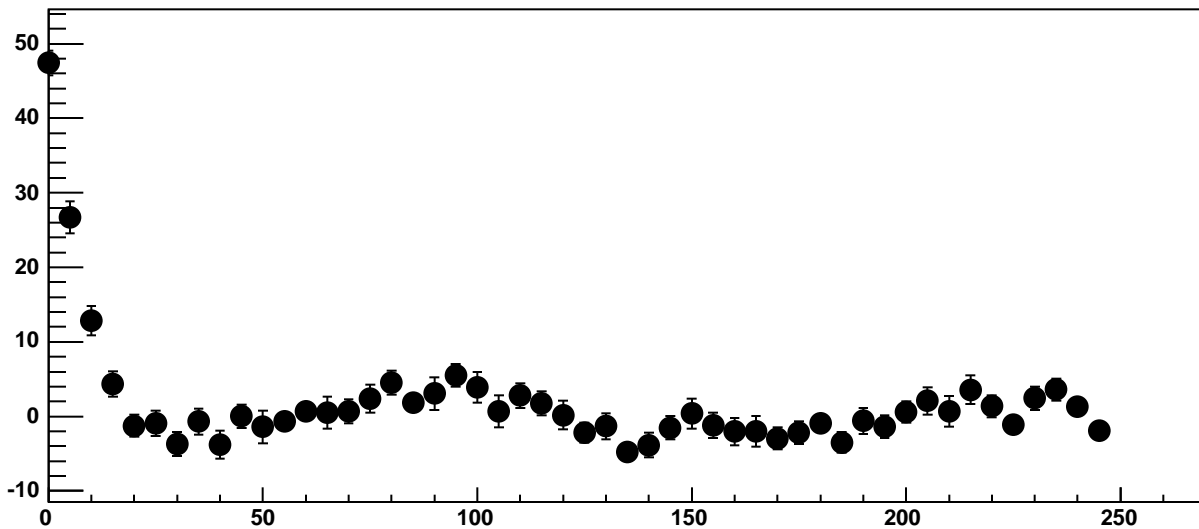


$\chi^2 / \text{ndf}$	113.6 / 41
p0	$365.9 \pm 2.91$
p1	$0.5057 \pm 0.7475$
p2	$160.5 \pm 2.967$
p3	$25.04 \pm 0.8007$
p4	$-0.1151 \pm 0.006578$

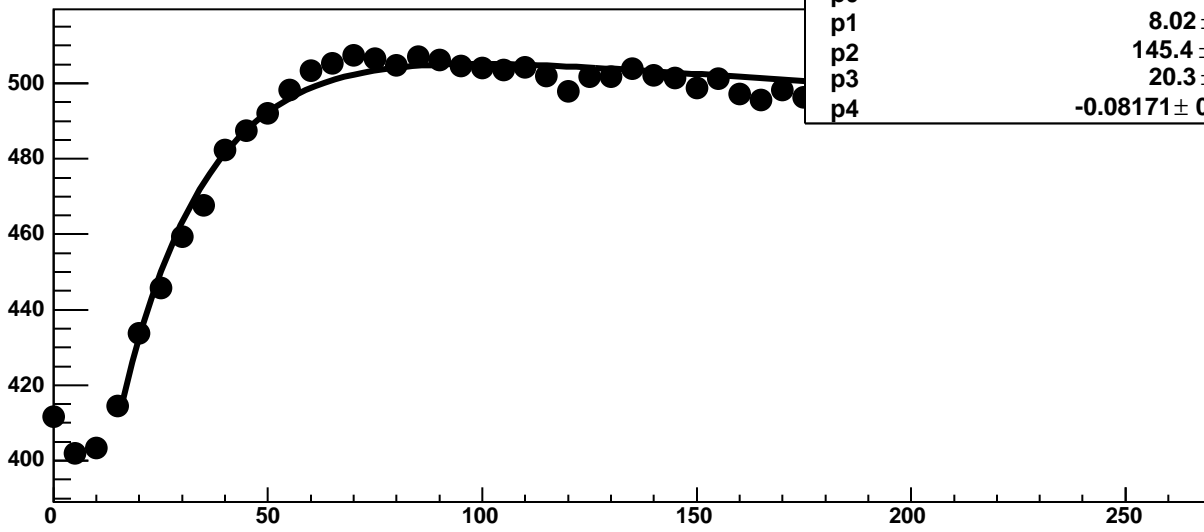
Chip 1, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold

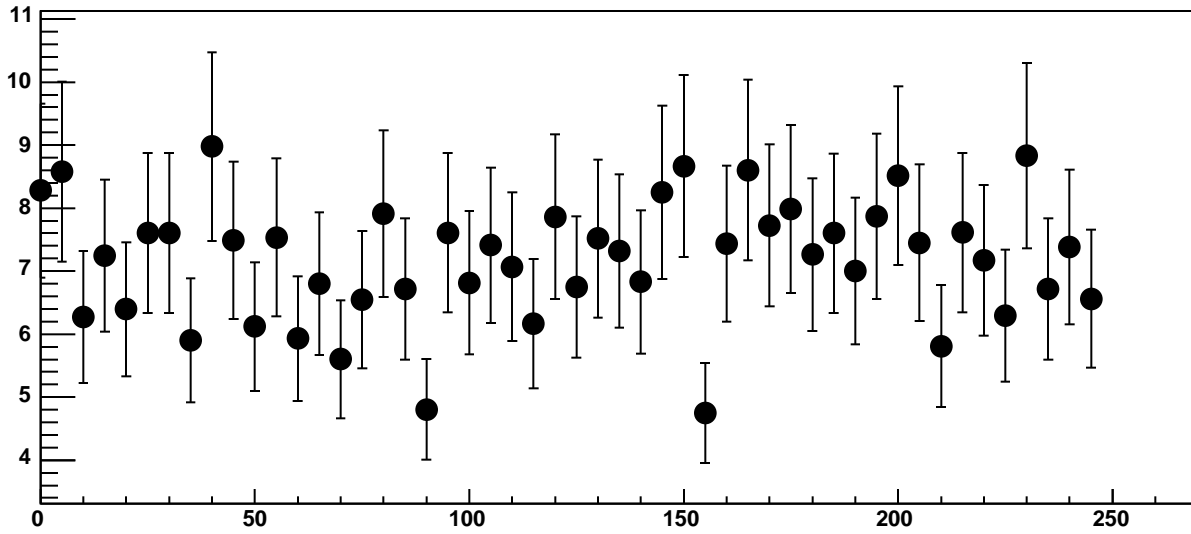


Chip 1, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

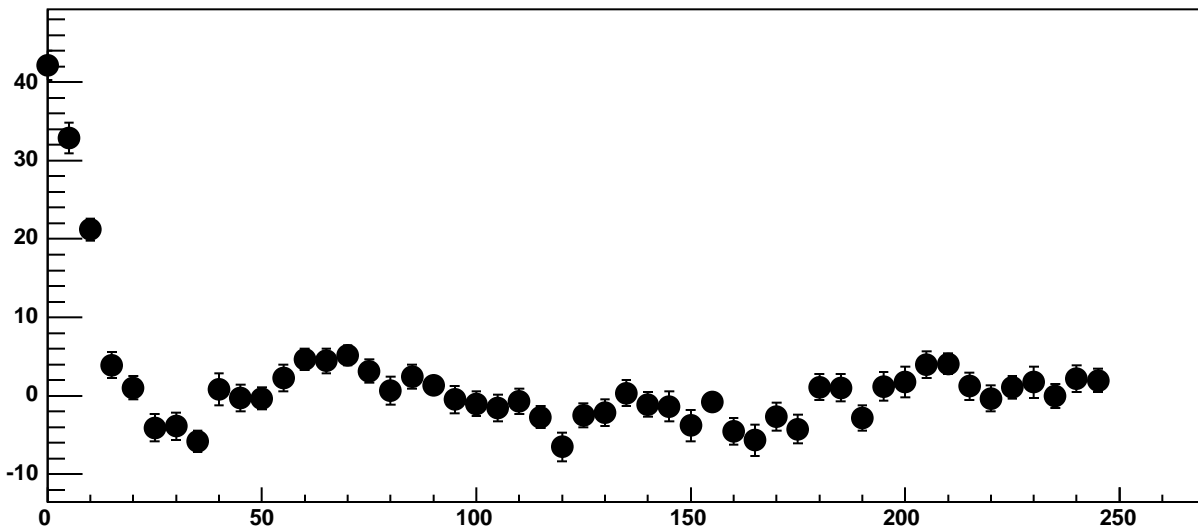


$\chi^2 / \text{ndf}$	156 / 41
p0	$368.8 \pm 0.5405$
p1	$8.02 \pm 0.3328$
p2	$145.4 \pm 0.5765$
p3	$20.3 \pm 0.5739$
p4	$-0.08171 \pm 0.006282$

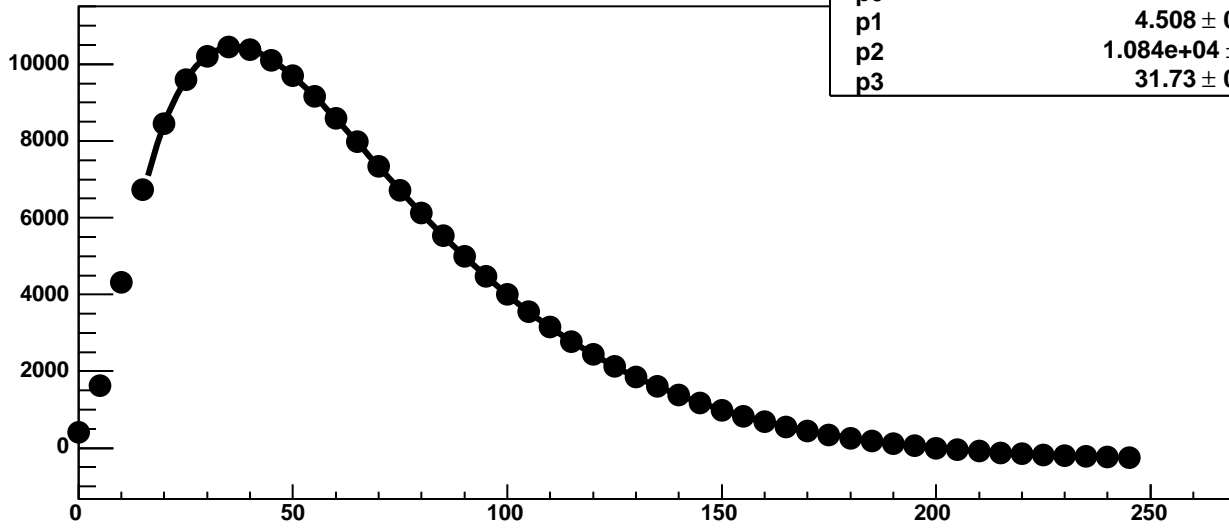
Chip 1, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

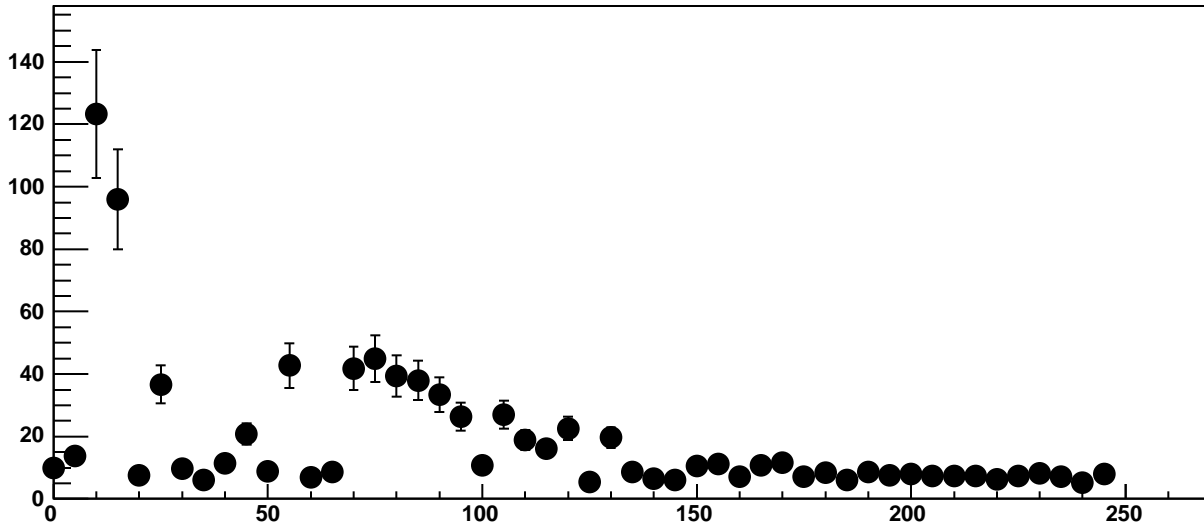


Chip 1, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold

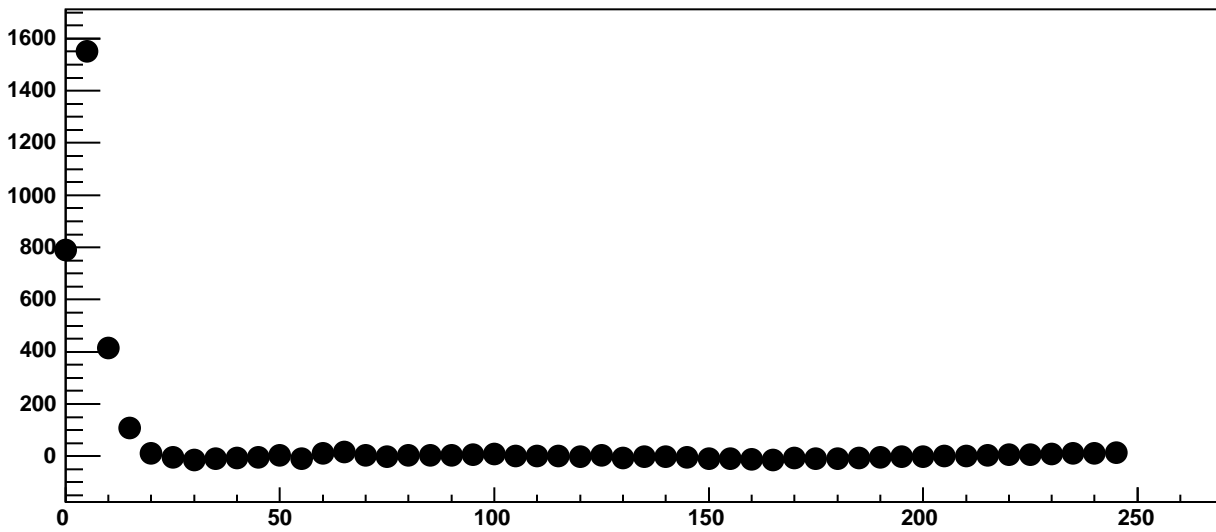


$\chi^2 / \text{ndf}$	690 / 42
p0	$-379.5 \pm 0.5952$
p1	$4.508 \pm 0.007971$
p2	$1.084\text{e}+04 \pm 0.9023$
p3	$31.73 \pm 0.005901$

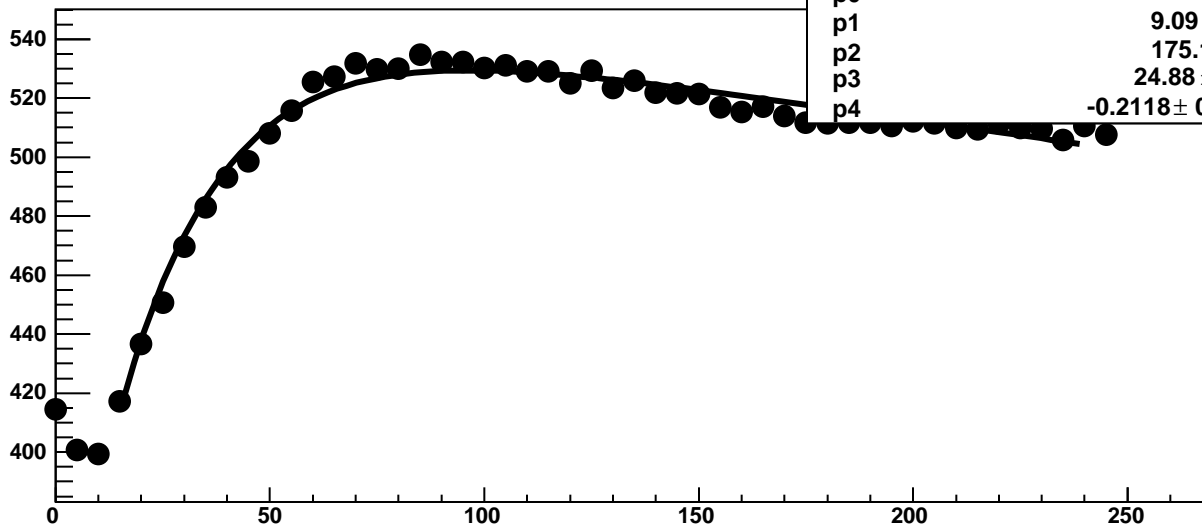
Chip 1, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold

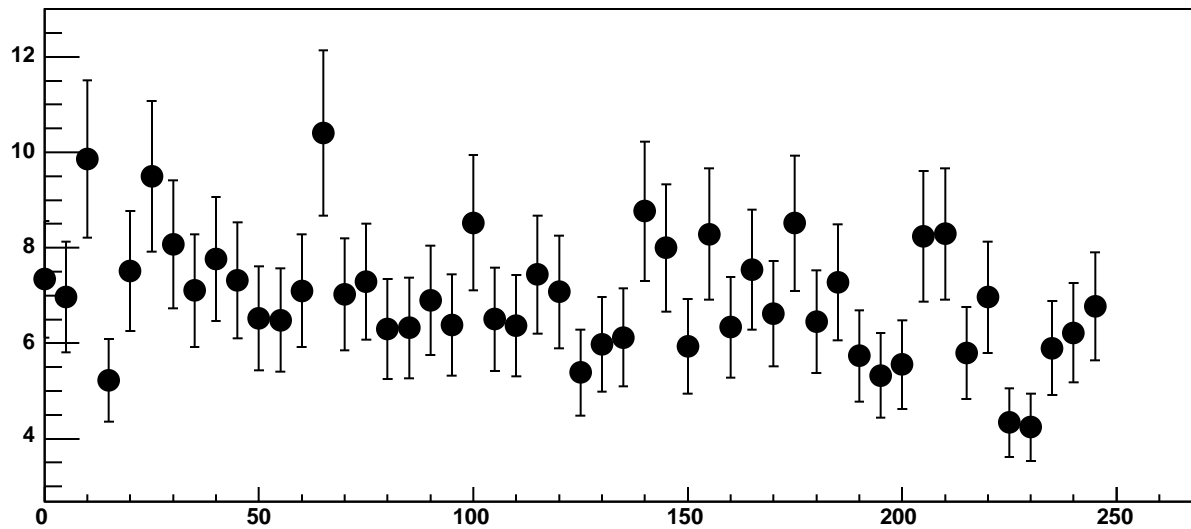


Chip 1, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold

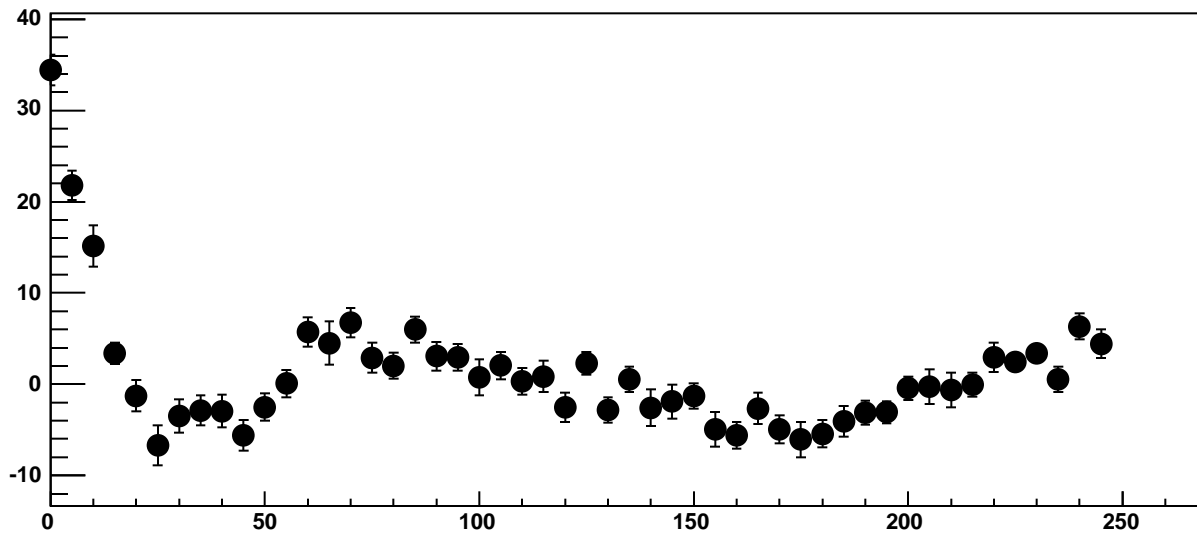


$\chi^2 / \text{ndf}$	237.9 / 41
p0	$378.1 \pm 2.837$
p1	$9.09 \pm 0.4682$
p2	$175.1 \pm 2.932$
p3	$24.88 \pm 0.4956$
p4	$-0.2118 \pm 0.006256$

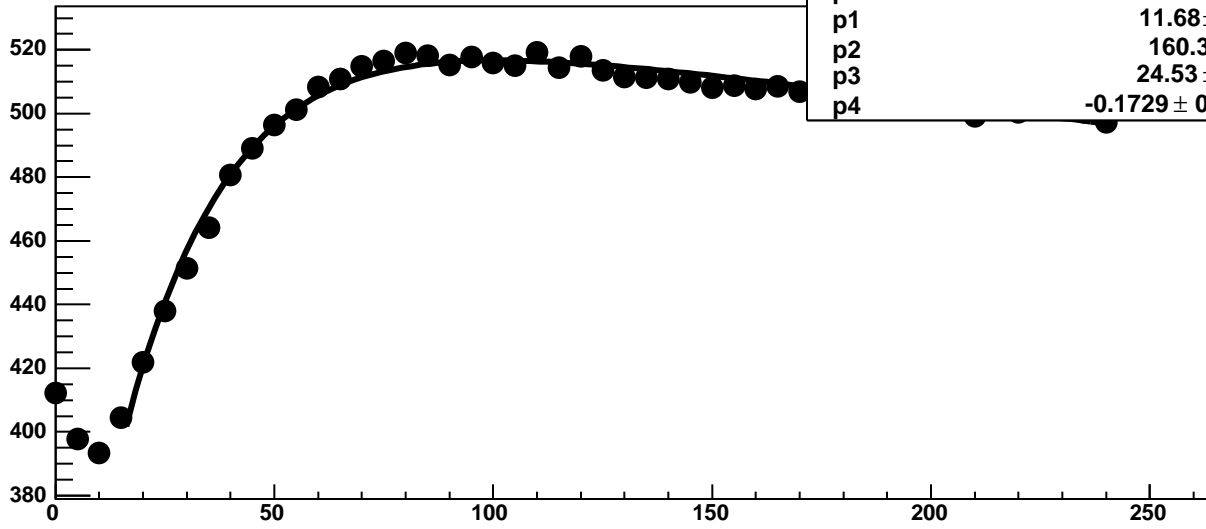
Chip 1, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

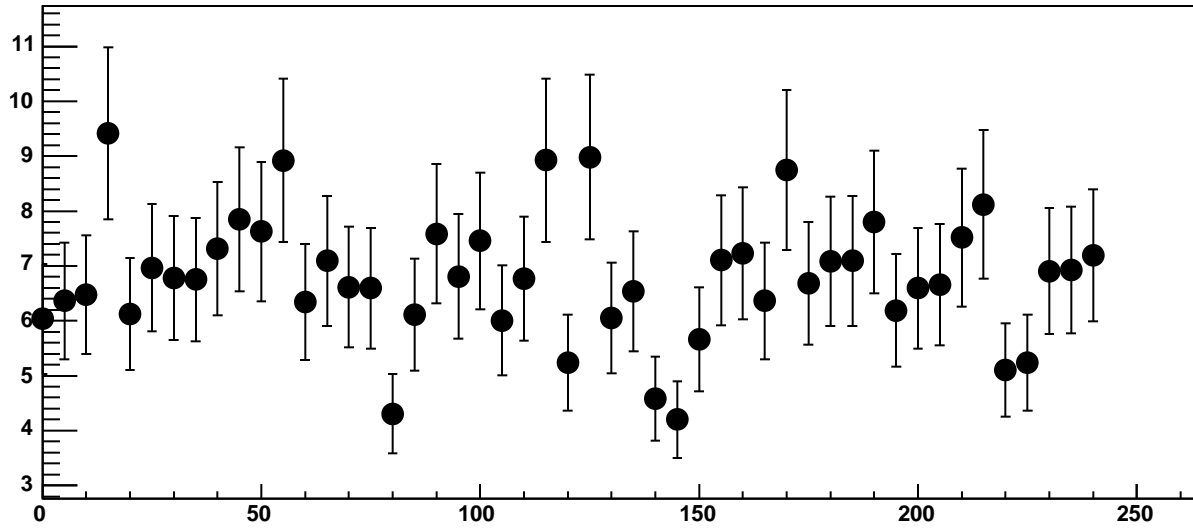


Chip 1, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

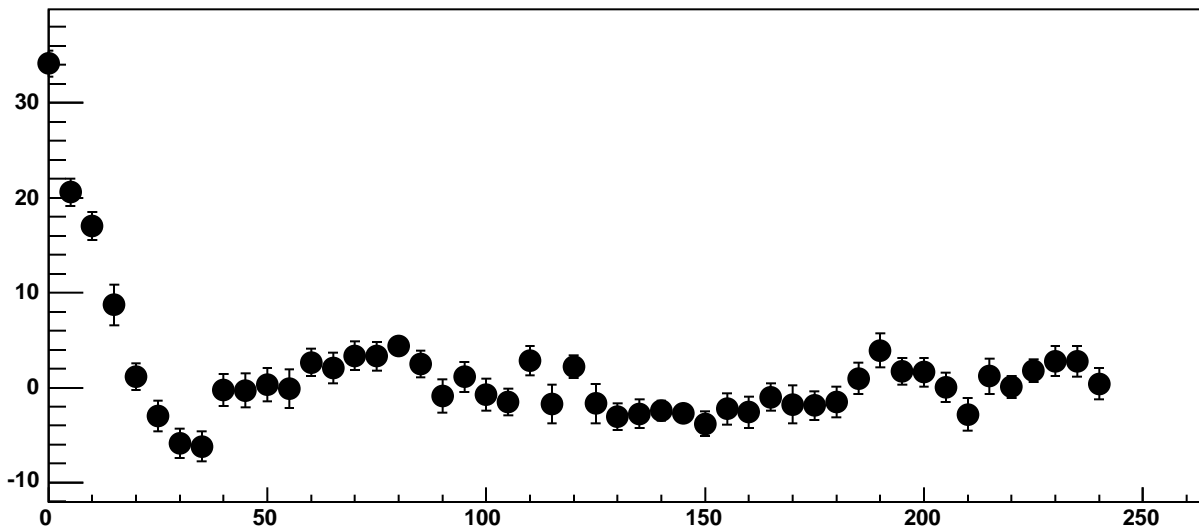


$\chi^2 / \text{ndf}$	156 / 41
p0	$376.1 \pm 2.799$
p1	$11.68 \pm 0.5033$
p2	$160.3 \pm 2.884$
p3	$24.53 \pm 0.5348$
p4	$-0.1729 \pm 0.006619$

Chip 1, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold

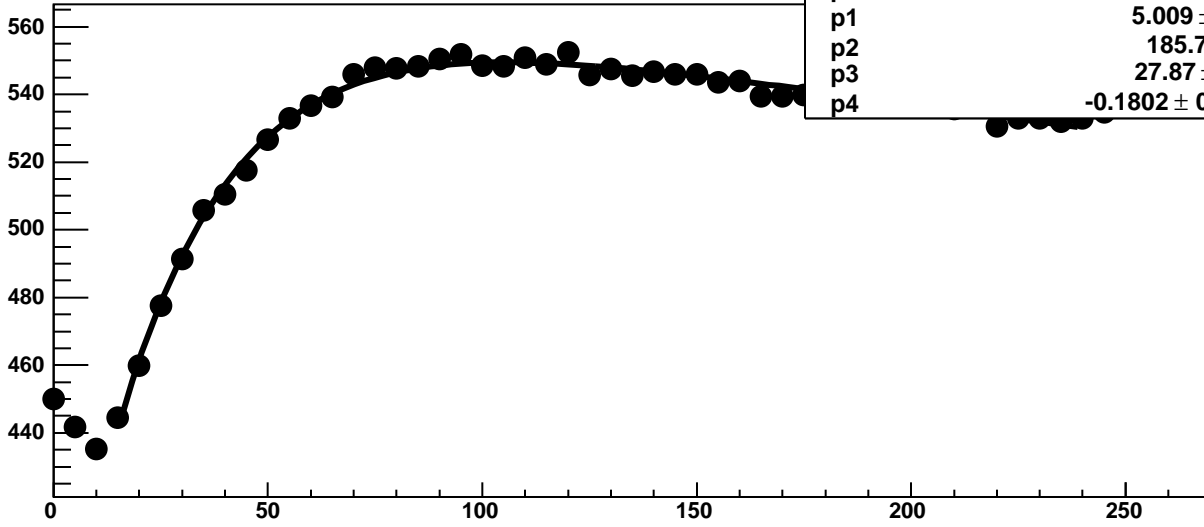


Chip 1, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold



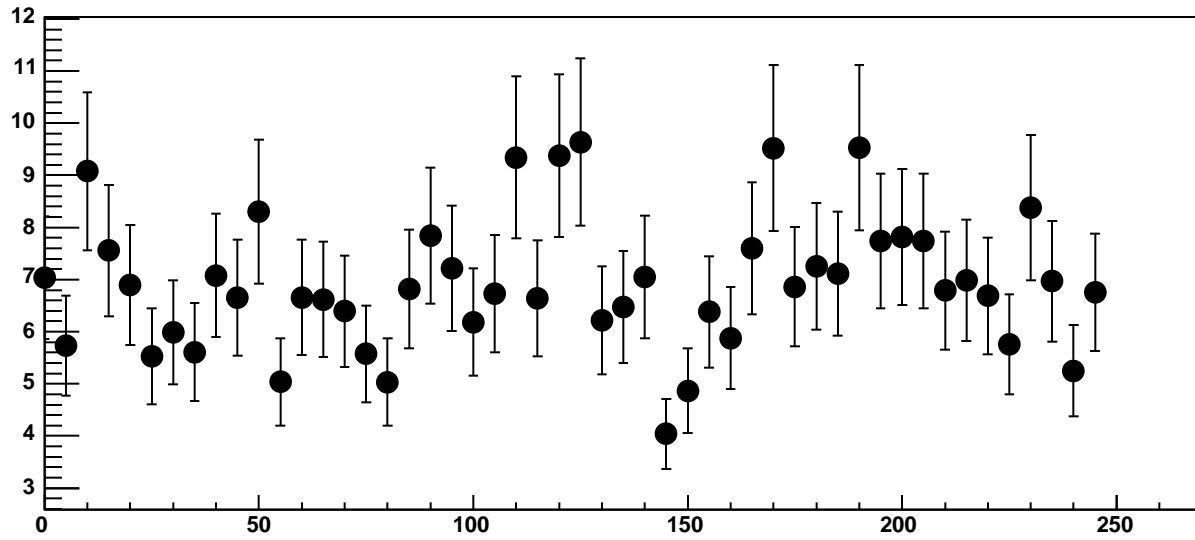


Chip 1, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold

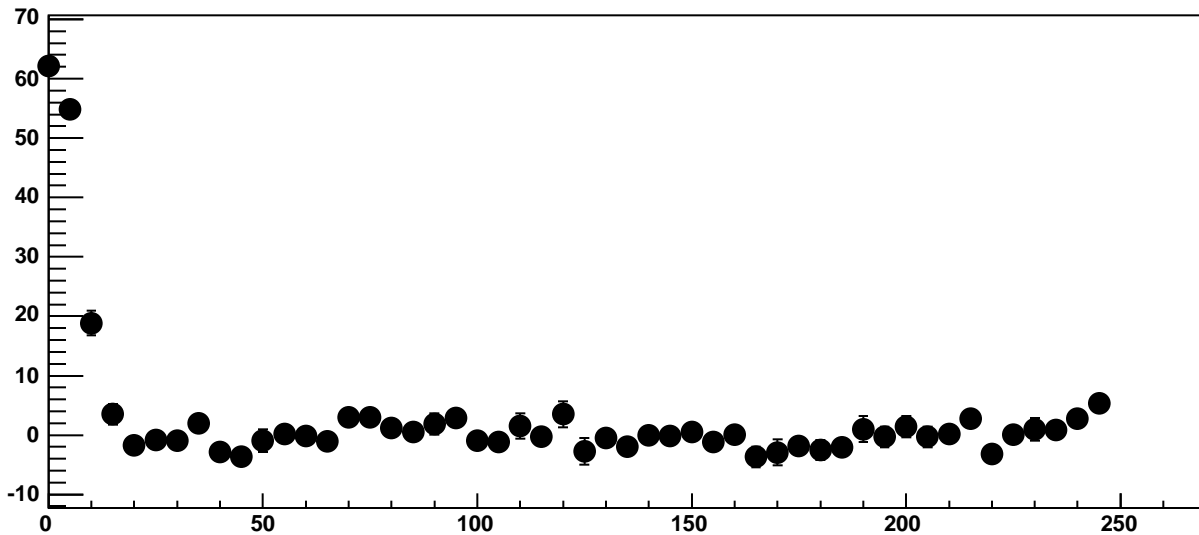


$\chi^2 / \text{ndf}$	66.88 / 41
p0	$386.9 \pm 2.811$
p1	$5.009 \pm 0.5536$
p2	$185.7 \pm 2.902$
p3	$27.87 \pm 0.6508$
p4	$-0.1802 \pm 0.007492$

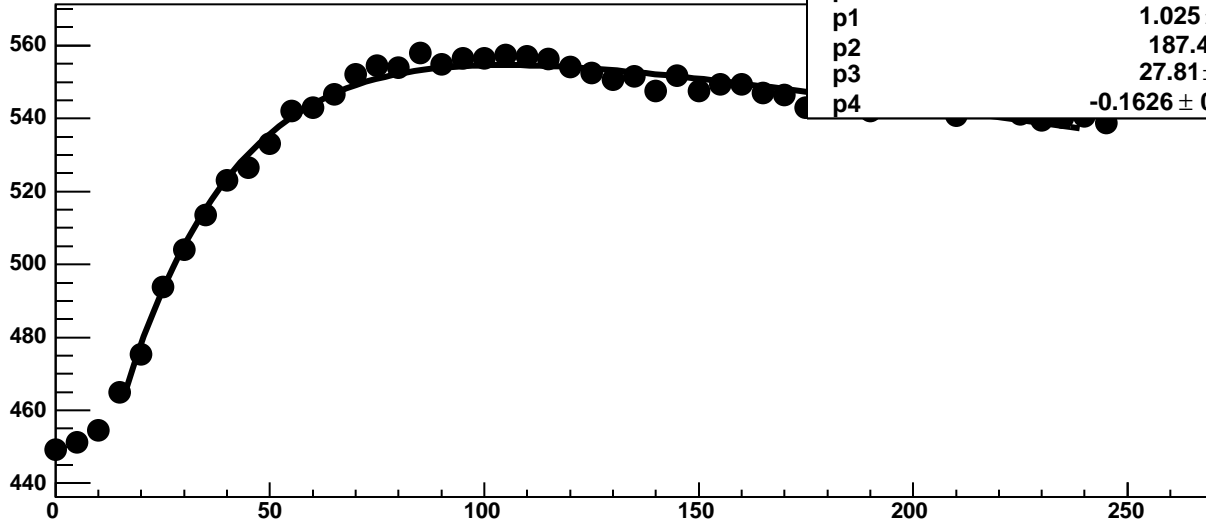
Chip 1, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

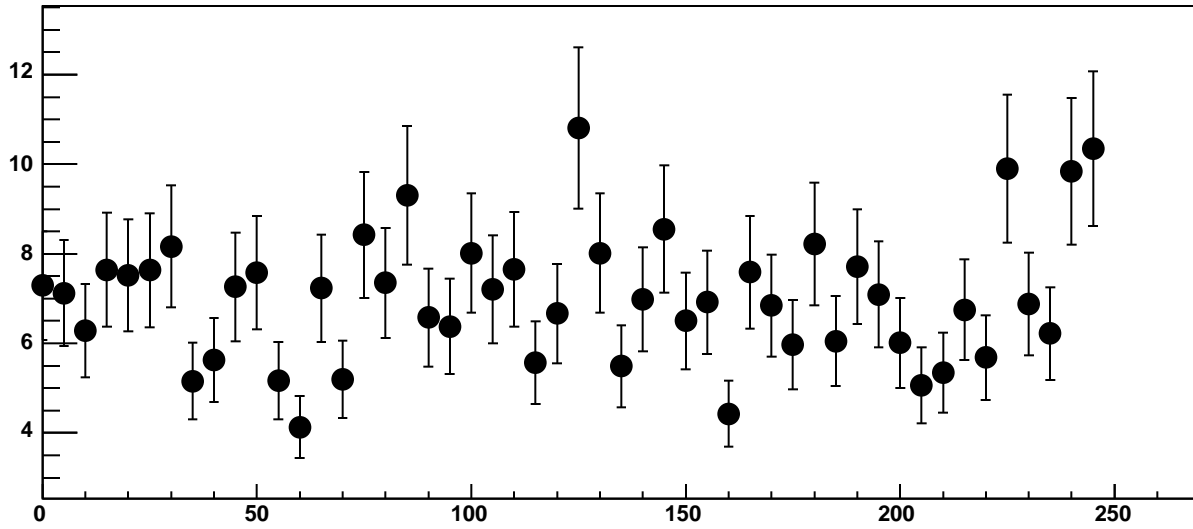


Chip 1, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold

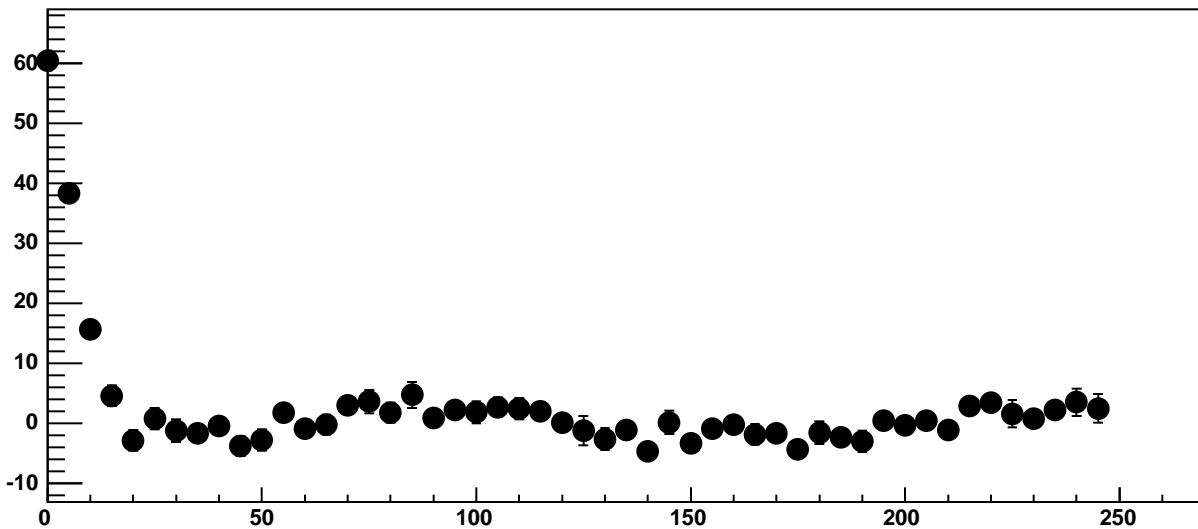


$\chi^2 / \text{ndf}$	102.4 / 41
p0	$388.6 \pm 2.804$
p1	$1.025 \pm 0.6401$
p2	$187.4 \pm 2.883$
p3	$27.81 \pm 0.7158$
p4	$-0.1626 \pm 0.007591$

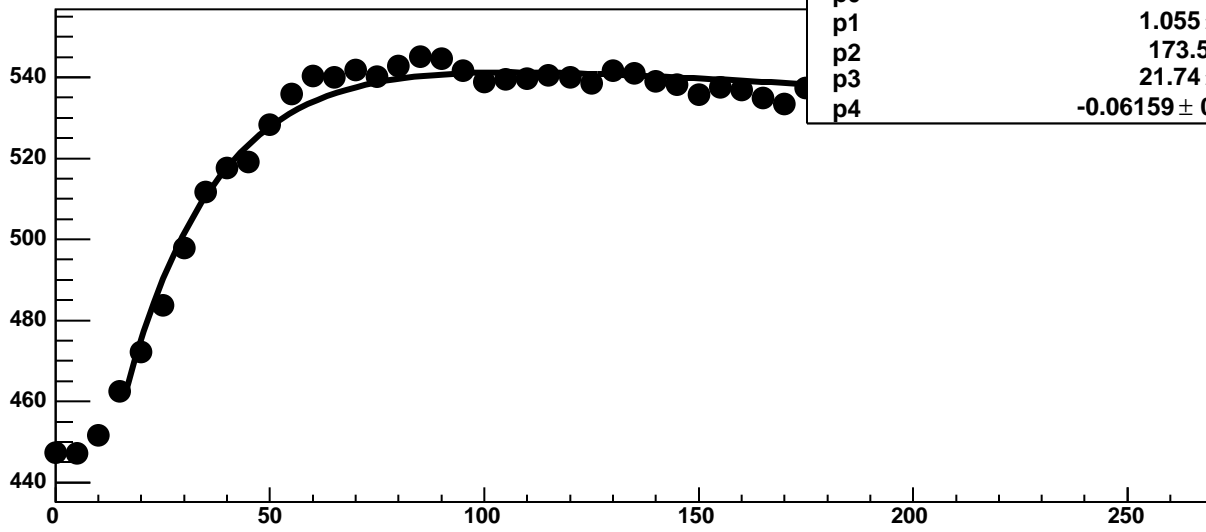
Chip 1, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold

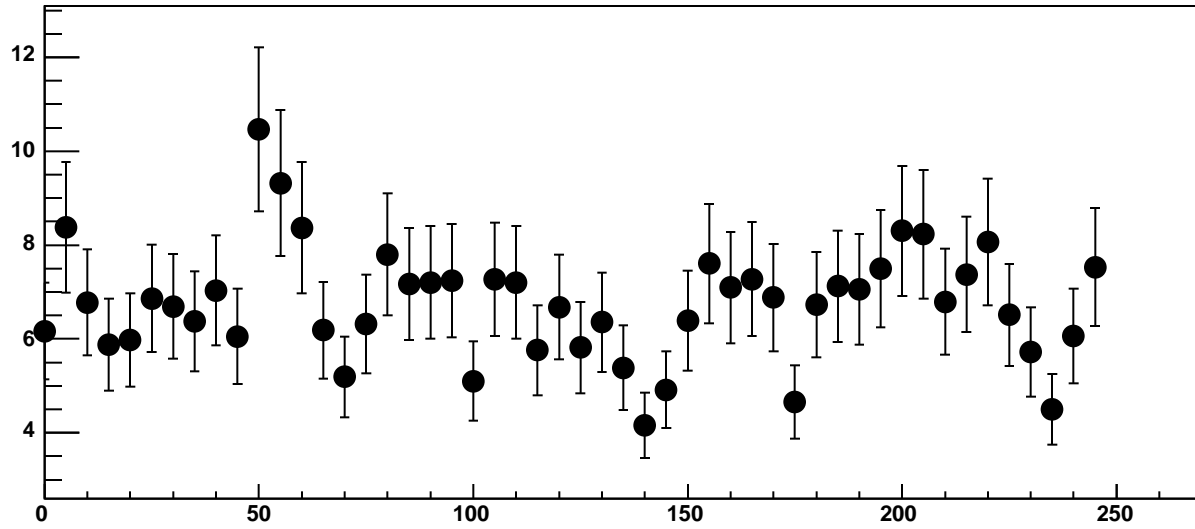


Chip 1, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

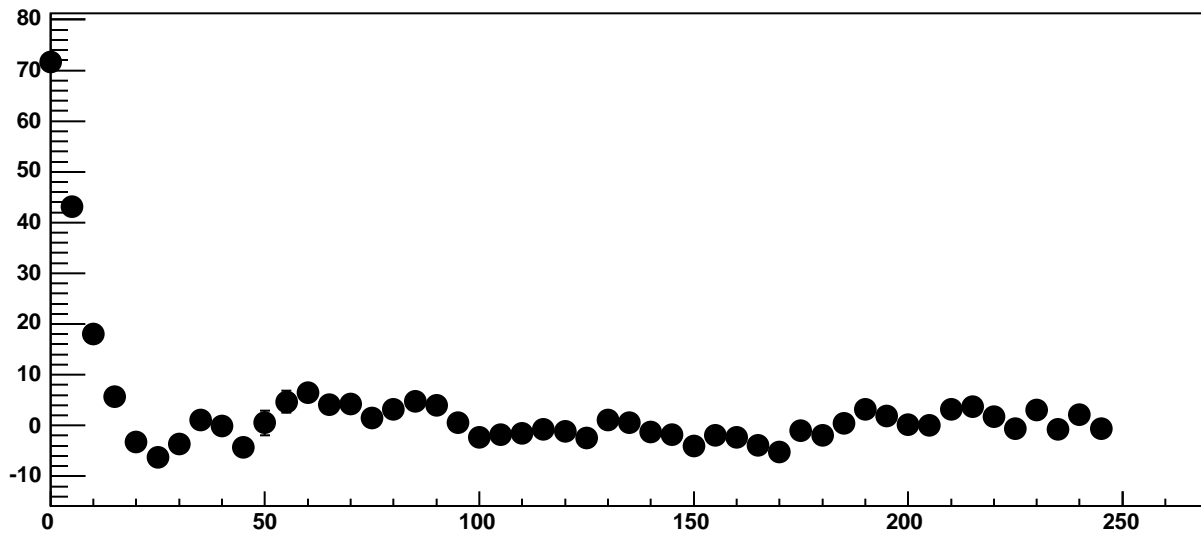


$\chi^2 / \text{ndf}$	179 / 41
p0	$375.6 \pm 2.74$
p1	$1.055 \pm 0.5951$
p2	$173.5 \pm 2.765$
p3	$21.74 \pm 0.6021$
p4	$-0.06159 \pm 0.005641$

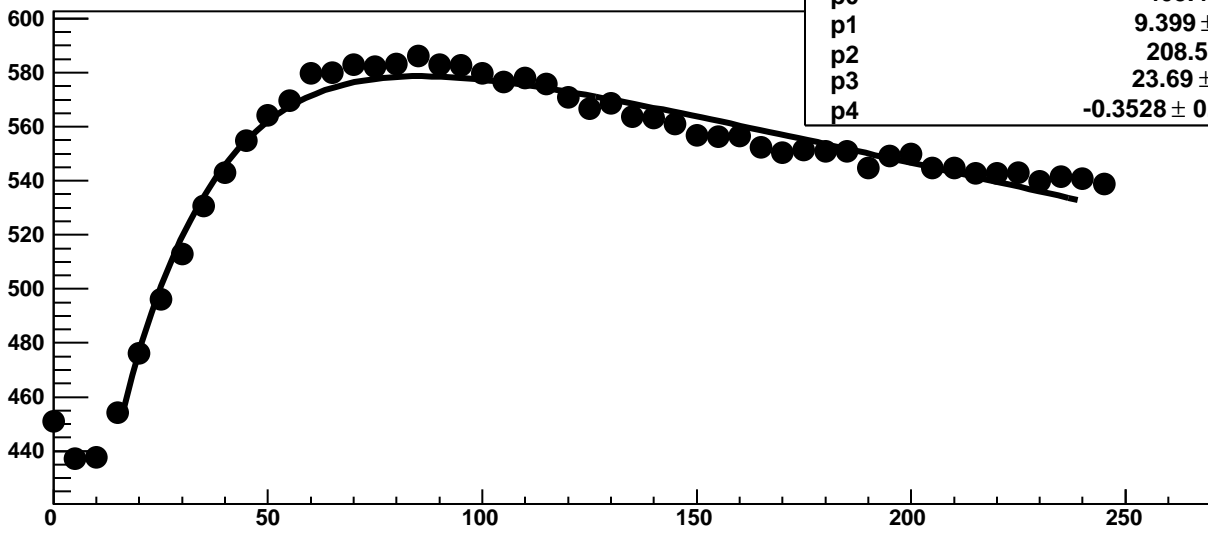
Chip 1, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



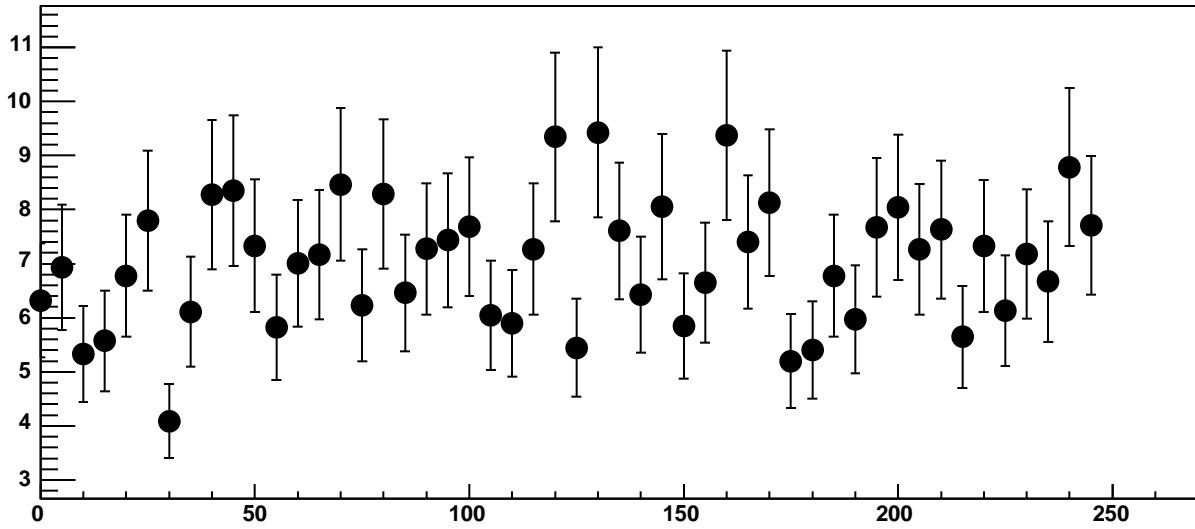
Chip 1, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold



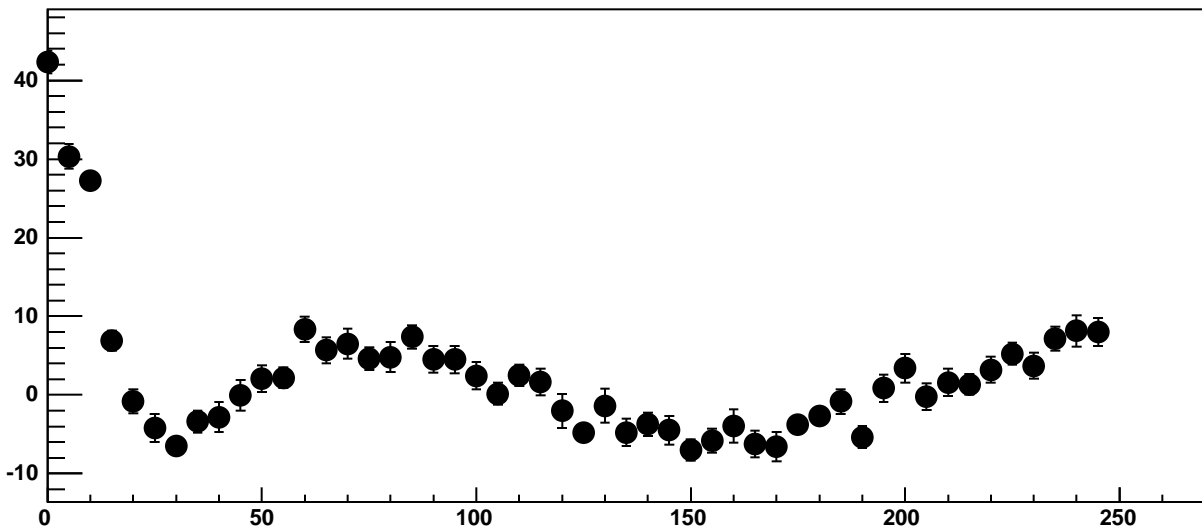
Chip 1, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold



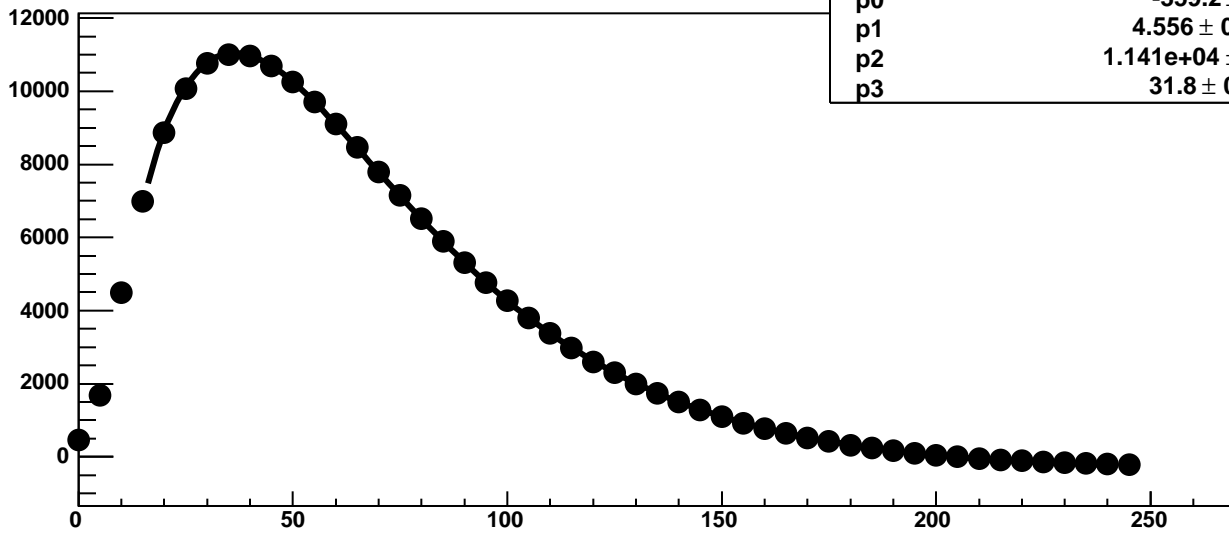
Chip 1, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

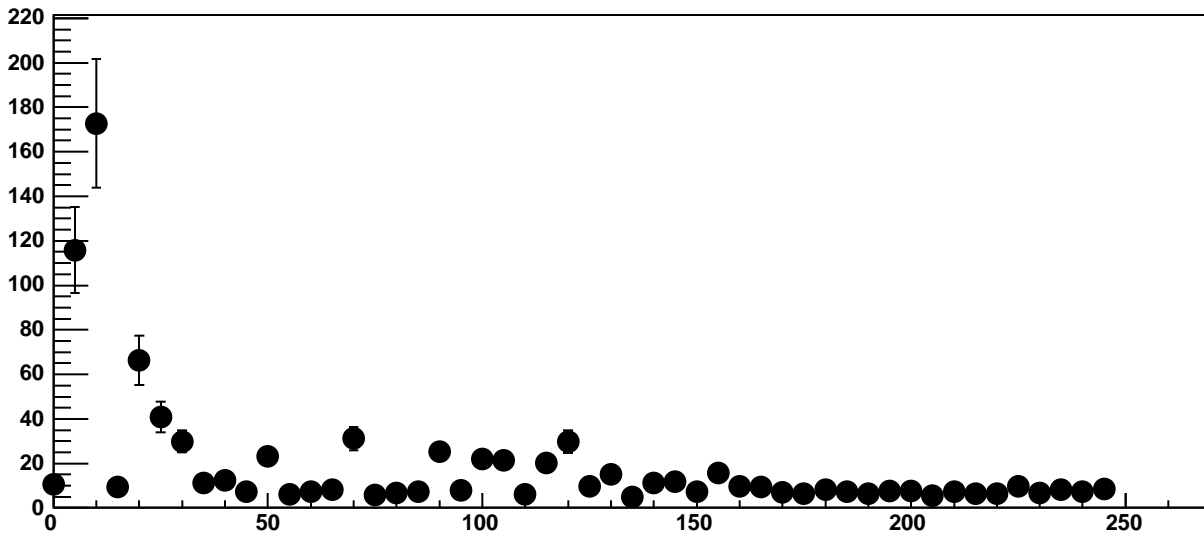


Chip 1, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold

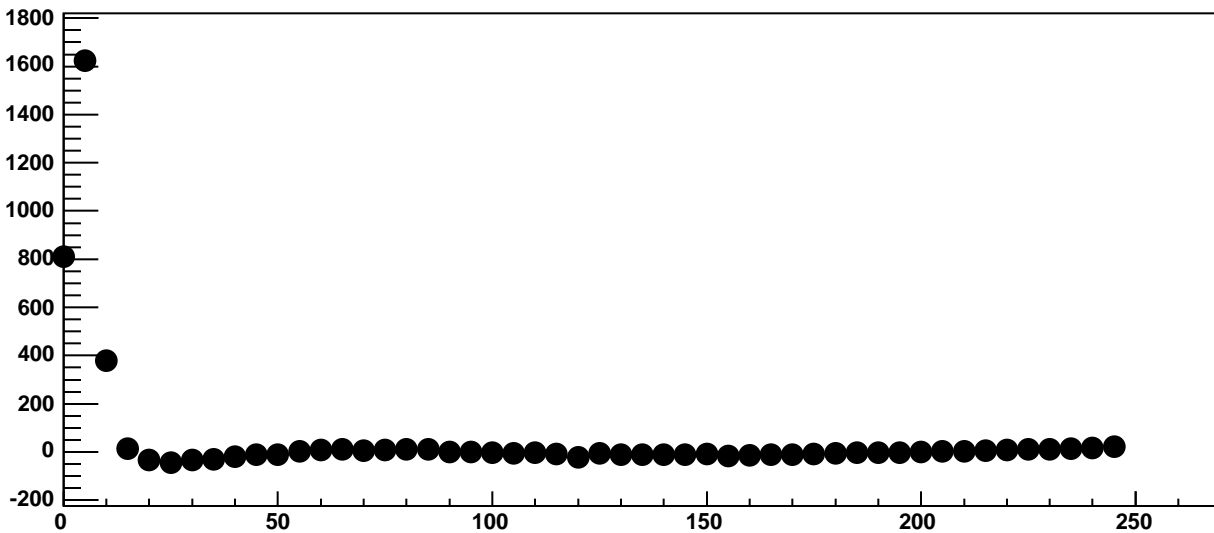


$\chi^2 / \text{ndf}$	1141 / 42
p0	-359.2 ± 0.5573
p1	4.556 ± 0.005542
p2	1.141e+04 ± 0.9649
p3	31.8 ± 0.004641

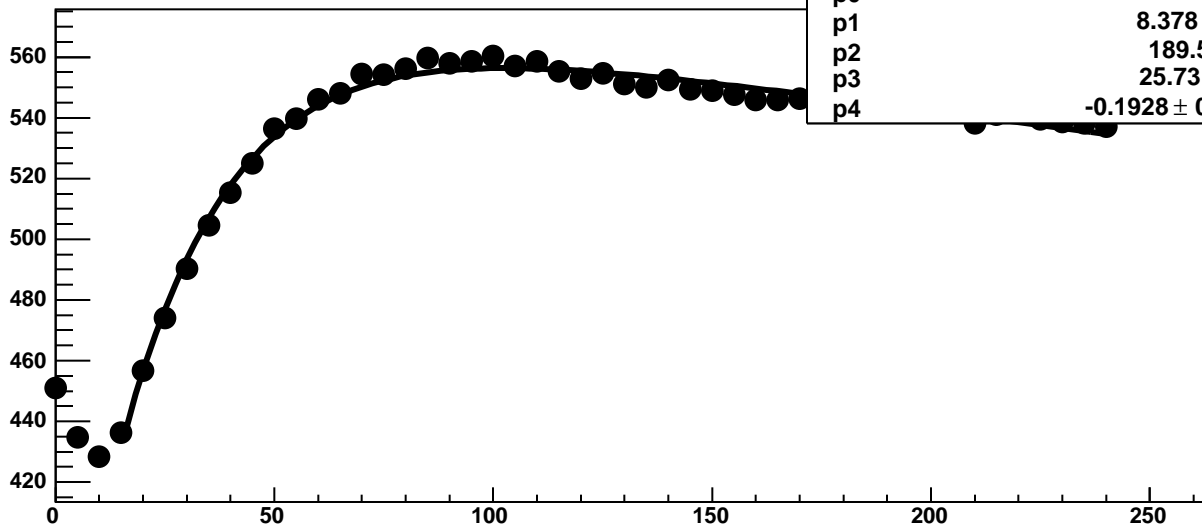
Chip 1, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold

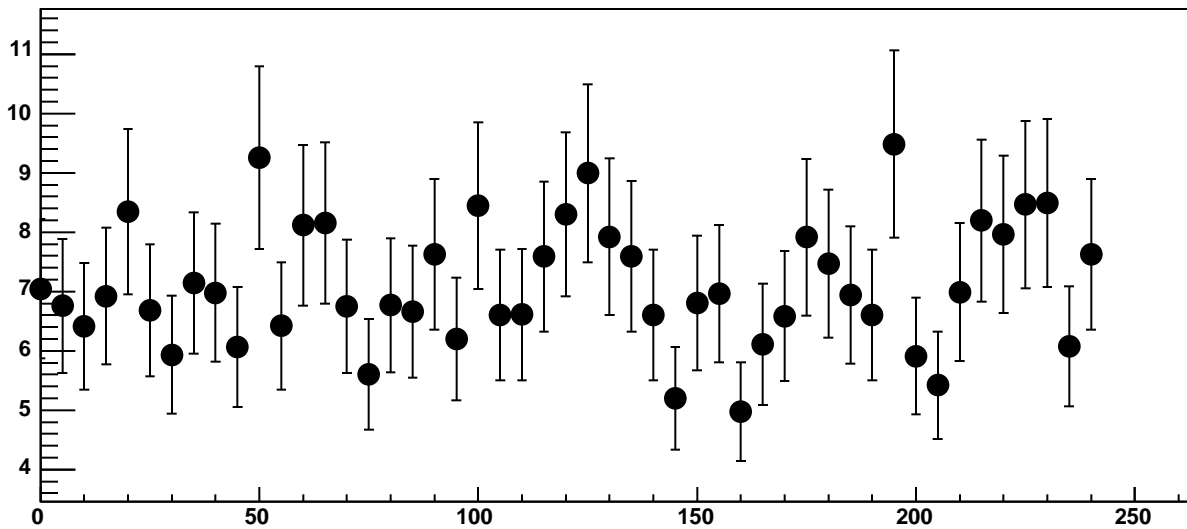


Chip 1, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold

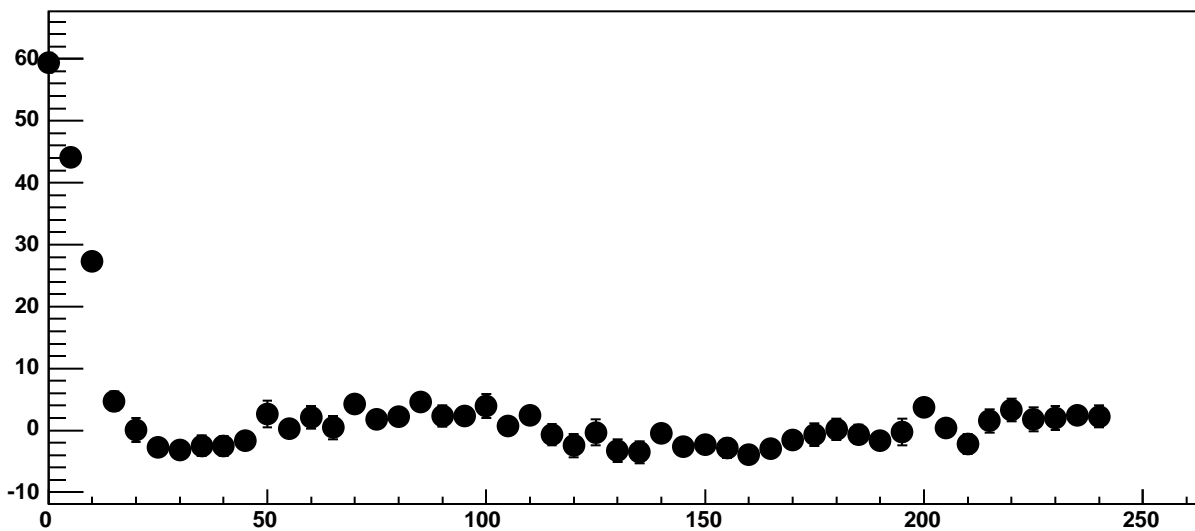


$\chi^2 / \text{ndf}$	116.9 / 41
p0	$389.9 \pm 2.973$
p1	$8.378 \pm 0.4878$
p2	$189.5 \pm 3.065$
p3	$25.73 \pm 0.5331$
p4	$-0.1928 \pm 0.007473$

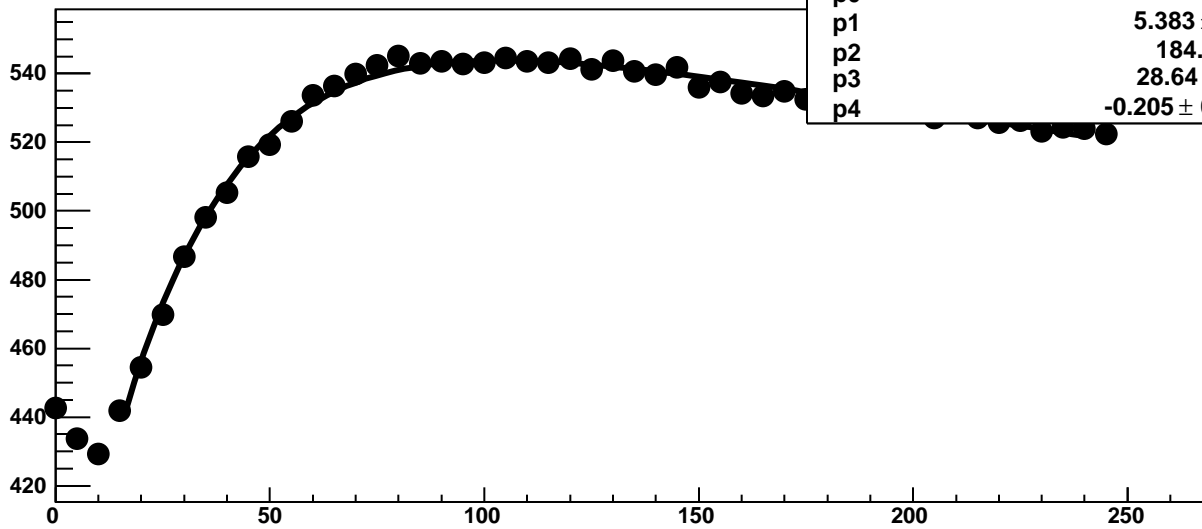
Chip 1, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold

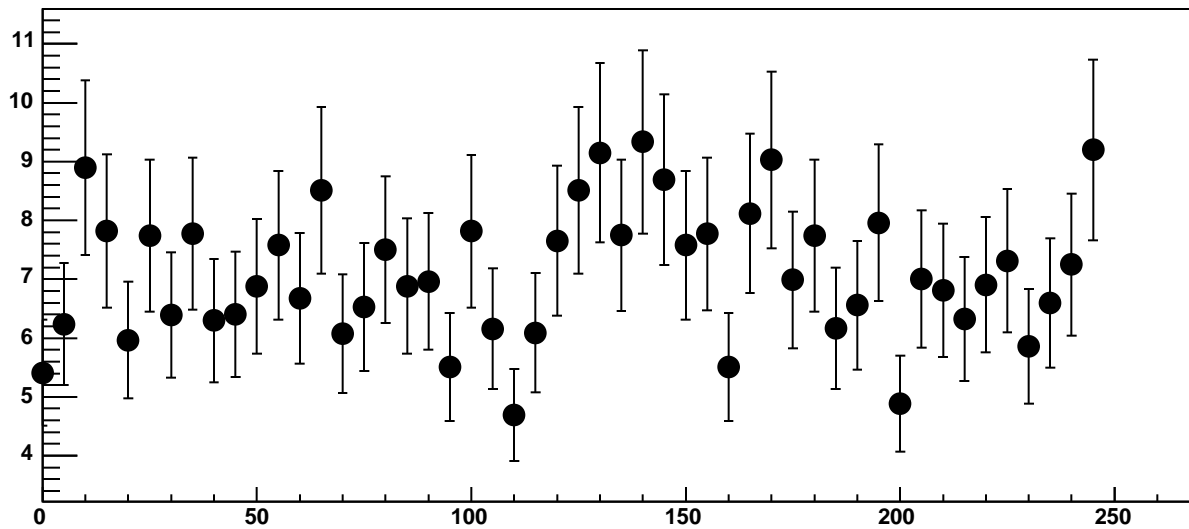


Chip 1, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

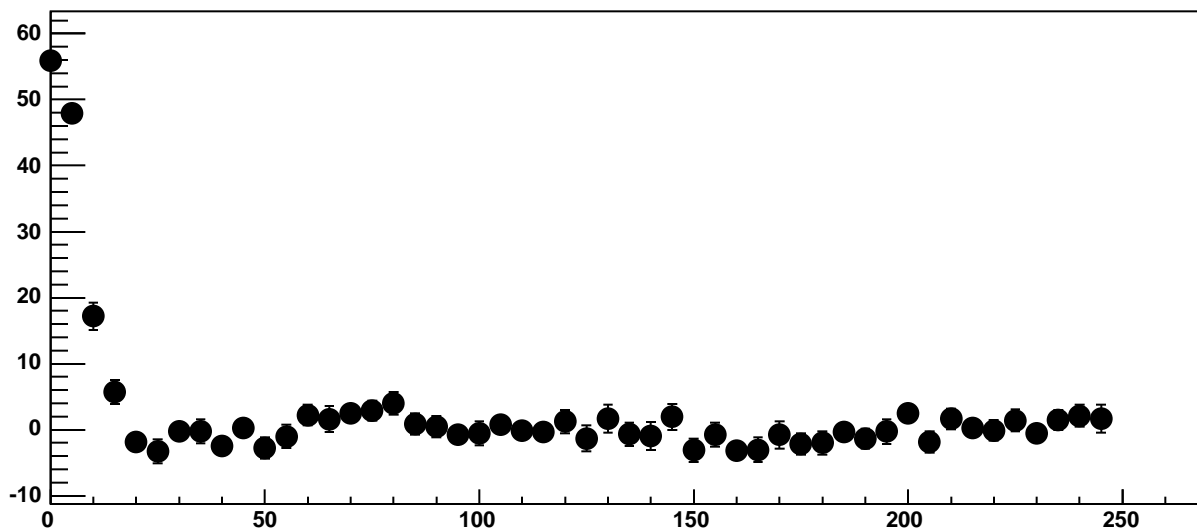


$\chi^2 / \text{ndf}$	68.1 / 41
p0	$385.6 \pm 2.94$
p1	$5.383 \pm 0.5825$
p2	$184.3 \pm 3.05$
p3	$28.64 \pm 0.7012$
p4	$-0.205 \pm 0.007871$

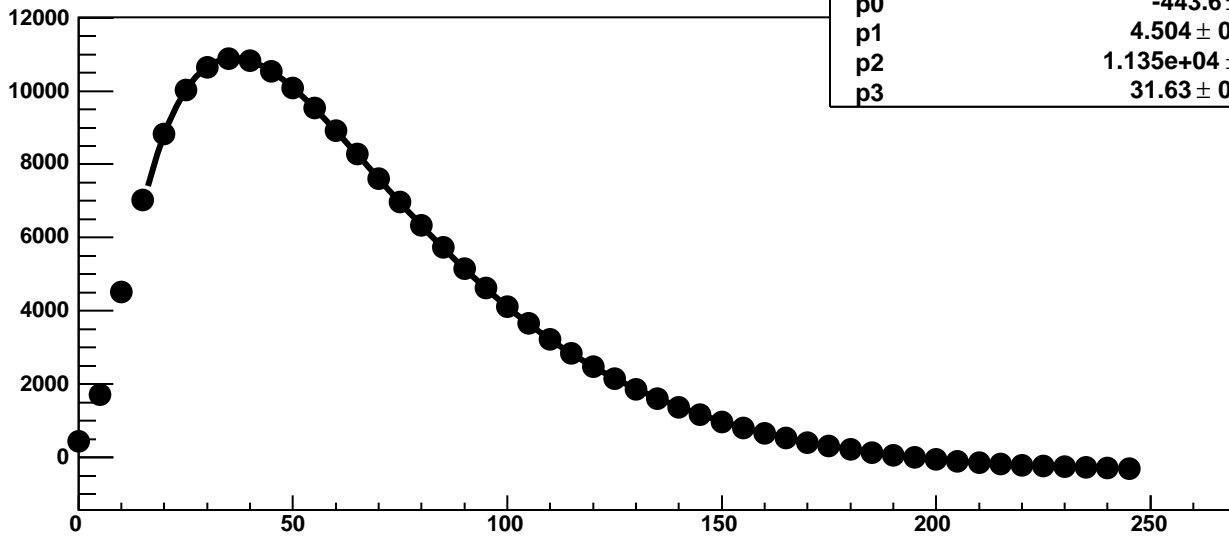
Chip 1, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

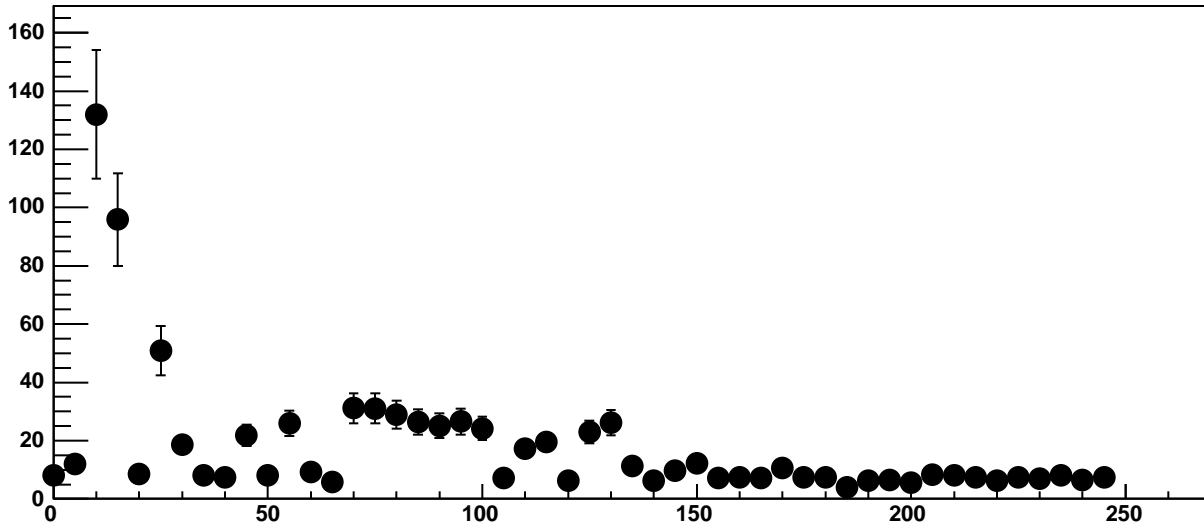


Chip 1, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold

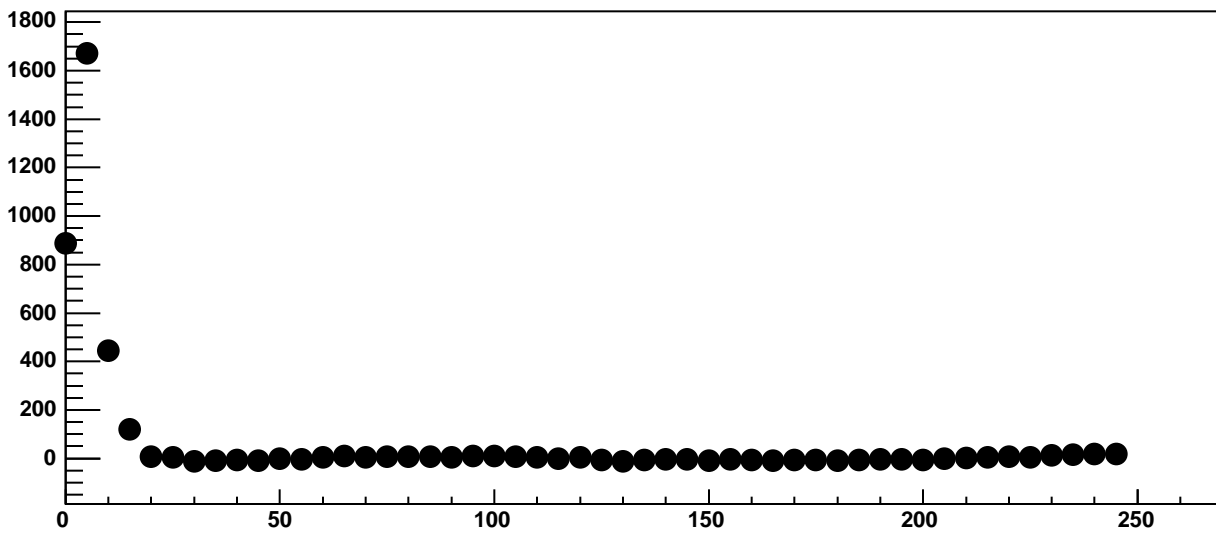


$\chi^2 / \text{ndf}$	727.1 / 42
p0	-443.6 ± 0.5722
p1	4.504 ± 0.008045
p2	1.135e+04 ± 0.9199
p3	31.63 ± 0.005709

Chip 1, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold

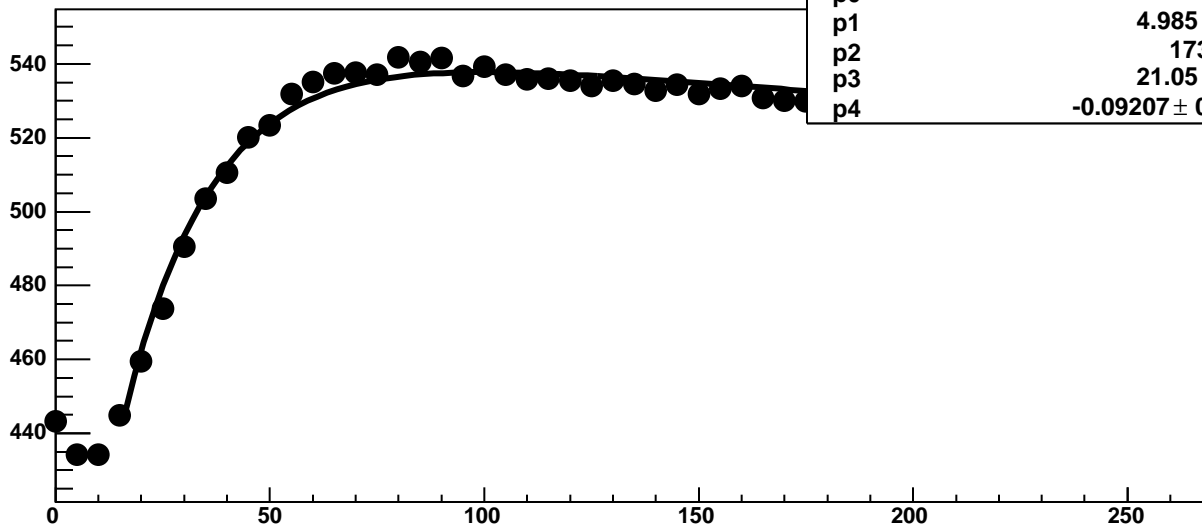


Chip 1, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold



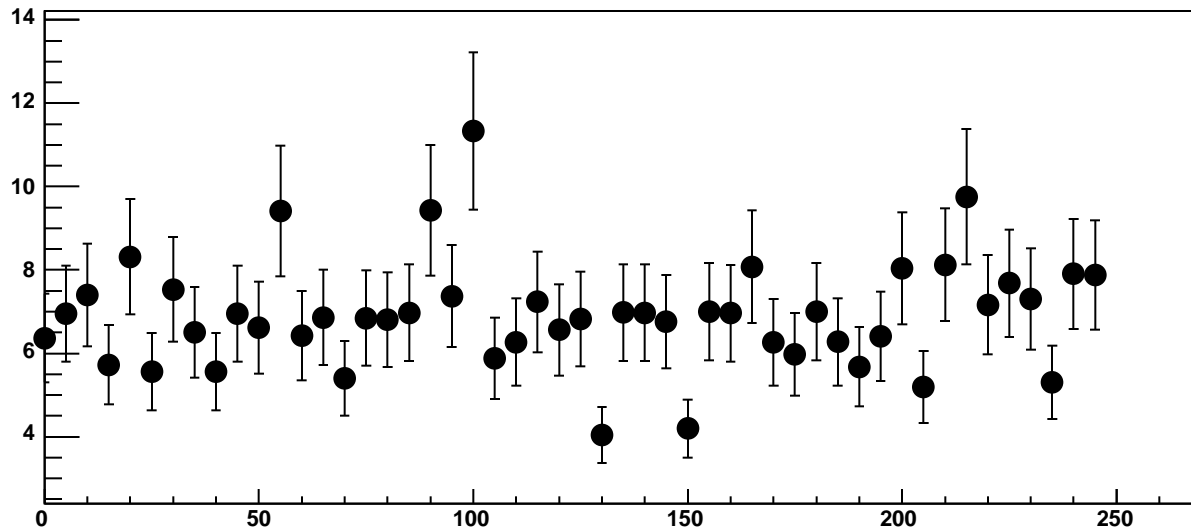


Chip 1, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

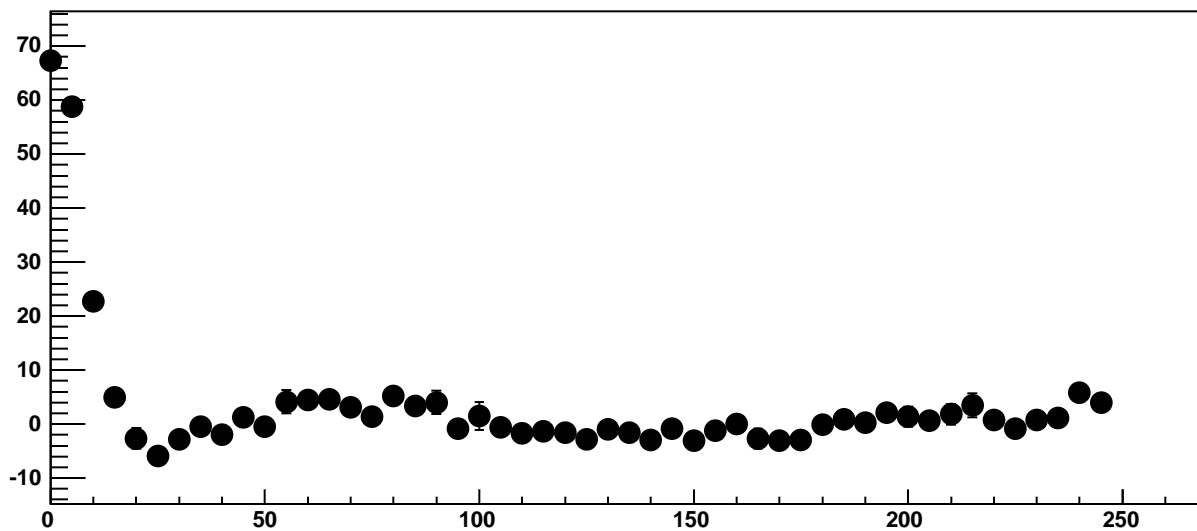


$\chi^2 / \text{ndf}$	143.8 / 41
p0	$375.4 \pm 2.811$
p1	$4.985 \pm 0.4837$
p2	$173 \pm 2.848$
p3	$21.05 \pm 0.4997$
p4	$-0.09207 \pm 0.006023$

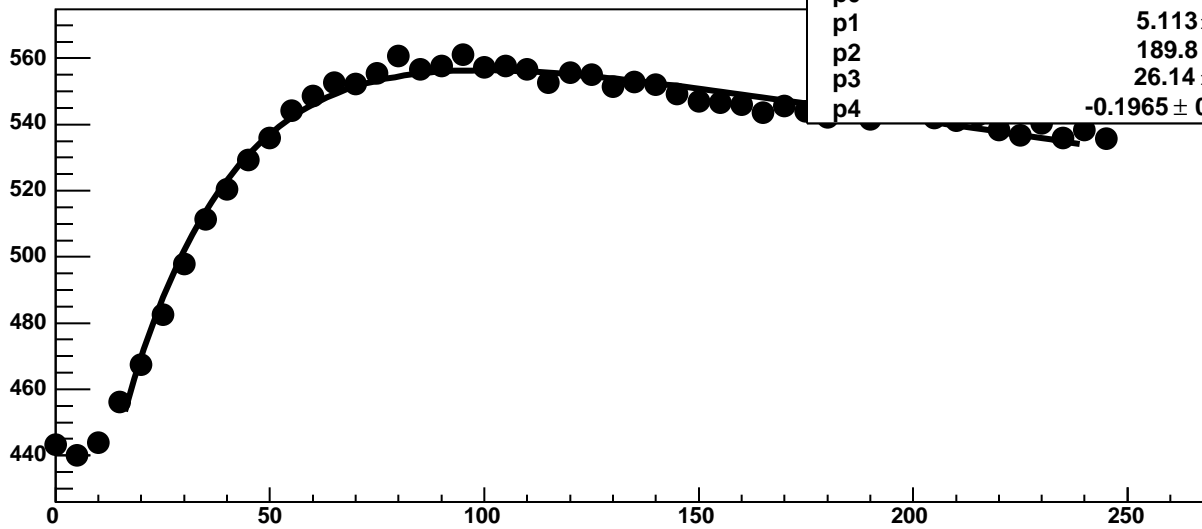
Chip 1, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

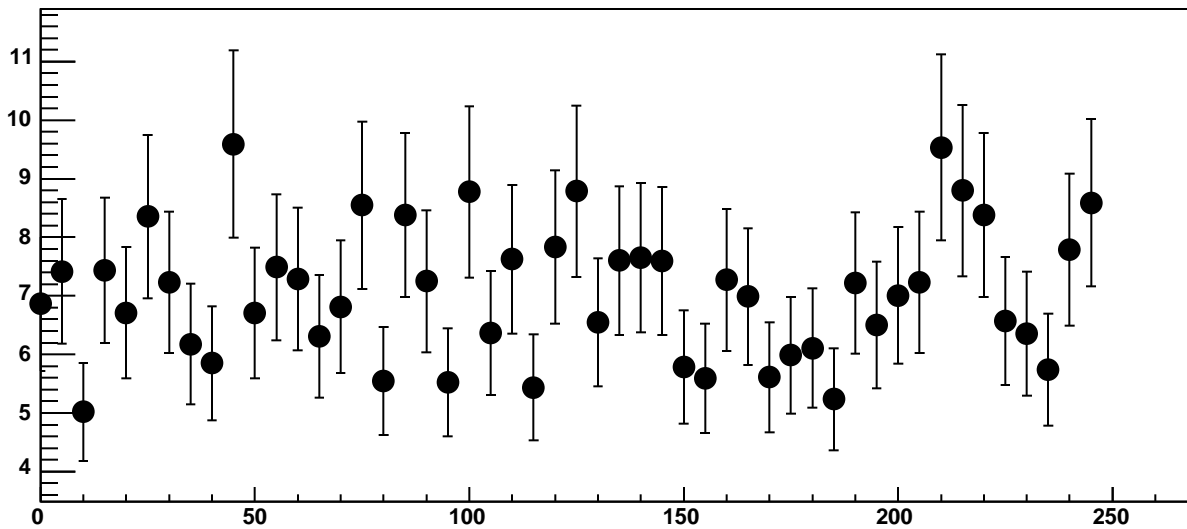


Chip 1, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

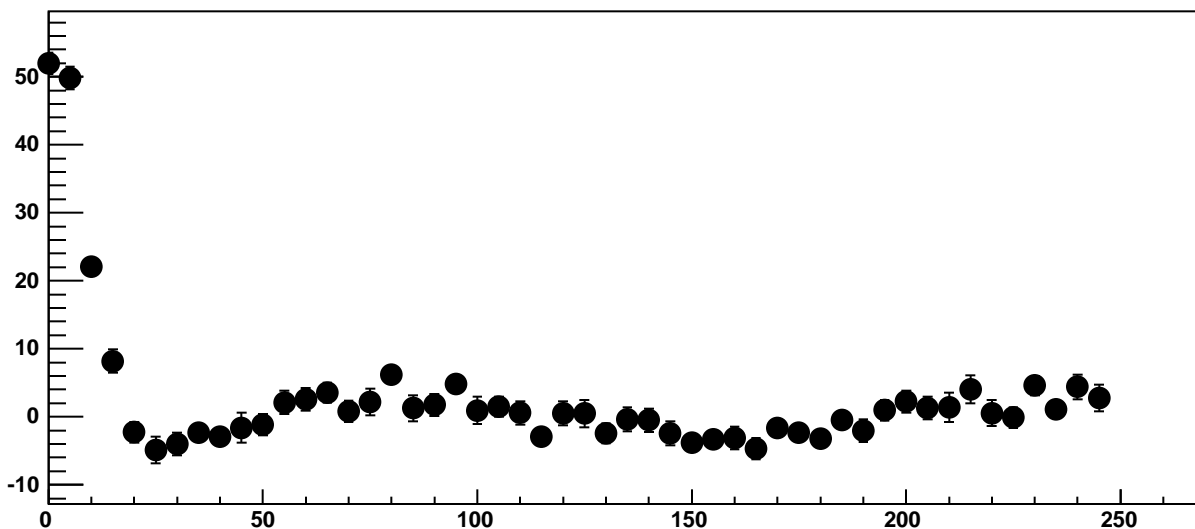


$\chi^2 / \text{ndf}$	171.7 / 41
p0	$390.3 \pm 0.6882$
p1	$5.113 \pm 0.3645$
p2	$189.8 \pm 0.7447$
p3	$26.14 \pm 0.6286$
p4	$-0.1965 \pm 0.007787$

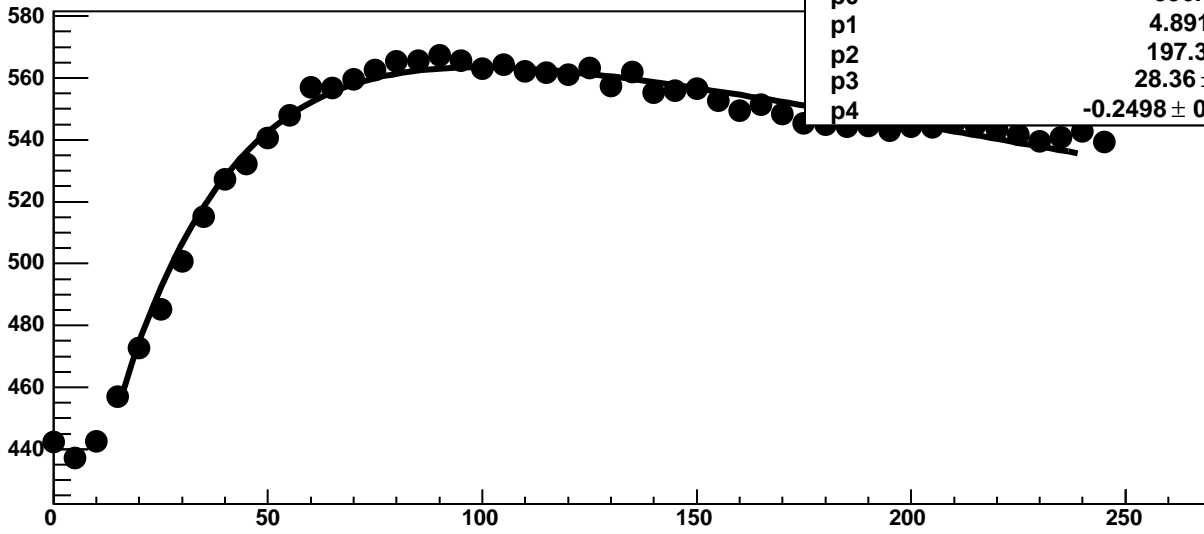
Chip 1, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



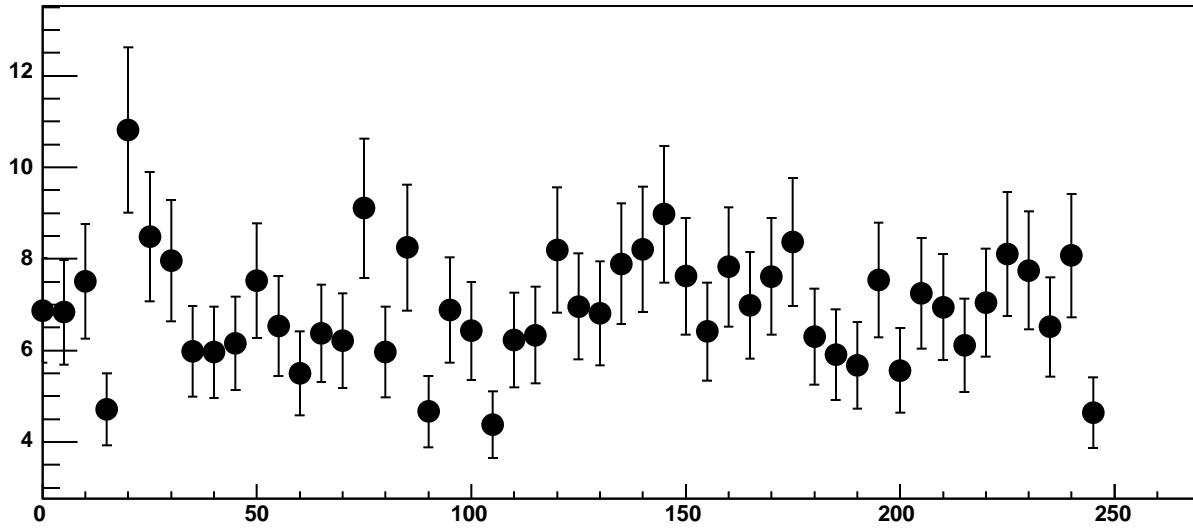
Chip 1, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold



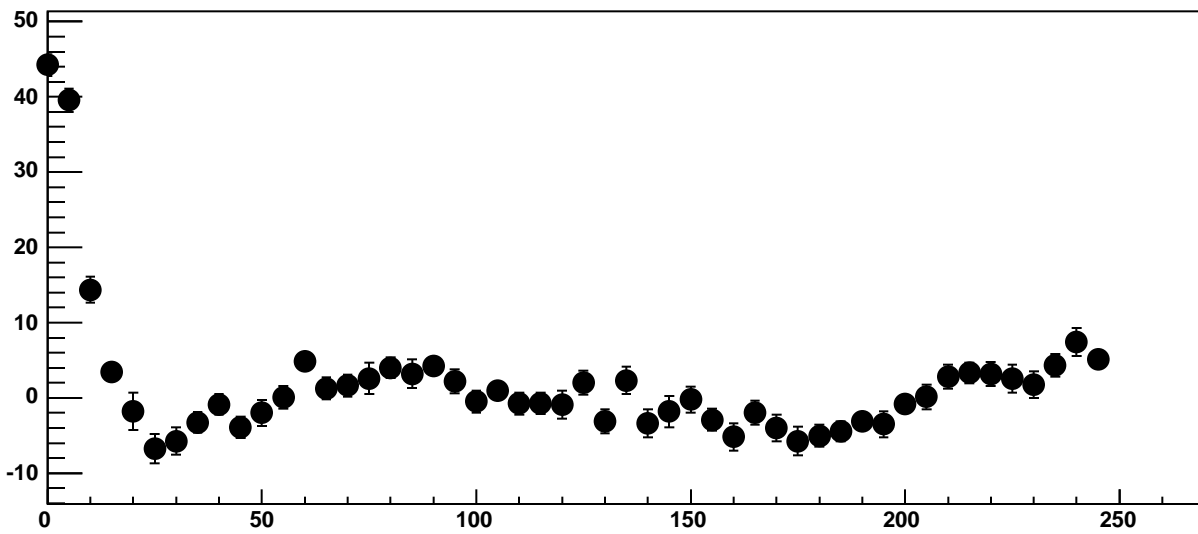
Chip 1, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold



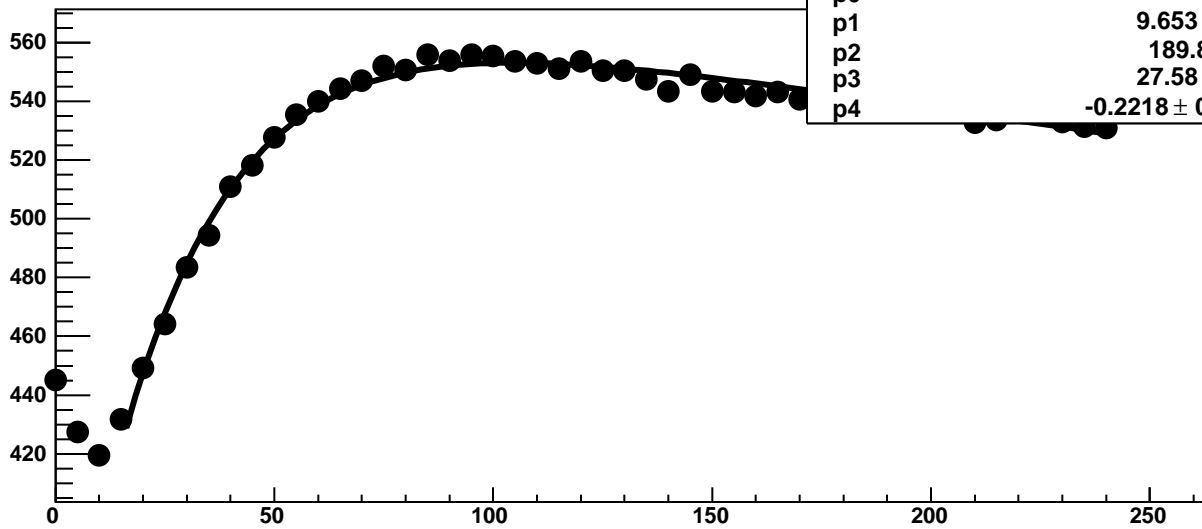
Chip 1, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold

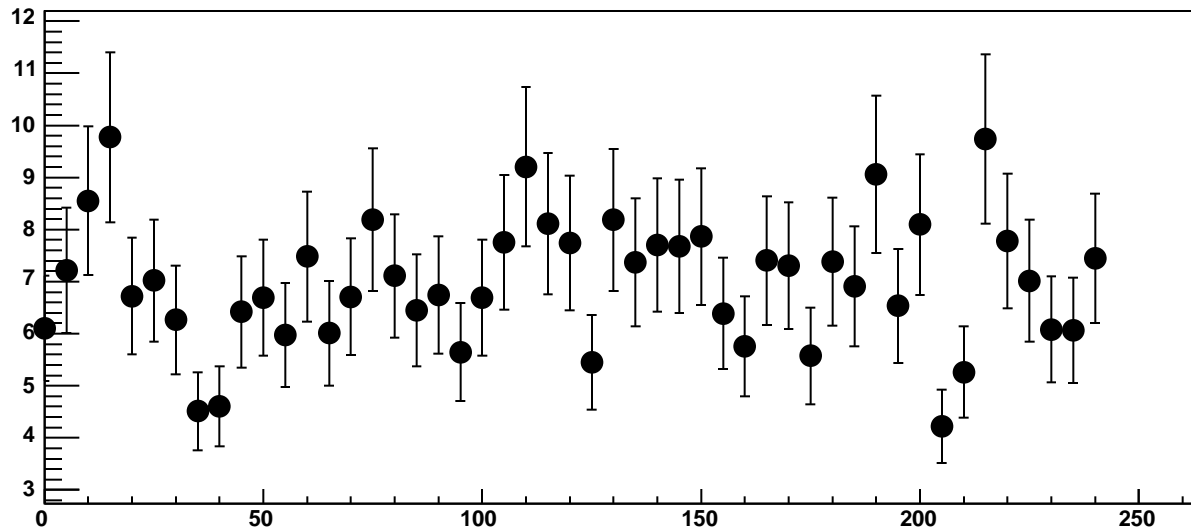


Chip 1, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold

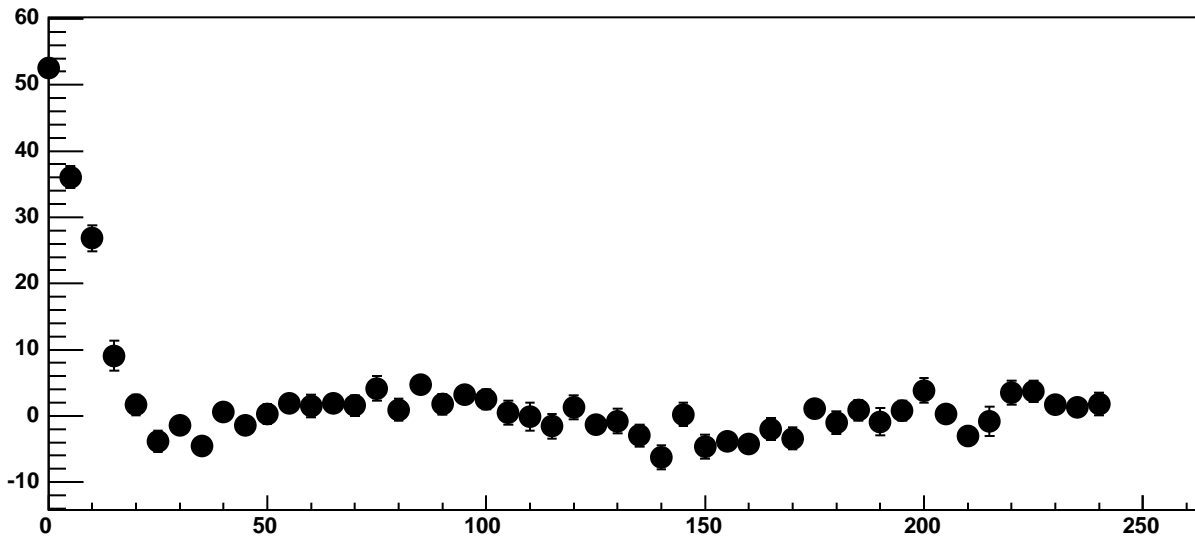


$\chi^2 / \text{ndf}$	148.2 / 41
p0	$390.4 \pm 2.844$
p1	$9.653 \pm 0.4992$
p2	$189.8 \pm 2.957$
p3	$27.58 \pm 0.5429$
p4	$-0.2218 \pm 0.007548$

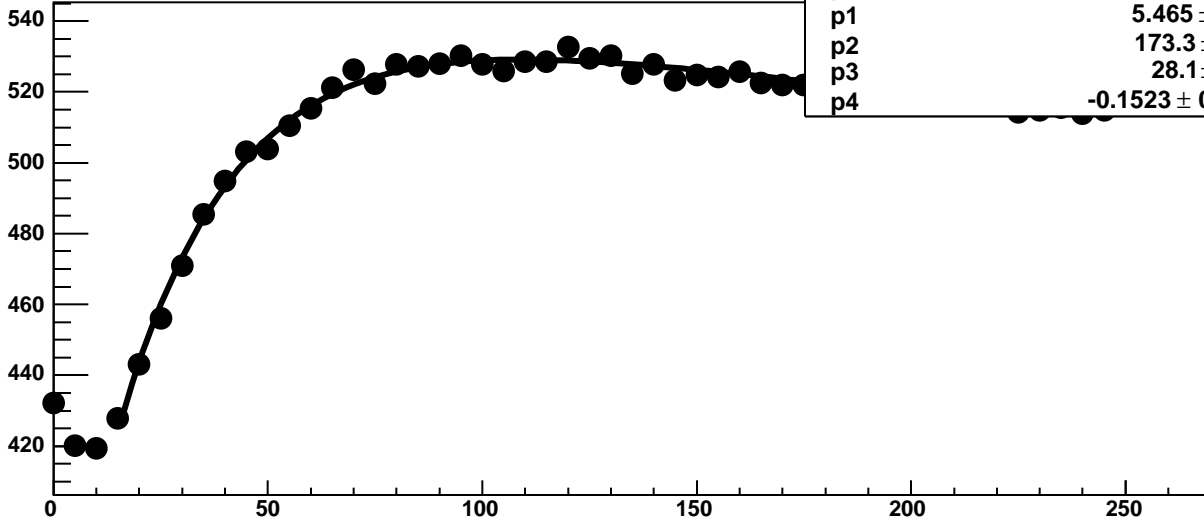
Chip 1, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold

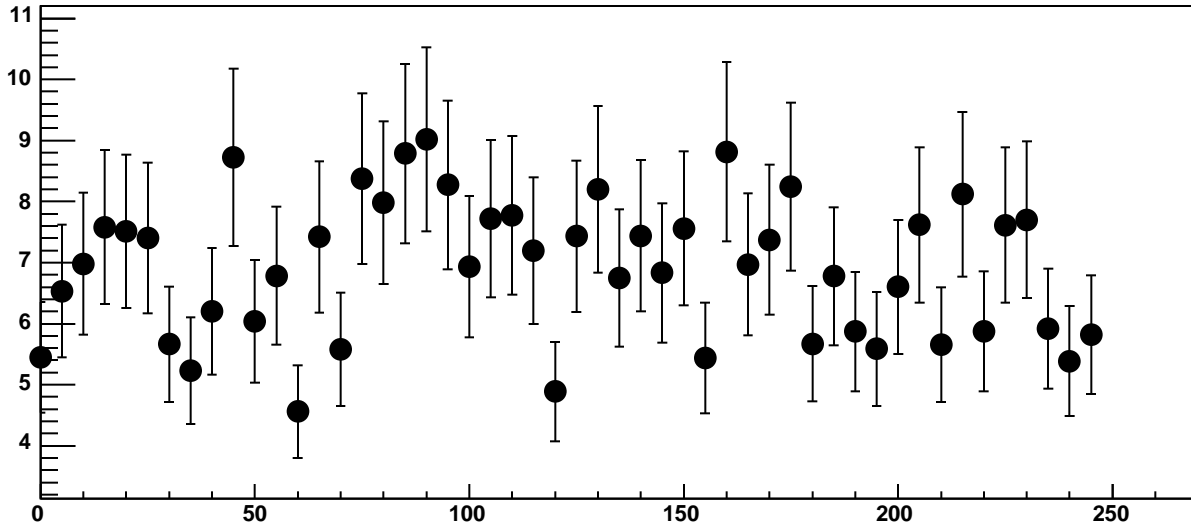


Chip 1, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold

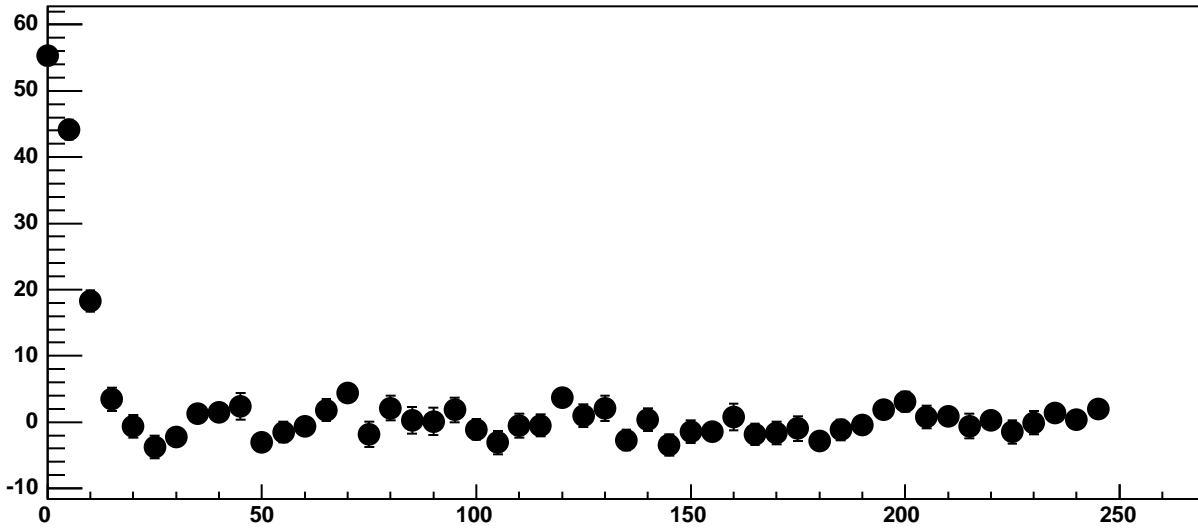


$\chi^2 / \text{ndf}$	81.08 / 41
p0	$376 \pm 0.7644$
p1	$5.465 \pm 0.4024$
p2	$173.3 \pm 0.8303$
p3	$28.1 \pm 0.7587$
p4	$-0.1523 \pm 0.008371$

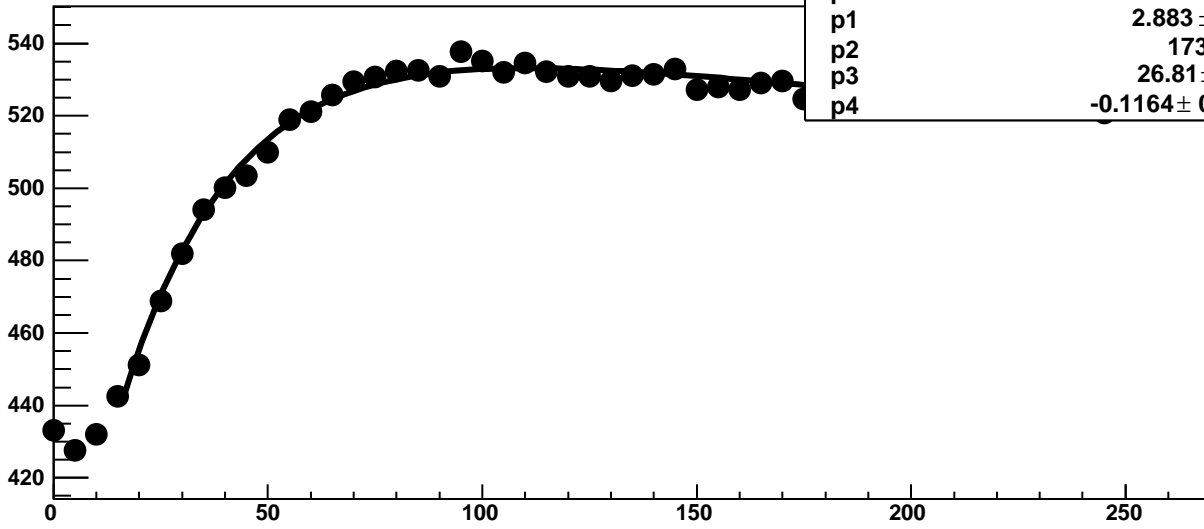
Chip 1, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

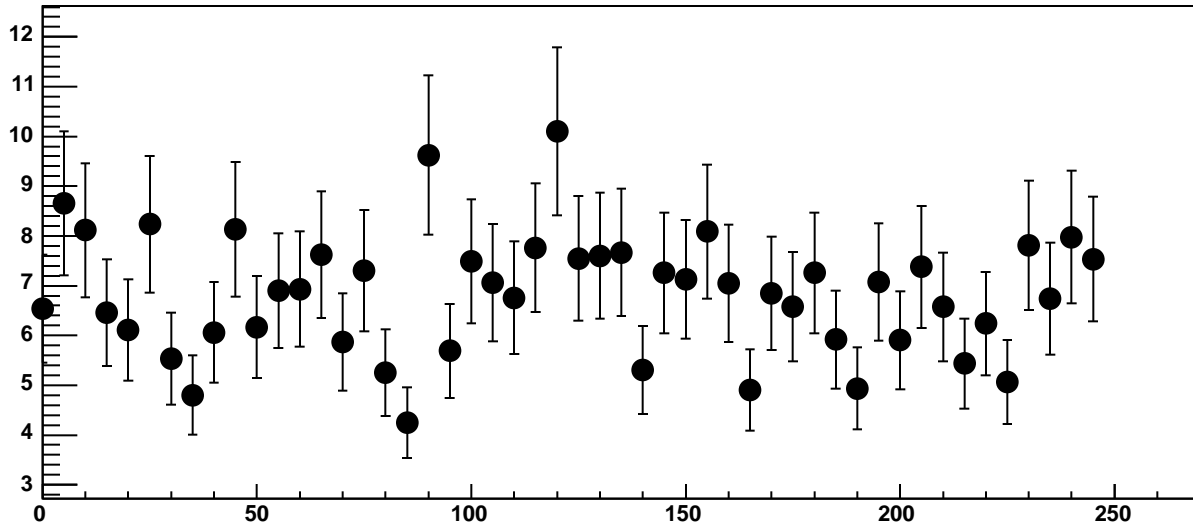


Chip 1, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold

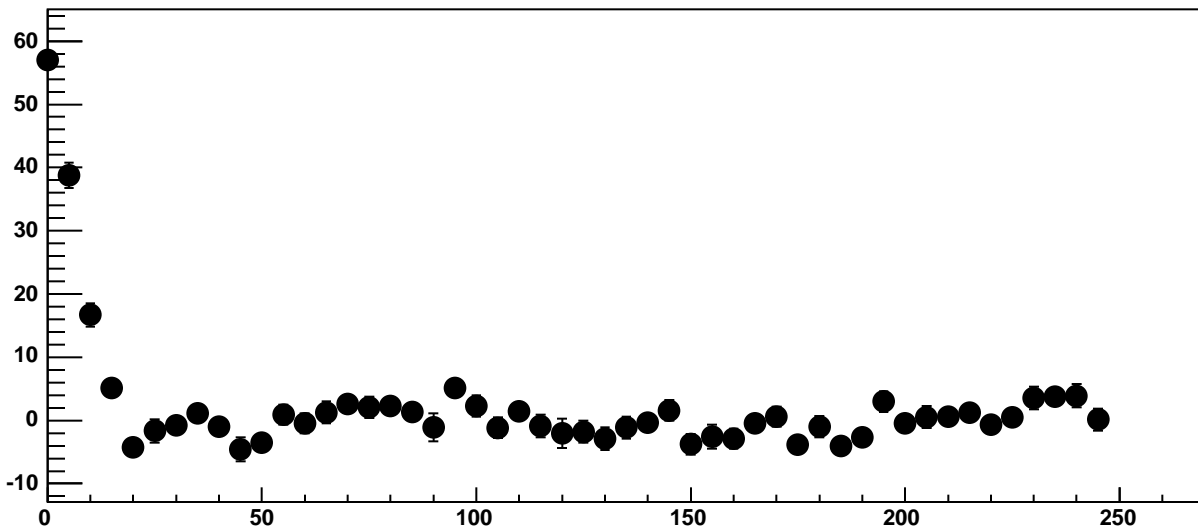


$\chi^2 / \text{ndf}$	124.3 / 41
p0	$375.9 \pm 2.756$
p1	$2.883 \pm 0.5956$
p2	$173 \pm 2.819$
p3	$26.81 \pm 0.6897$
p4	$-0.1164 \pm 0.007071$

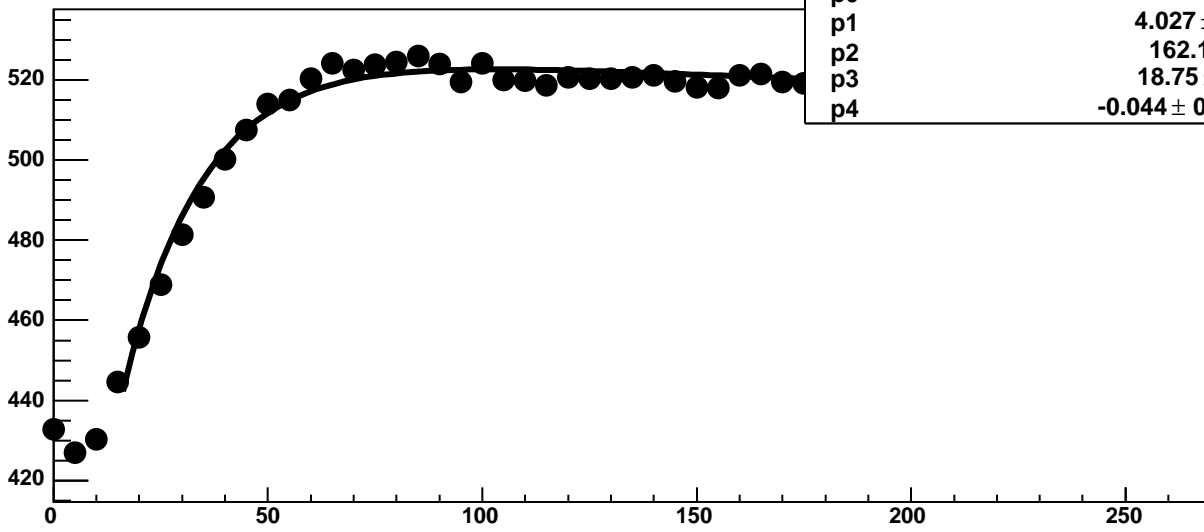
Chip 1, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold

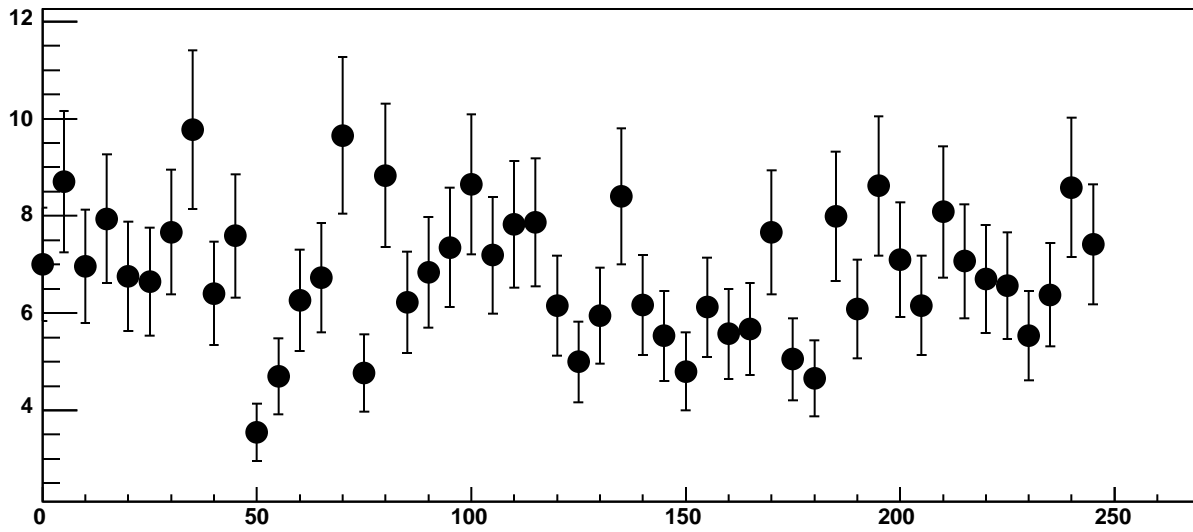


Chip 1, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

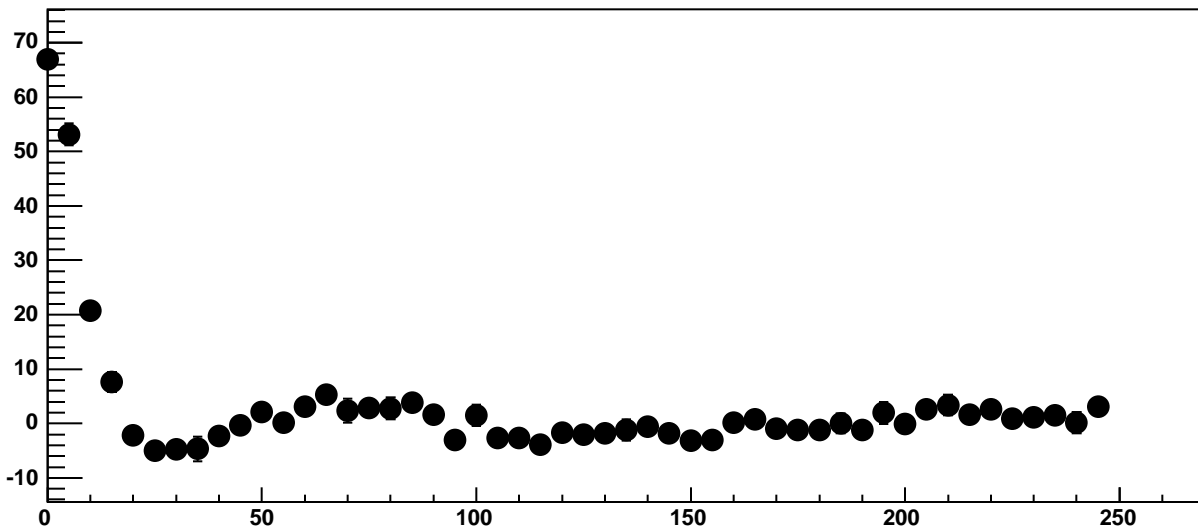


$\chi^2 / \text{ndf}$	138.7 / 41
p0	$365.6 \pm 2.701$
p1	$4.027 \pm 0.5567$
p2	$162.1 \pm 2.718$
p3	$18.75 \pm 0.5391$
p4	$-0.044 \pm 0.005563$

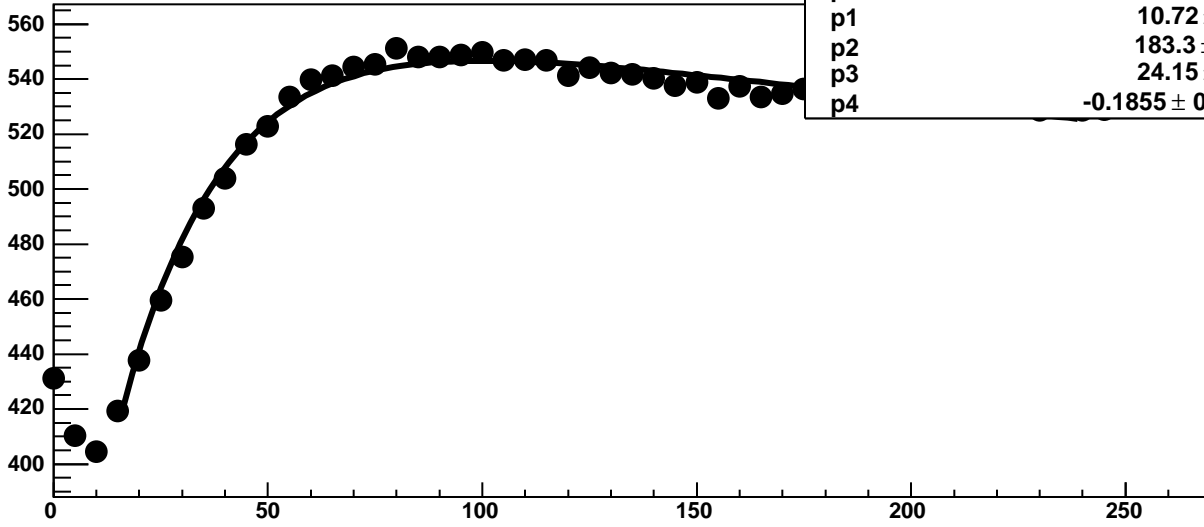
Chip 1, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

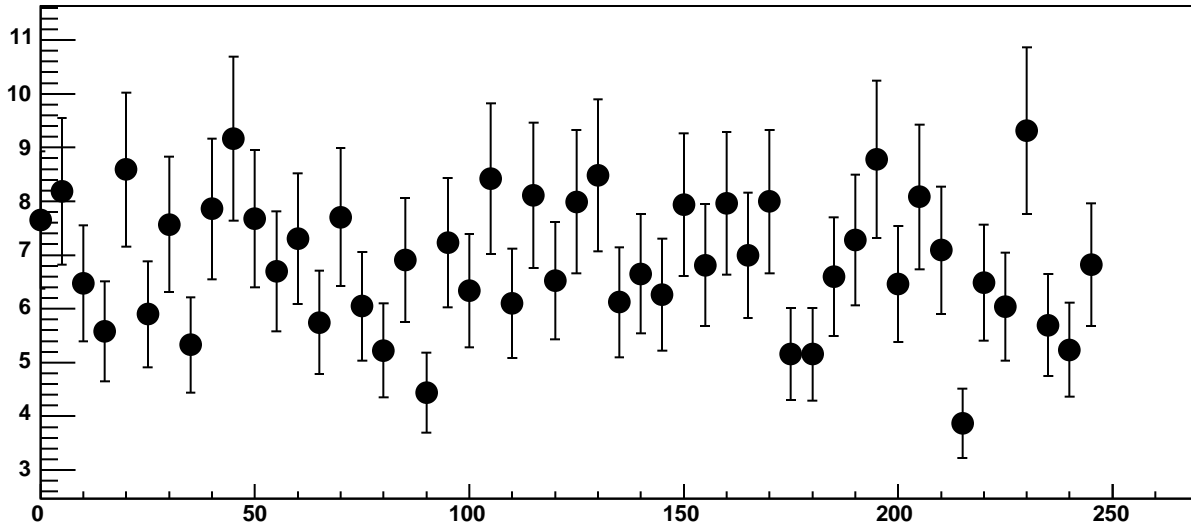


Chip 1, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

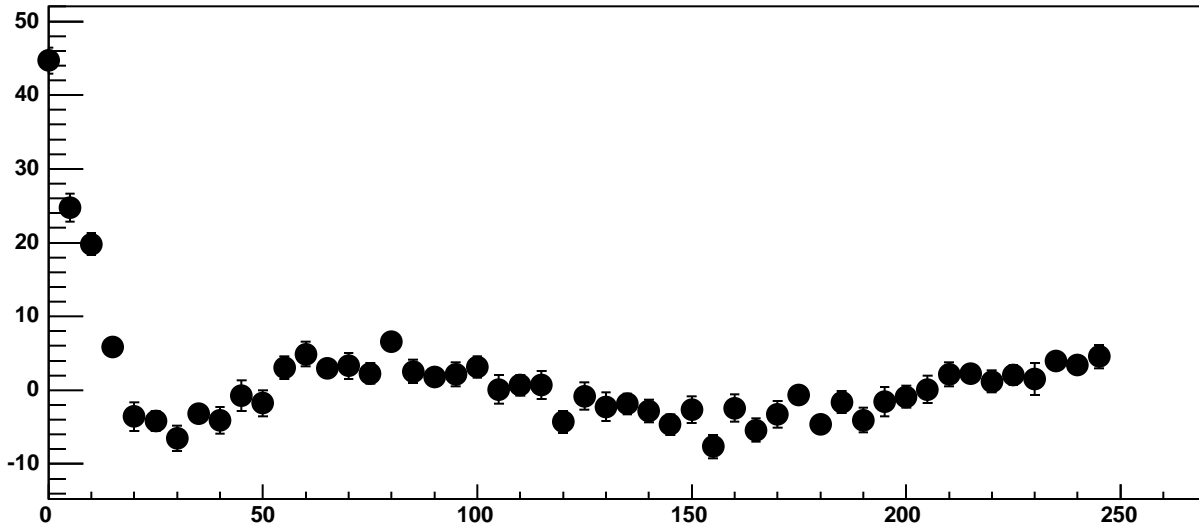


$\chi^2 / \text{ndf}$	246.6 / 41
p0	$384.5 \pm 0.5526$
p1	$10.72 \pm 0.2001$
p2	$183.3 \pm 0.6118$
p3	$24.15 \pm 0.4441$
p4	$-0.1855 \pm 0.006494$

Chip 1, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold

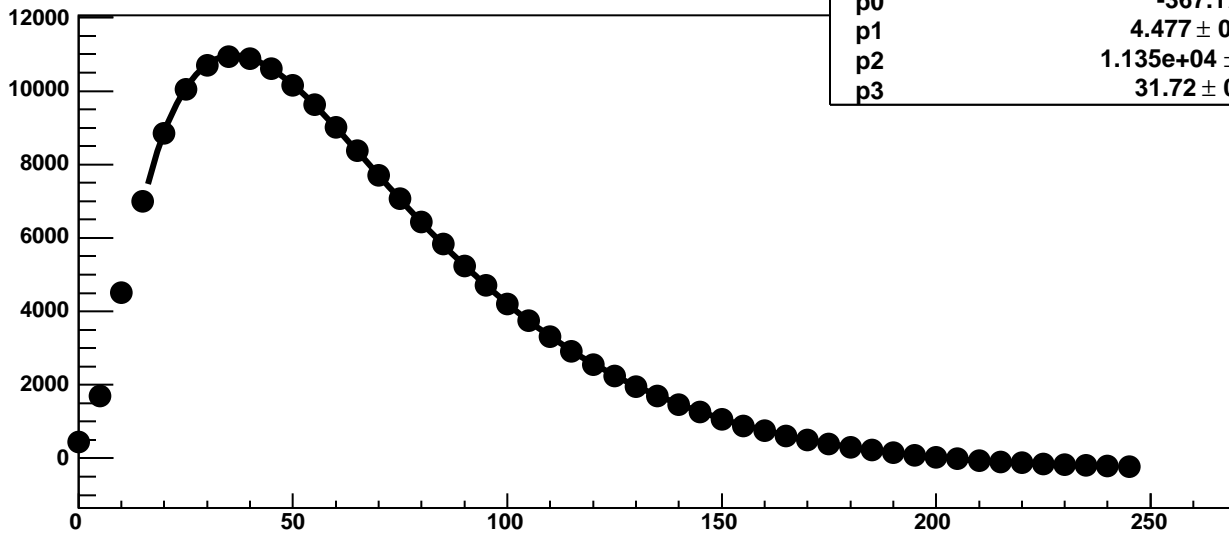


Chip 1, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold



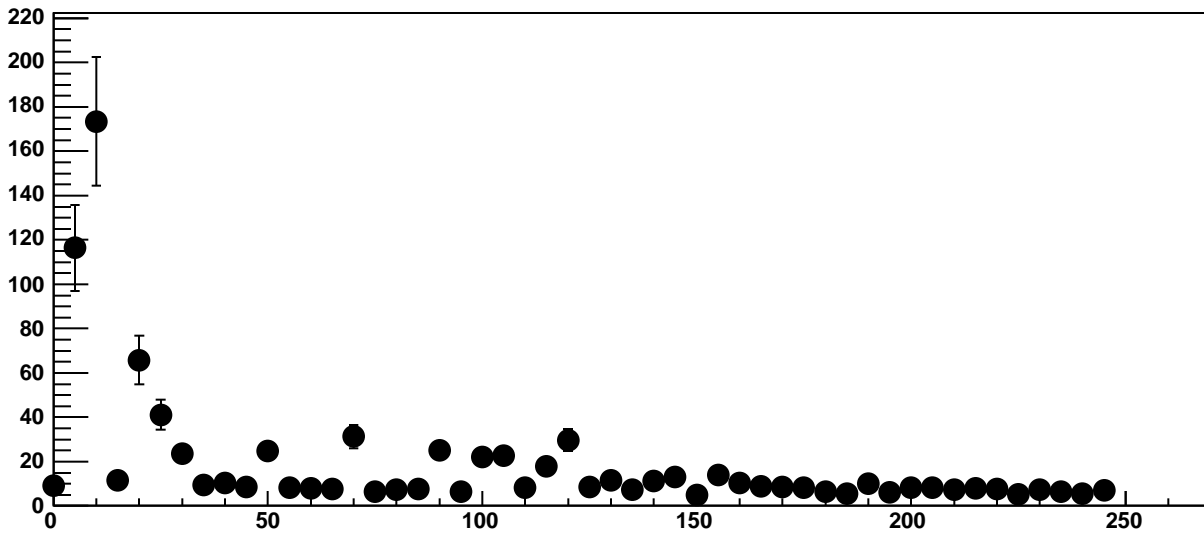


Chip 1, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold

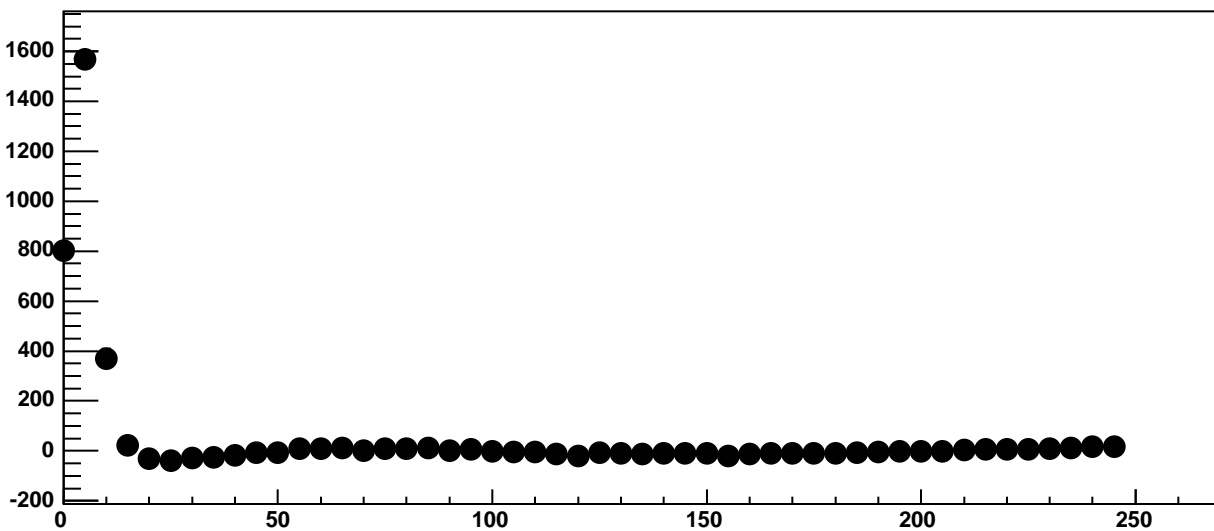


$\chi^2 / \text{ndf}$	1292 / 42
p0	-367.1 ± 0.5321
p1	4.477 ± 0.006477
p2	1.135e+04 ± 0.9984
p3	31.72 ± 0.004781

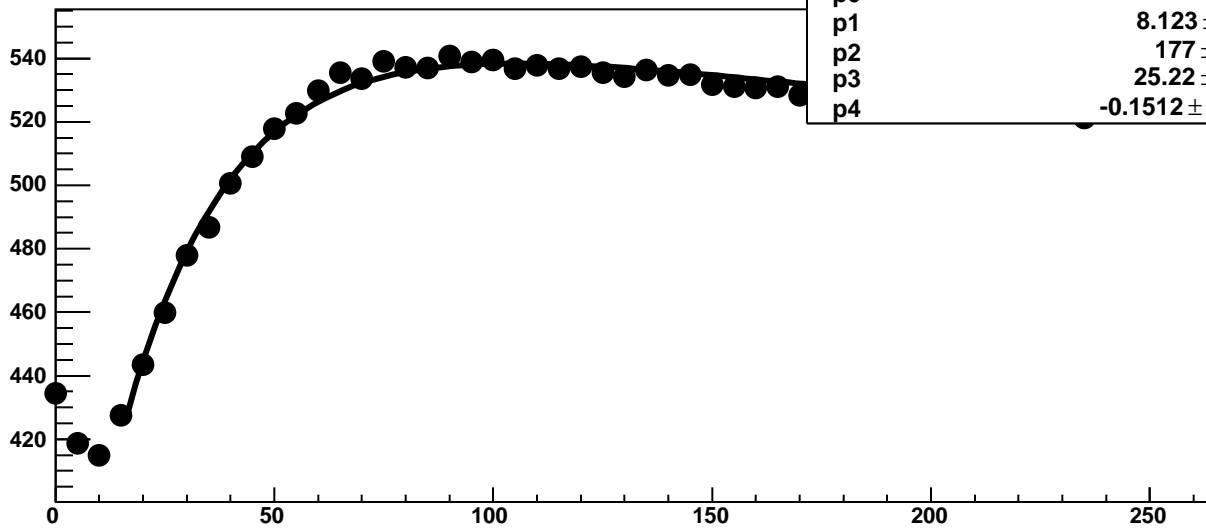
Chip 1, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold

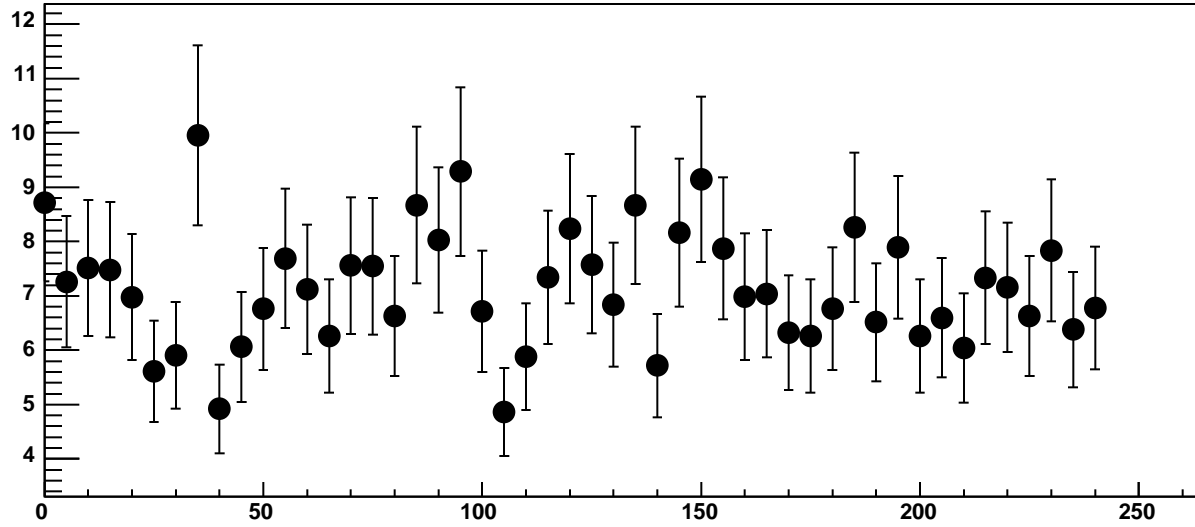


Chip 1, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

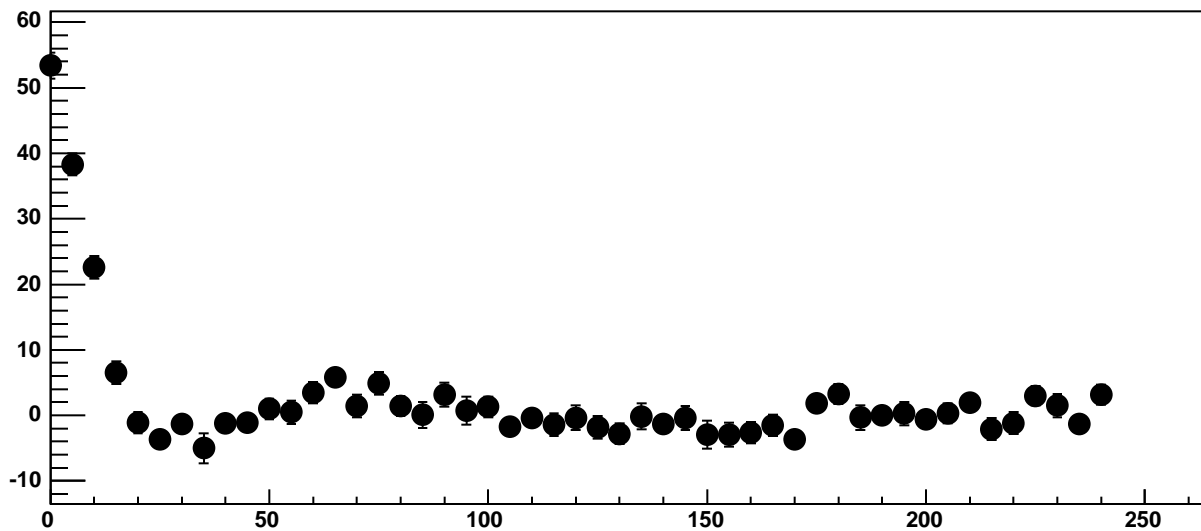


$\chi^2 / \text{ndf}$	108.6 / 41
p0	$379.9 \pm 0.6448$
p1	$8.123 \pm 0.3037$
p2	$177 \pm 0.7324$
p3	$25.22 \pm 0.5866$
p4	$-0.1512 \pm 0.00762$

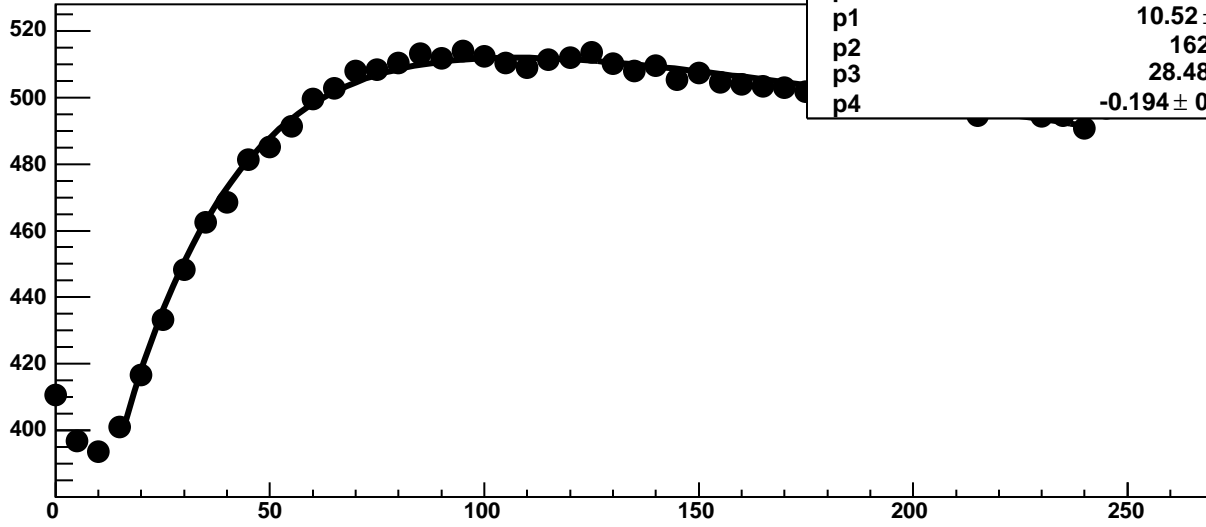
Chip 1, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

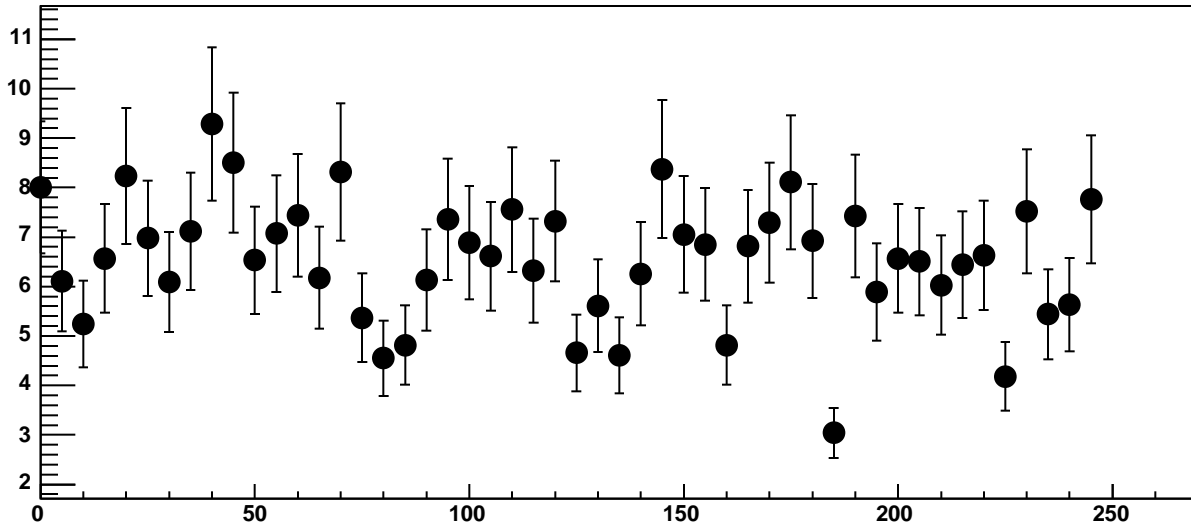


Chip 1, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

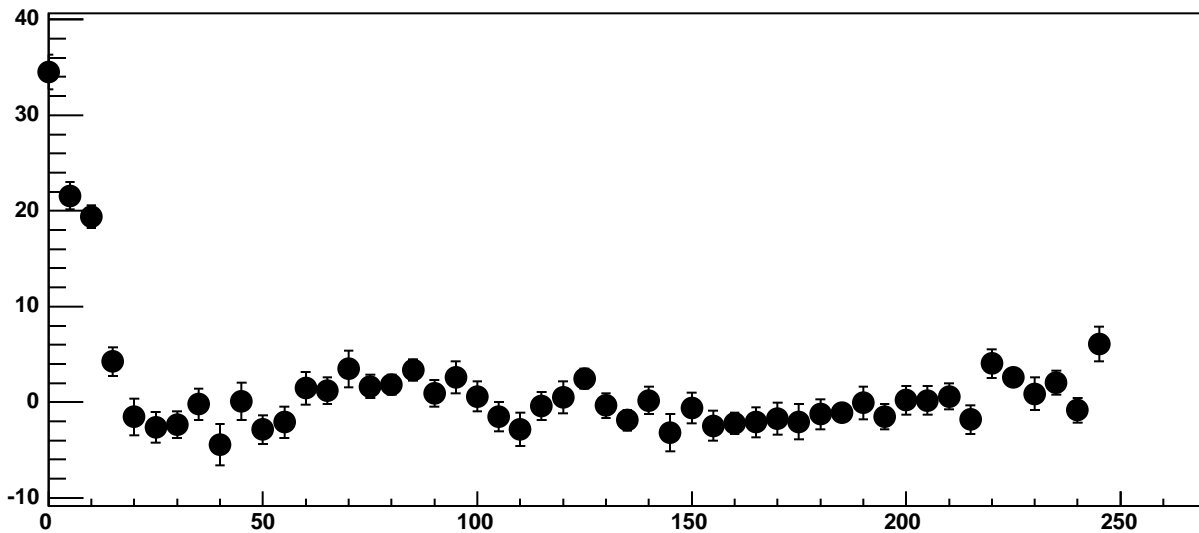


$\chi^2 / \text{ndf}$	95.37 / 41
p0	$374.1 \pm 2.612$
p1	$10.52 \pm 0.5338$
p2	$162 \pm 2.715$
p3	$28.48 \pm 0.619$
p4	$-0.194 \pm 0.006976$

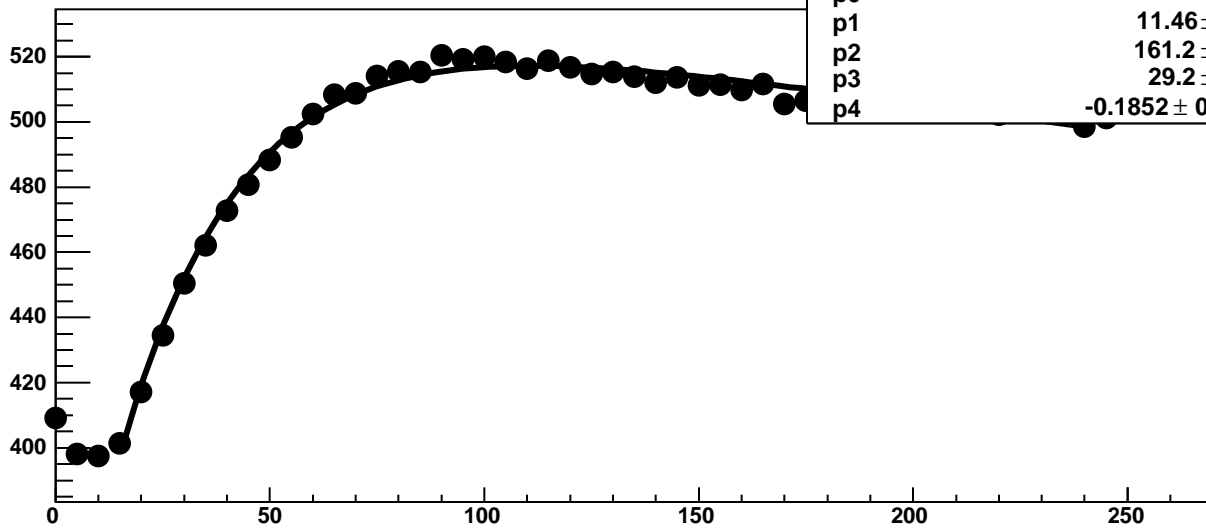
Chip 1, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



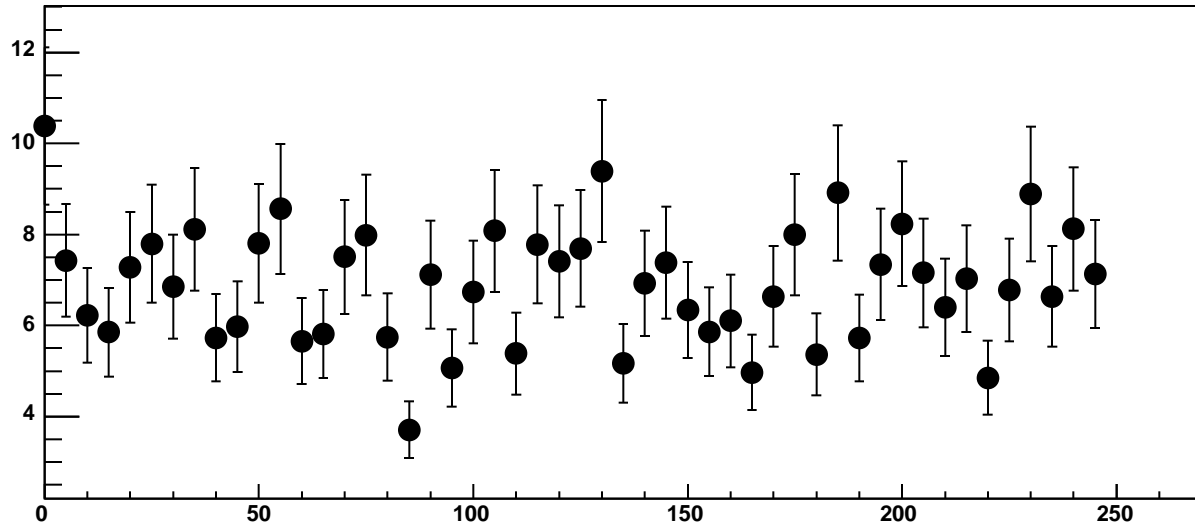
Chip 1, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold



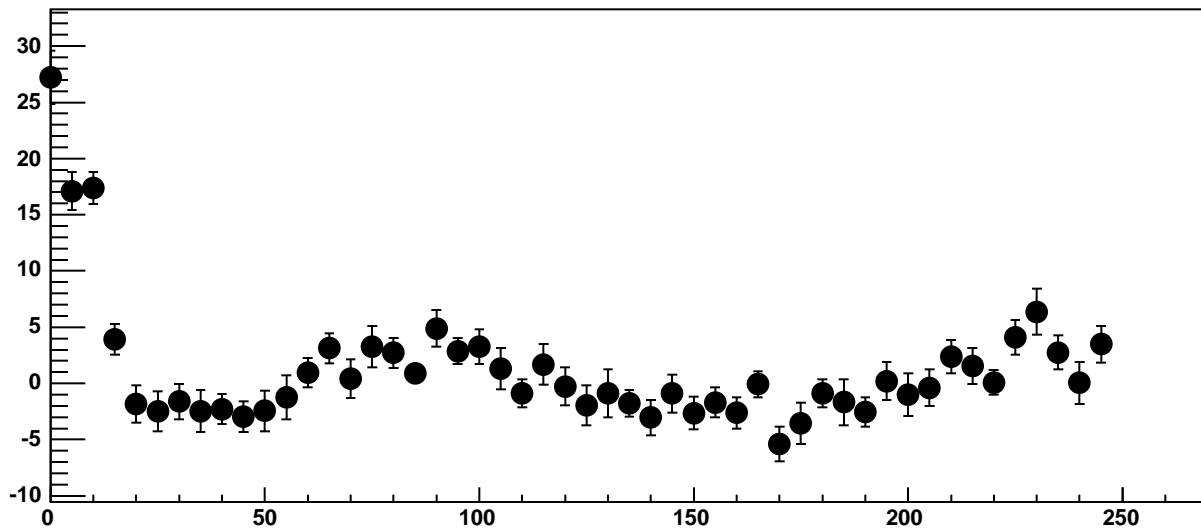
Chip 1, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold



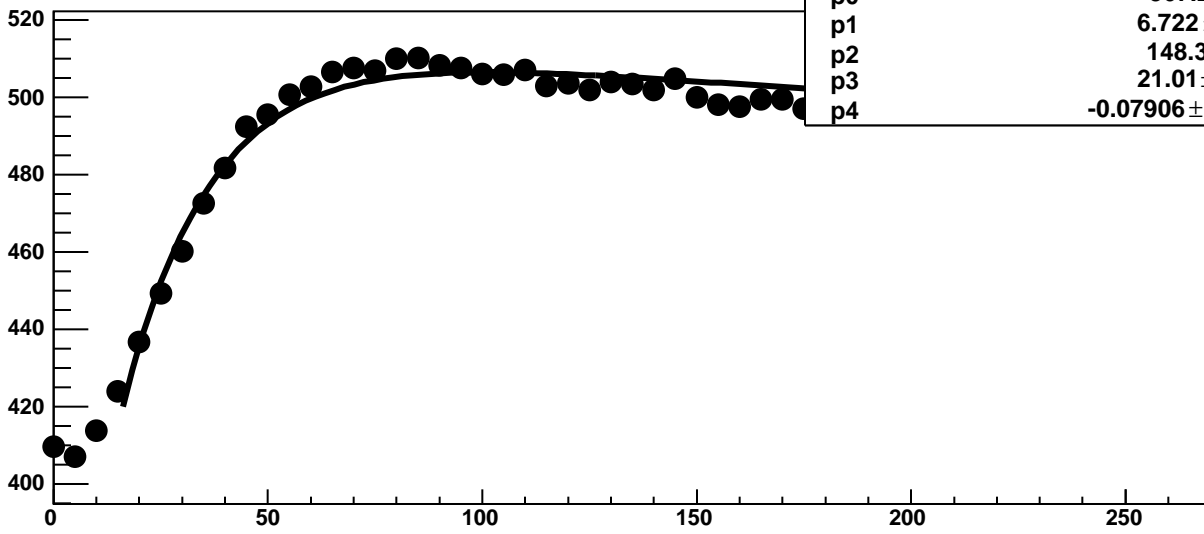
Chip 1, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold

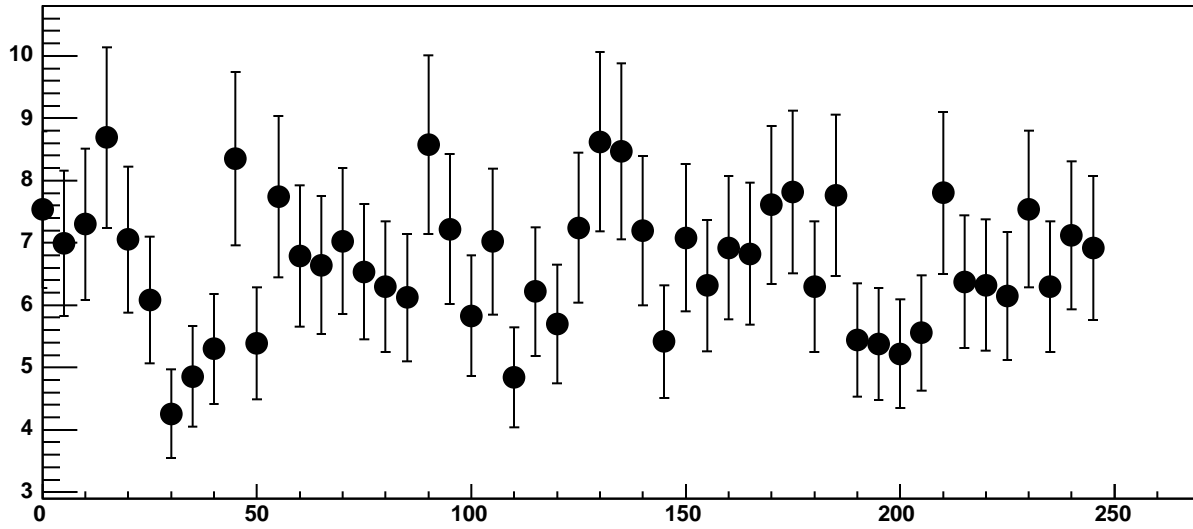


Chip 1, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold

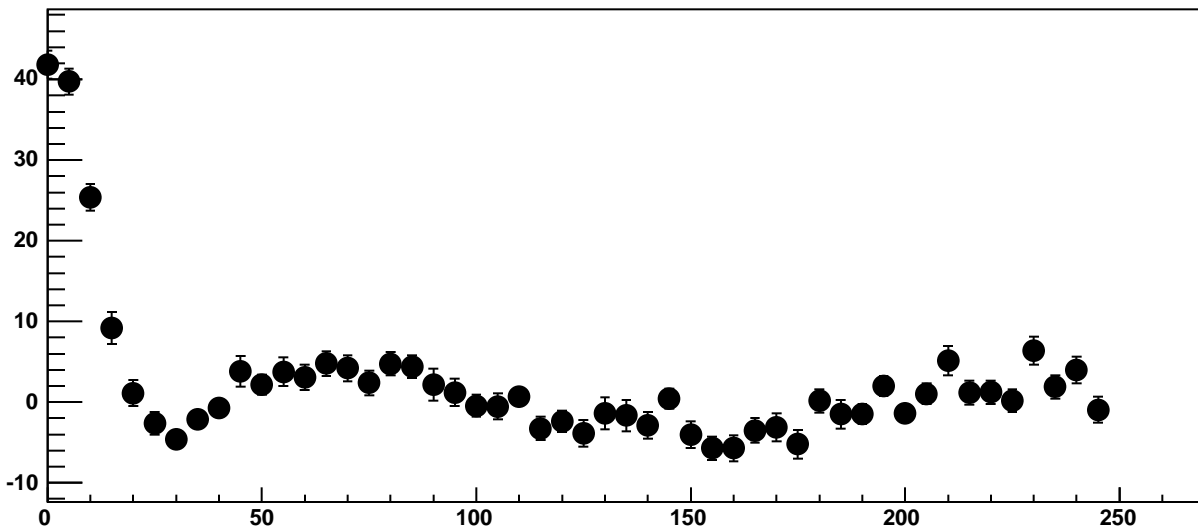


$\chi^2 / \text{ndf}$	216.3 / 41
p0	$367.2 \pm 2.731$
p1	$6.722 \pm 0.5601$
p2	$148.3 \pm 2.768$
p3	$21.01 \pm 0.5362$
p4	$-0.07906 \pm 0.00571$

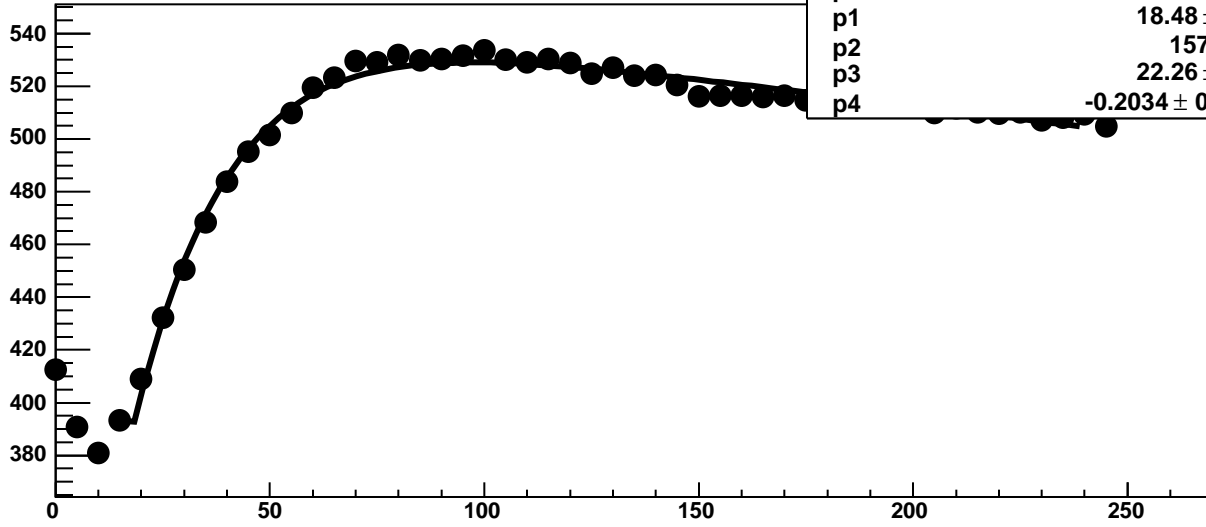
Chip 1, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold

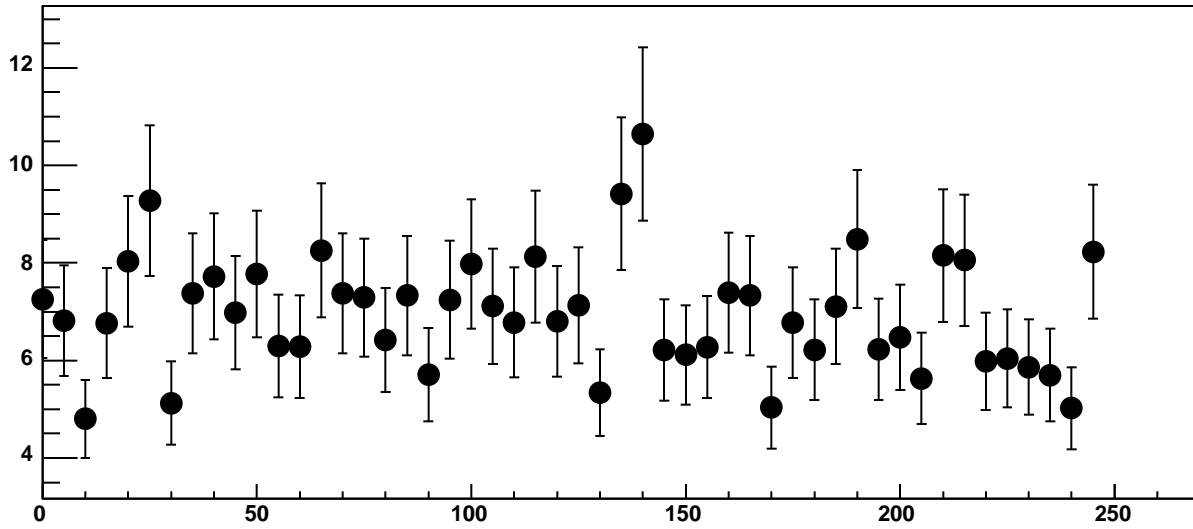


Chip 1, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

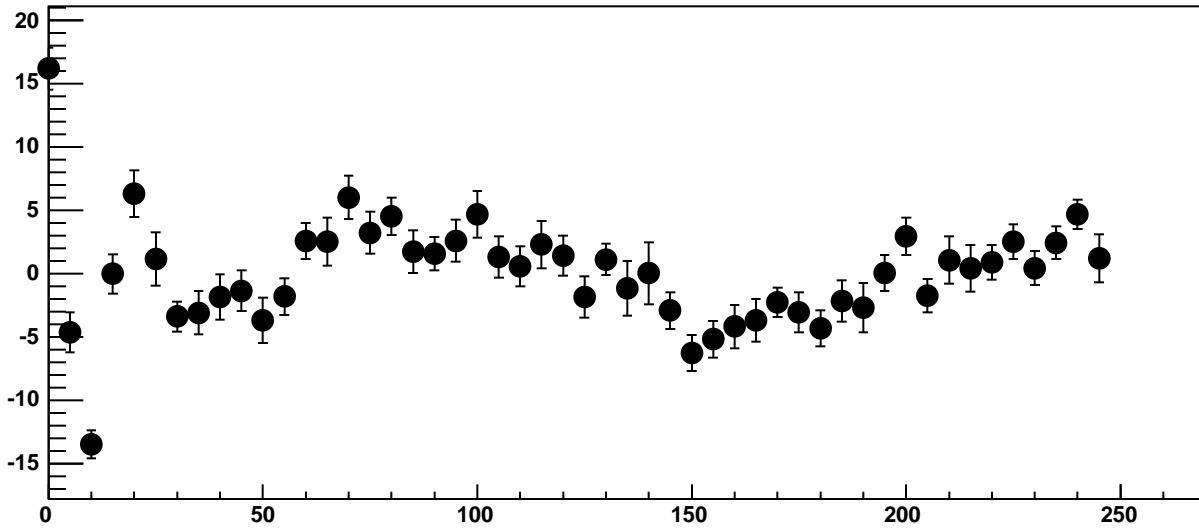


$\chi^2 / \text{ndf}$	177.6 / 41
p0	$392.7 \pm 1.509$
p1	$18.48 \pm 0.3168$
p2	$157 \pm 1.919$
p3	$22.26 \pm 0.4688$
p4	$-0.2034 \pm 0.006693$

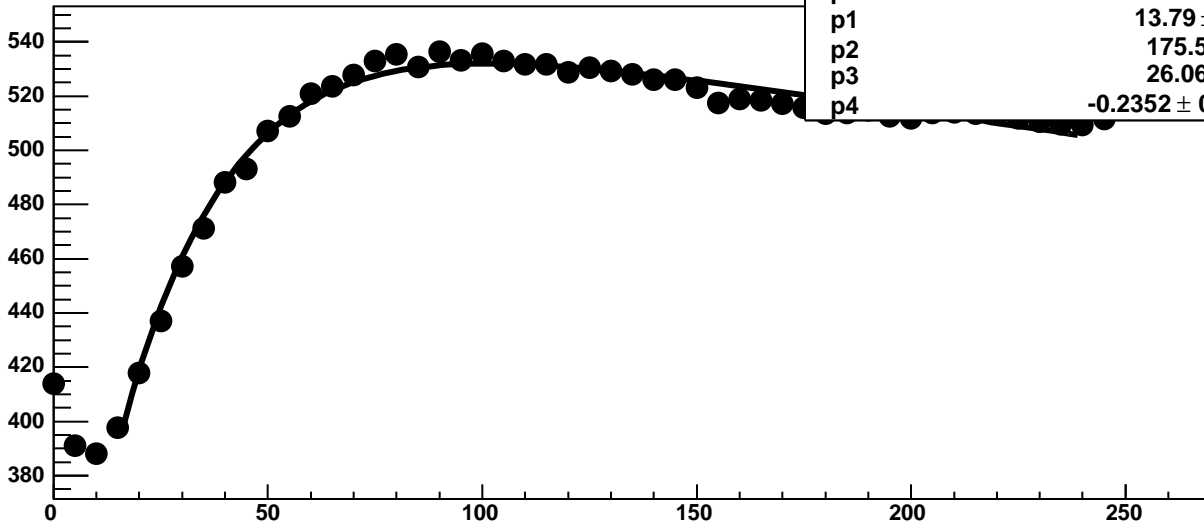
Chip 1, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

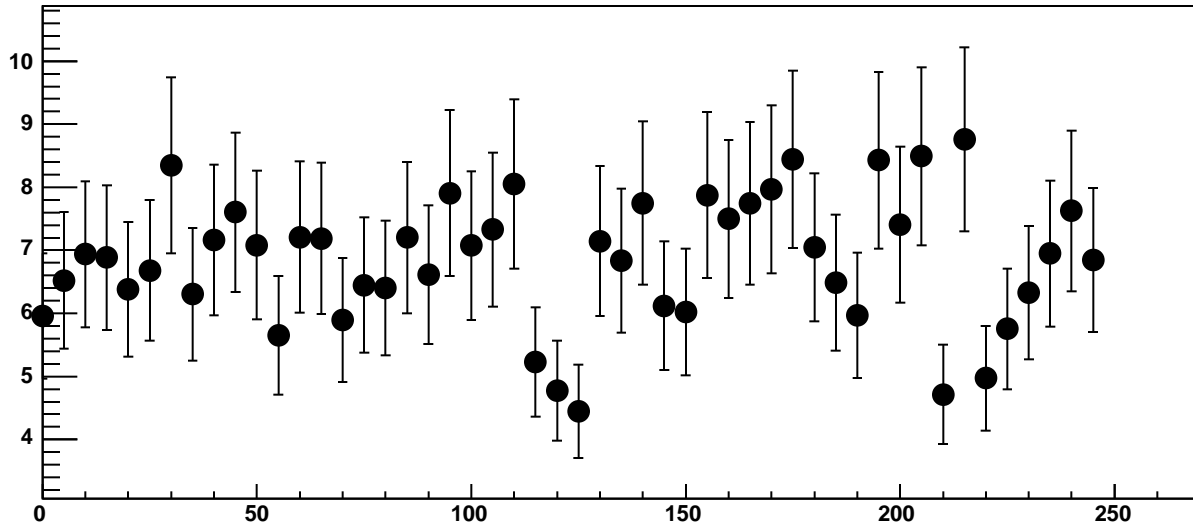


Chip 1, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

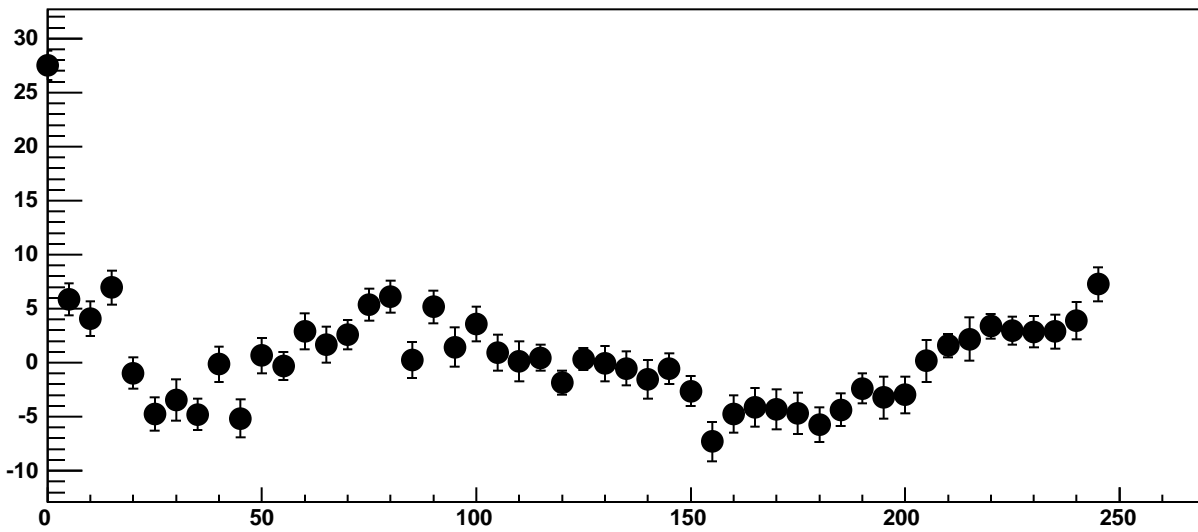


$\chi^2 / \text{ndf}$	216.2 / 41
p0	$383.2 \pm 2.842$
p1	$13.79 \pm 0.4669$
p2	$175.5 \pm 2.959$
p3	$26.06 \pm 0.454$
p4	$-0.2352 \pm 0.006911$

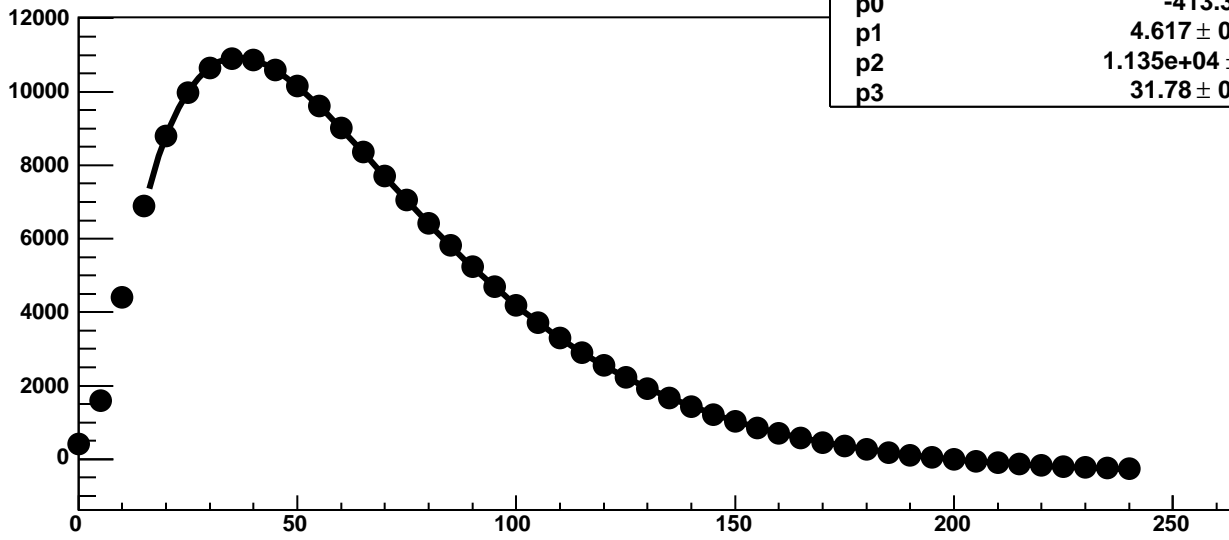
Chip 1, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

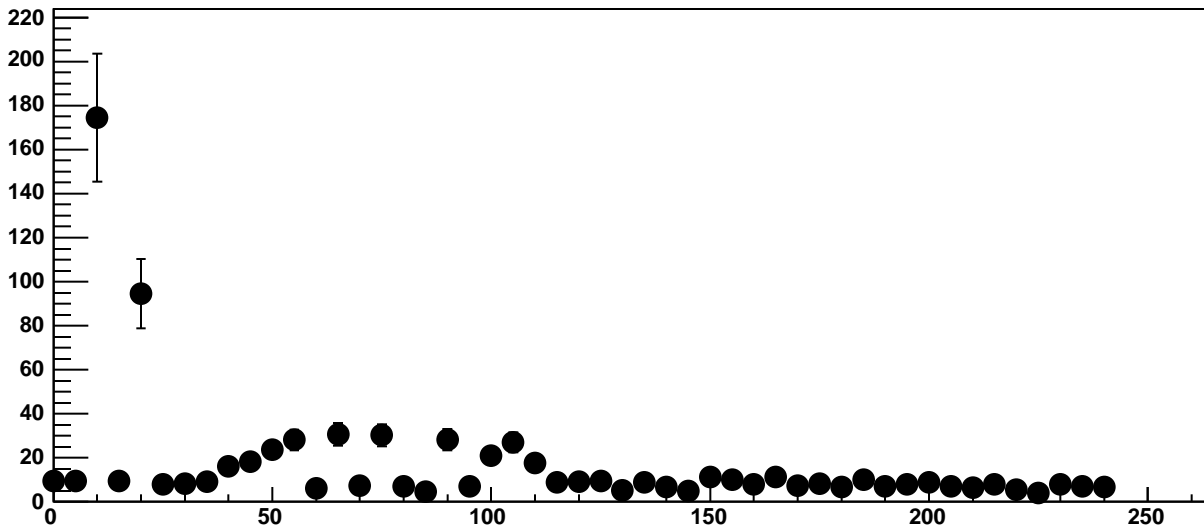


Chip 1, Channel 6, Enable 5!, DAC=4000, ADC Mean vs Hold

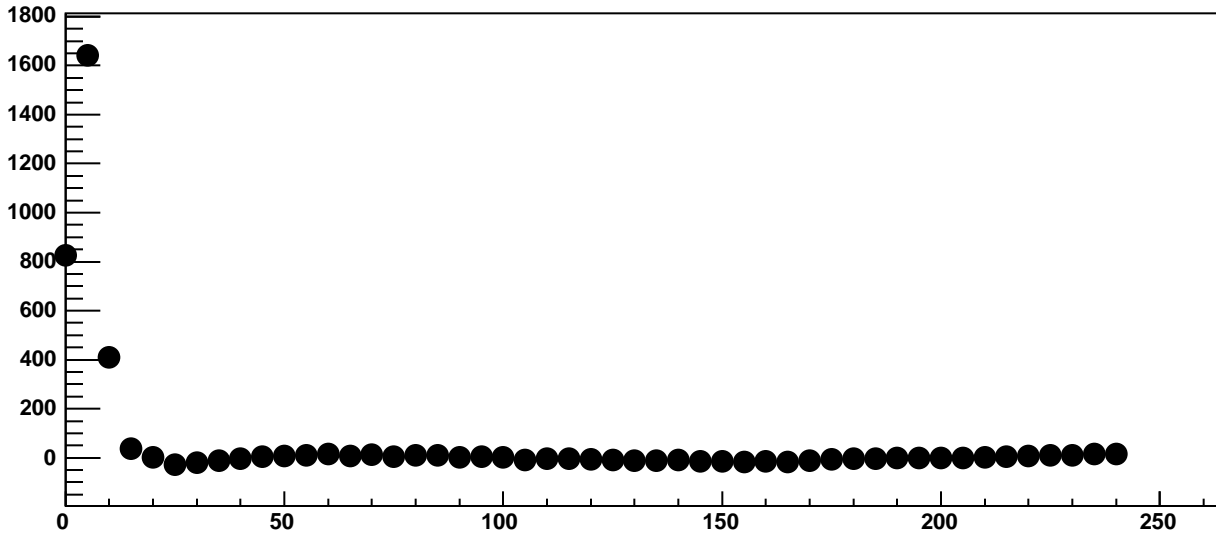


$\chi^2 / \text{ndf}$	1851 / 42
p0	-413.3 ± 0.518
p1	4.617 ± 0.005745
p2	1.135e+04 ± 0.9372
p3	31.78 ± 0.004417

Chip 1, Channel 6, Enable 5!, DAC=4000, ADC Noise vs Hold

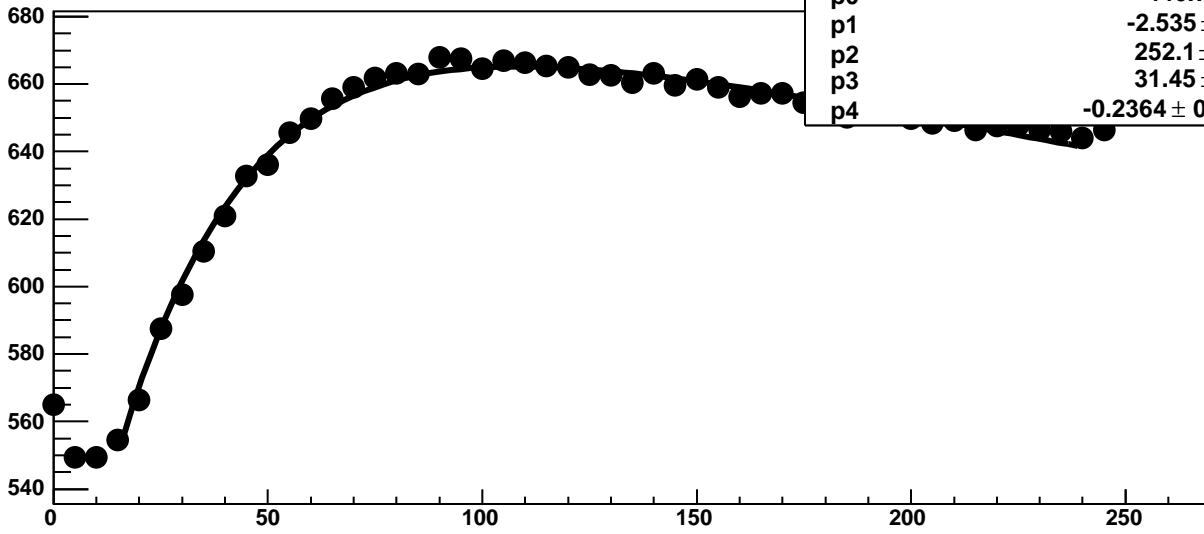


Chip 1, Channel 6, Enable 5!, DAC=4000, ADC Residuals vs Hold



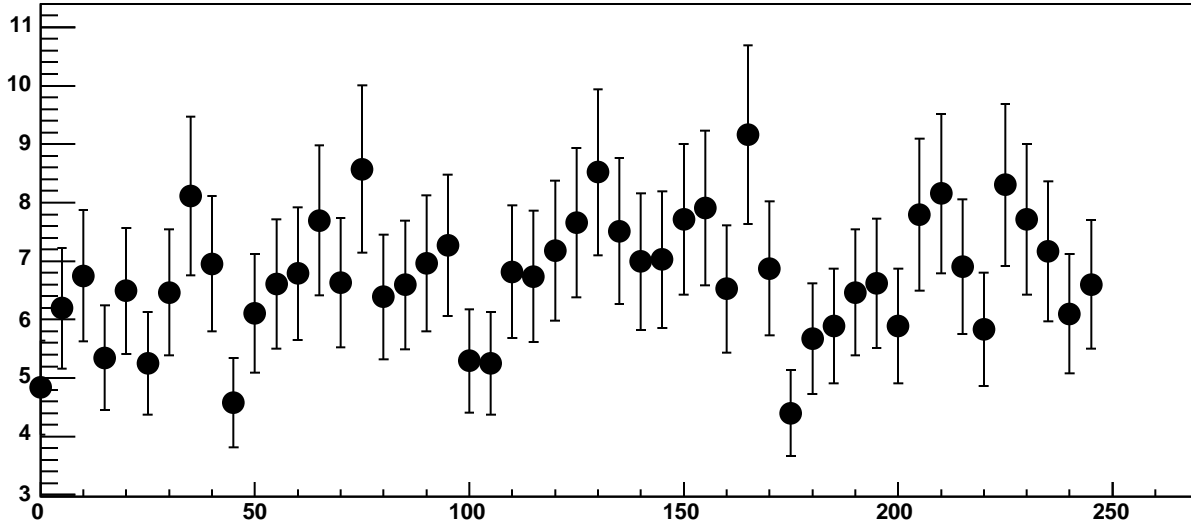


Chip 1, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

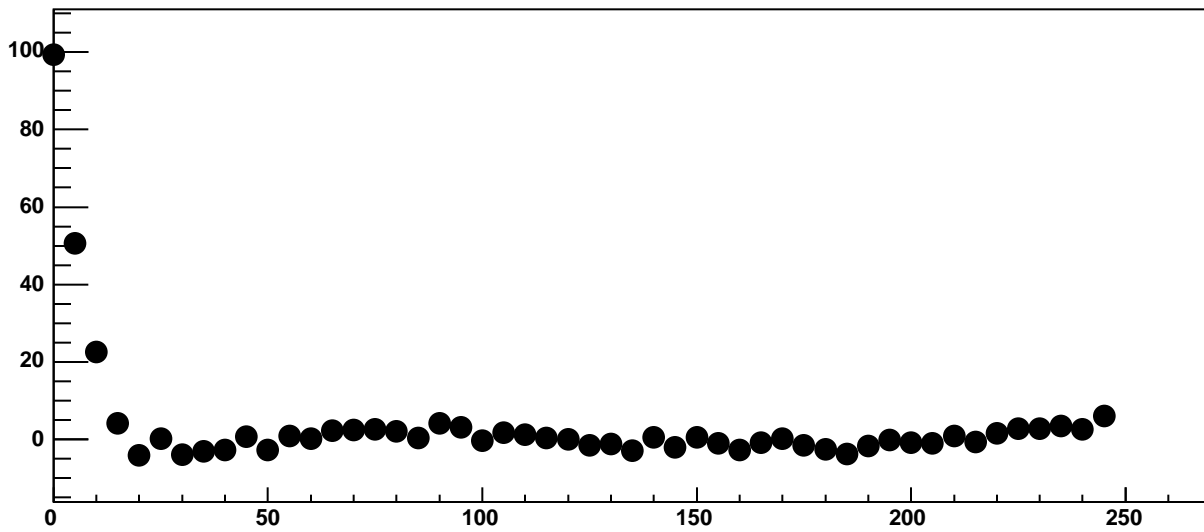


$\chi^2 / \text{ndf}$	96.48 / 41
p0	$446.7 \pm 0.847$
p1	$-2.535 \pm 0.3833$
p2	$252.1 \pm 0.9396$
p3	$31.45 \pm 0.6847$
p4	$-0.2364 \pm 0.008876$

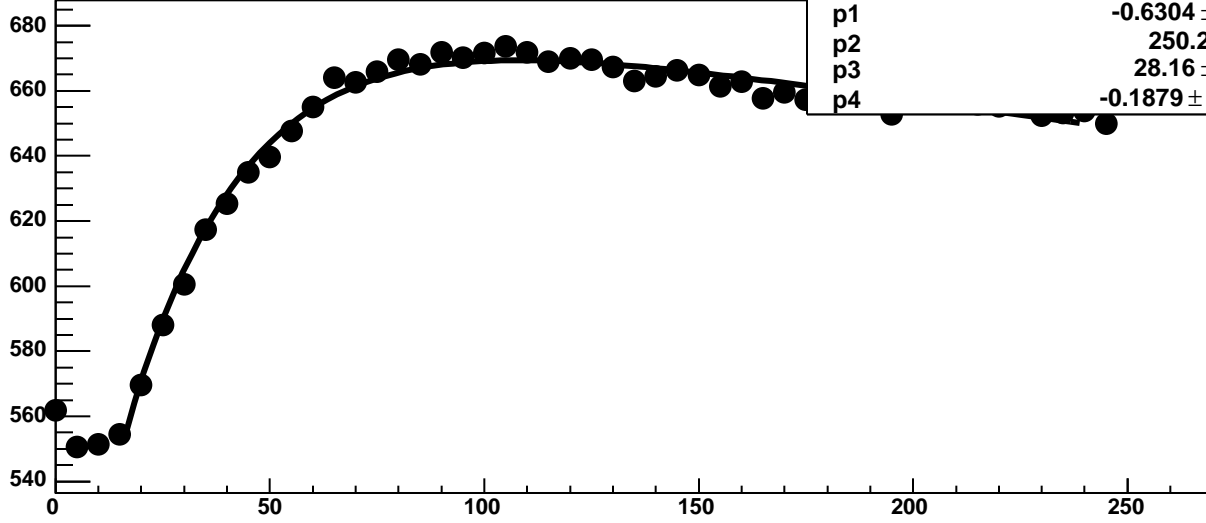
Chip 1, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

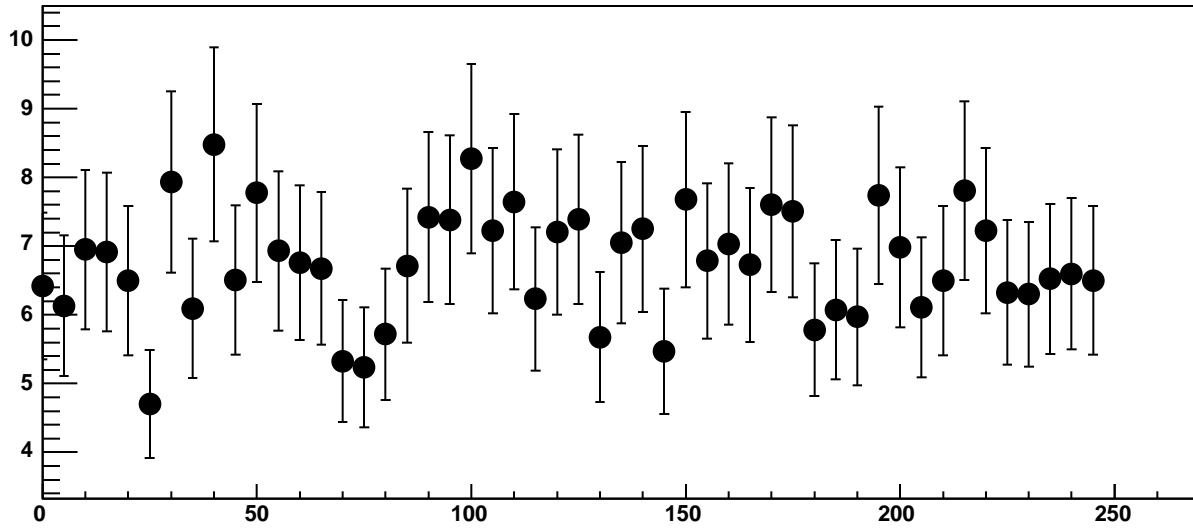


Chip 1, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold

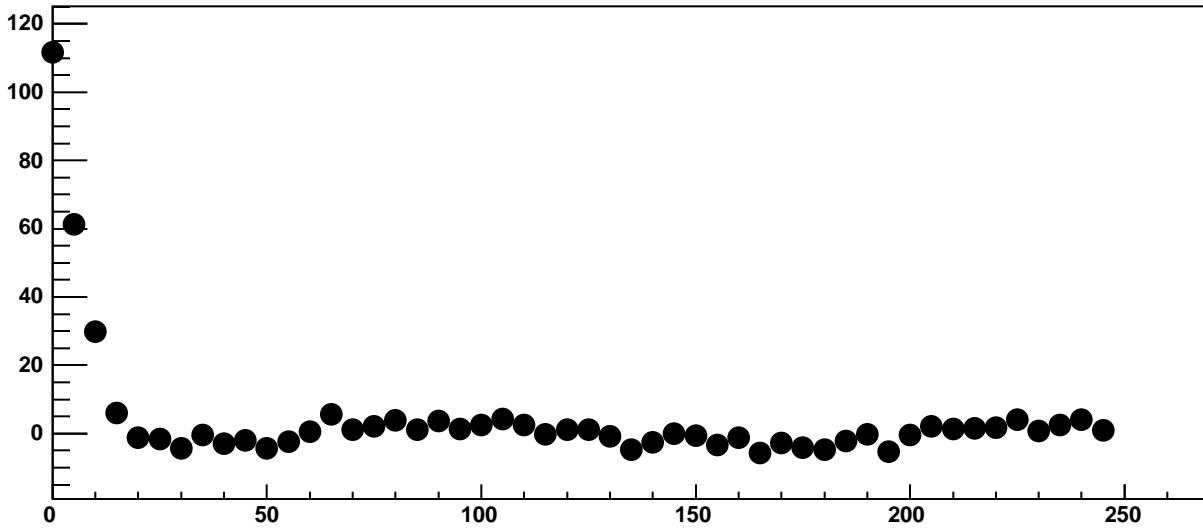


$\chi^2 / \text{ndf}$	165 / 41
p0	$444.9 \pm 2.838$
p1	$-0.6304 \pm 0.4956$
p2	$250.2 \pm 2.909$
p3	$28.16 \pm 0.5795$
p4	$-0.1879 \pm 0.00746$

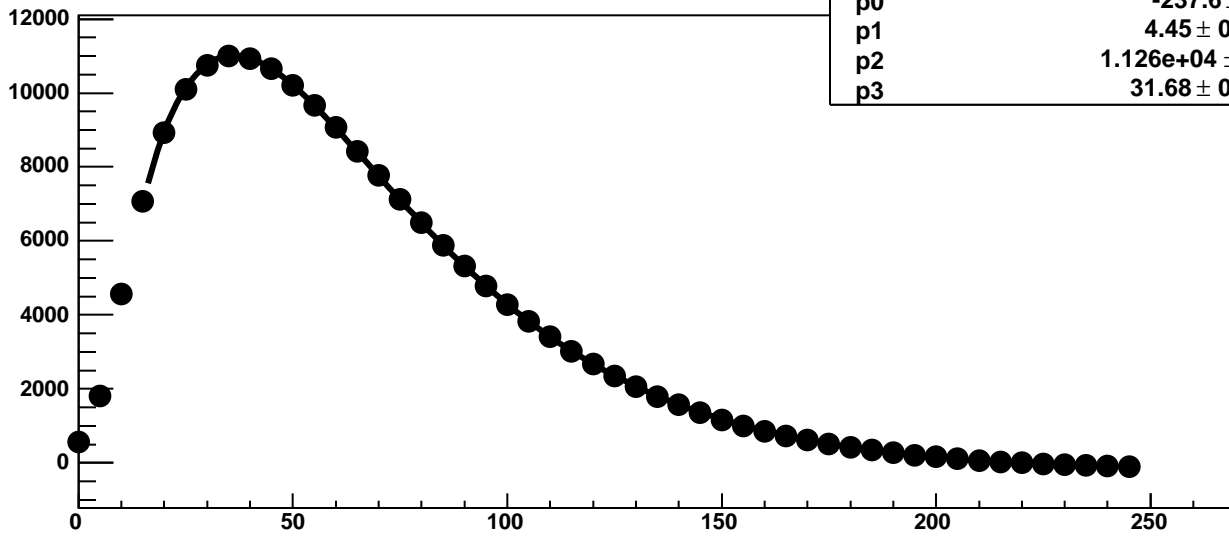
Chip 1, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

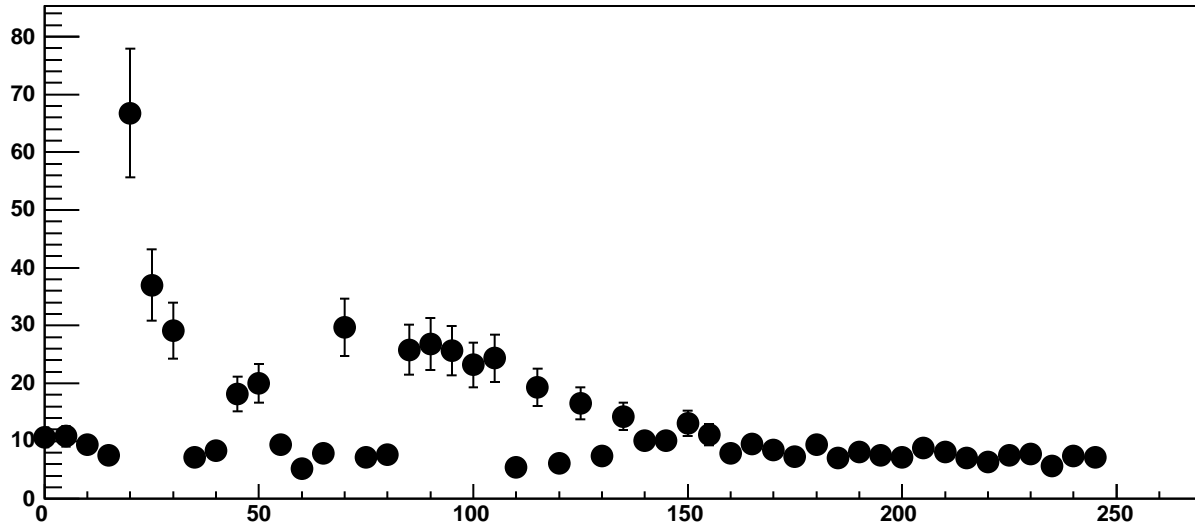


Chip 1, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold

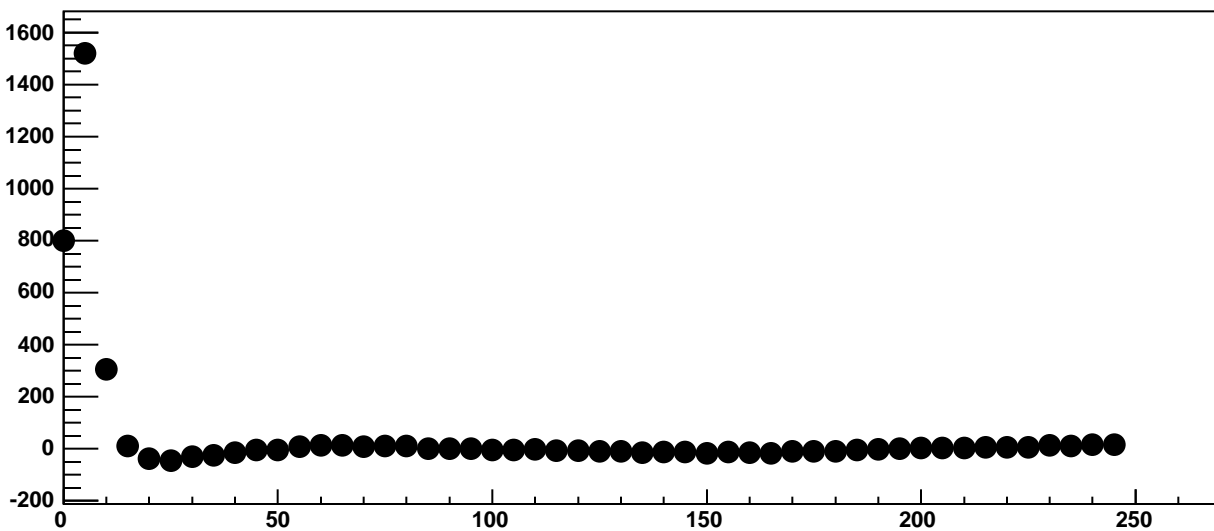


$\chi^2 / \text{ndf}$	1314 / 42
p0	-237.6 ± 0.5682
p1	4.45 ± 0.004907
p2	1.126e+04 ± 0.9345
p3	31.68 ± 0.004669

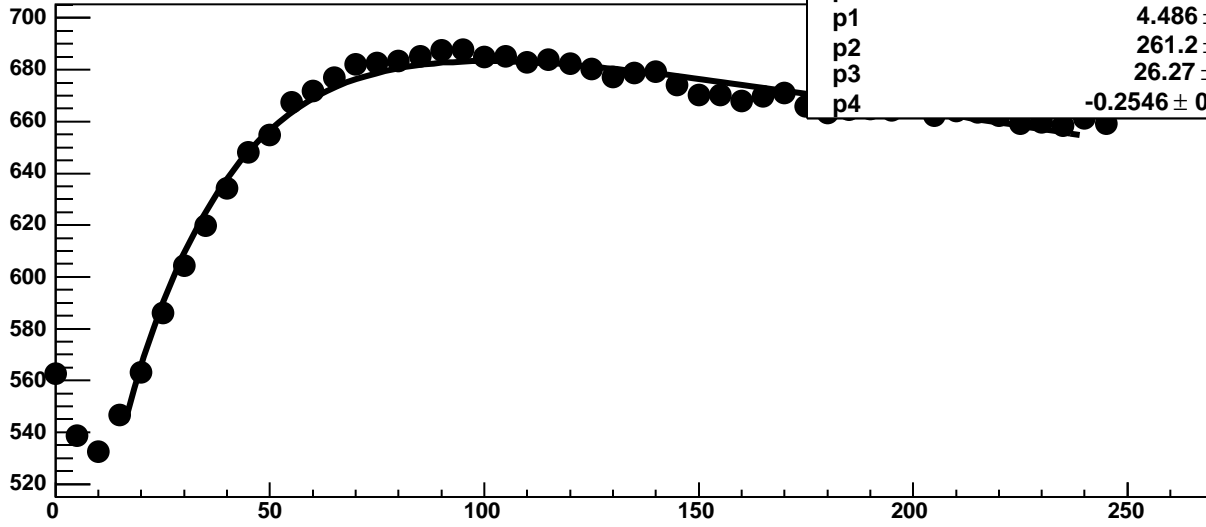
Chip 1, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold

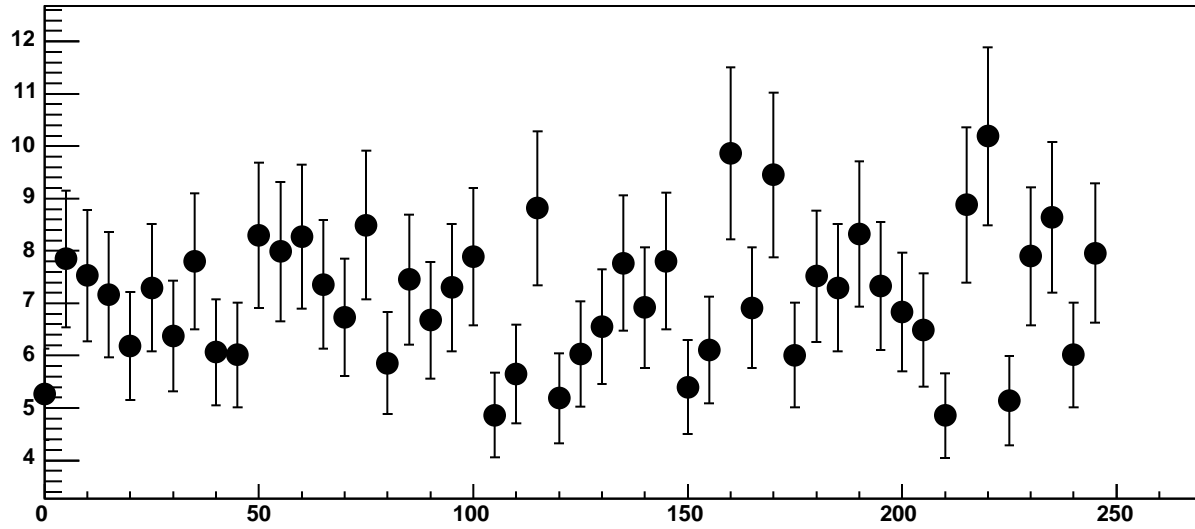


Chip 1, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

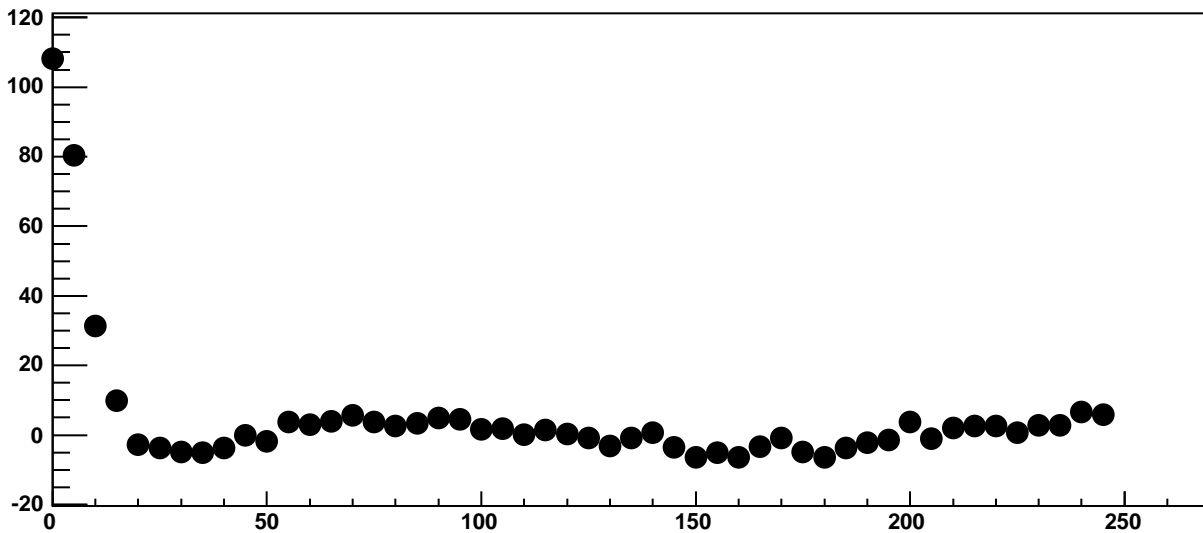


$\chi^2 / \text{ndf}$	265.6 / 41
p0	$453.3 \pm 0.6566$
p1	$4.486 \pm 0.2687$
p2	$261.2 \pm 0.7443$
p3	$26.27 \pm 0.4645$
p4	$-0.2546 \pm 0.007544$

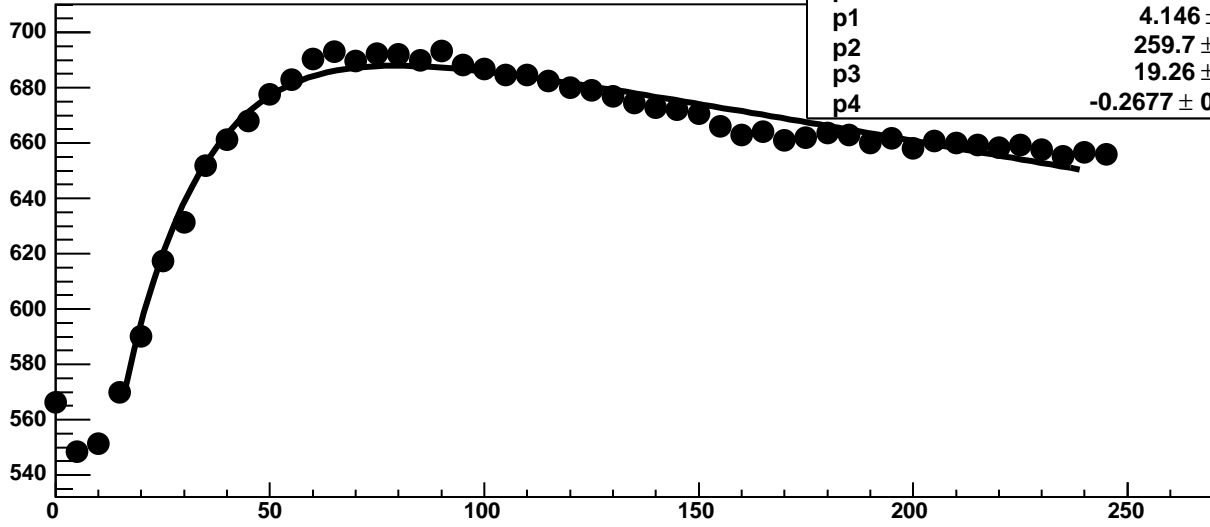
Chip 1, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

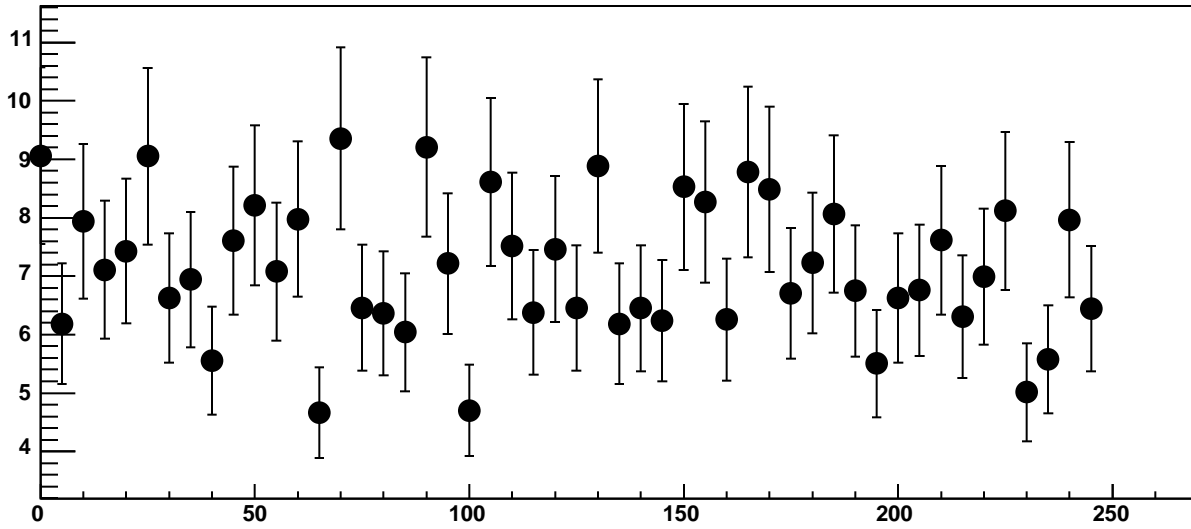


Chip 1, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

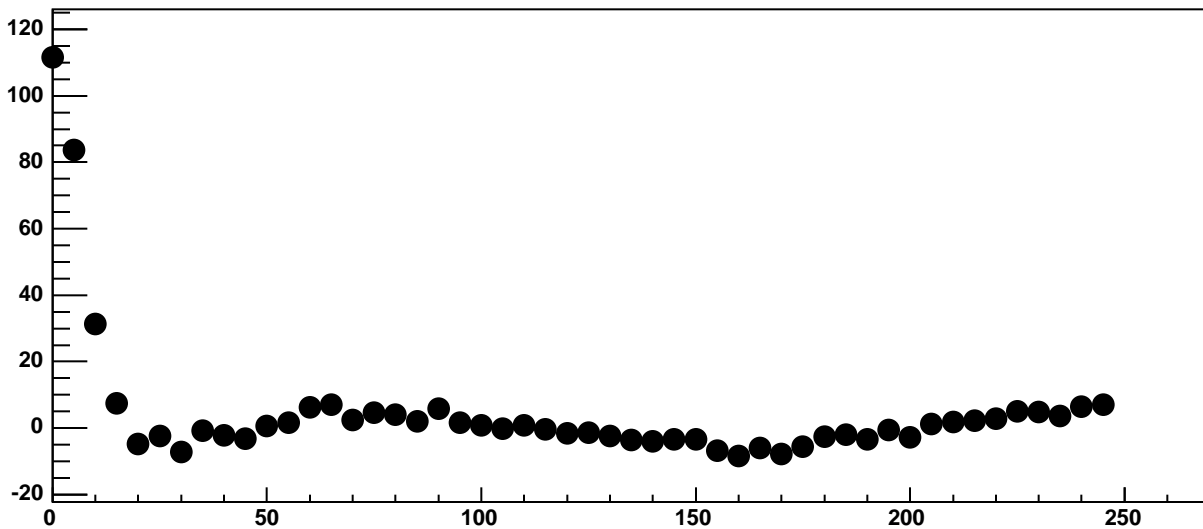


$\chi^2 / \text{ndf}$	321.2 / 41
p0	$453.6 \pm 0.5054$
p1	$4.146 \pm 0.2941$
p2	$259.7 \pm 0.5505$
p3	$19.26 \pm 0.3606$
p4	$-0.2677 \pm 0.005781$

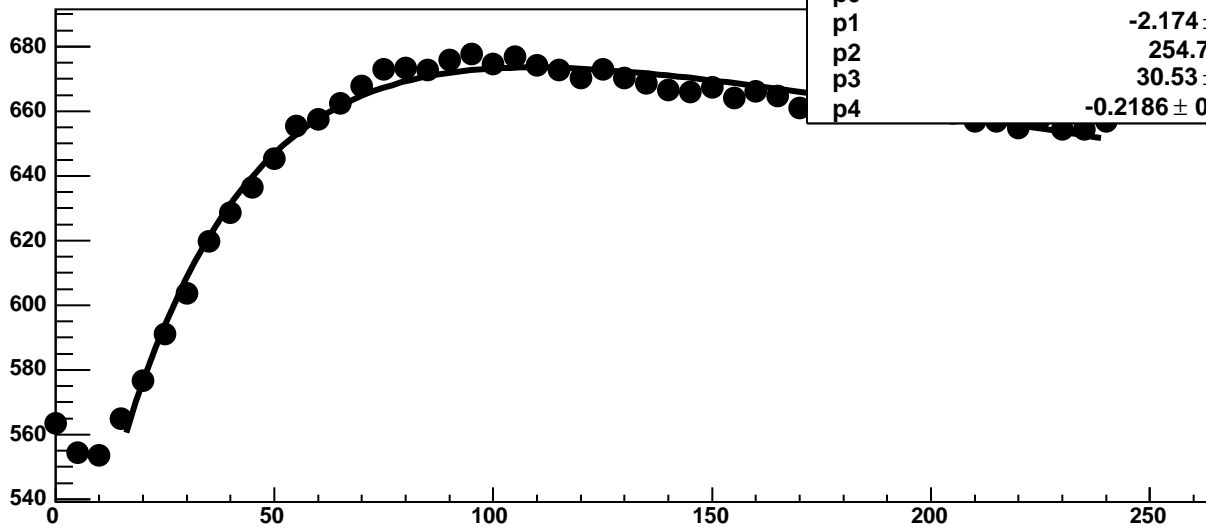
Chip 1, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

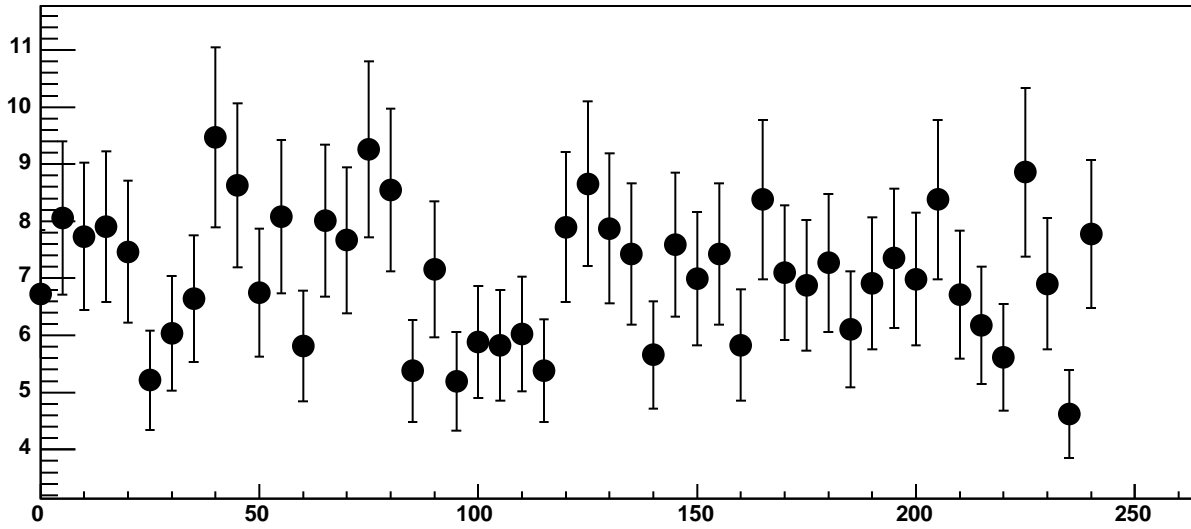


Chip 1, Channel 7, Enable 5, DAC=4000, ADC Mean vs Hold

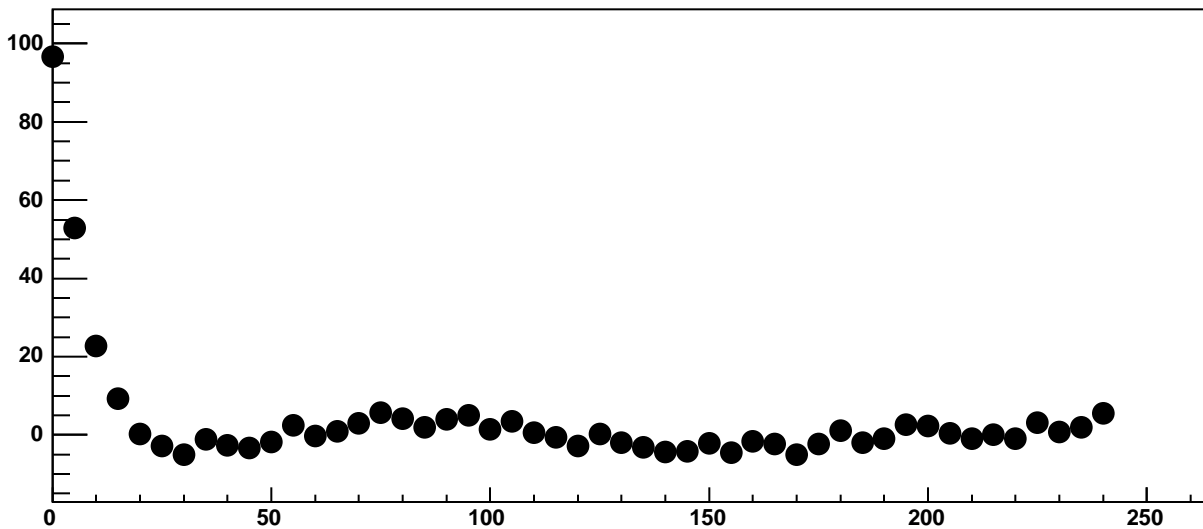


$\chi^2 / \text{ndf}$	173 / 41
p0	$449.8 \pm 2.892$
p1	$-2.174 \pm 0.5642$
p2	$254.7 \pm 2.979$
p3	$30.53 \pm 0.6652$
p4	$-0.2186 \pm 0.007774$

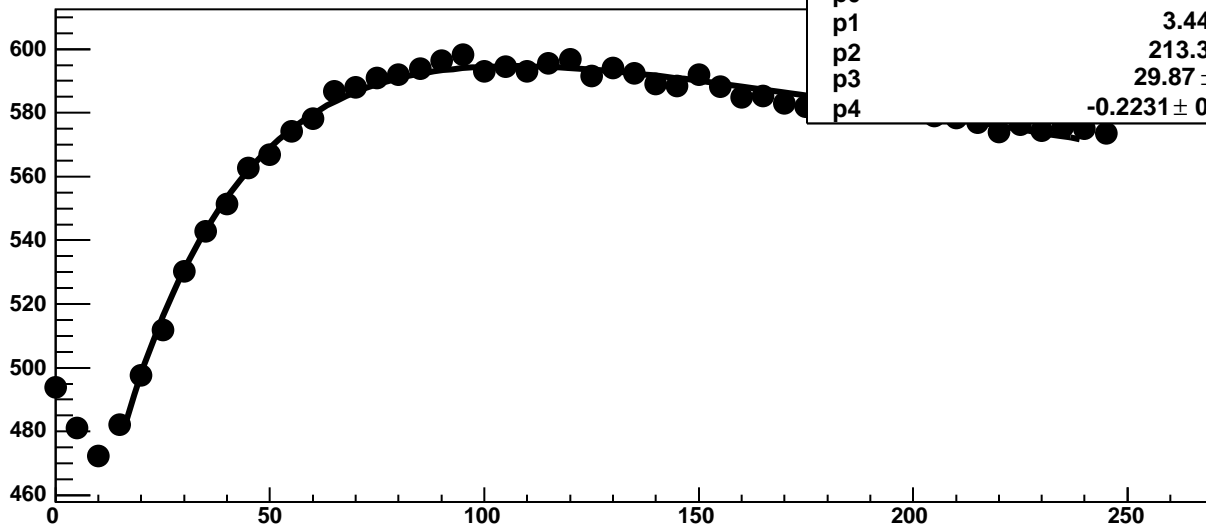
Chip 1, Channel 7, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 7, Enable 5, DAC=4000, ADC Residuals vs Hold

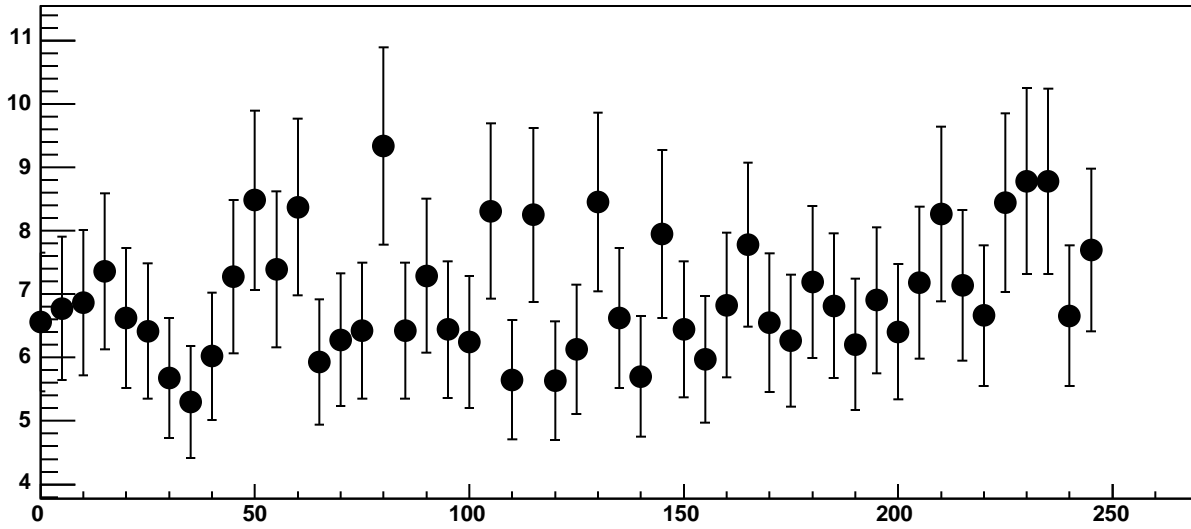


Chip 1, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

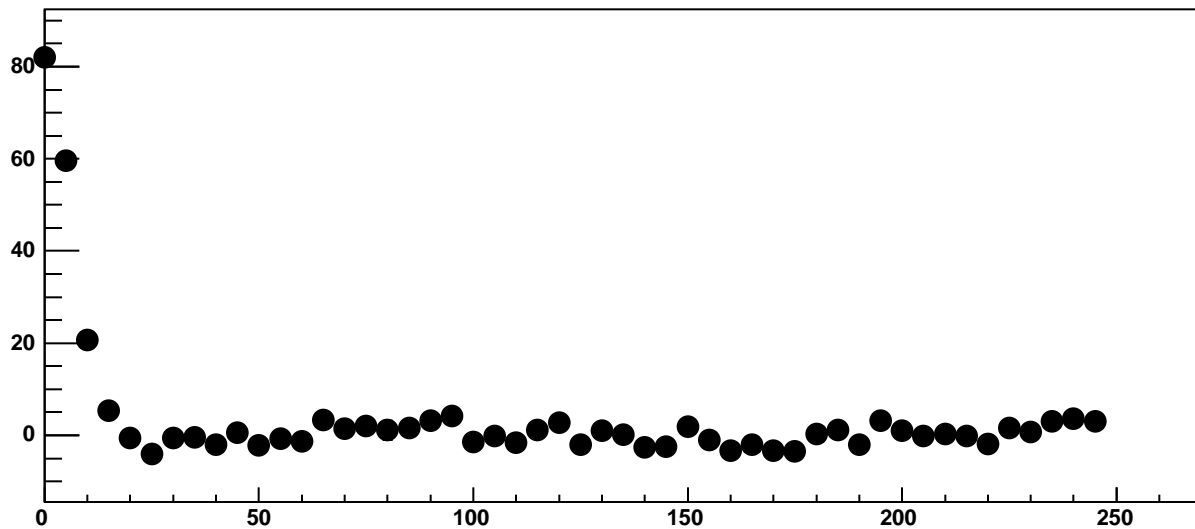


$\chi^2 / \text{ndf}$	95.14 / 41
p0	$411 \pm 2.916$
p1	$3.44 \pm 0.539$
p2	$213.3 \pm 3.028$
p3	$29.87 \pm 0.6555$
p4	$-0.2231 \pm 0.008419$

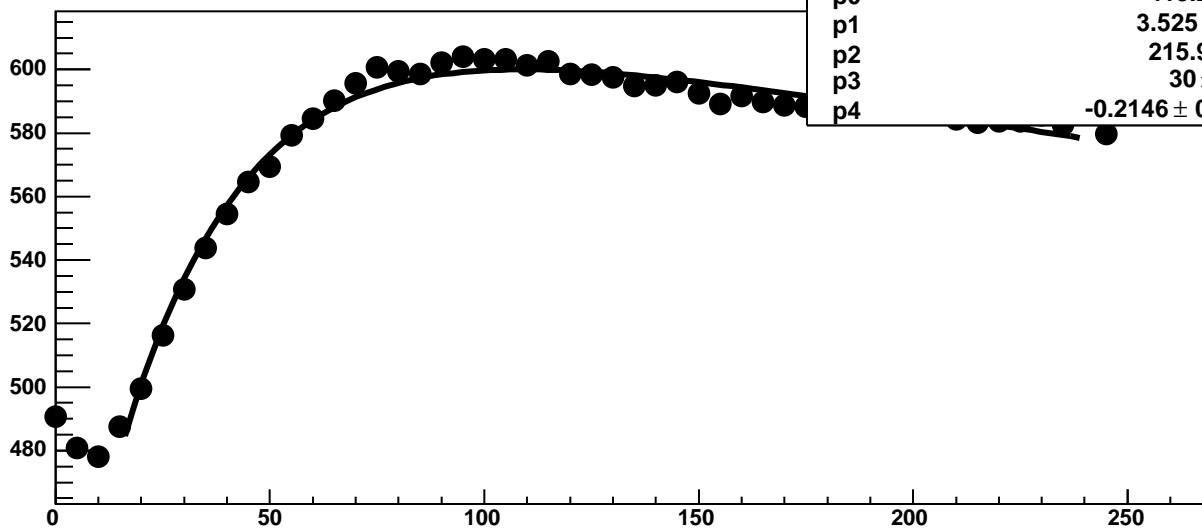
Chip 1, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

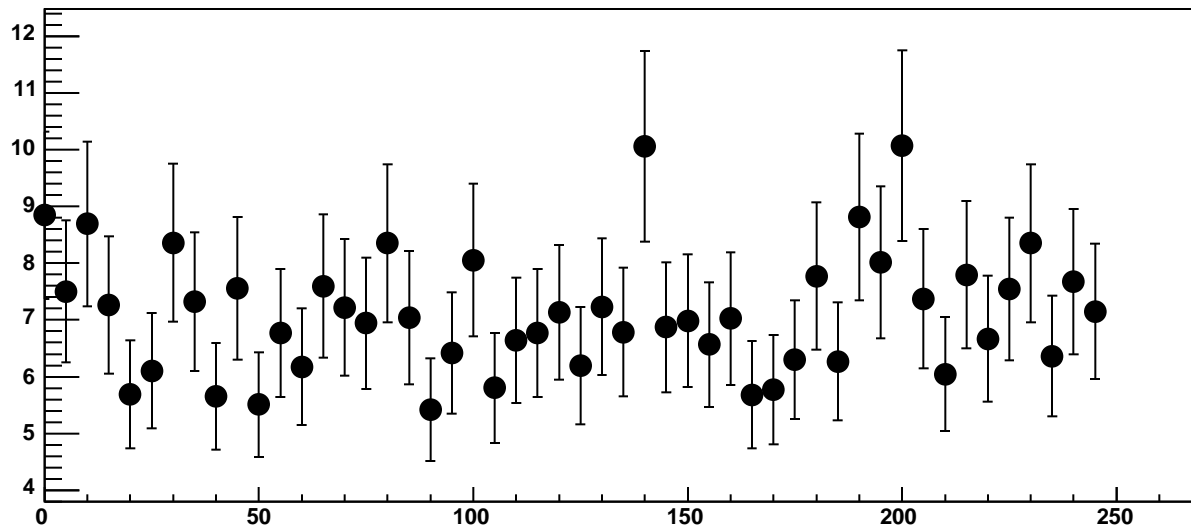


Chip 1, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

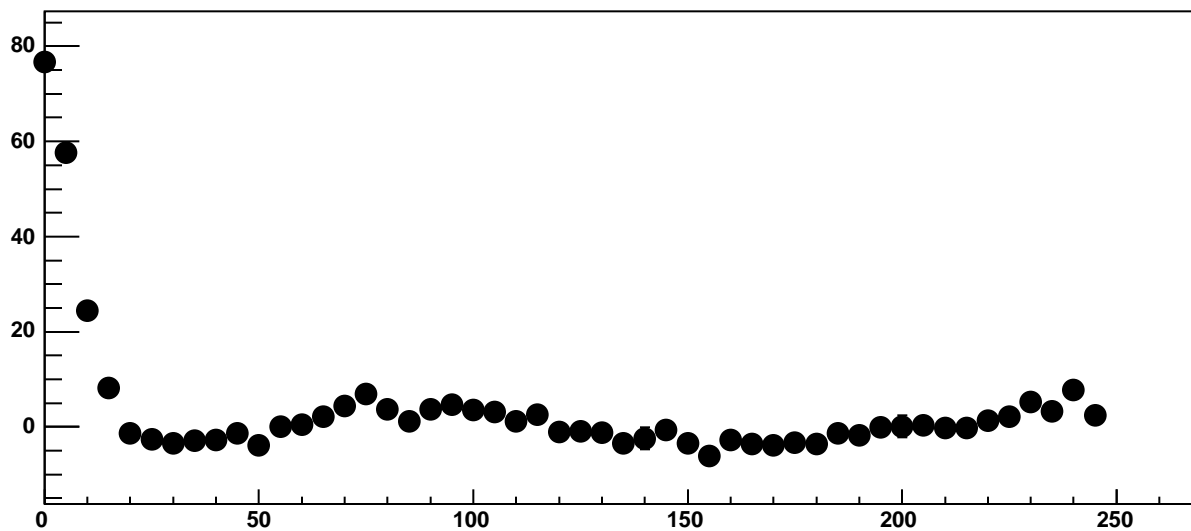


$\chi^2 / \text{ndf}$	205.8 / 41
p0	$413.2 \pm 2.929$
p1	$3.525 \pm 0.5192$
p2	$215.9 \pm 3.037$
p3	$30 \pm 0.6134$
p4	$-0.2146 \pm 0.008196$

Chip 1, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold

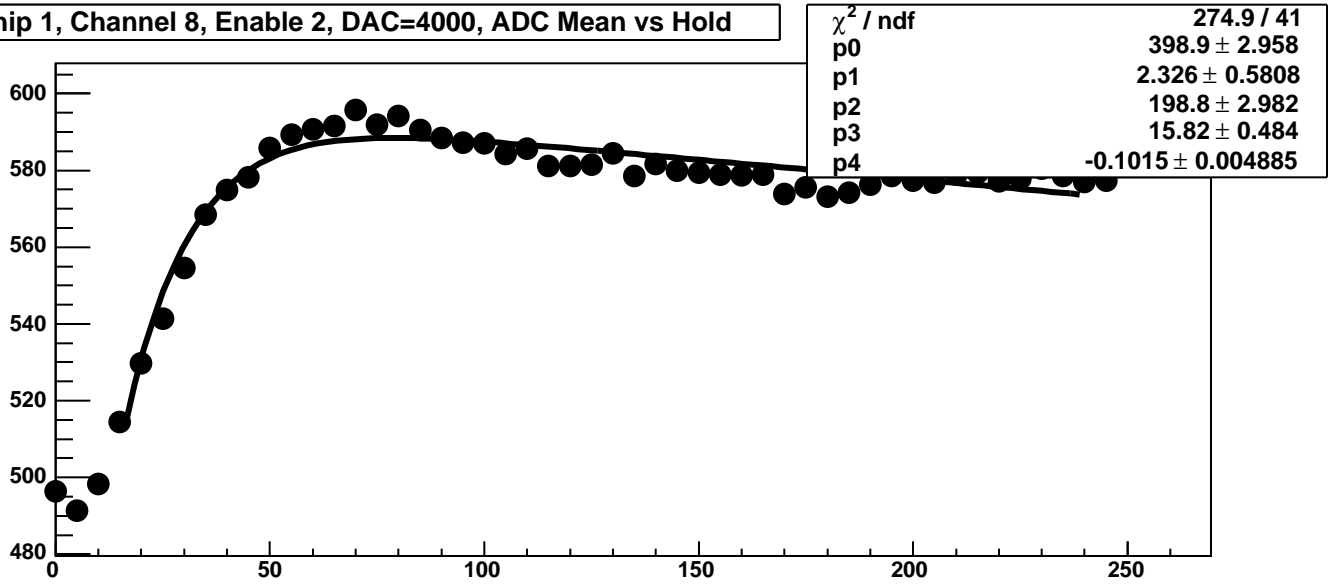


Chip 1, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

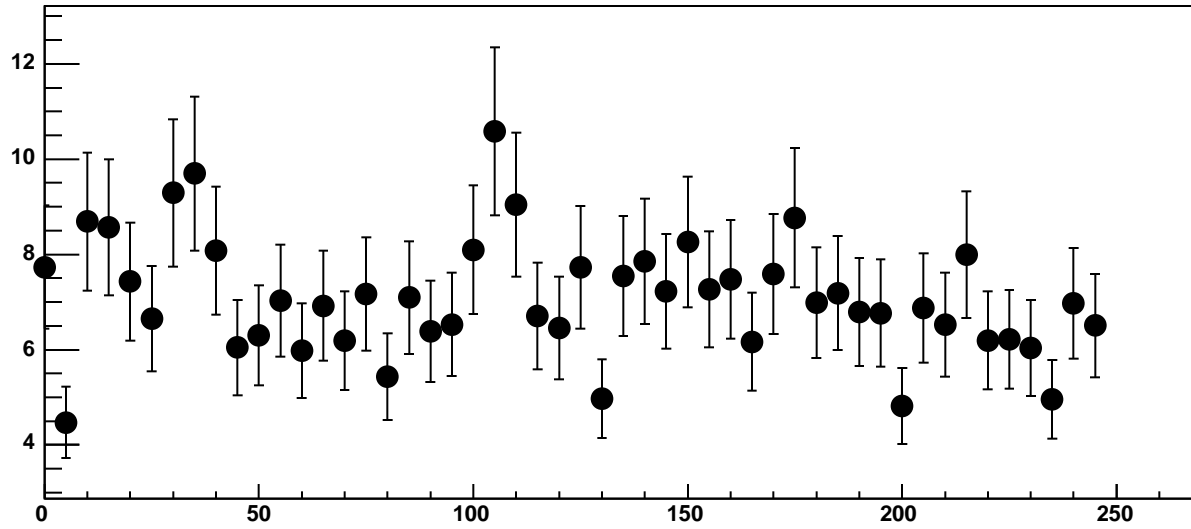




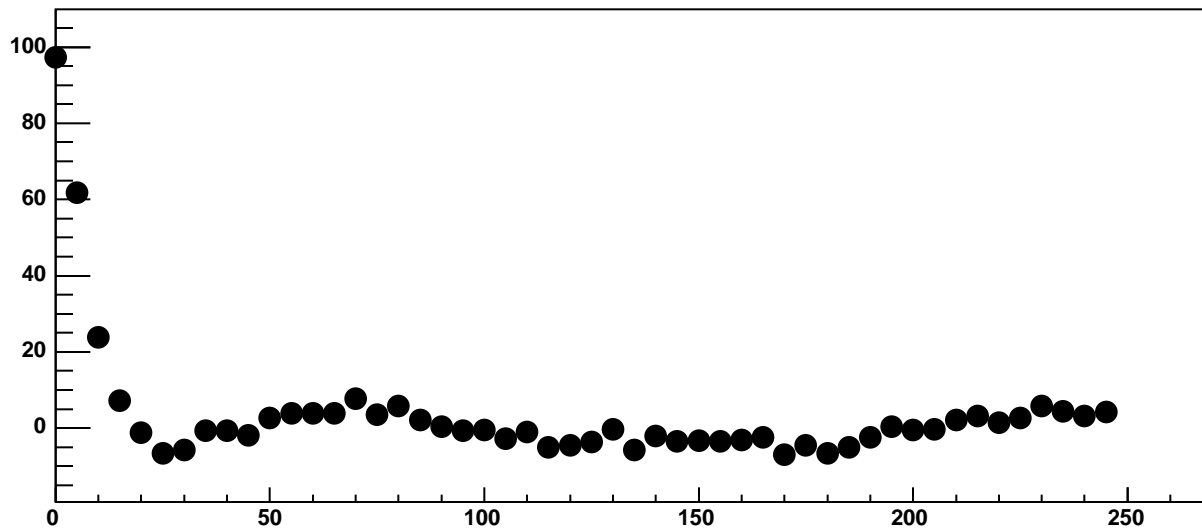
Chip 1, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold



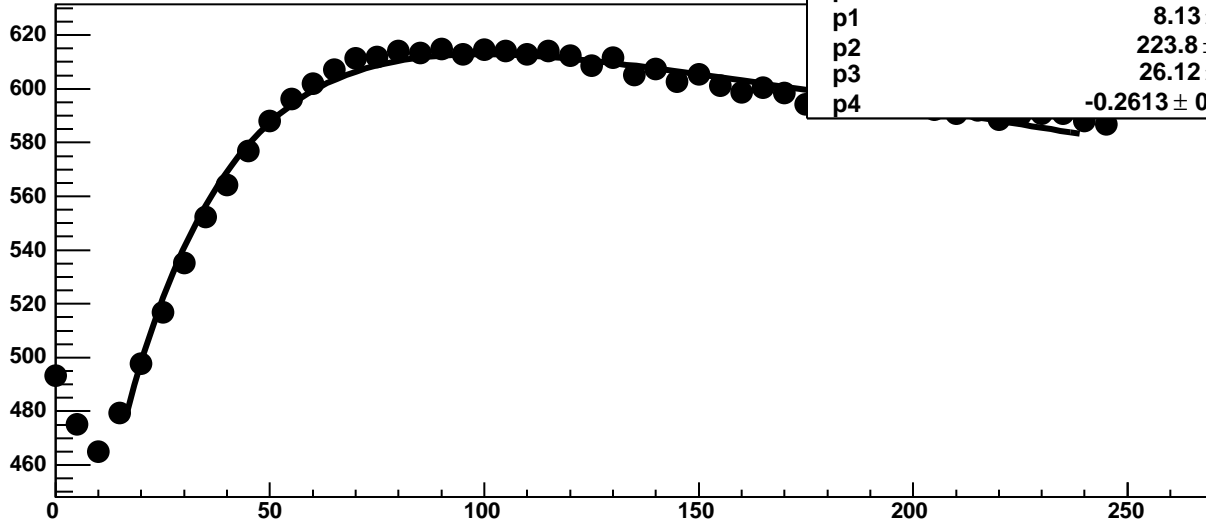
Chip 1, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold

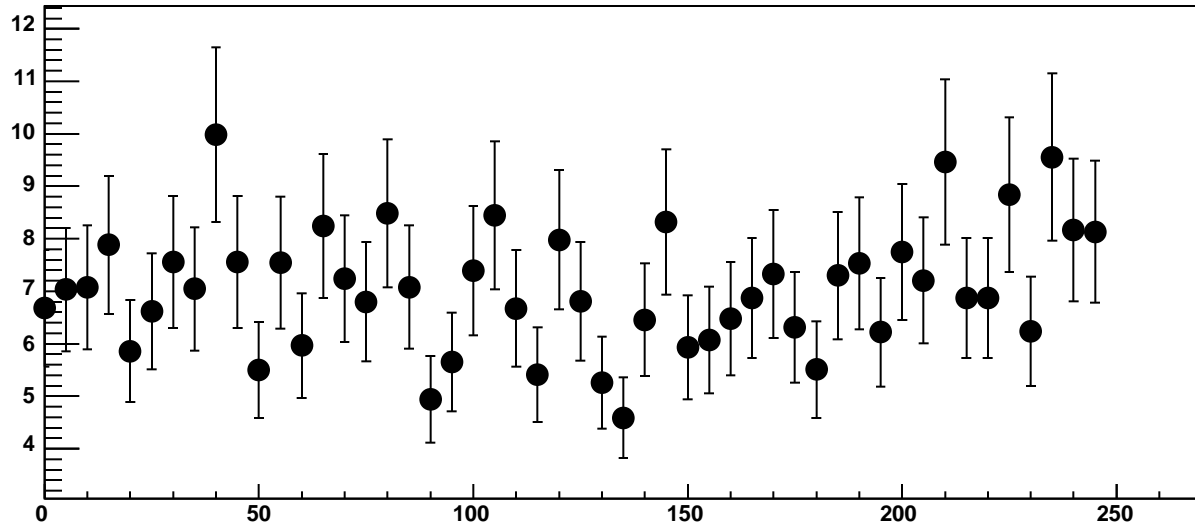


Chip 1, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

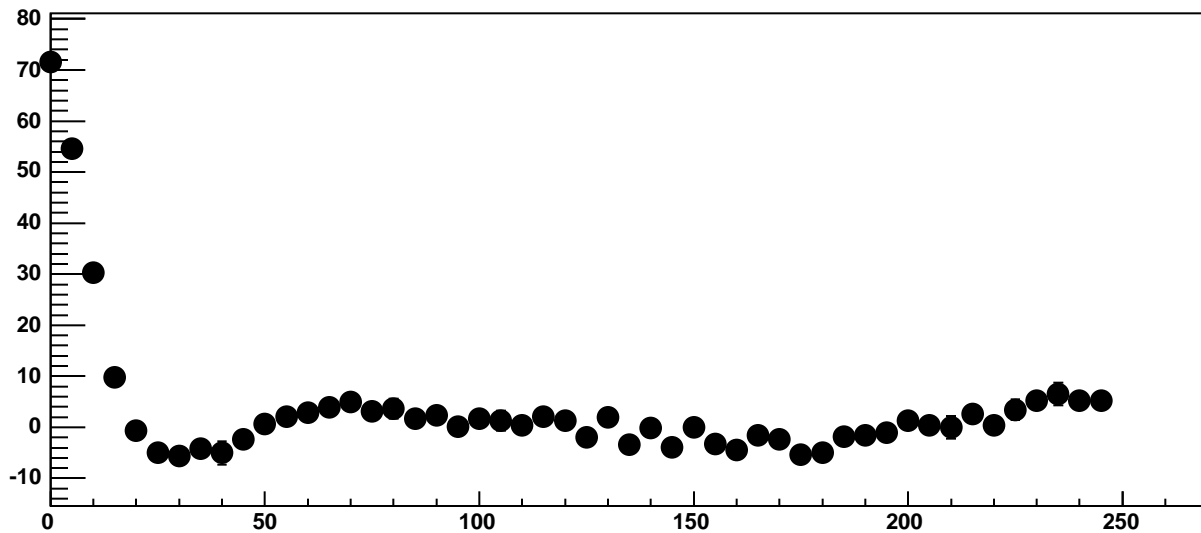


$\chi^2 / \text{ndf}$	205.4 / 41
p0	$419.7 \pm 0.6313$
p1	$8.13 \pm 0.2371$
p2	$223.8 \pm 0.7069$
p3	$26.12 \pm 0.4671$
p4	$-0.2613 \pm 0.007685$

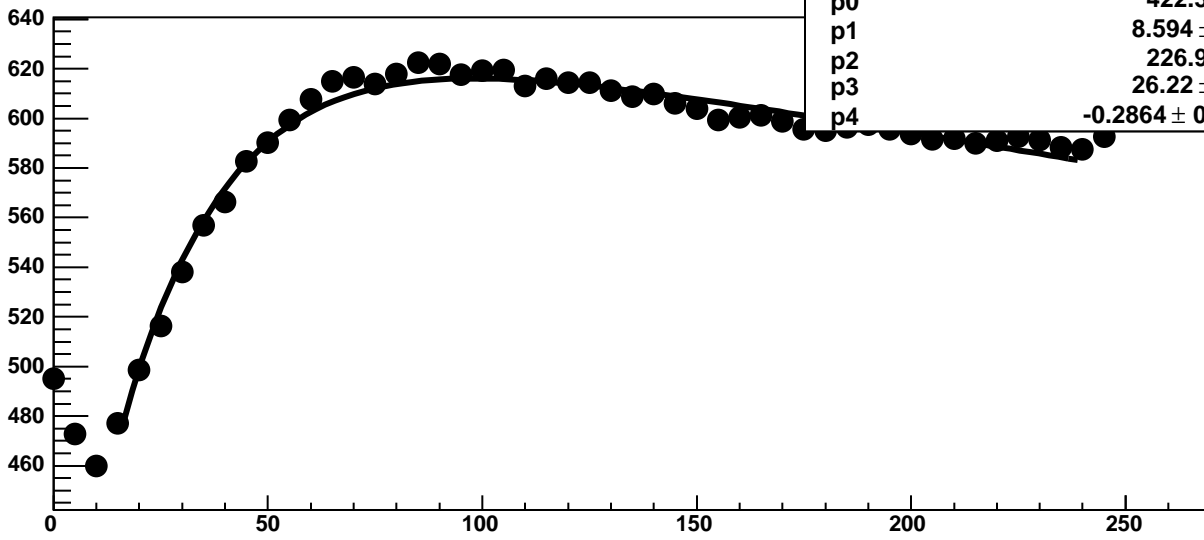
Chip 1, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

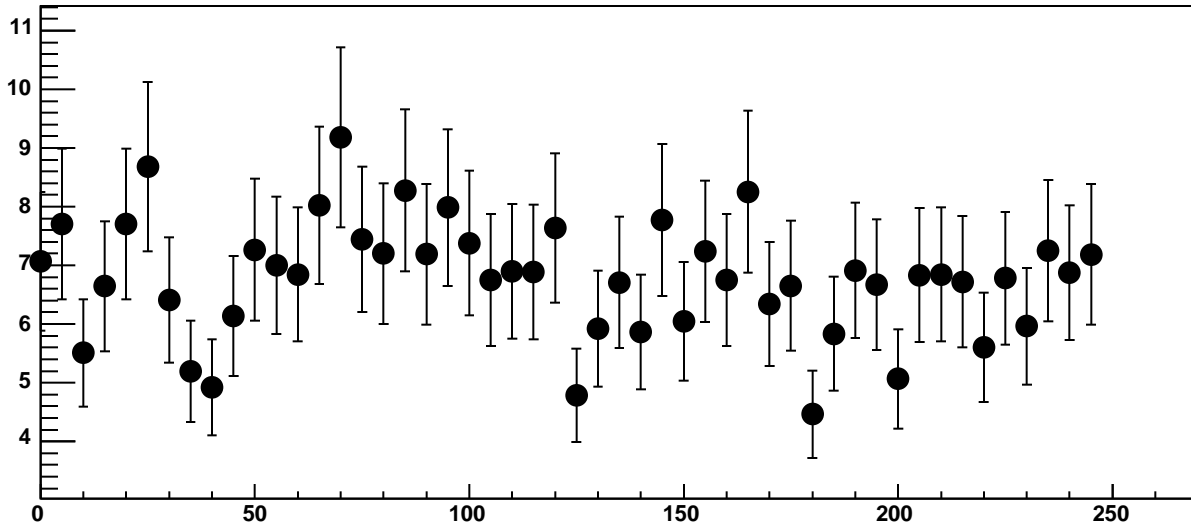


Chip 1, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

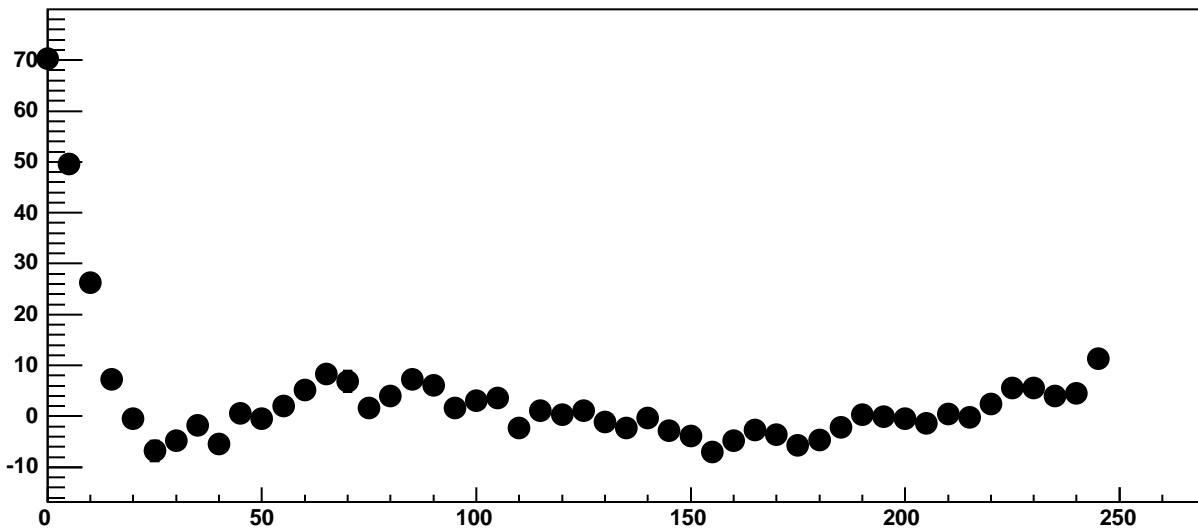


$\chi^2 / \text{ndf}$	295.9 / 41
p0	$422.3 \pm 2.826$
p1	$8.594 \pm 0.3964$
p2	$226.9 \pm 2.933$
p3	$26.22 \pm 0.4076$
p4	$-0.2864 \pm 0.006975$

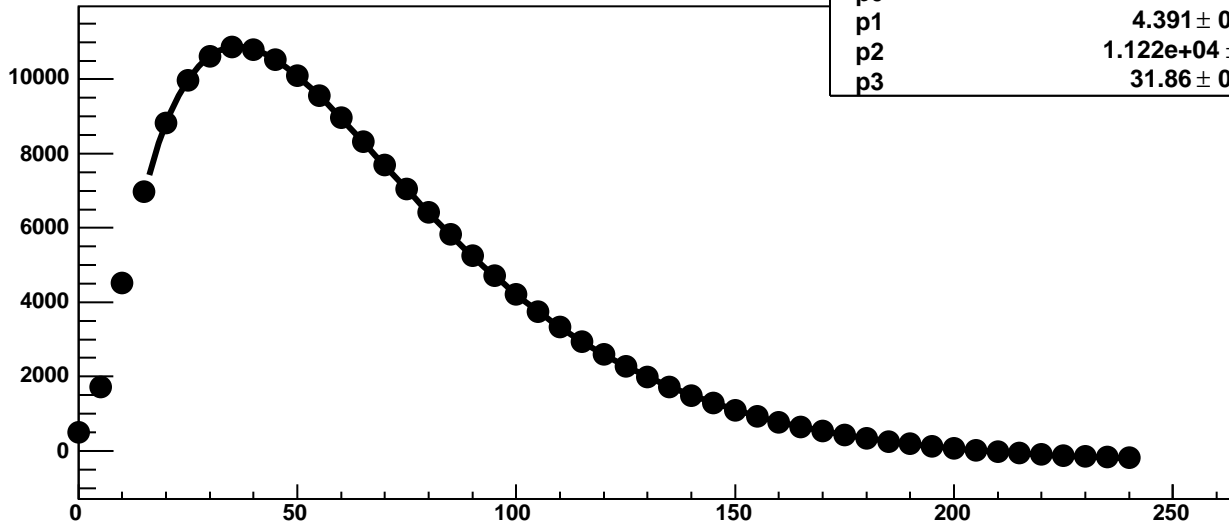
Chip 1, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

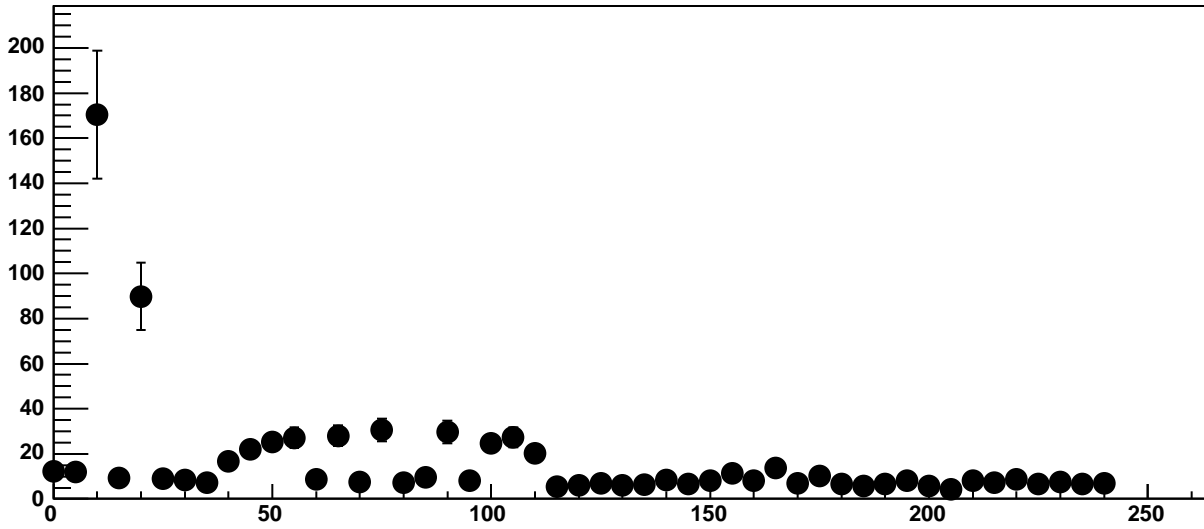


Chip 1, Channel 8, Enable 5!, DAC=4000, ADC Mean vs Hold

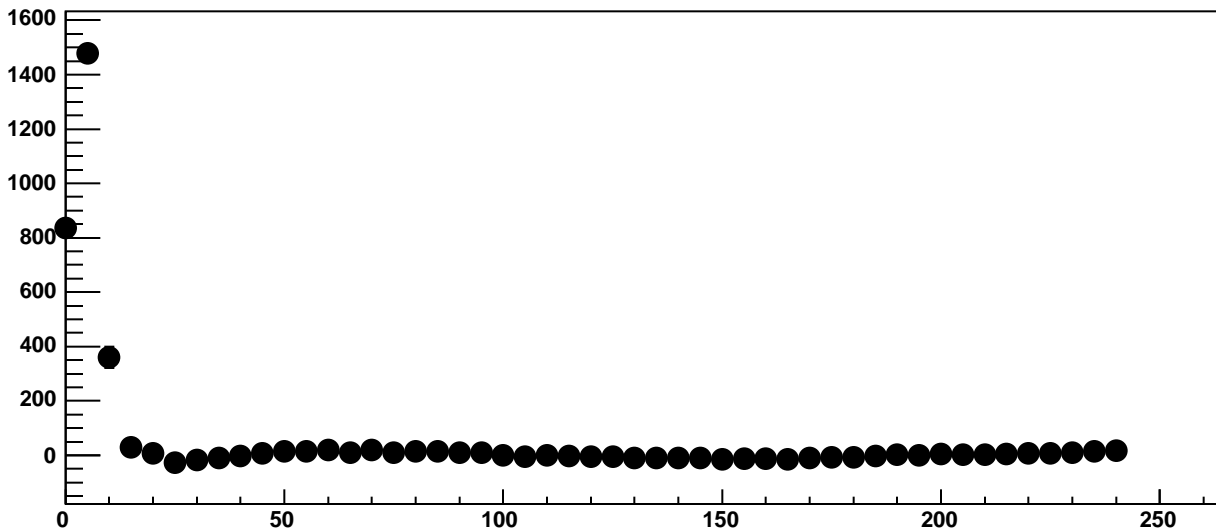


$\chi^2 / \text{ndf}$	1585 / 42
p0	-340.1 ± 0.5302
p1	4.391 ± 0.005863
p2	1.122e+04 ± 0.9658
p3	31.86 ± 0.004435

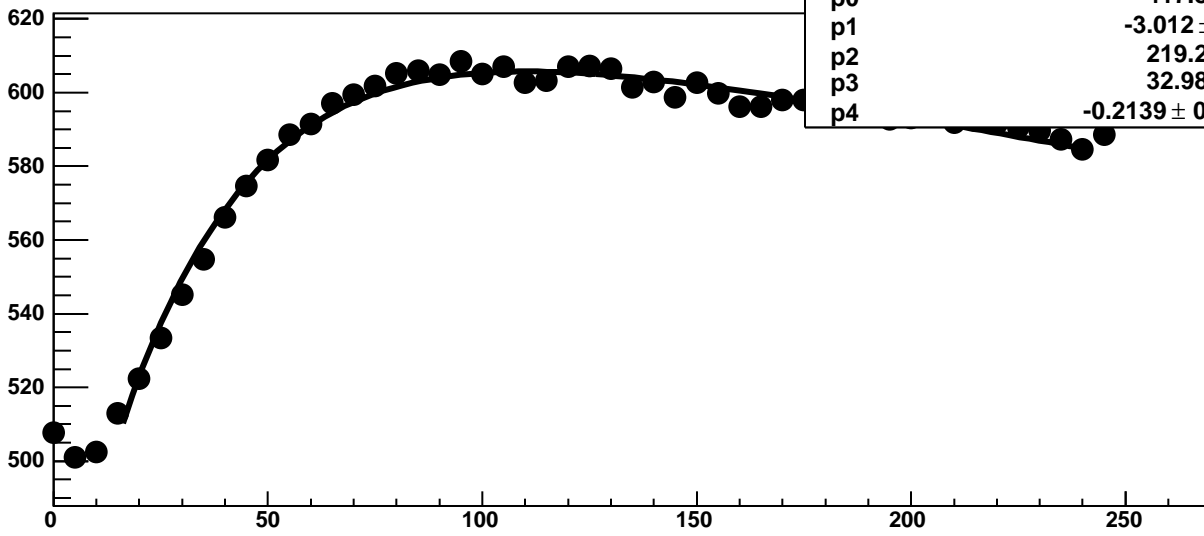
Chip 1, Channel 8, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 8, Enable 5!, DAC=4000, ADC Residuals vs Hold

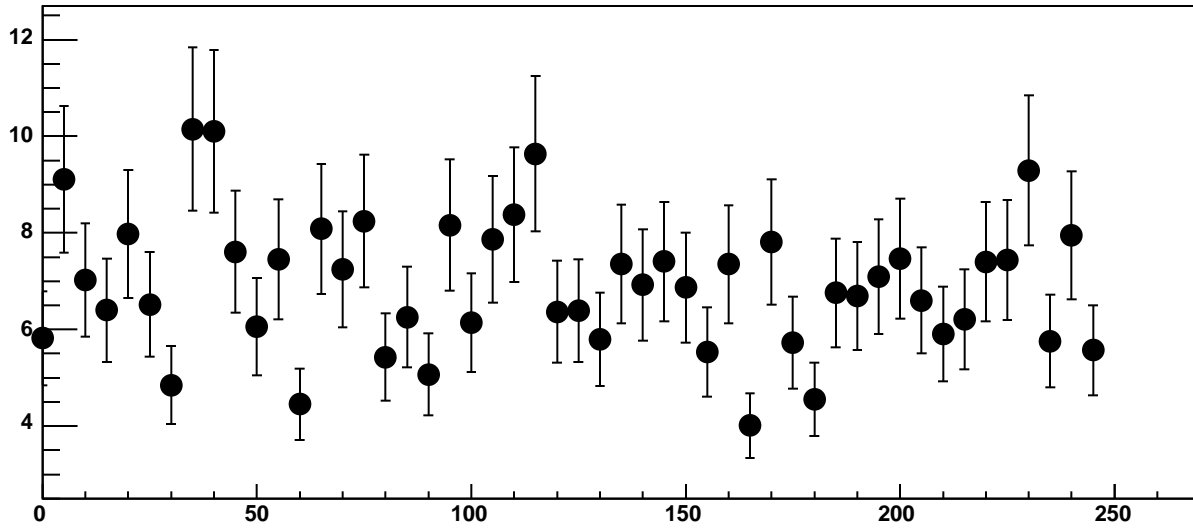


Chip 1, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold

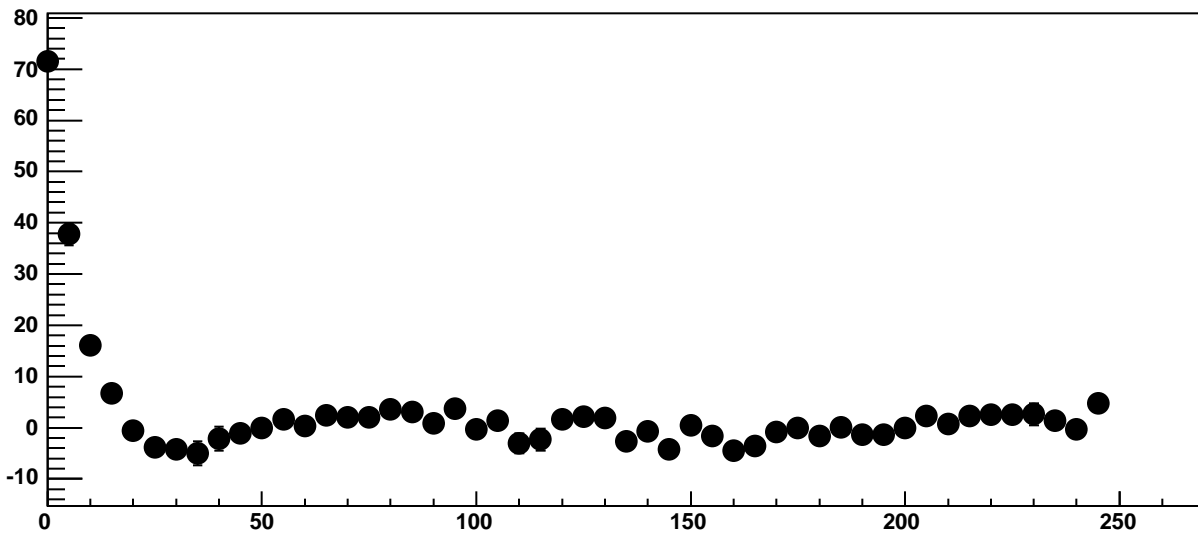


$\chi^2 / \text{ndf}$	128.7 / 41
p0	$417.8 \pm 2.807$
p1	$-3.012 \pm 0.6374$
p2	$219.2 \pm 2.915$
p3	$32.98 \pm 0.794$
p4	$-0.2139 \pm 0.008615$

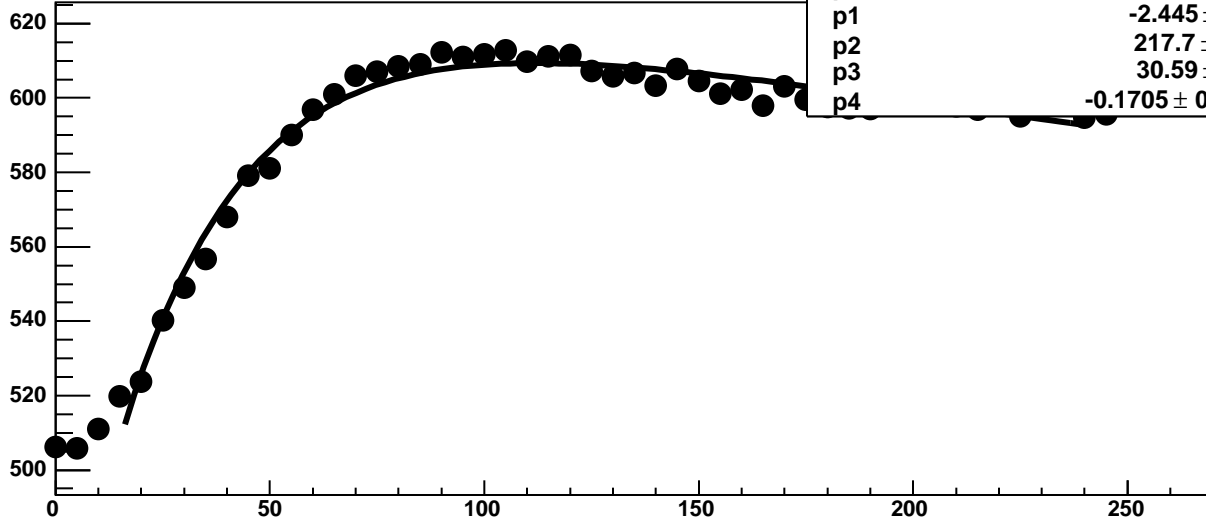
Chip 1, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold

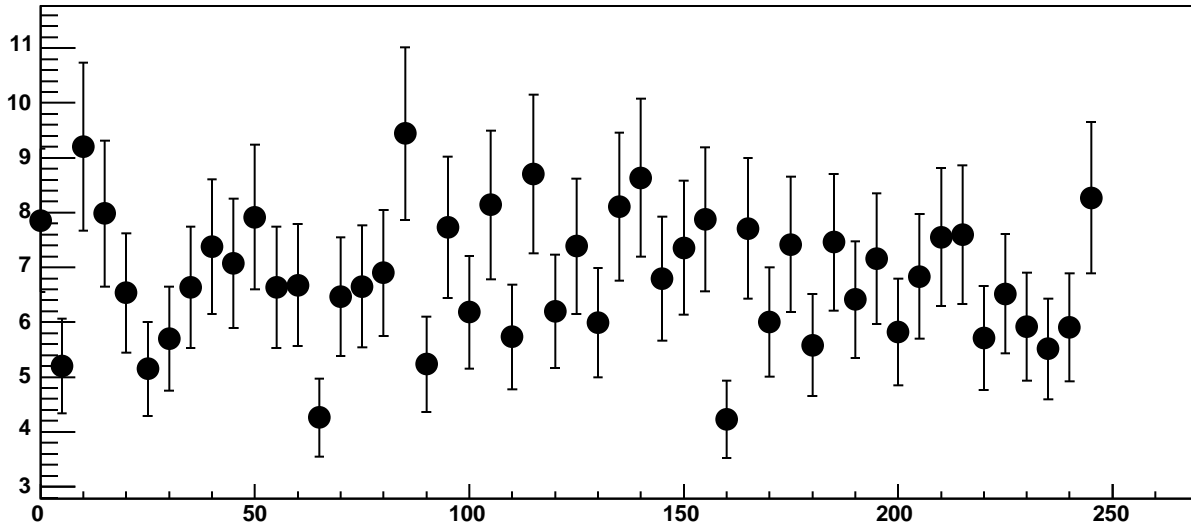


Chip 1, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

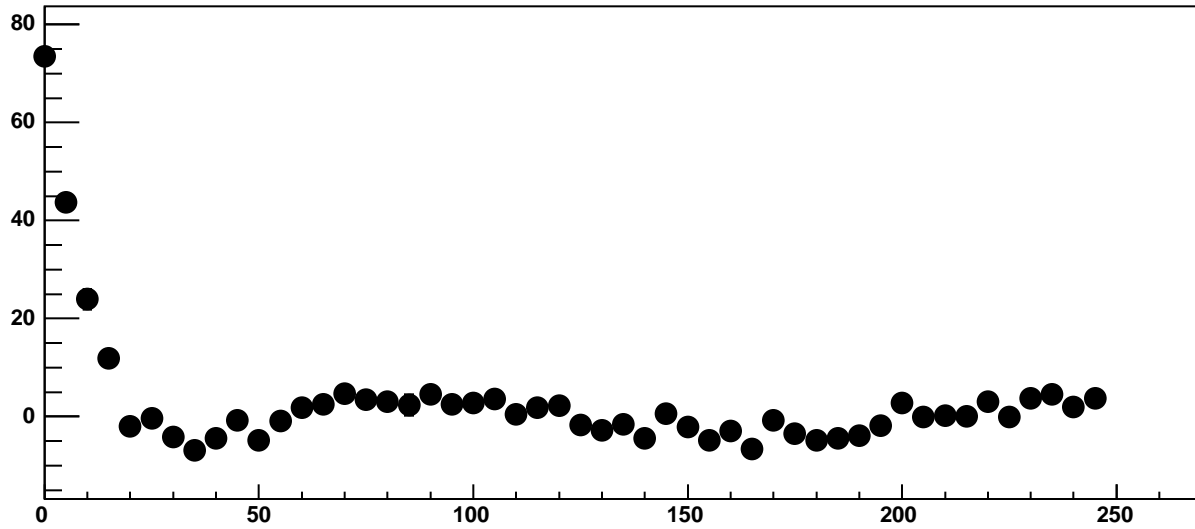


$\chi^2 / \text{ndf}$	253.3 / 41
p0	$416.3 \pm 0.8174$
p1	$-2.445 \pm 0.5097$
p2	$217.7 \pm 0.8786$
p3	$30.59 \pm 0.7907$
p4	$-0.1705 \pm 0.008298$

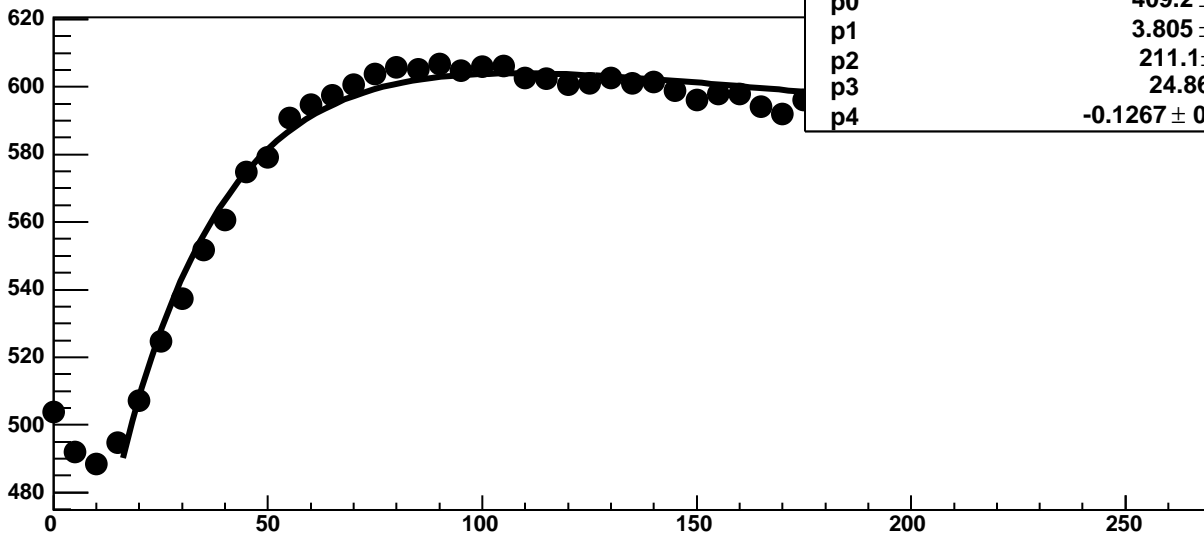
Chip 1, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

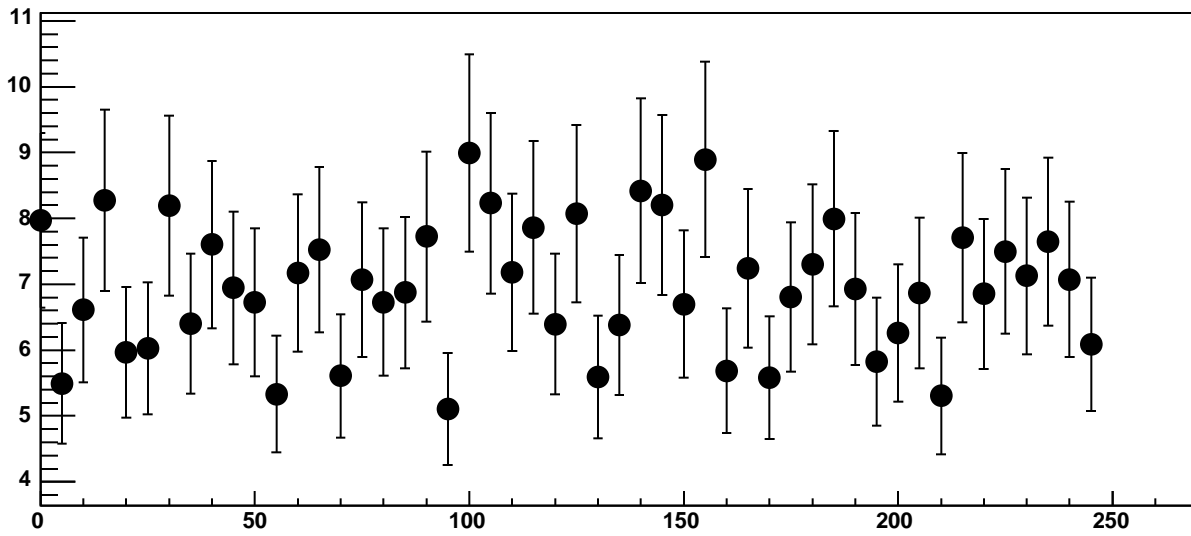


Chip 1, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

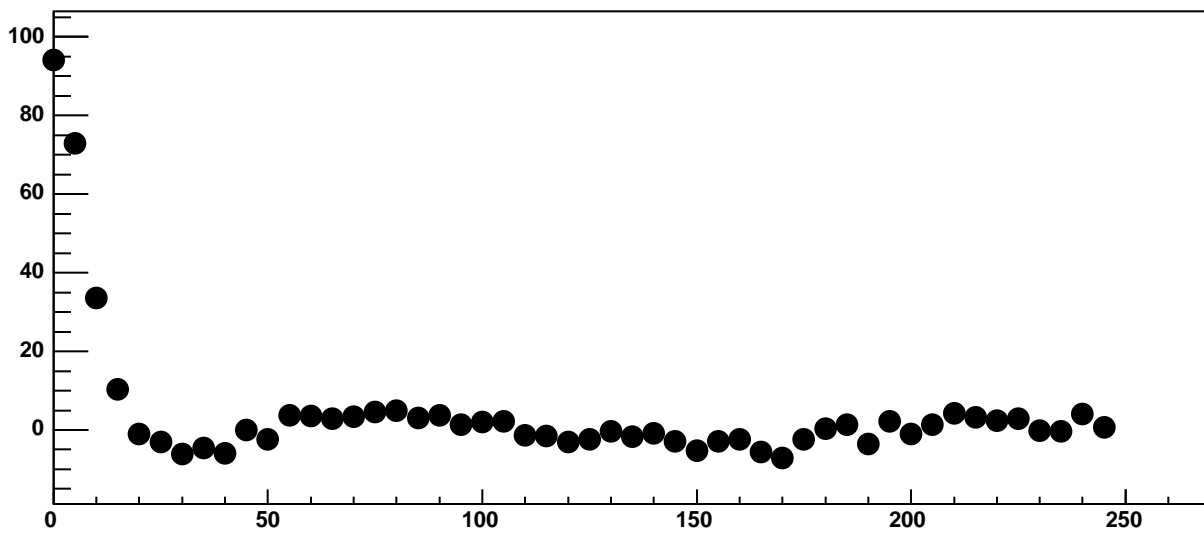


$\chi^2 / \text{ndf}$	232.8 / 41
p0	$409.2 \pm 0.6594$
p1	$3.805 \pm 0.3606$
p2	$211.1 \pm 0.7203$
p3	$24.86 \pm 0.571$
p4	$-0.1267 \pm 0.007485$

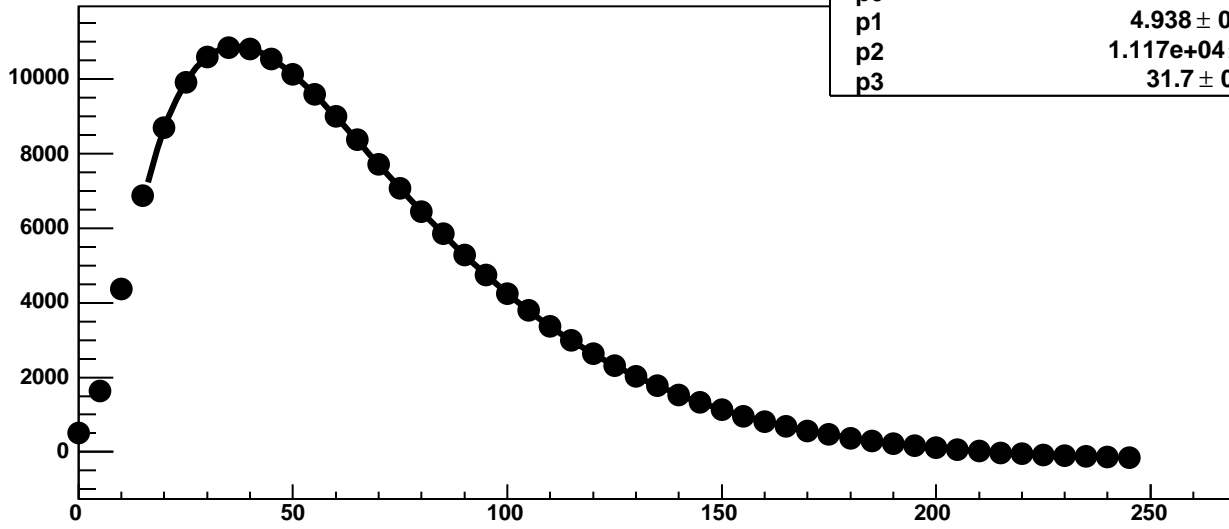
Chip 1, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold

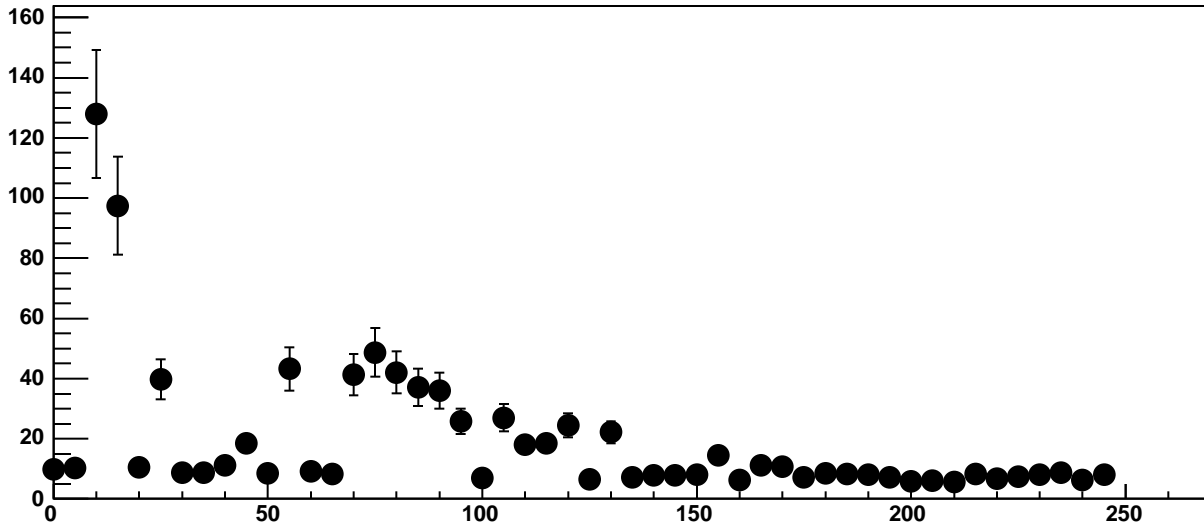


Chip 1, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold

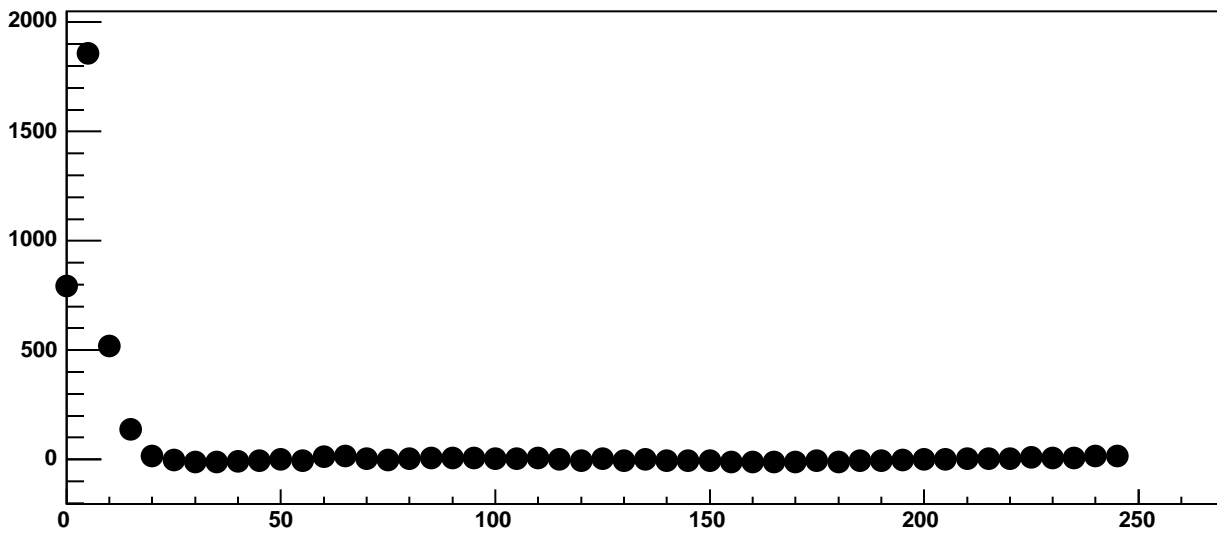


$\chi^2 / \text{ndf}$	677.3 / 42
p0	-288.3 ± 0.6055
p1	4.938 ± 0.009123
p2	1.117e+04 ± 0.9921
p3	31.7 ± 0.006062

Chip 1, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold

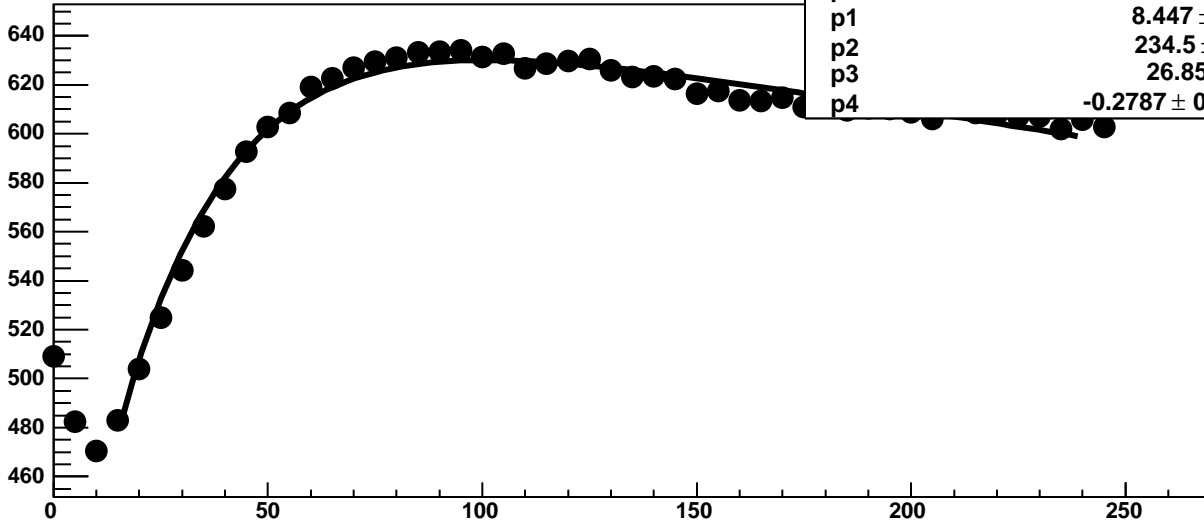


Chip 1, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold



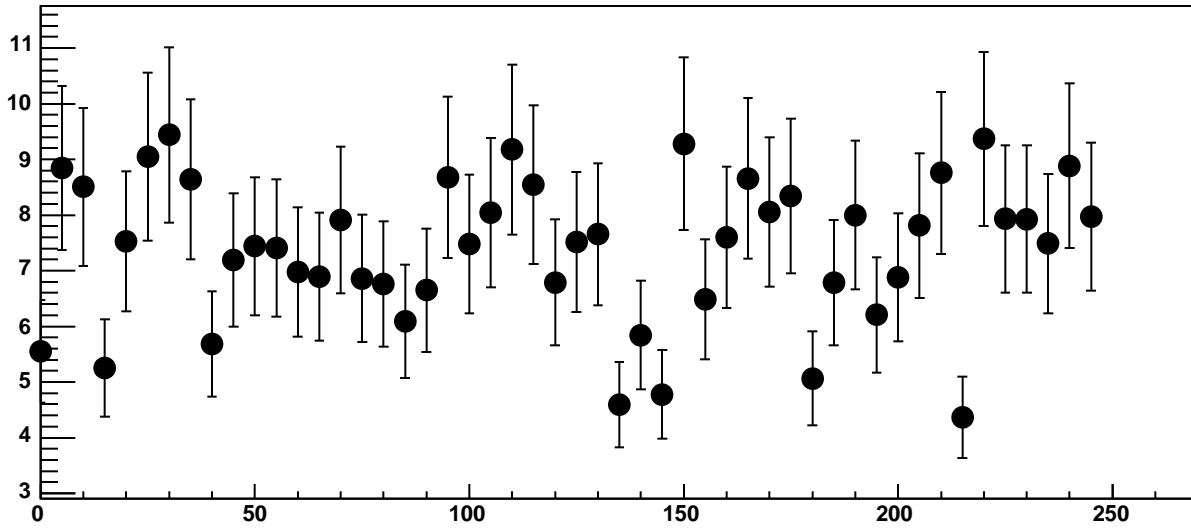


Chip 1, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

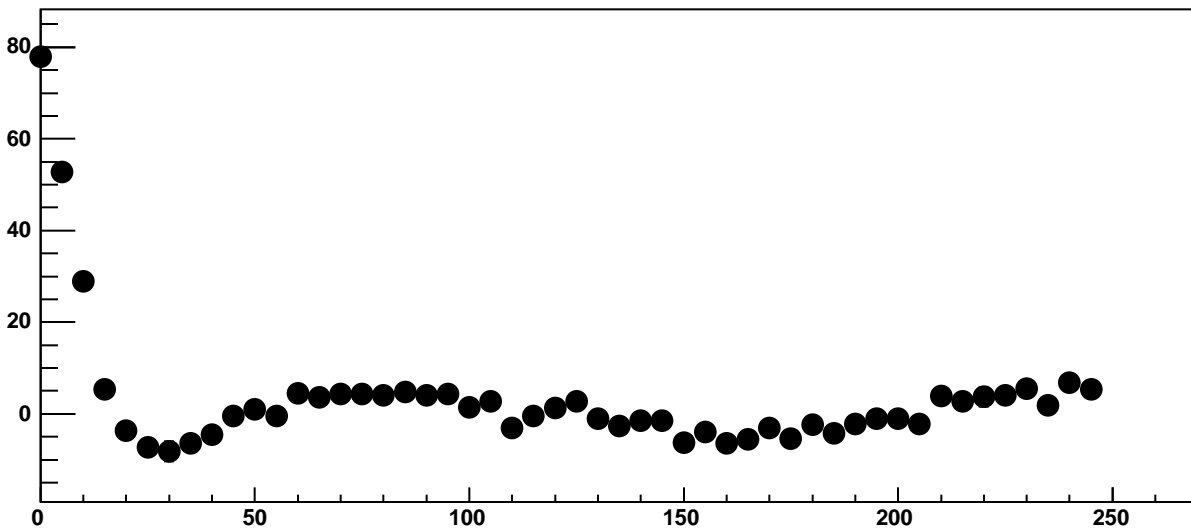


$\chi^2 / \text{ndf}$	252.9 / 41
p0	$428.8 \pm 0.6876$
p1	$8.447 \pm 0.1875$
p2	$234.5 \pm 0.7847$
p3	$26.85 \pm 0.445$
p4	$-0.2787 \pm 0.008267$

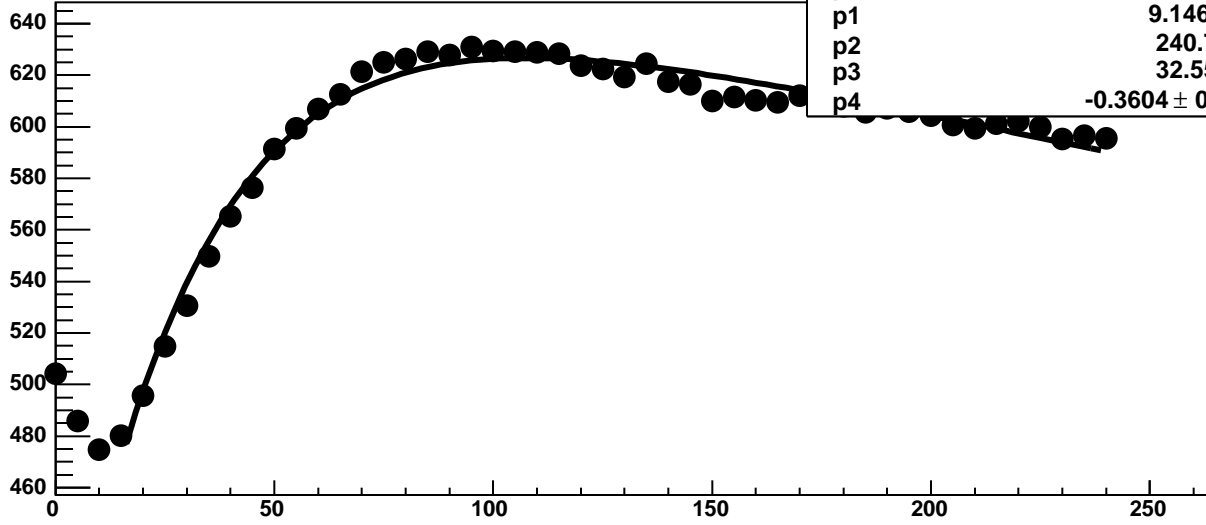
Chip 1, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

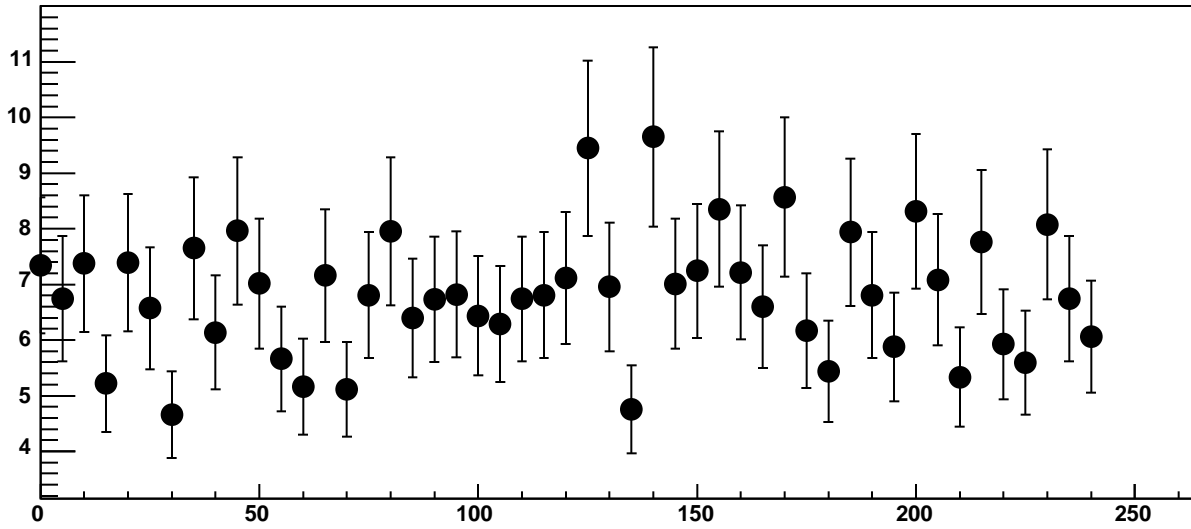


Chip 1, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold

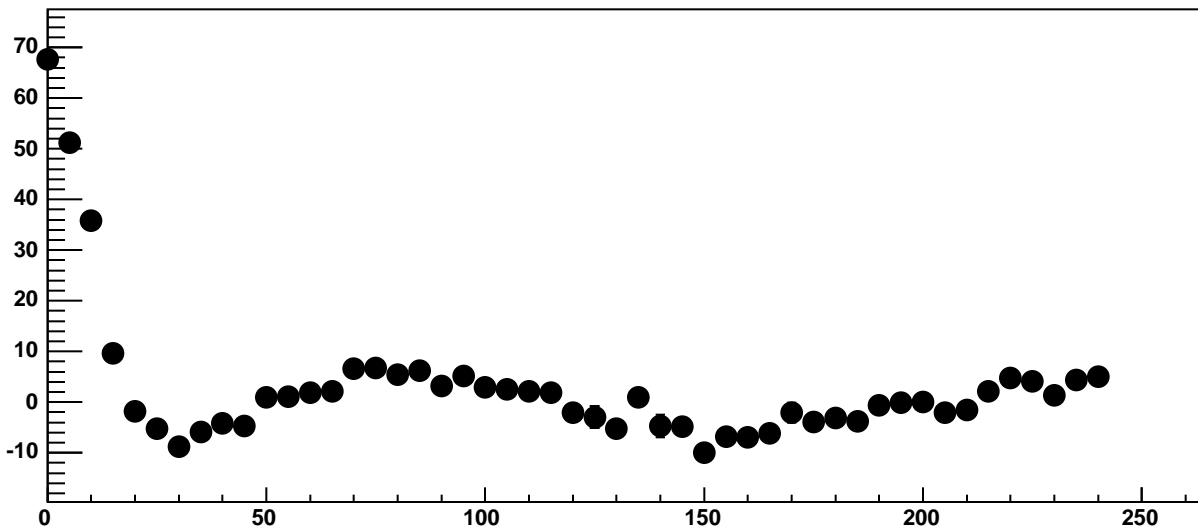


$\chi^2 / \text{ndf}$	461.6 / 41
p0	$433.1 \pm 2.805$
p1	$9.146 \pm 0.425$
p2	$240.7 \pm 2.98$
p3	$32.55 \pm 0.47$
p4	$-0.3604 \pm 0.008276$

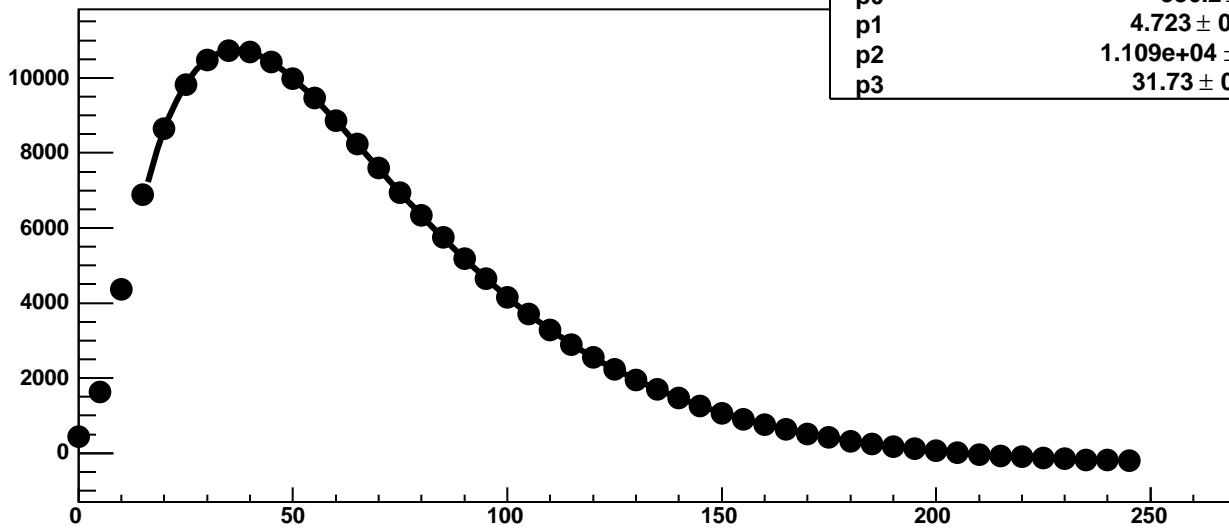
Chip 1, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold

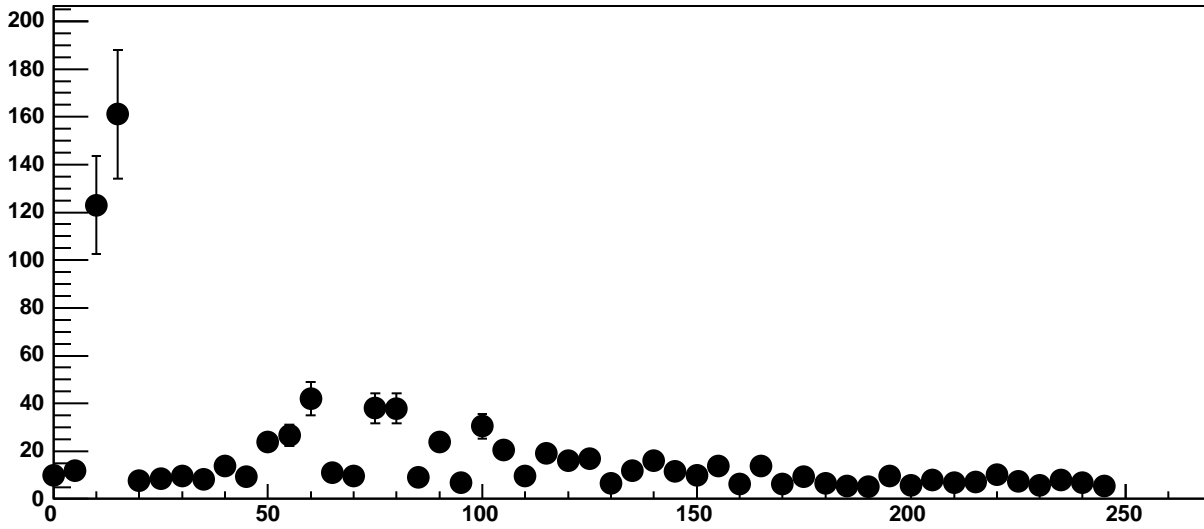


Chip 1, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold

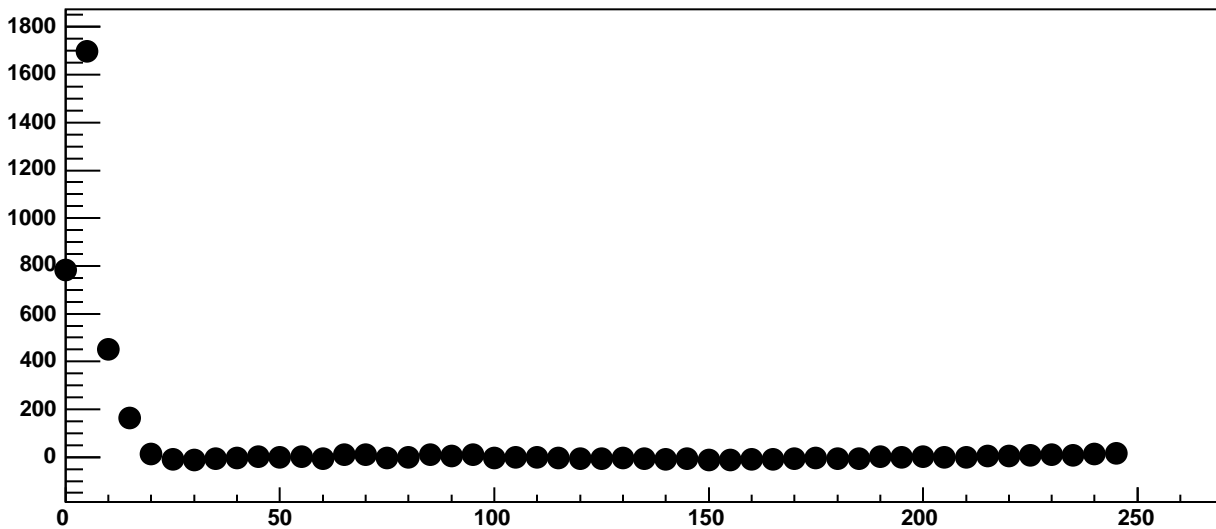


$\chi^2 / \text{ndf}$	573.9 / 42
p0	-336.2 ± 0.5748
p1	4.723 ± 0.008145
p2	1.109e+04 ± 0.9904
p3	31.73 ± 0.005882

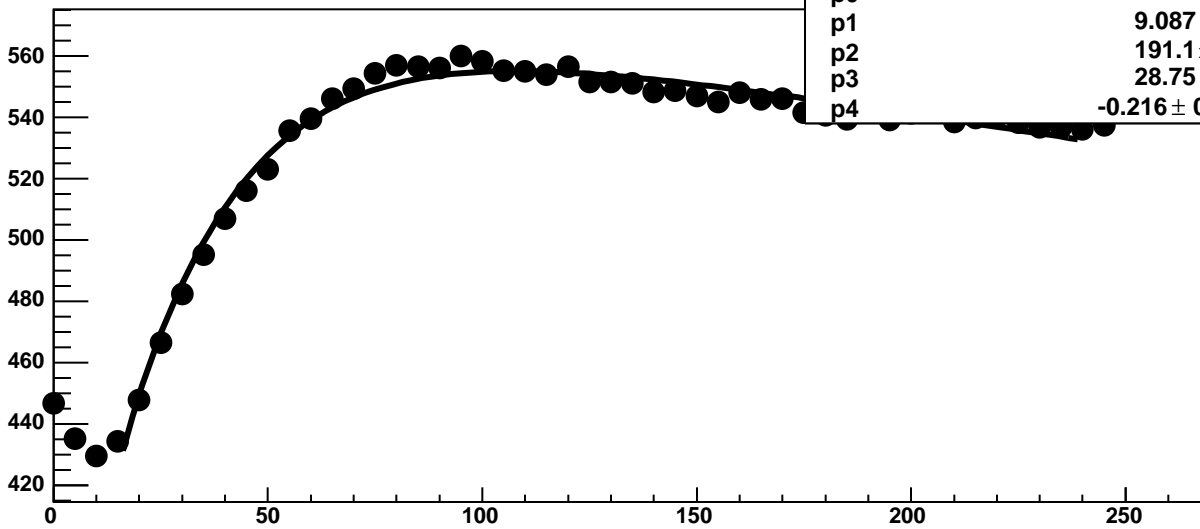
Chip 1, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold

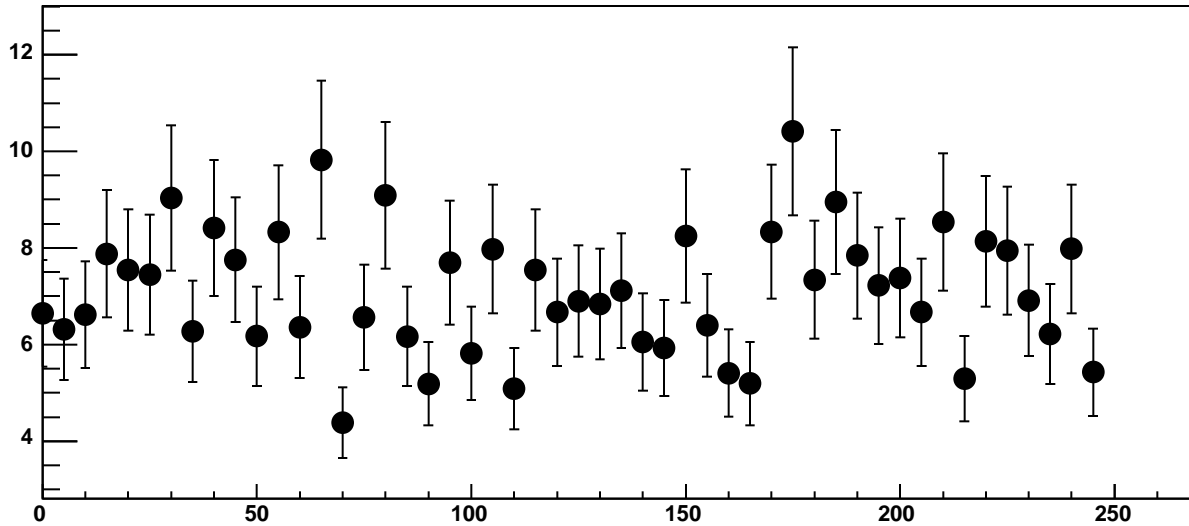


Chip 1, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

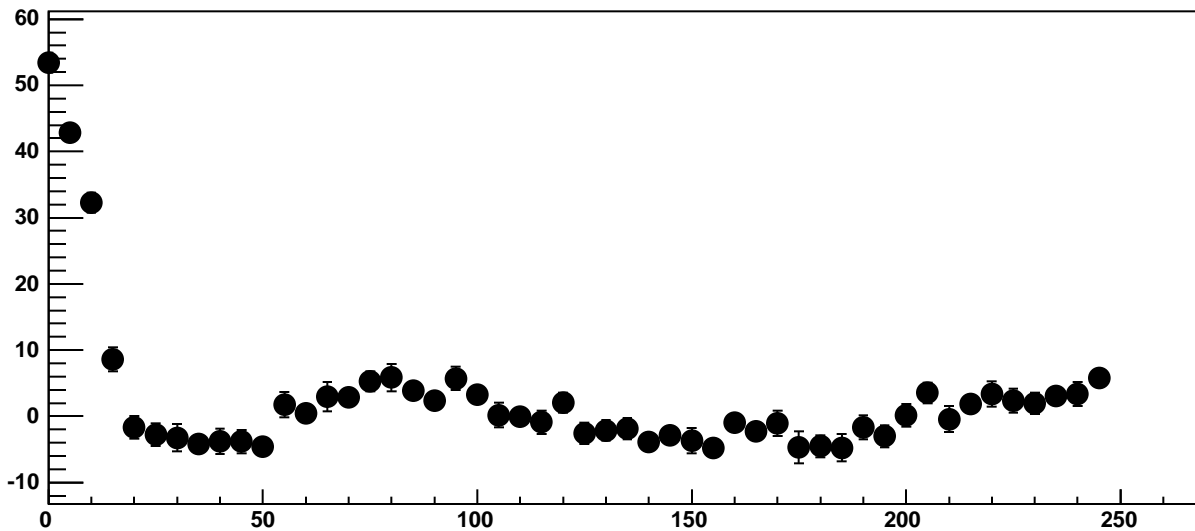


$\chi^2 / \text{ndf}$	197 / 41
p0	$391.4 \pm 0.7355$
p1	$9.087 \pm 0.2803$
p2	$191.1 \pm 0.8065$
p3	$28.75 \pm 0.6087$
p4	$-0.216 \pm 0.008509$

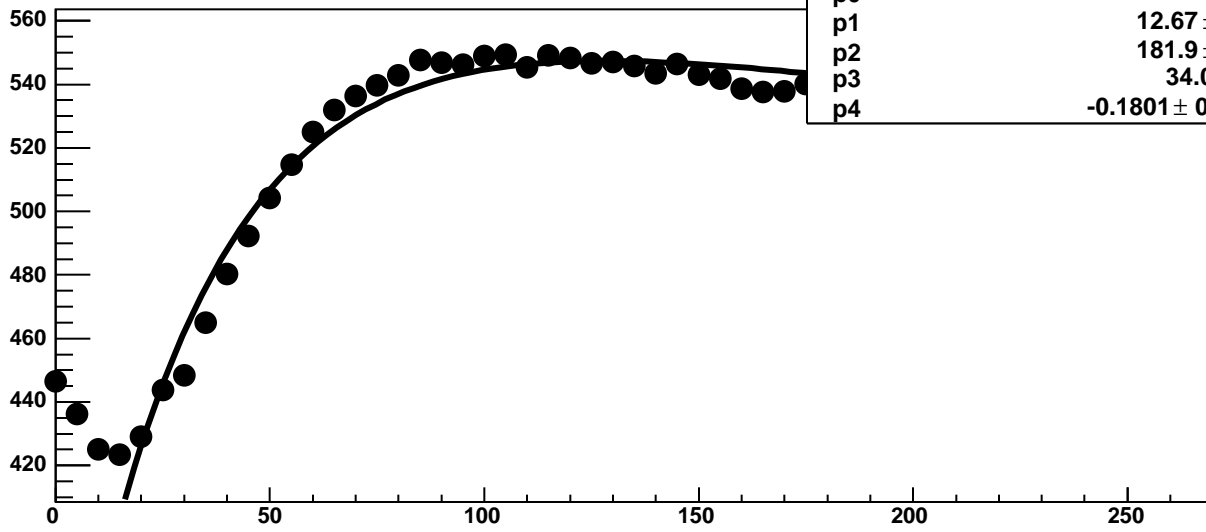
Chip 1, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

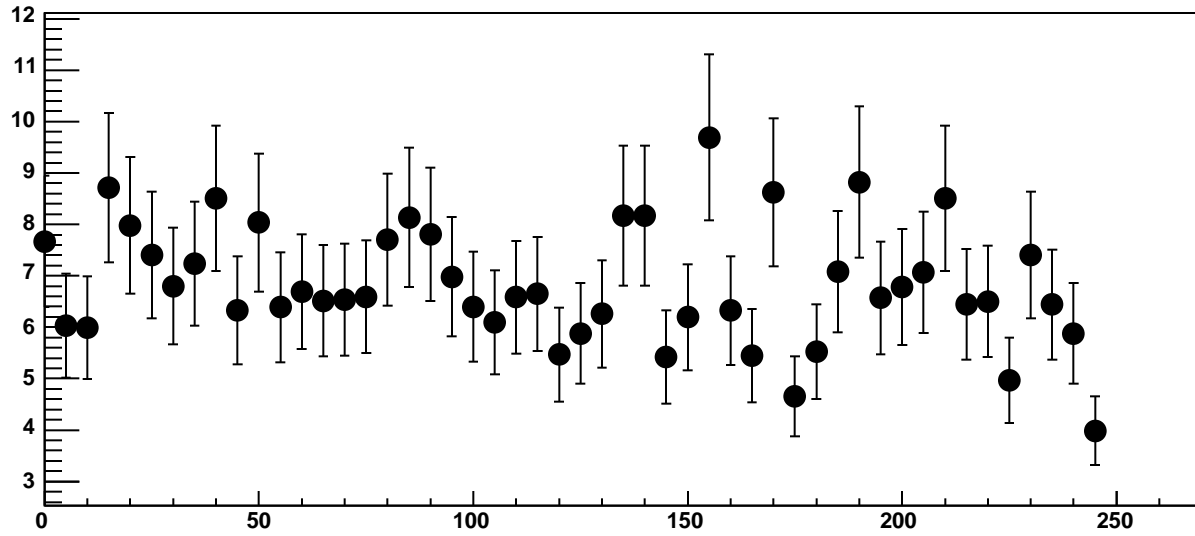


Chip 1, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

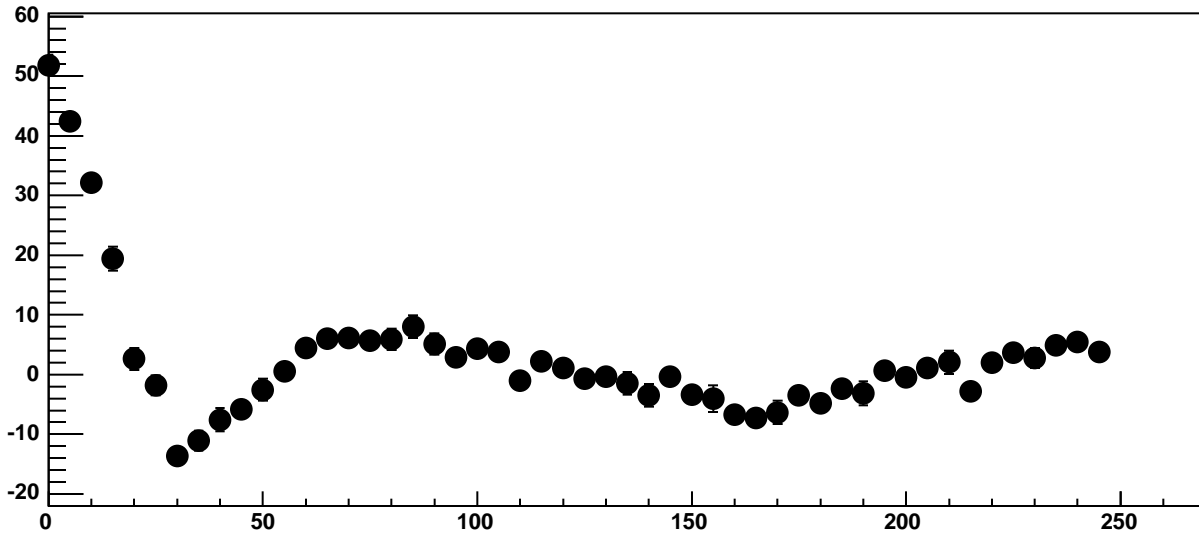


$\chi^2 / \text{ndf}$	524.1 / 41
p0	$392.4 \pm 0.7983$
p1	$12.67 \pm 0.2539$
p2	$181.9 \pm 0.9877$
p3	$34.01 \pm 0.7$
p4	$-0.1801 \pm 0.009473$

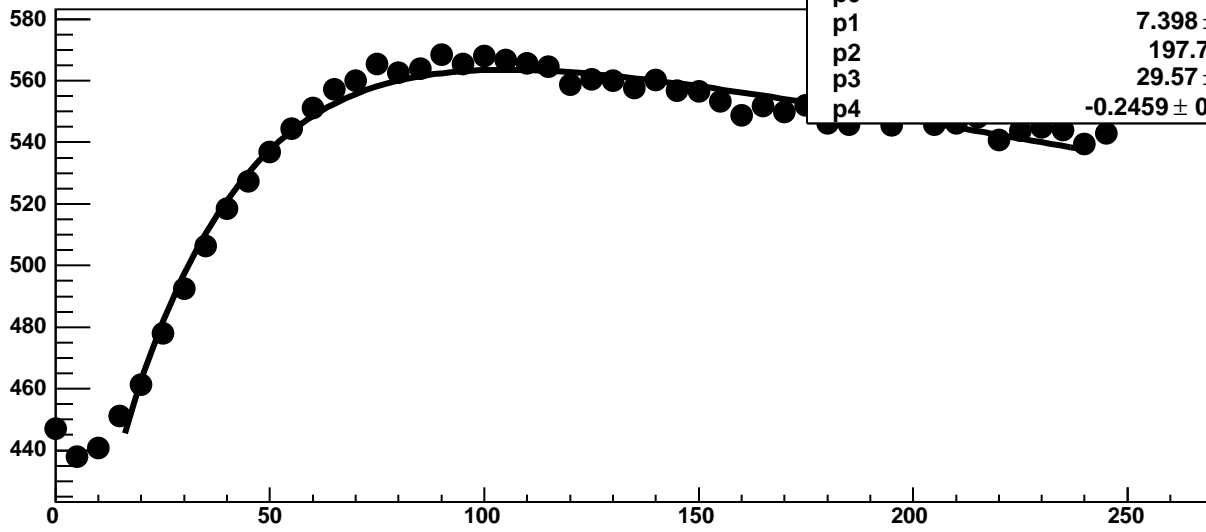
Chip 1, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

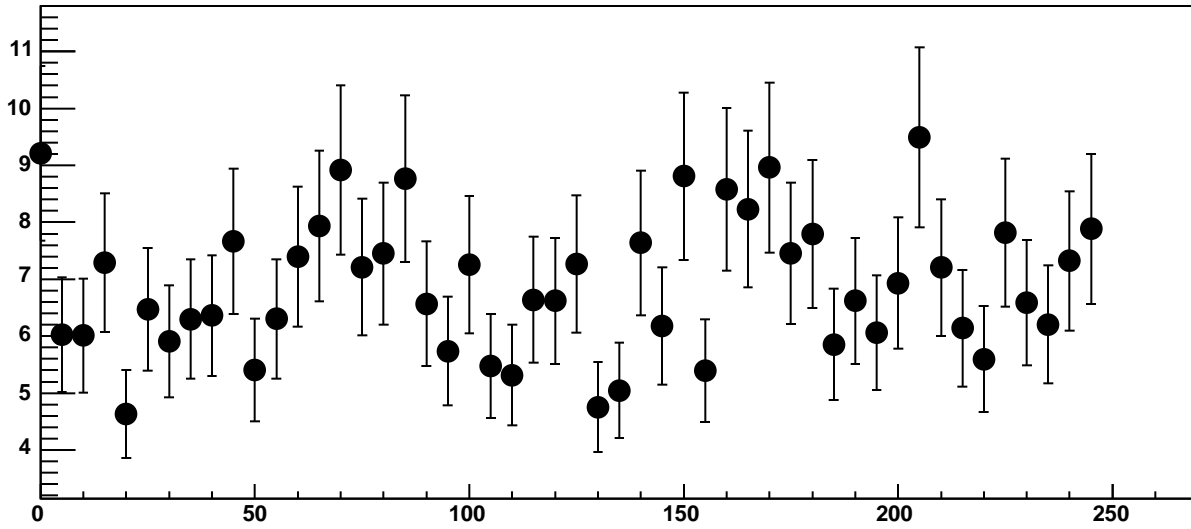


Chip 1, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold

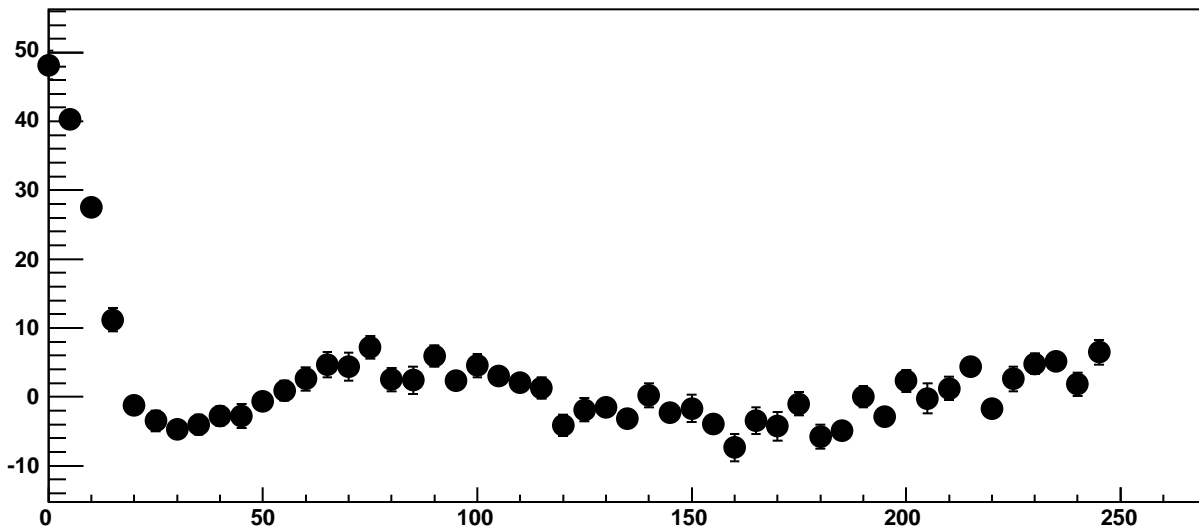


$\chi^2 / \text{ndf}$	272.7 / 41
p0	$397.1 \pm 2.817$
p1	$7.398 \pm 0.4949$
p2	$197.7 \pm 2.944$
p3	$29.57 \pm 0.5629$
p4	$-0.2459 \pm 0.007608$

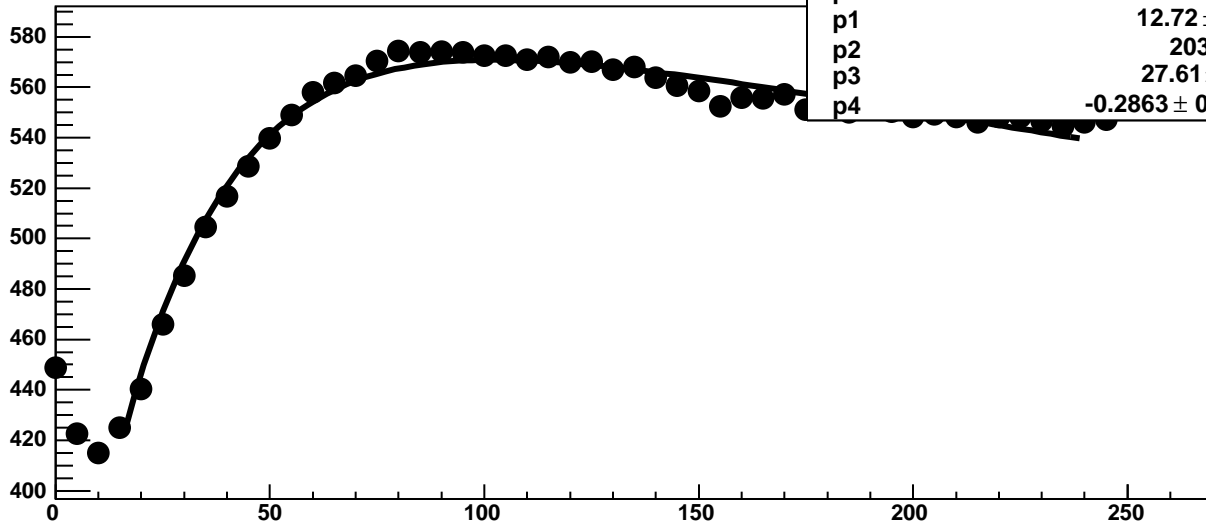
Chip 1, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold



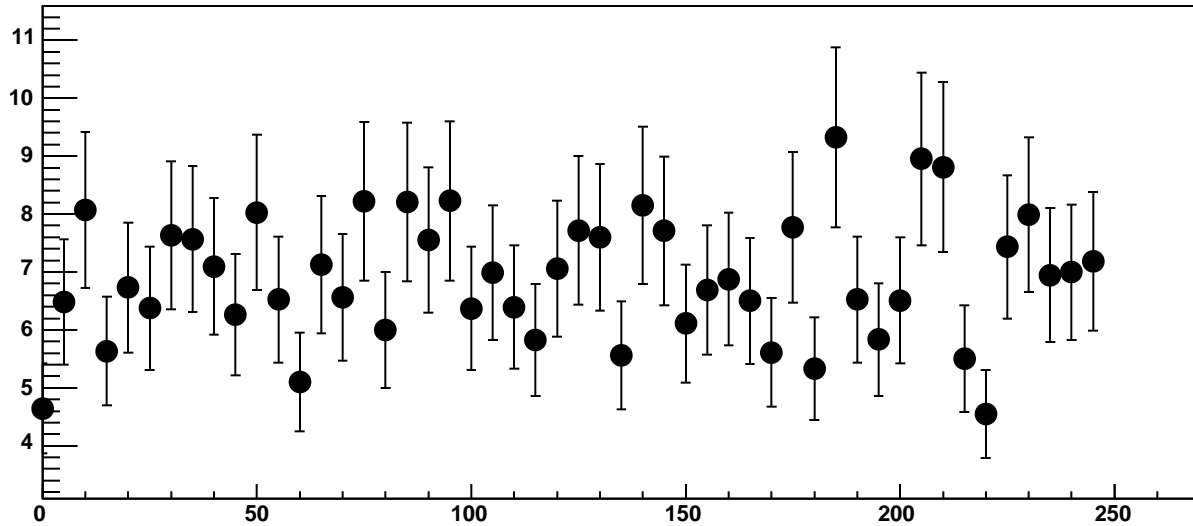
Chip 1, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold



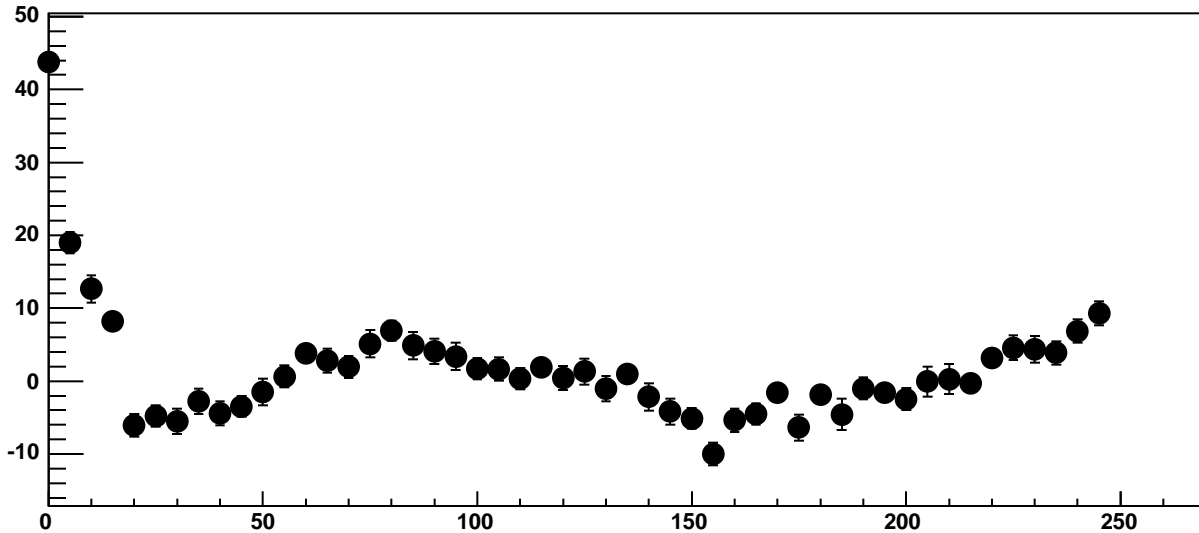
Chip 1, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold



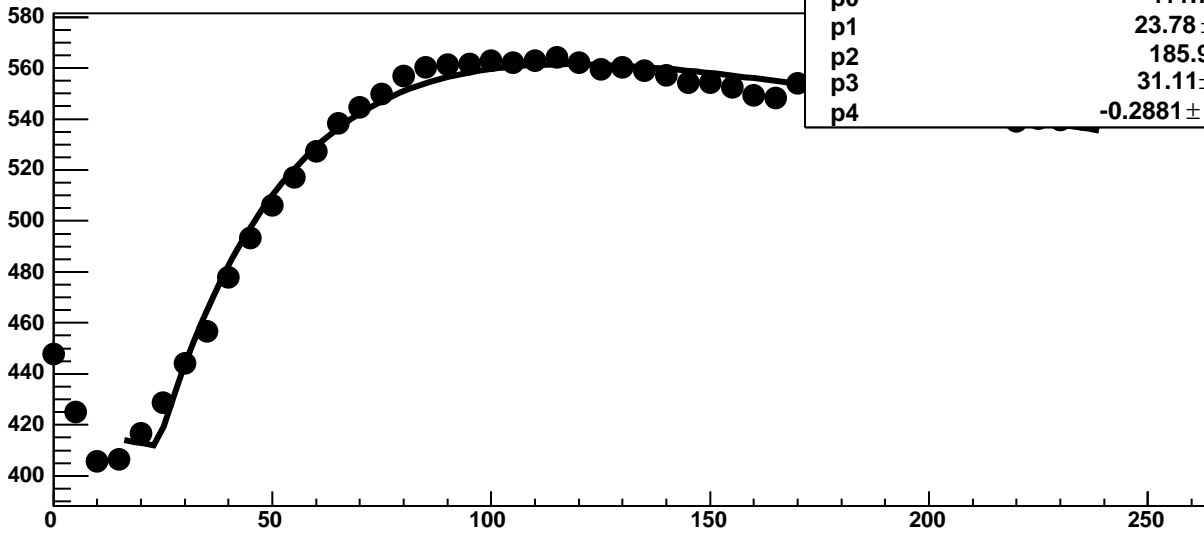
Chip 1, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

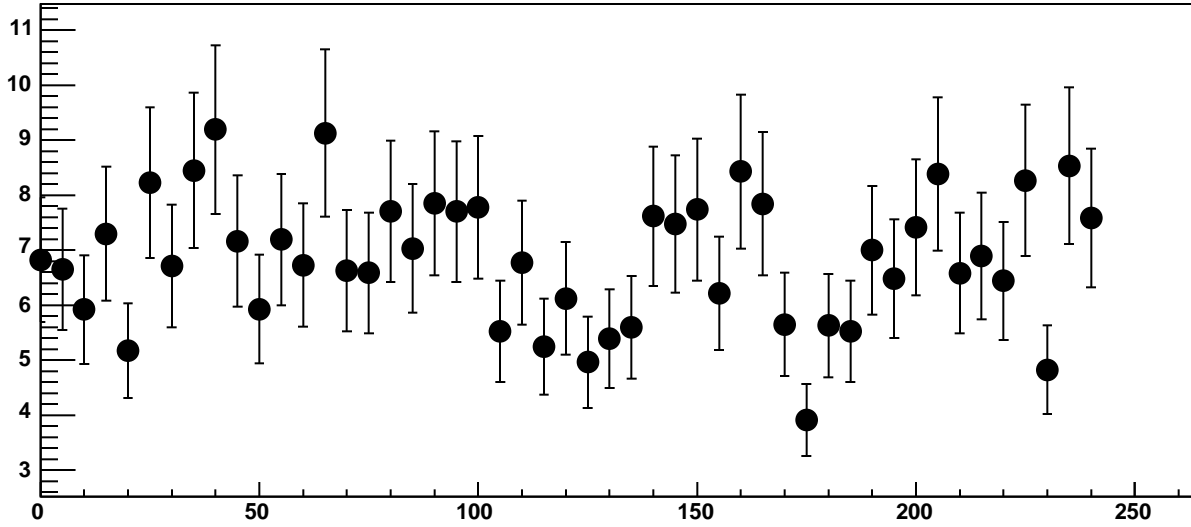


Chip 1, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold

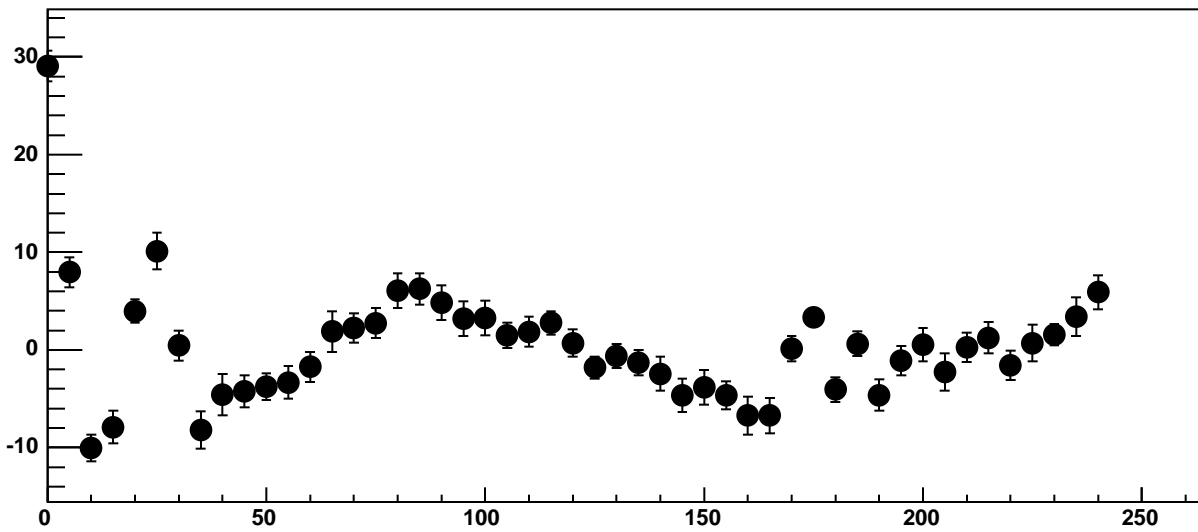


$\chi^2 / \text{ndf}$	267.6 / 41
p0	$411.7 \pm 0.924$
p1	$23.78 \pm 0.2872$
p2	$185.9 \pm 2.041$
p3	$31.11 \pm 0.6344$
p4	$-0.2881 \pm 0.01017$

Chip 1, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold

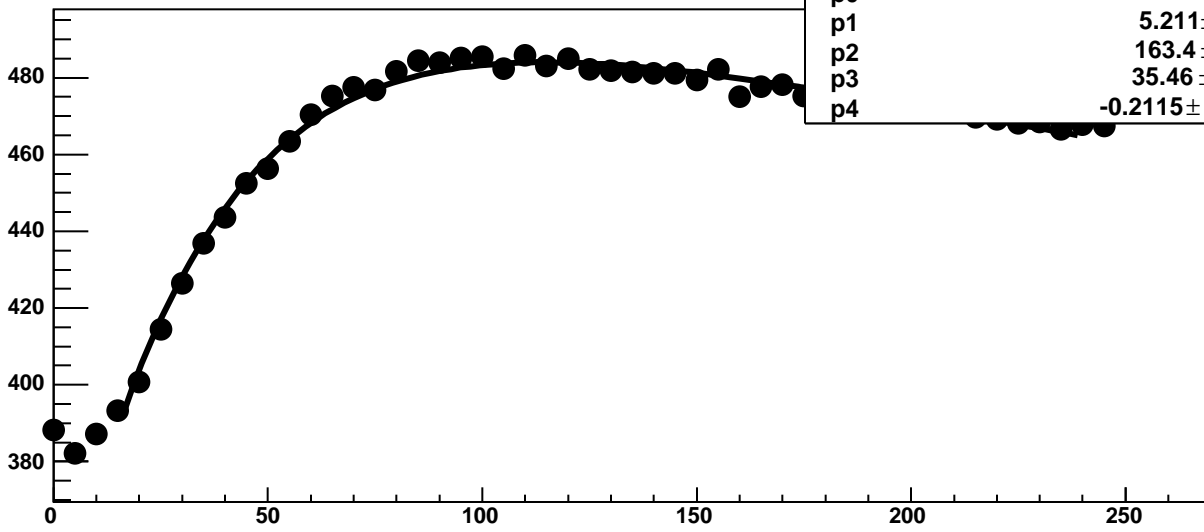


Chip 1, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold



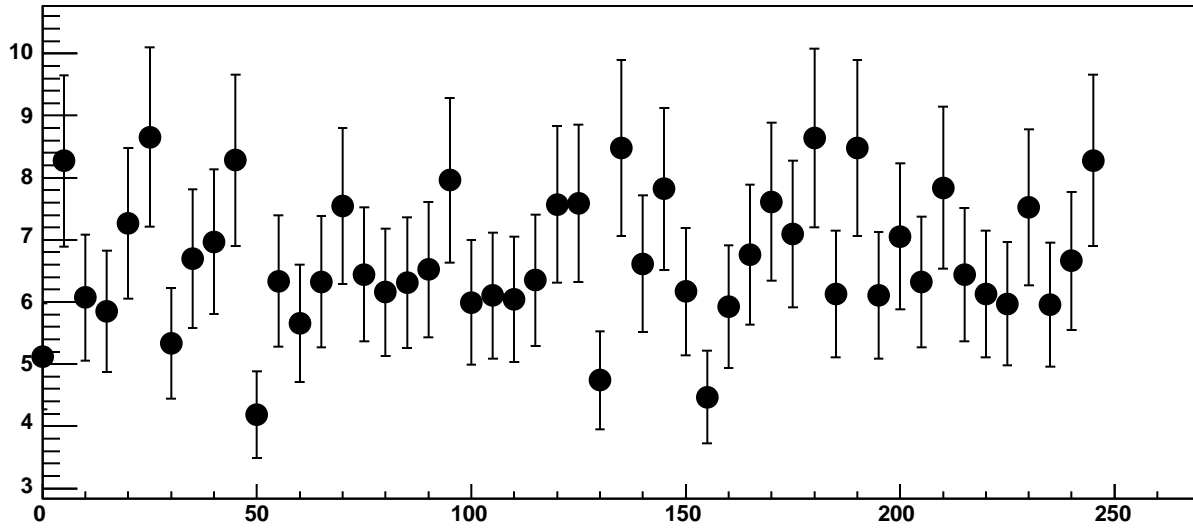


Chip 1, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold

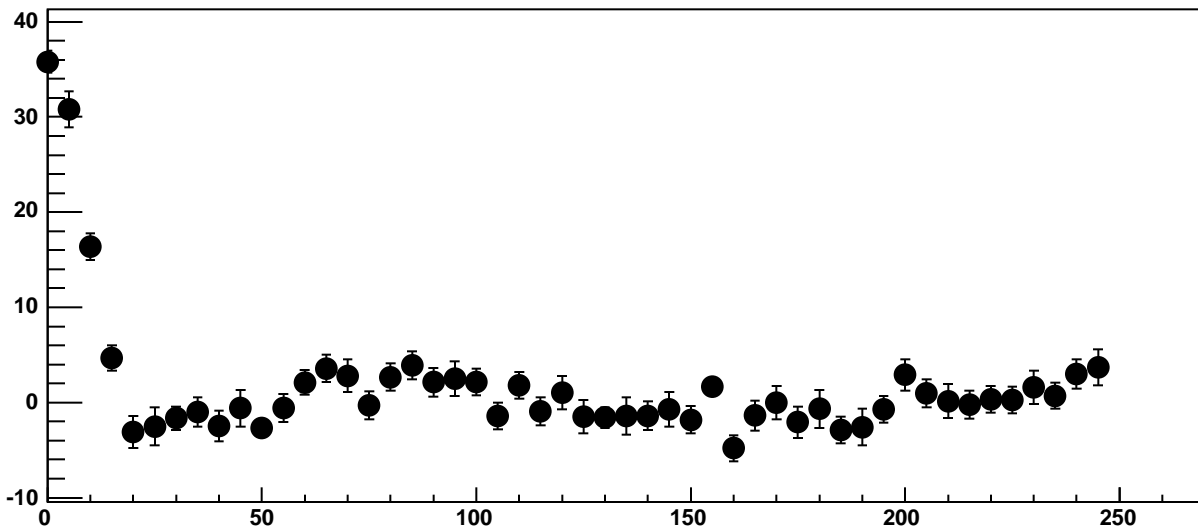


$\chi^2 / \text{ndf}$	100.6 / 41
p0	$351.3 \pm 2.264$
p1	$5.211 \pm 0.4289$
p2	$163.4 \pm 0.4558$
p3	$35.46 \pm 0.9884$
p4	$-0.2115 \pm 0.01024$

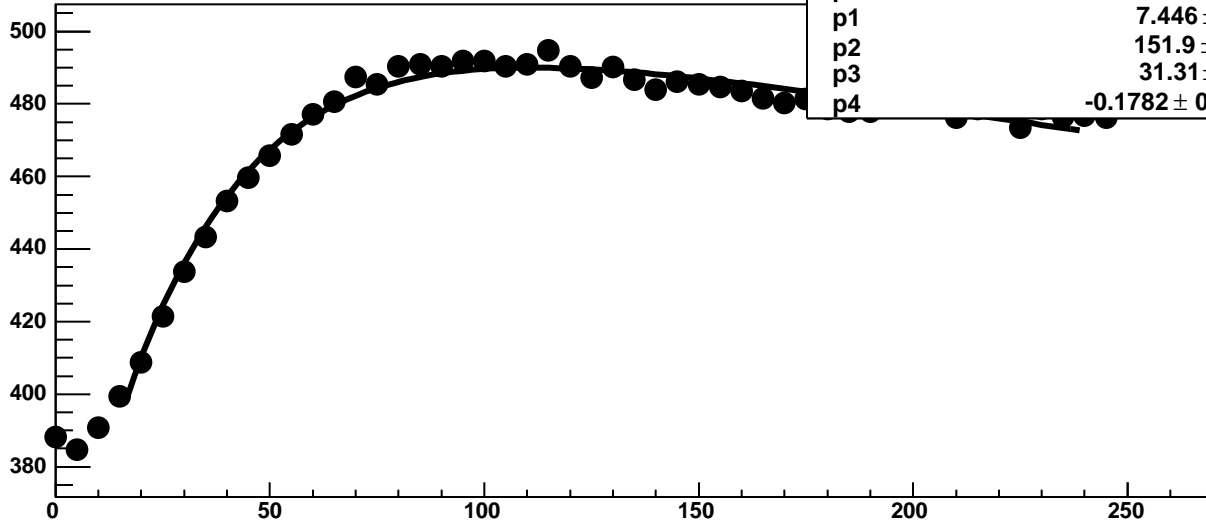
Chip 1, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold

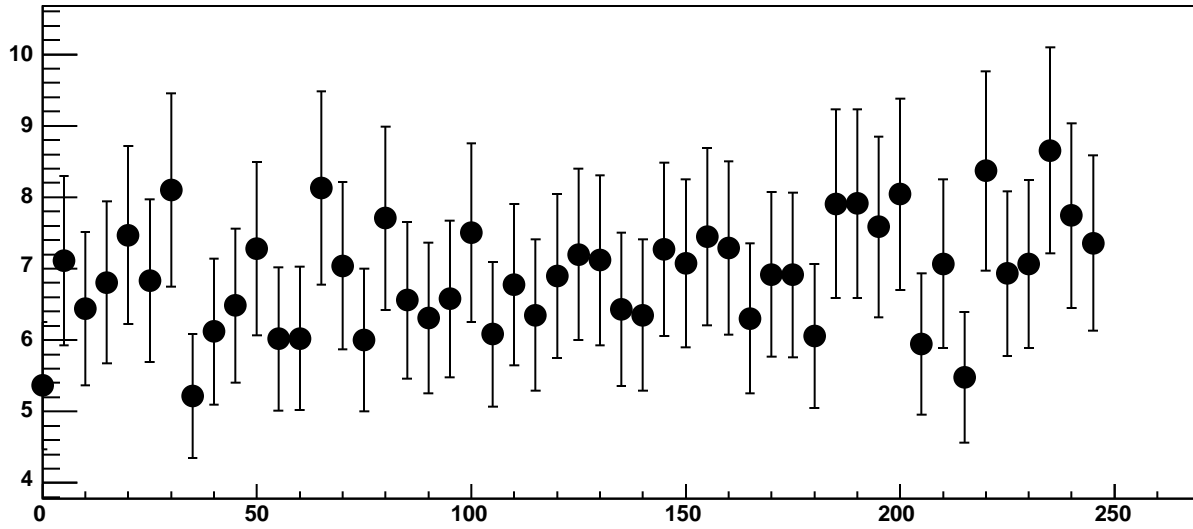


Chip 1, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

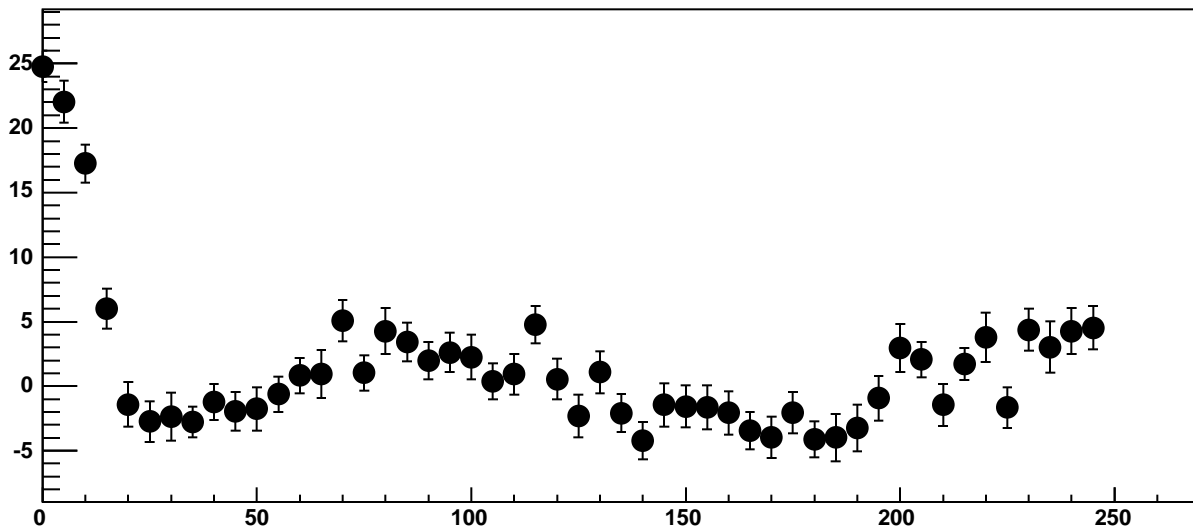


$\chi^2 / \text{ndf}$	144.6 / 41
p0	$362.2 \pm 1.591$
p1	$7.446 \pm 0.3568$
p2	$151.9 \pm 0.3326$
p3	$31.31 \pm 0.8657$
p4	$-0.1782 \pm 0.009395$

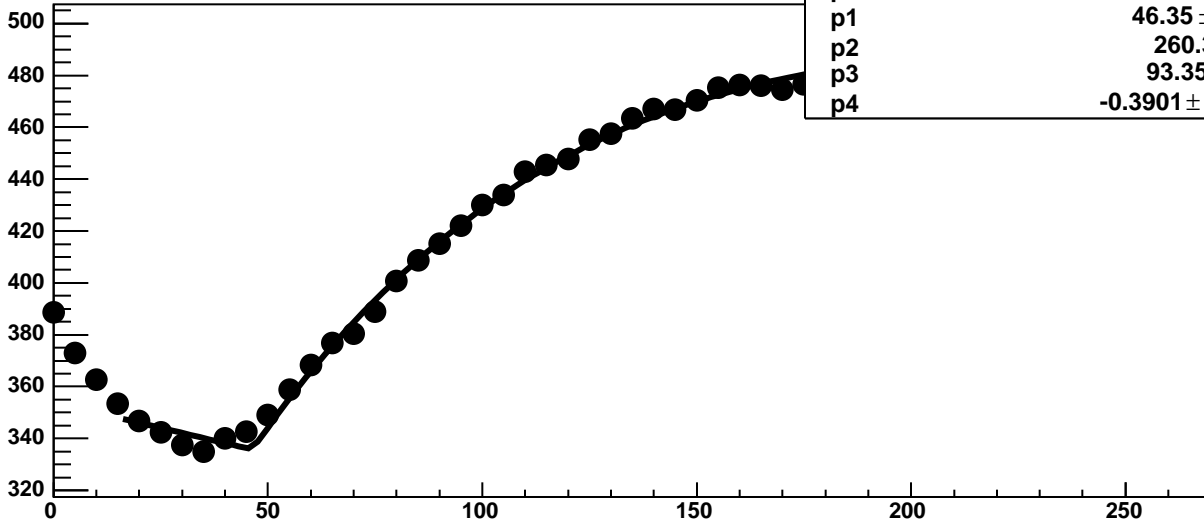
Chip 1, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold

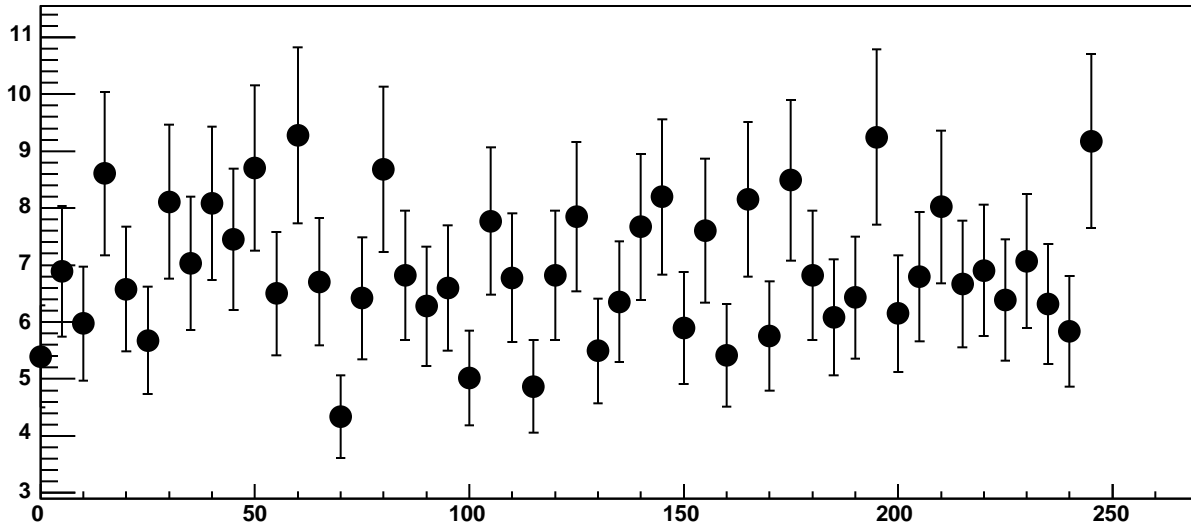


Chip 1, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

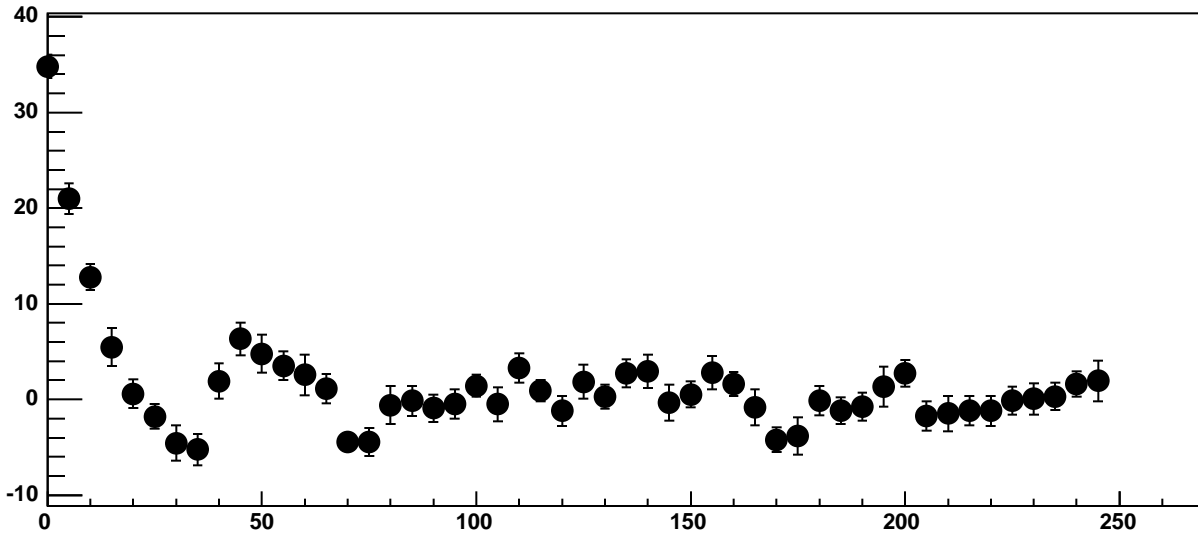


$\chi^2 / \text{ndf}$	127 / 41
p0	$335.8 \pm 0.884$
p1	$46.35 \pm 0.6294$
p2	$260.3 \pm 15.8$
p3	$93.35 \pm 4.747$
p4	$-0.3901 \pm 0.05184$

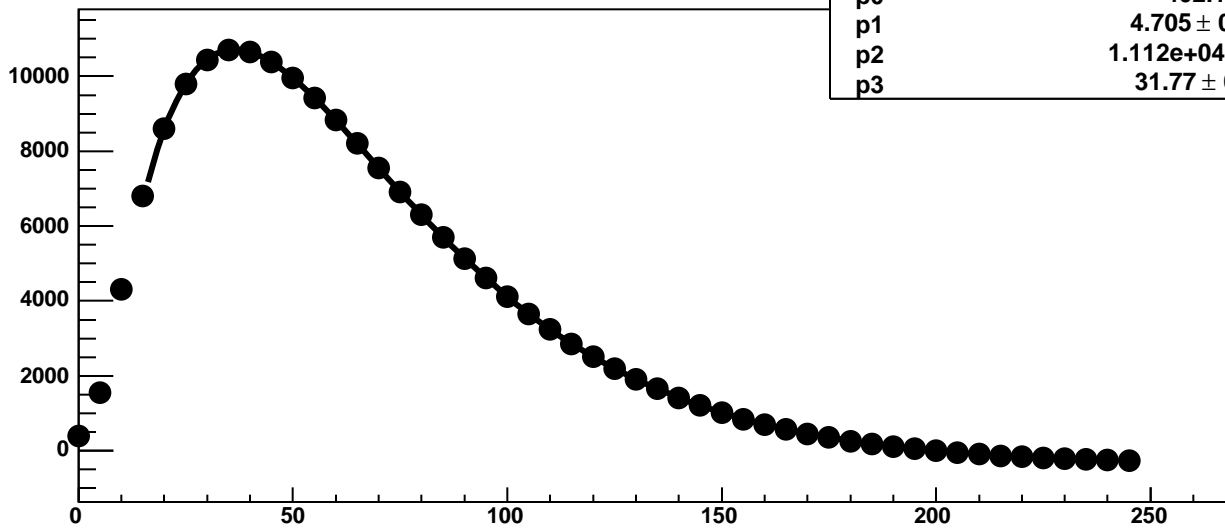
Chip 1, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

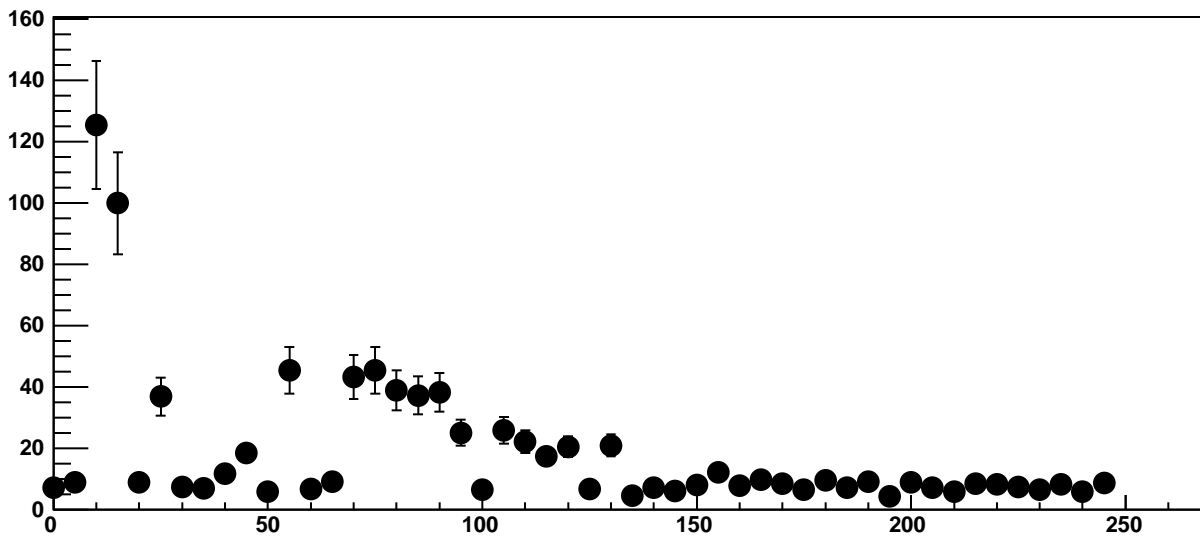


Chip 1, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold

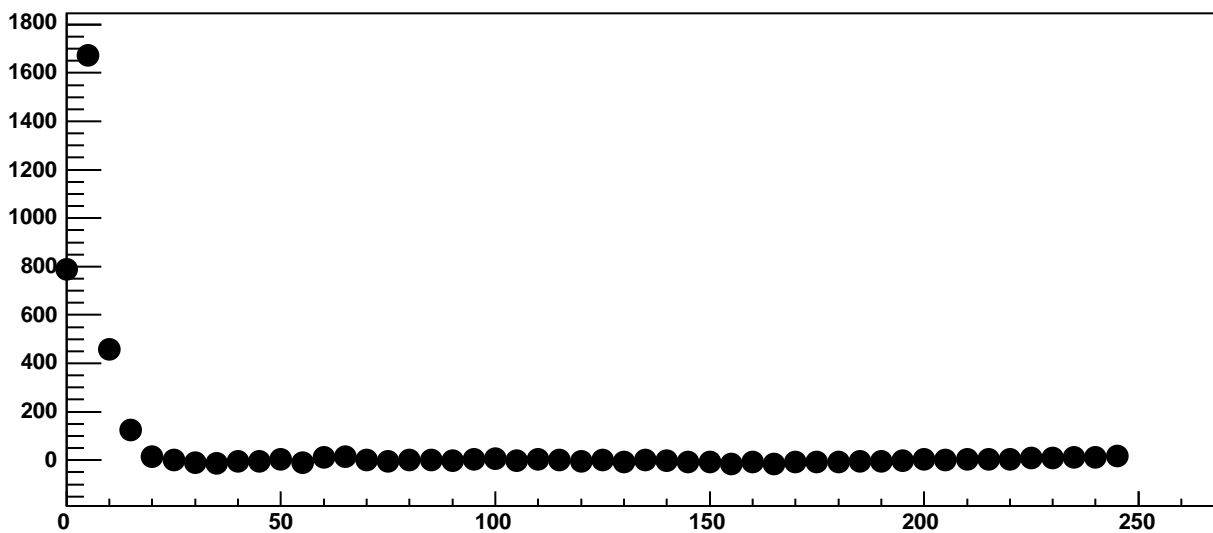


$\chi^2 / \text{ndf}$	723.7 / 42
p0	-402.1 ± 0.5941
p1	4.705 ± 0.008133
p2	1.112e+04 ± 0.8671
p3	31.77 ± 0.005591

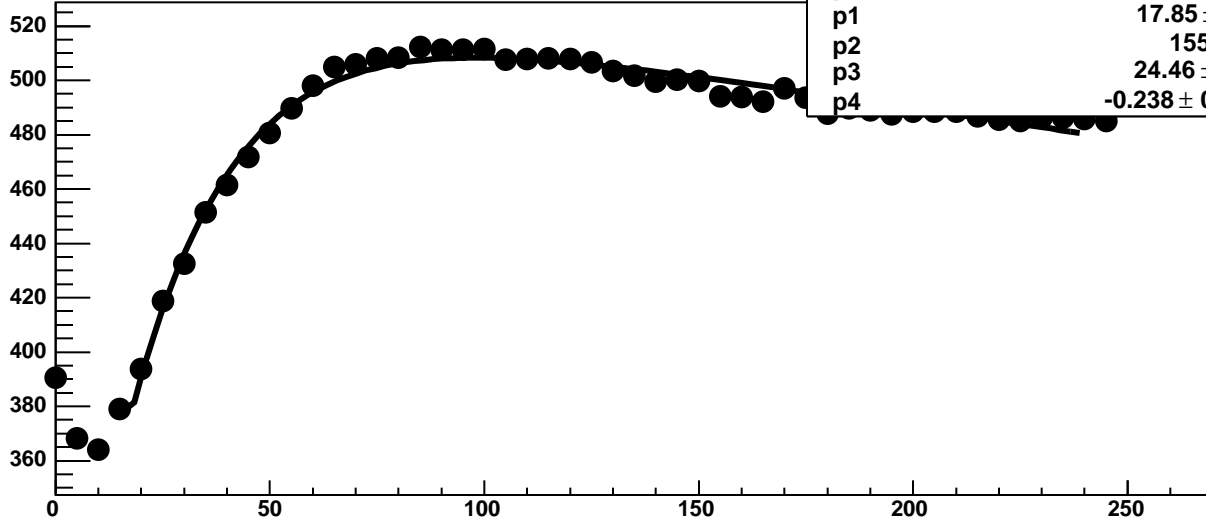
Chip 1, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold

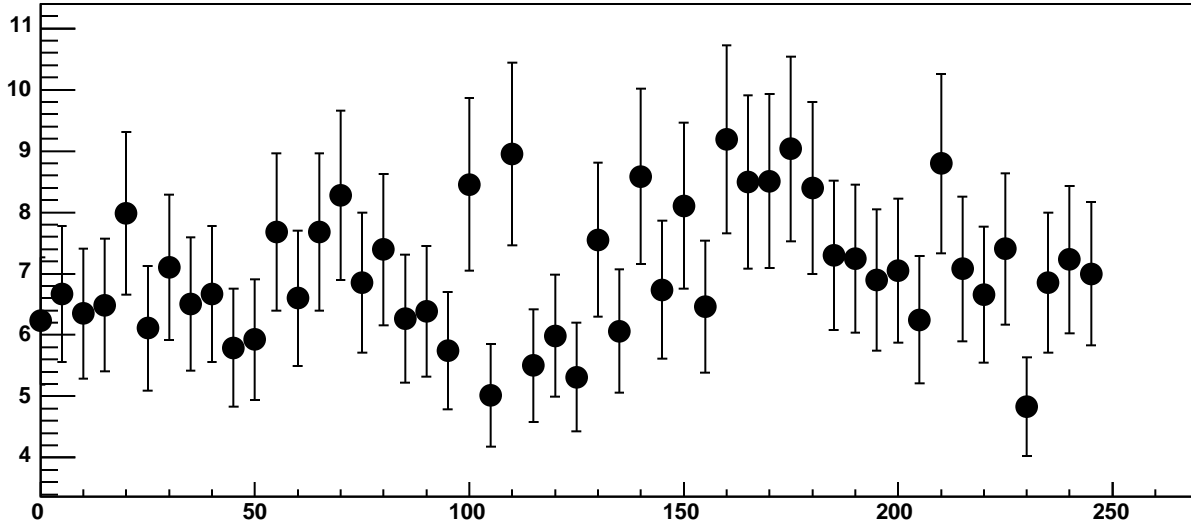


Chip 1, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

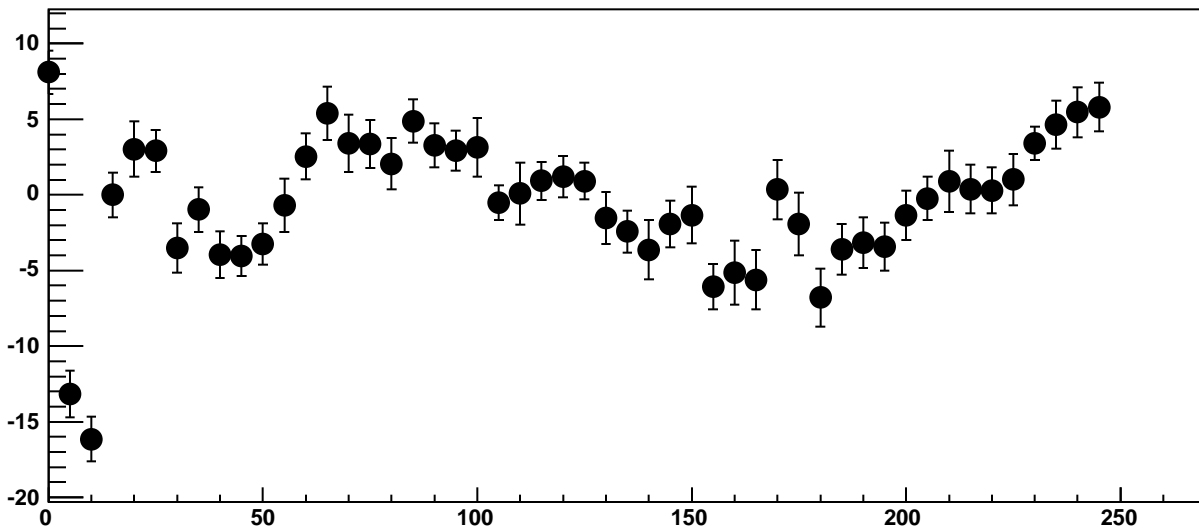


$\chi^2 / \text{ndf}$	178.1 / 41
p0	$378.3 \pm 1.433$
p1	$17.85 \pm 0.3372$
p2	$155 \pm 1.928$
p3	$24.46 \pm 0.5264$
p4	$-0.238 \pm 0.007341$

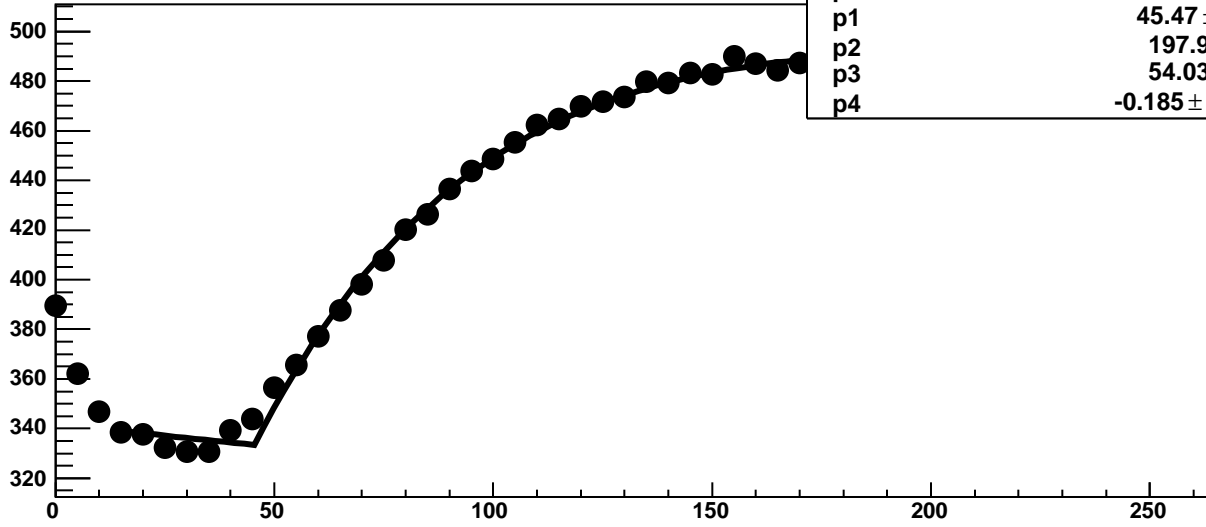
Chip 1, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

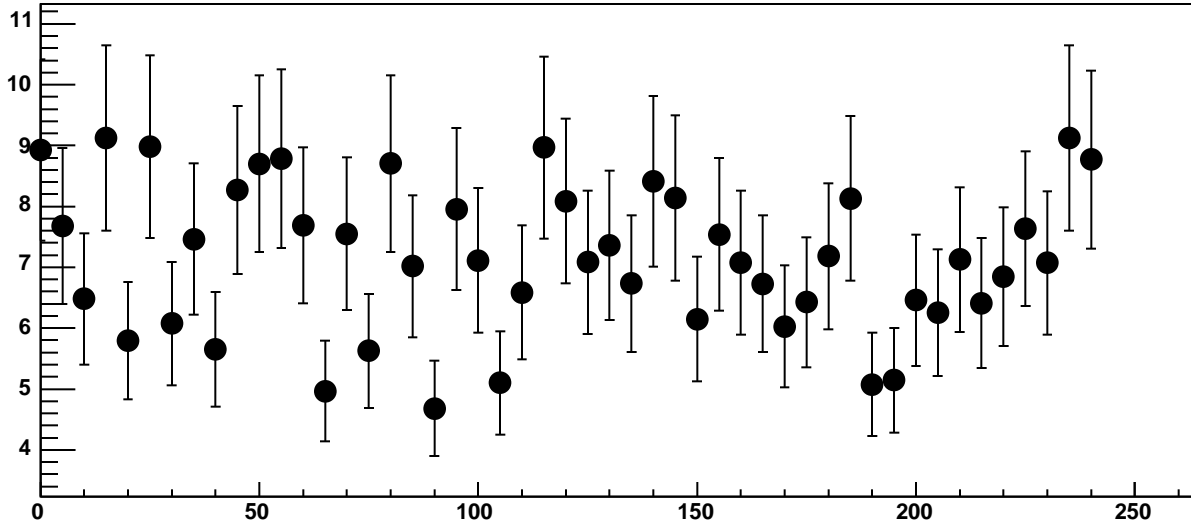


Chip 1, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold

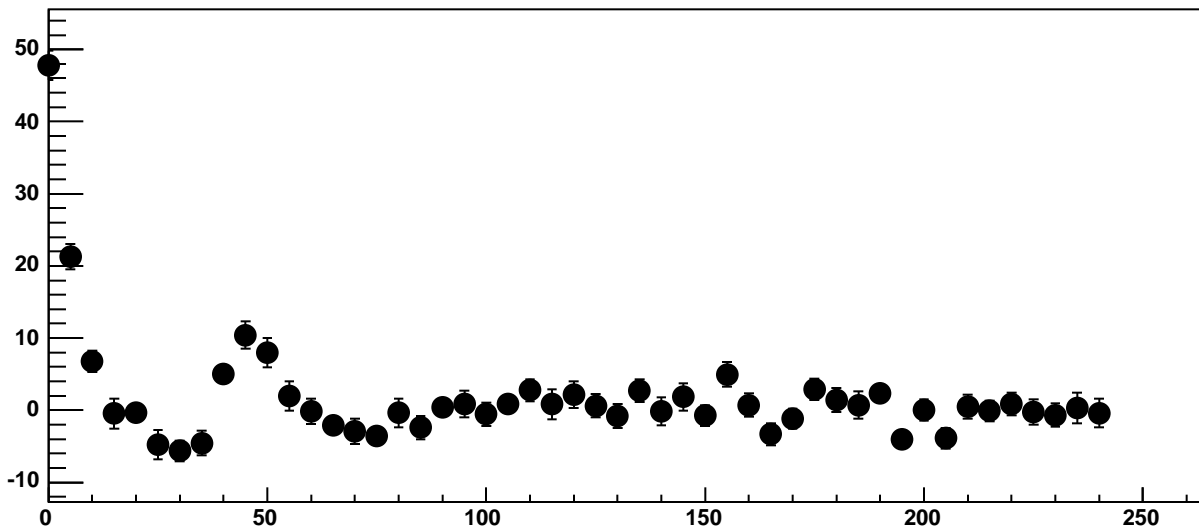


$\chi^2 / \text{ndf}$	160.3 / 41
p0	$333.4 \pm 0.6926$
p1	$45.47 \pm 0.4997$
p2	$197.9 \pm 6.642$
p3	$54.03 \pm 2.092$
p4	$-0.185 \pm 0.03007$

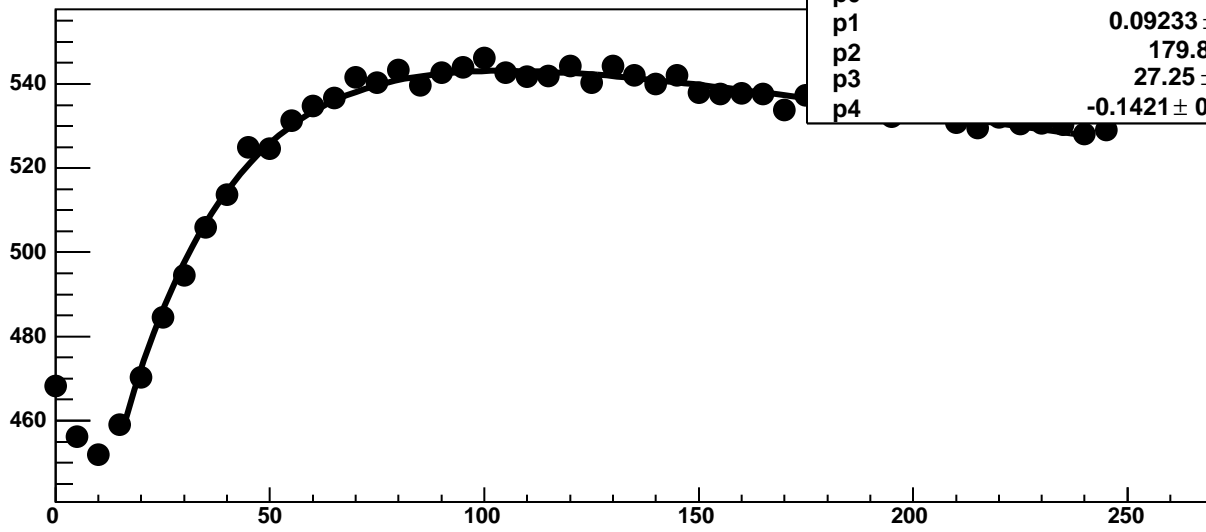
Chip 1, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

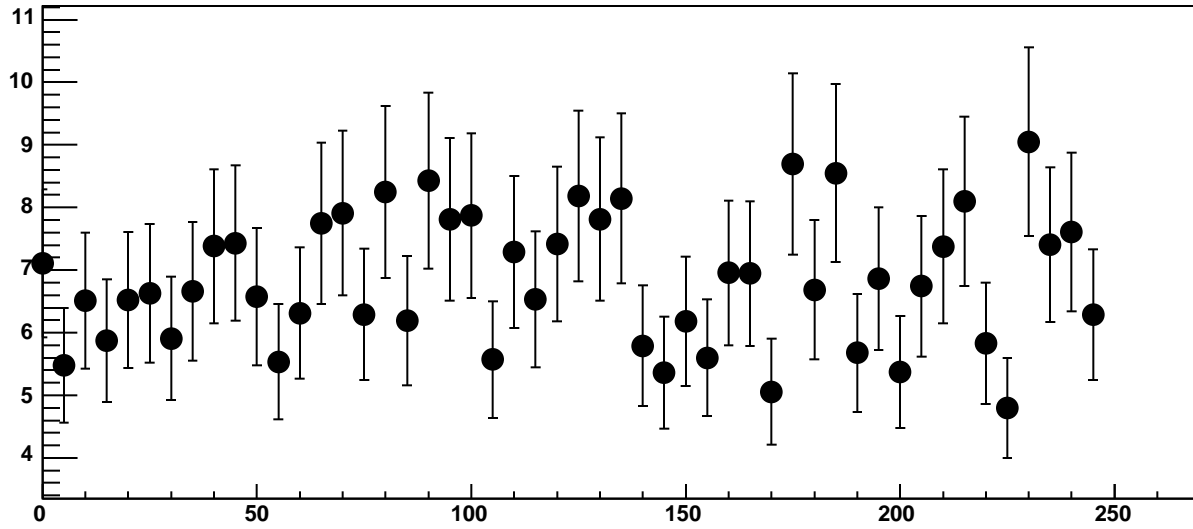


Chip 1, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

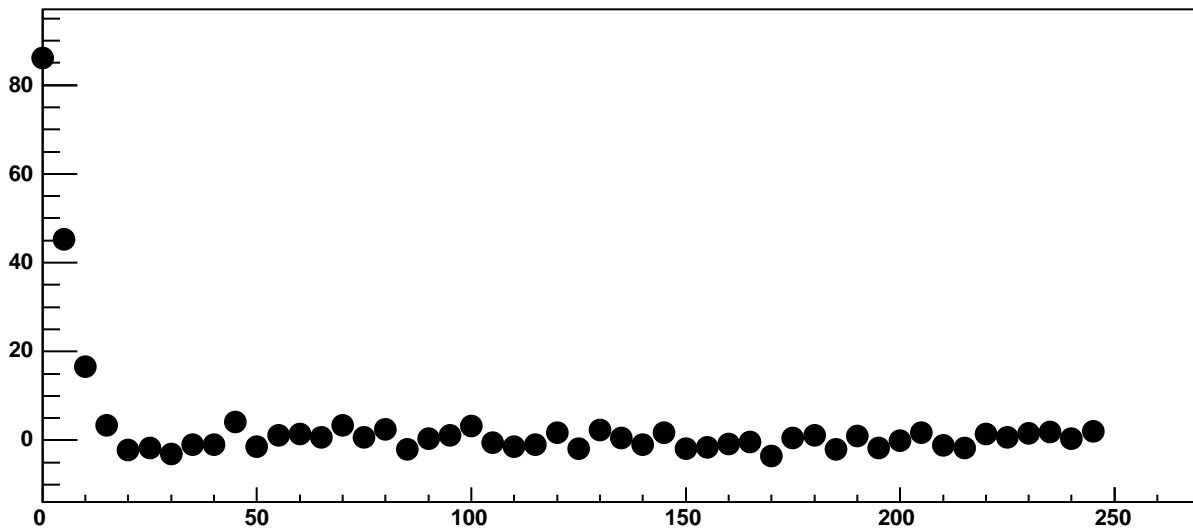


$\chi^2 / \text{ndf}$	64.49 / 41
p0	$382 \pm 2.86$
p1	$0.09233 \pm 0.6522$
p2	$179.8 \pm 2.932$
p3	$27.25 \pm 0.7624$
p4	$-0.1421 \pm 0.007467$

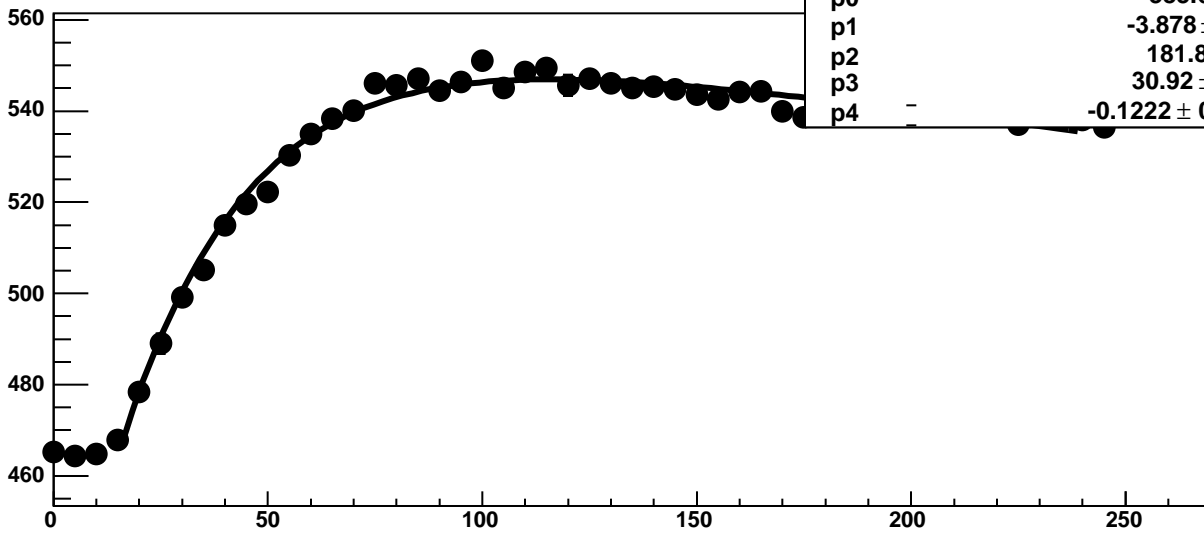
Chip 1, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

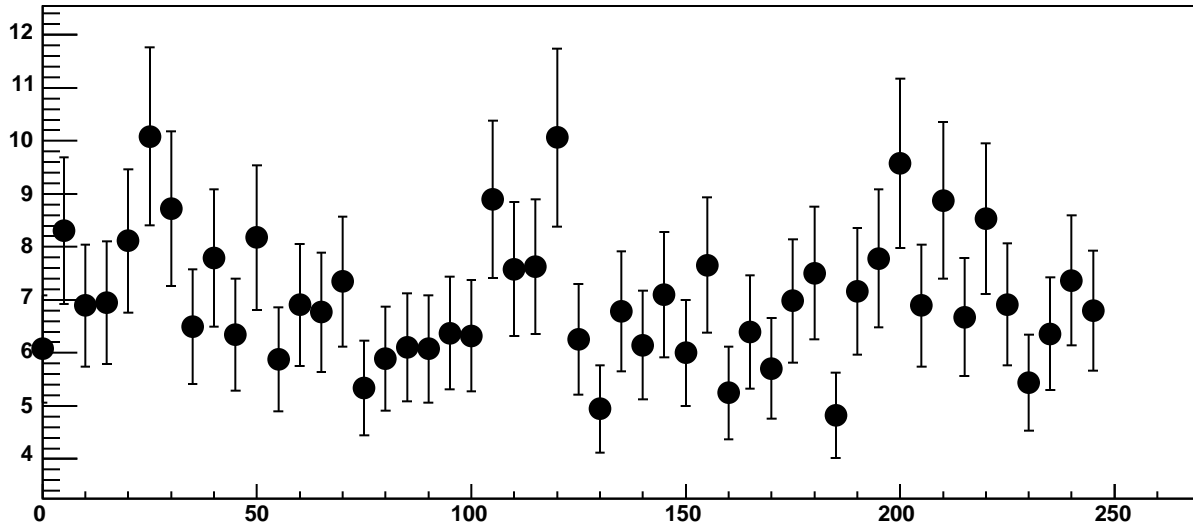


Chip 1, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold

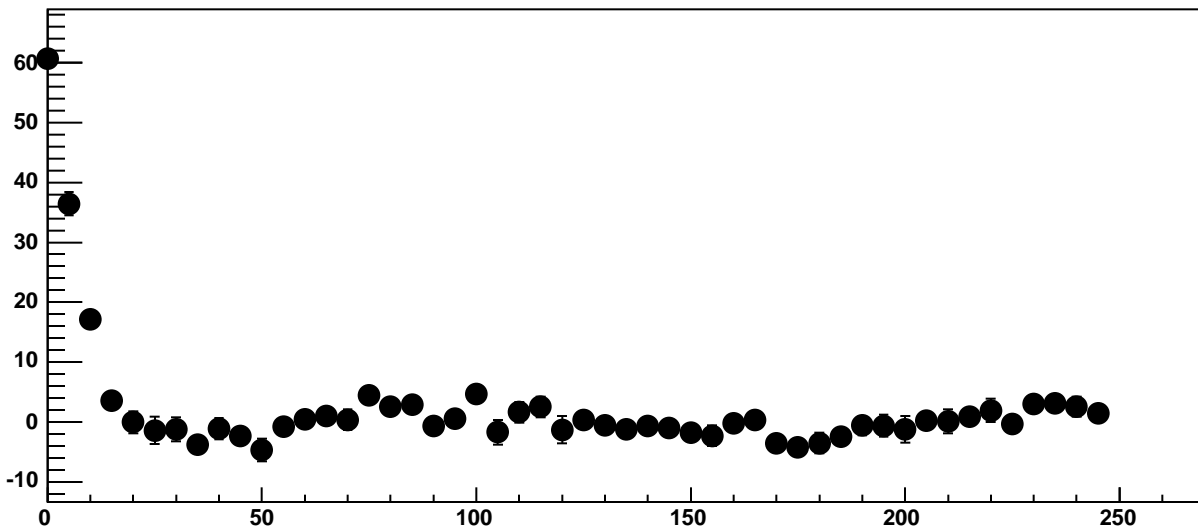


$\chi^2 / \text{ndf}$	100.9 / 41
p0	$383.6 \pm 2.871$
p1	$-3.878 \pm 0.8258$
p2	$181.8 \pm 2.943$
p3	$30.92 \pm 0.9435$
p4	$-0.1222 \pm 0.008051$

Chip 1, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold

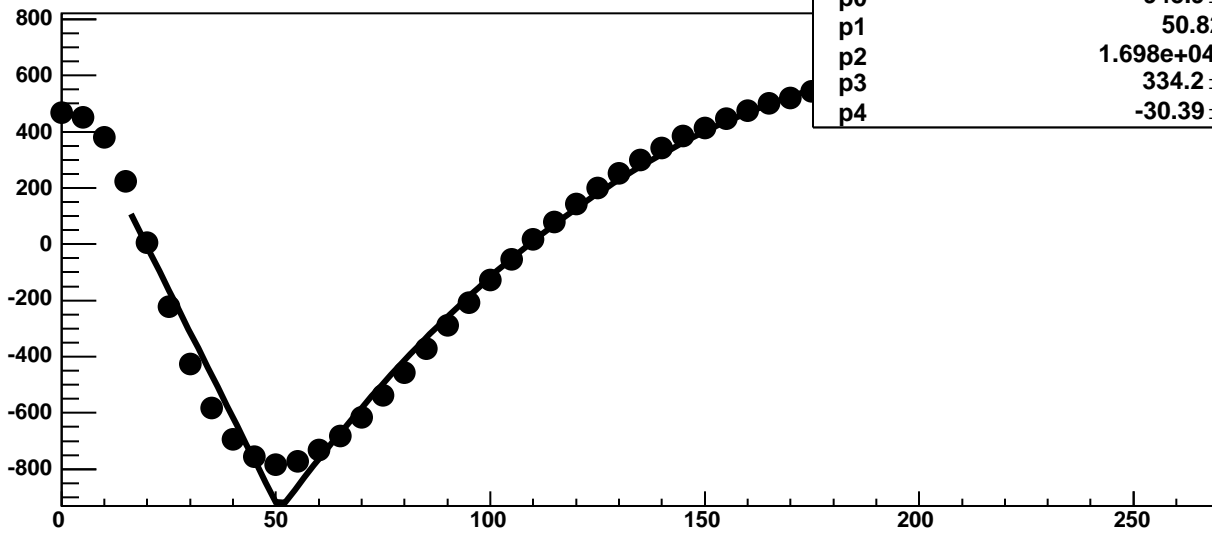


Chip 1, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold

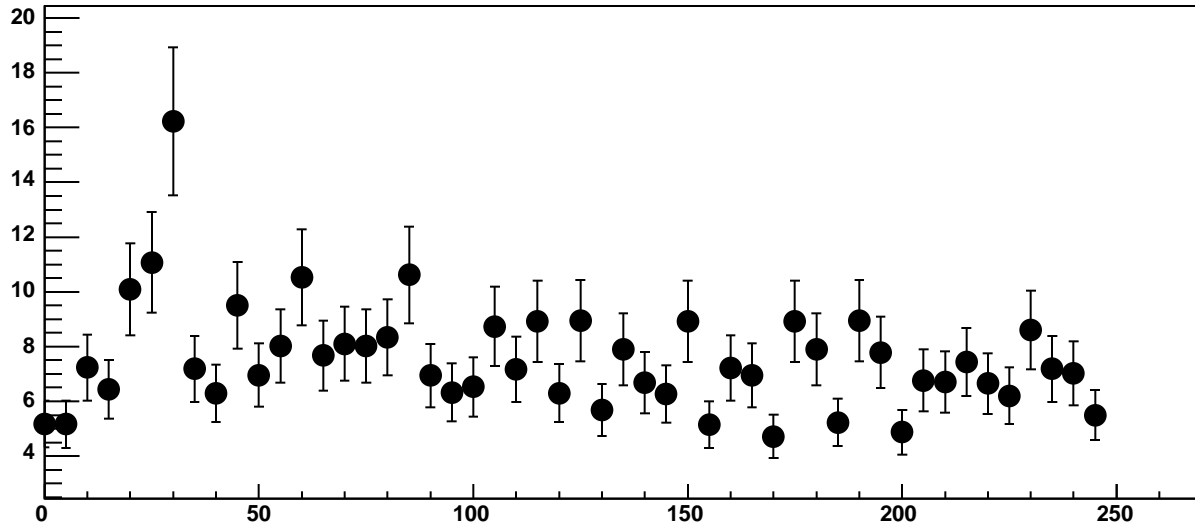




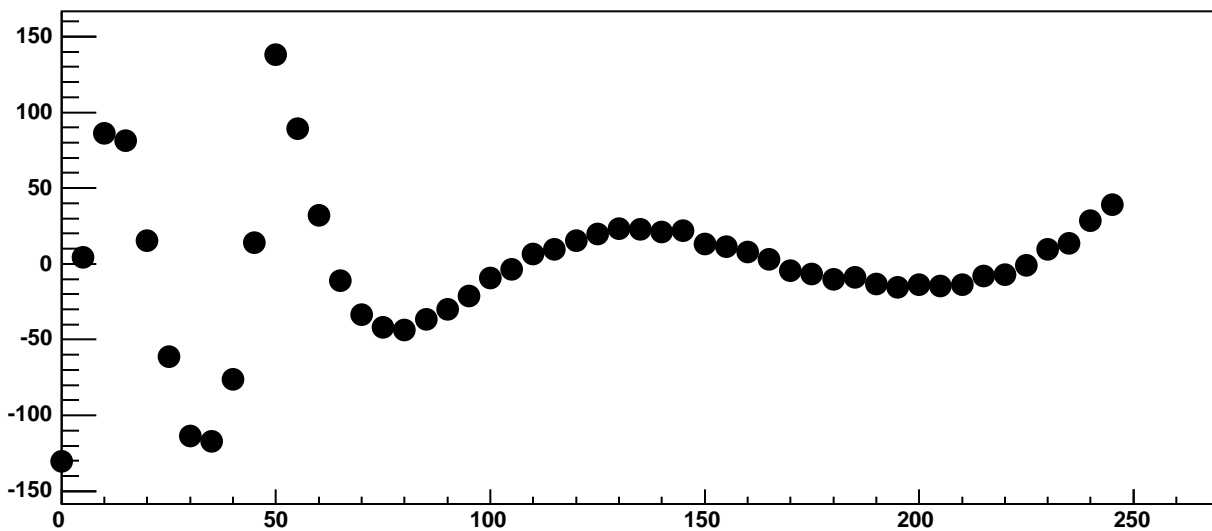
Chip 1, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold



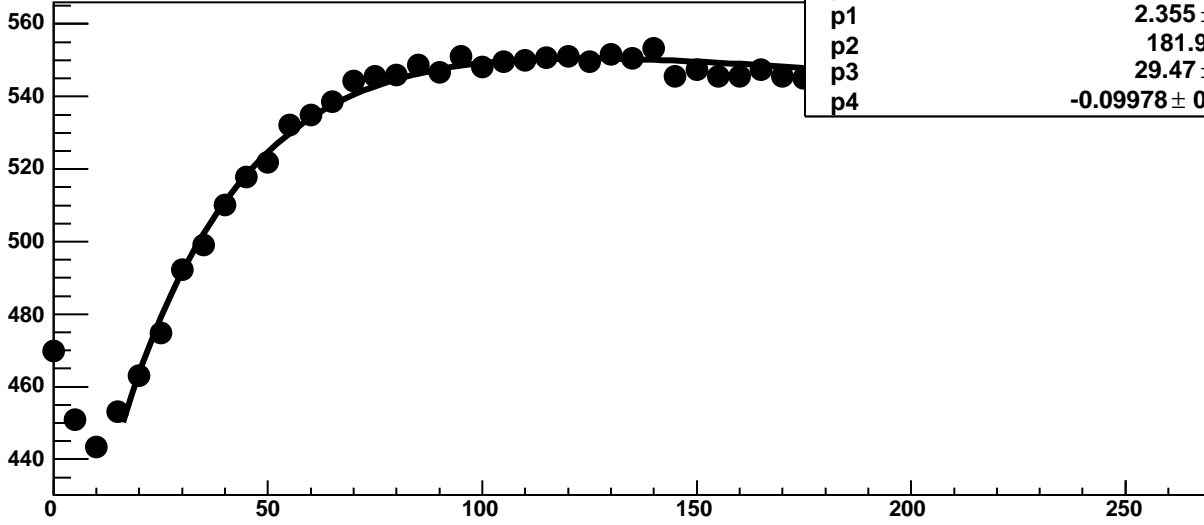
Chip 1, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

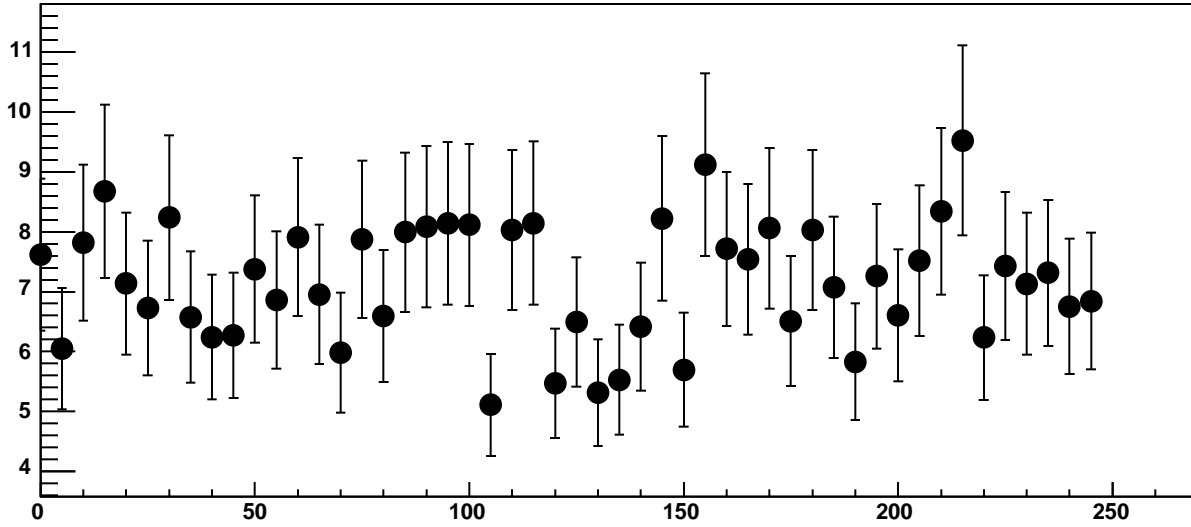


Chip 1, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

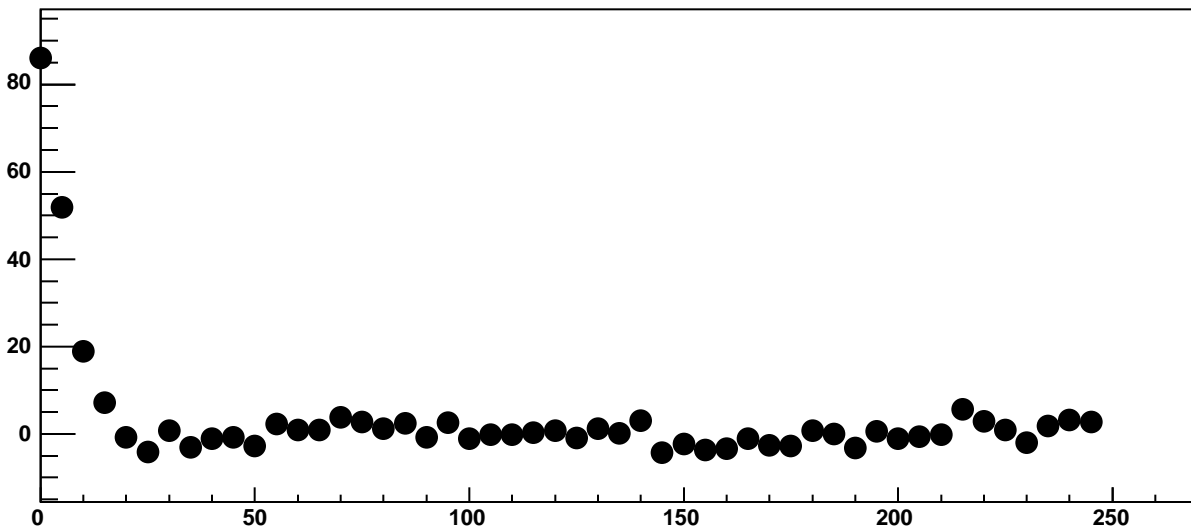


$\chi^2 / \text{ndf}$	98.45 / 41
p0	$383.6 \pm 2.968$
p1	$2.355 \pm 0.6782$
p2	$181.9 \pm 3.033$
p3	$29.47 \pm 0.7993$
p4	$-0.09978 \pm 0.008164$

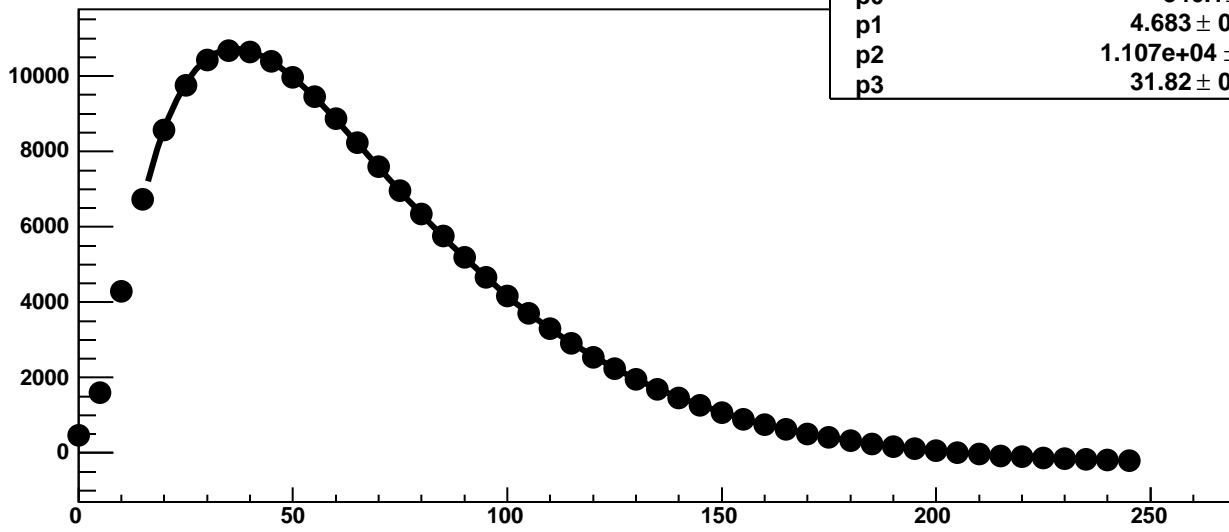
Chip 1, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

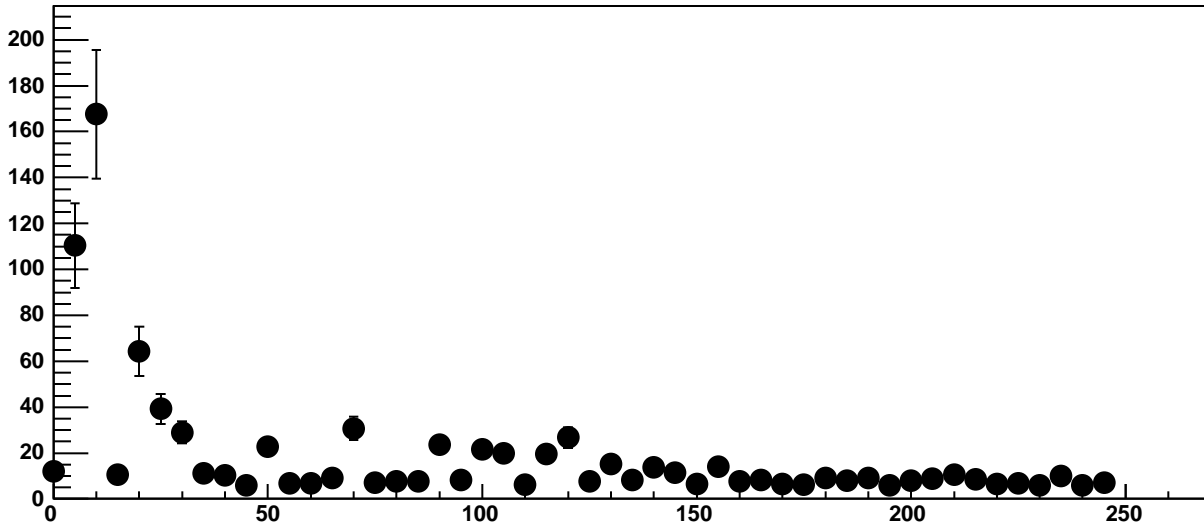


Chip 1, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold

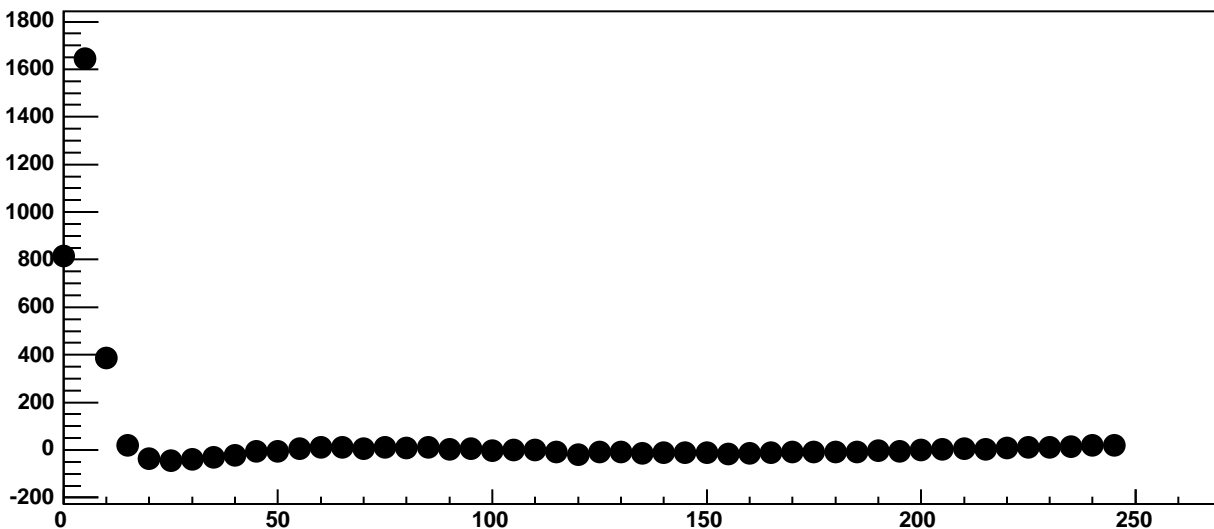


$\chi^2 / \text{ndf}$	1372 / 42
p0	-346.1 ± 0.5687
p1	4.683 ± 0.006127
p2	1.107e+04 ± 0.9404
p3	31.82 ± 0.004825

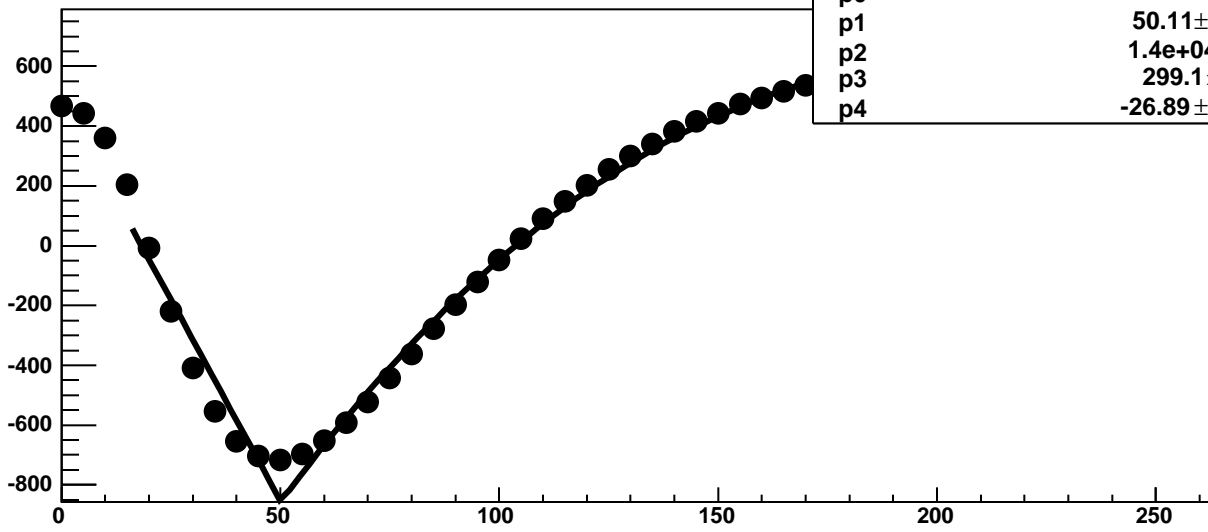
Chip 1, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold



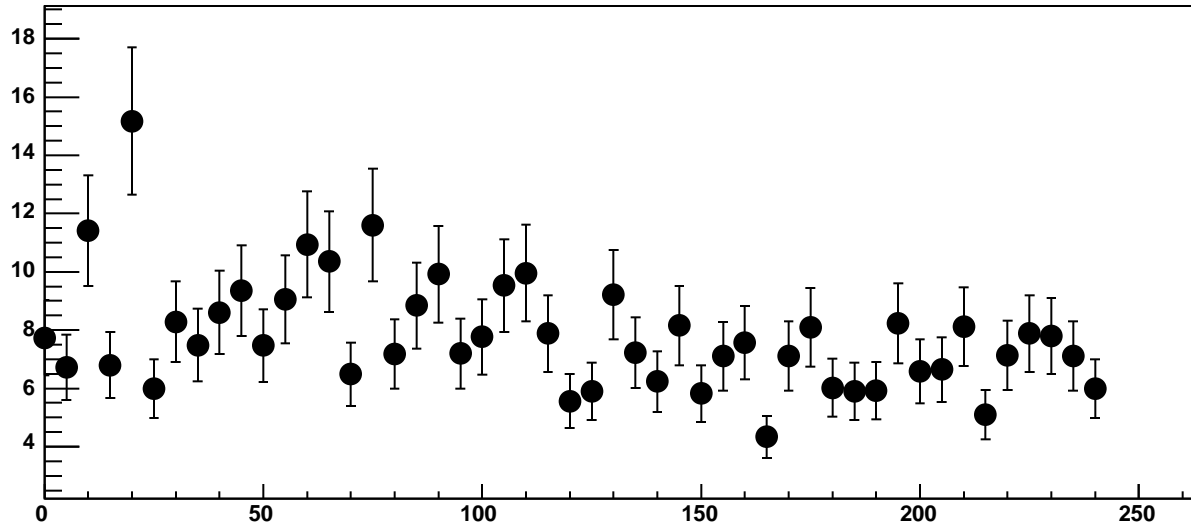
Chip 1, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold



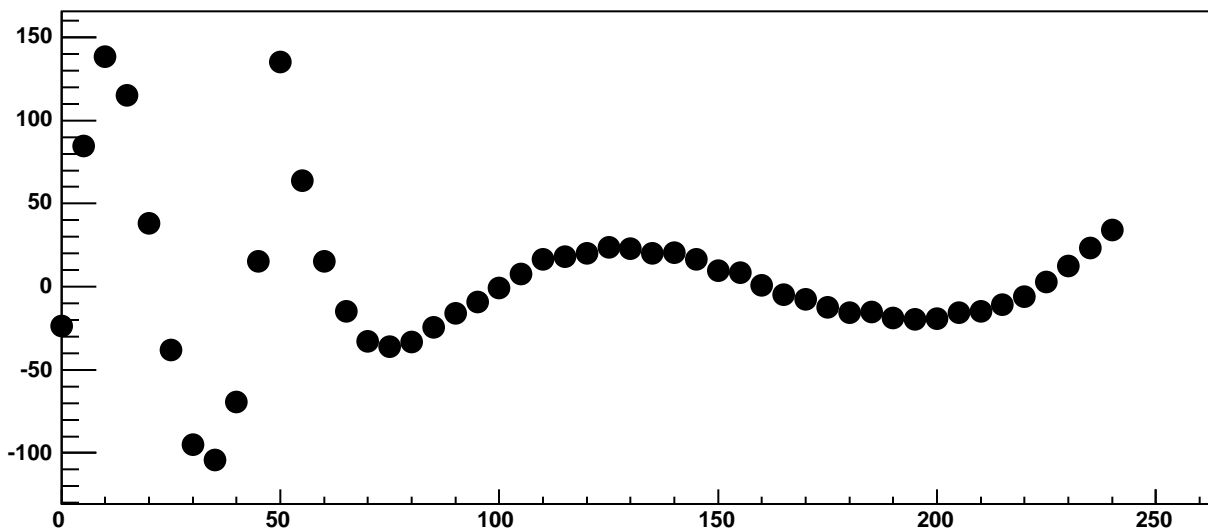
Chip 1, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold



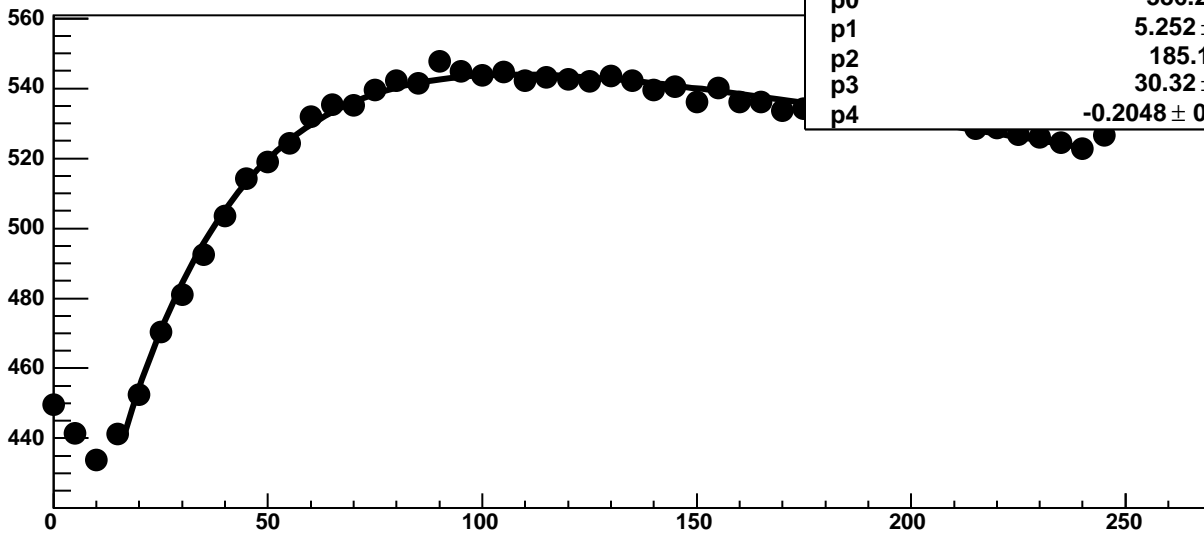
Chip 1, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold

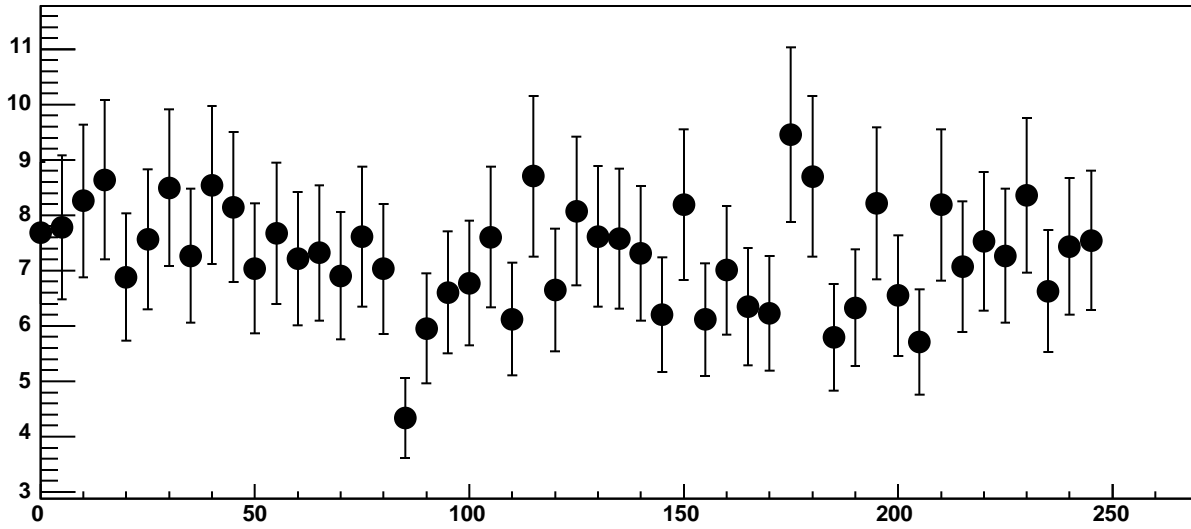


Chip 1, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold

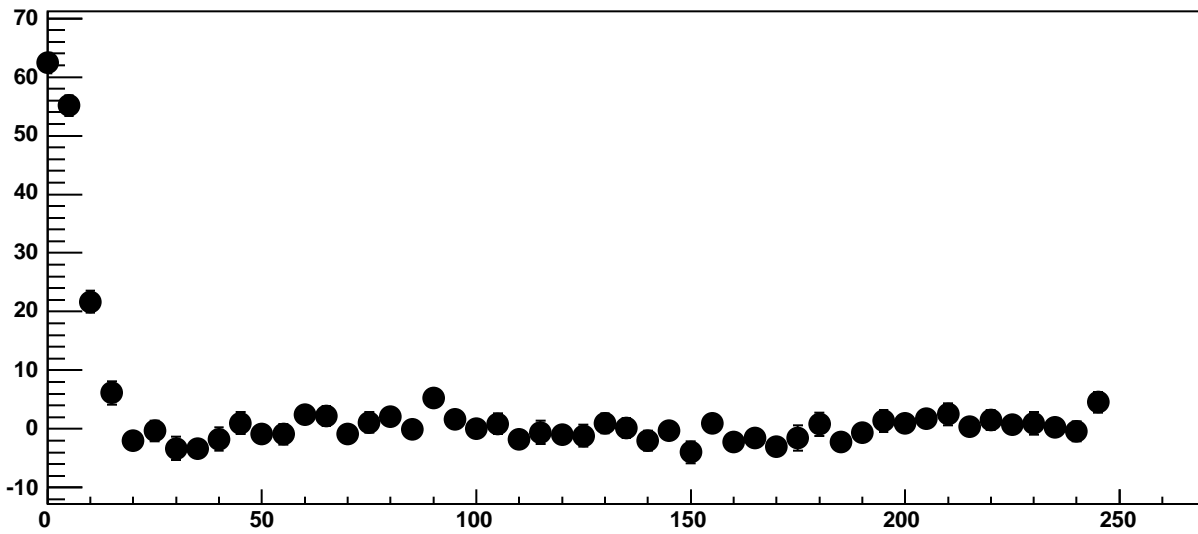


$\chi^2 / \text{ndf}$	68.61 / 41
p0	$386.2 \pm 3.016$
p1	$5.252 \pm 0.6303$
p2	$185.1 \pm 3.135$
p3	$30.32 \pm 0.7943$
p4	$-0.2048 \pm 0.008769$

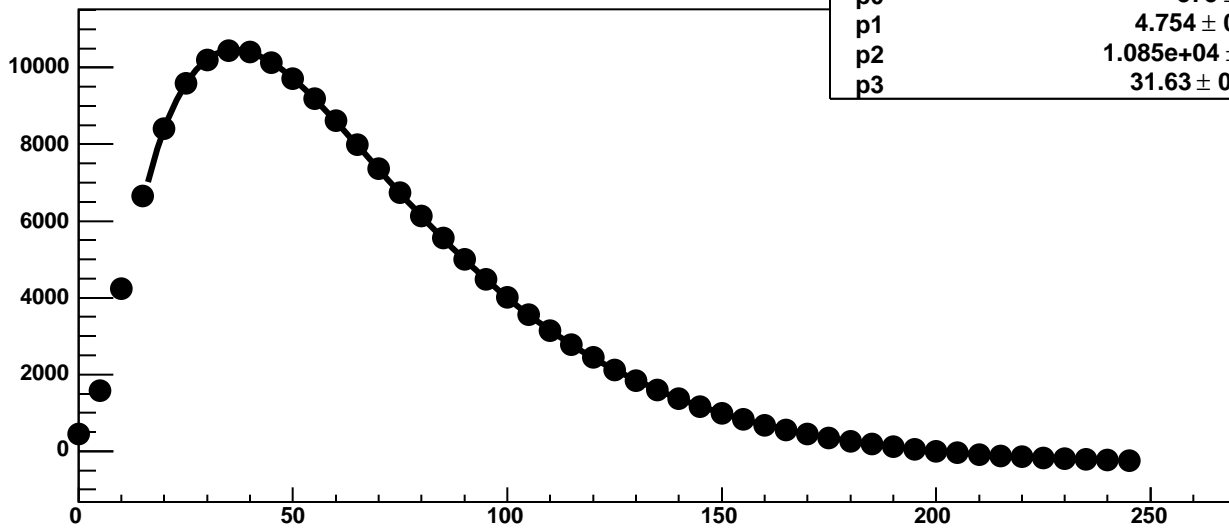
Chip 1, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold

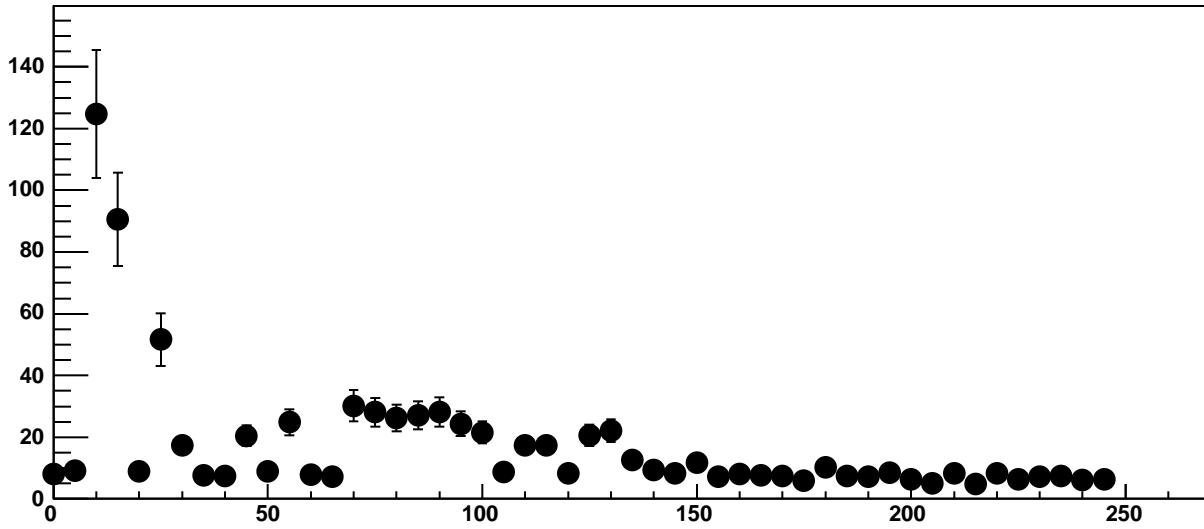


Chip 1, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold

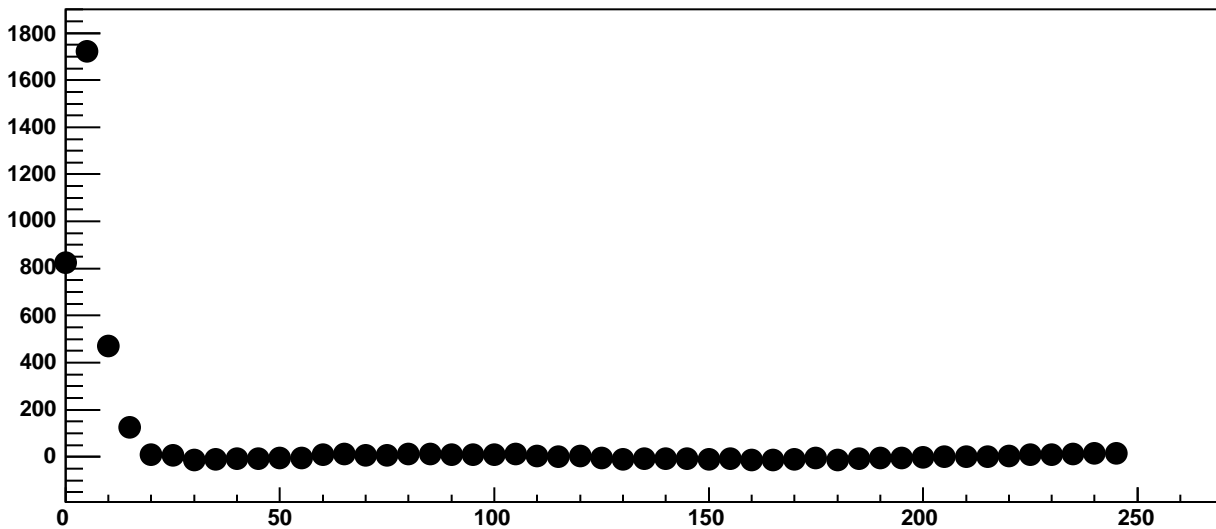


$\chi^2 / \text{ndf}$	816.9 / 42
p0	-375 ± 0.5705
p1	4.754 ± 0.008411
p2	1.085e+04 ± 0.9323
p3	31.63 ± 0.006195

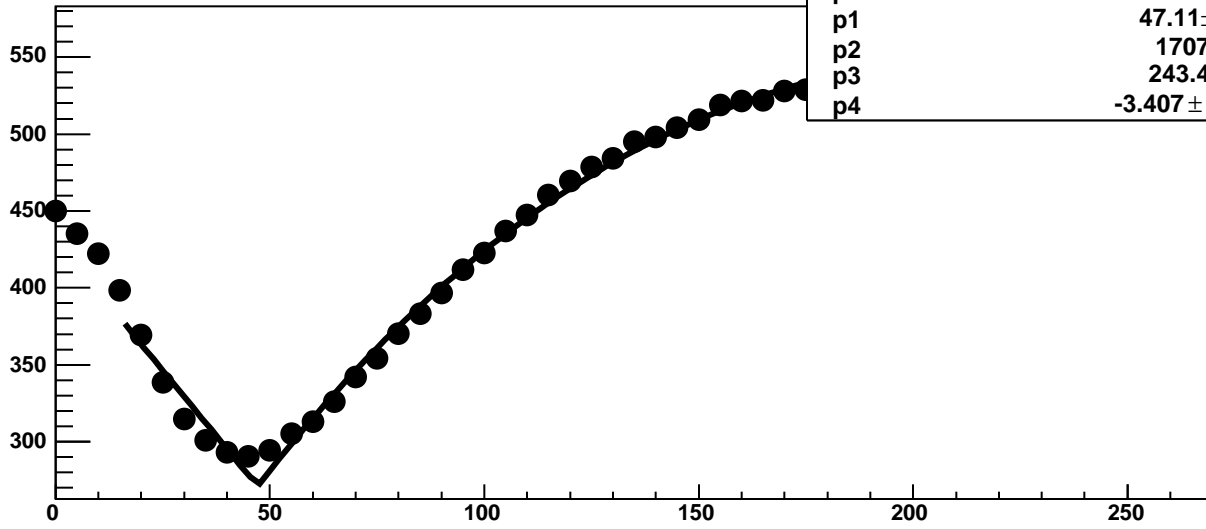
Chip 1, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold

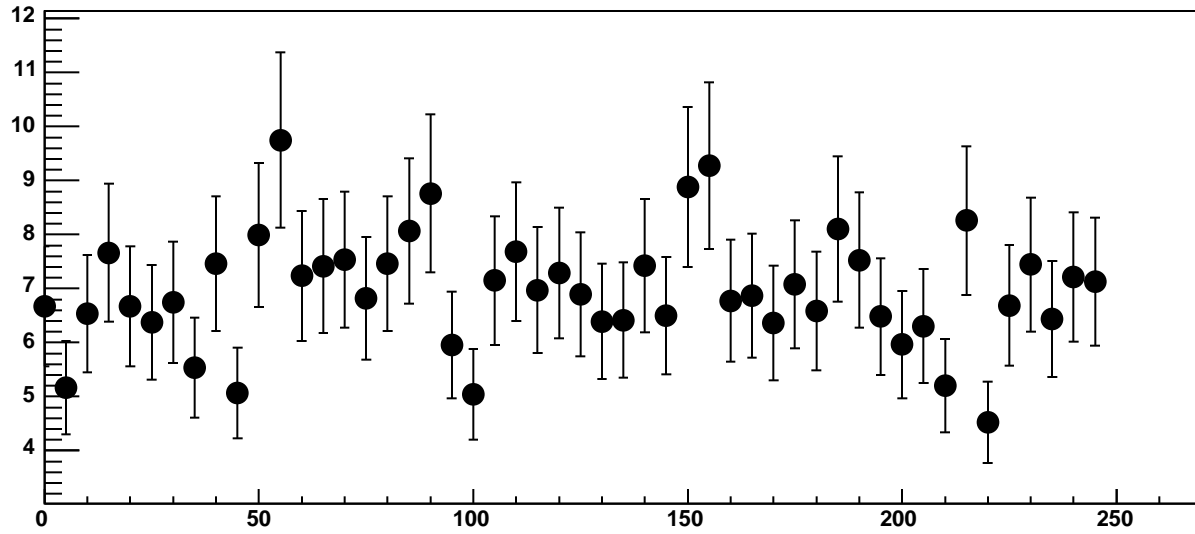


Chip 1, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

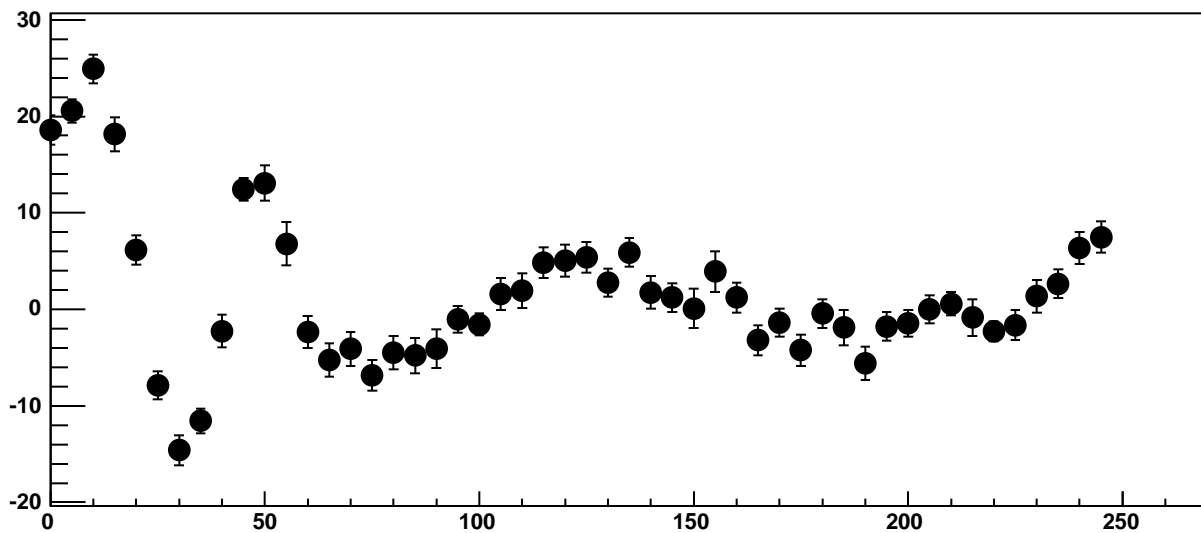


$\chi^2 / \text{ndf}$	664.8 / 41
p0	271 ± 0.6709
p1	47.11 ± 0.2035
p2	1707 ± 35.07
p3	243.4 ± 3.694
p4	-3.407 ± 0.05522

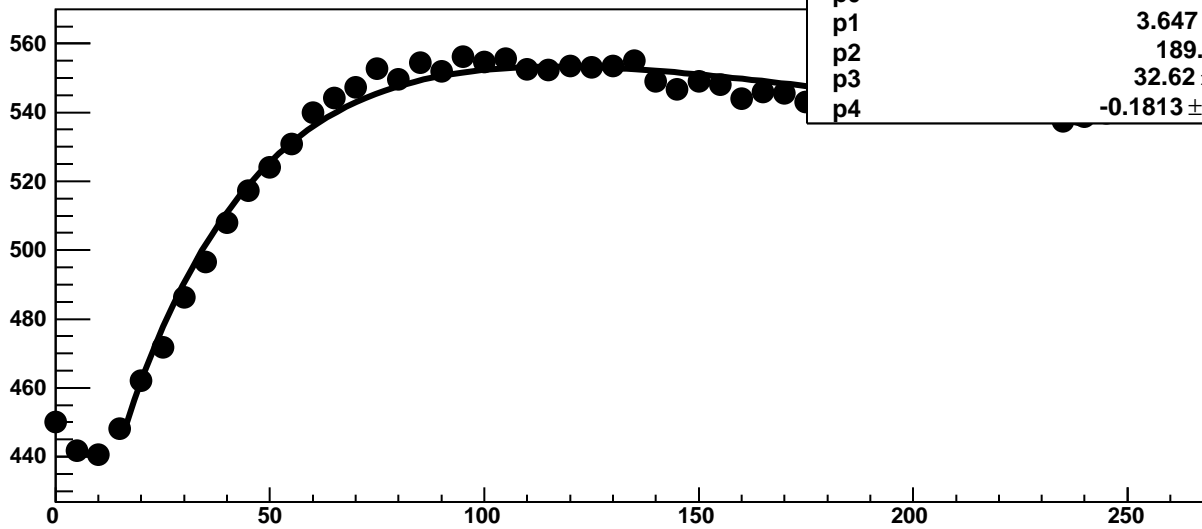
Chip 1, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

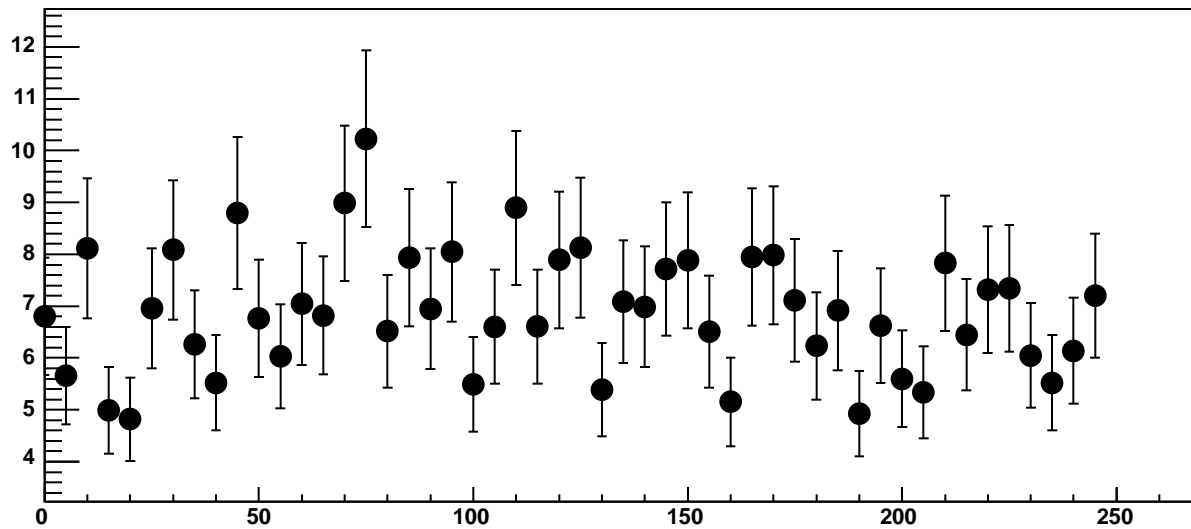


Chip 1, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

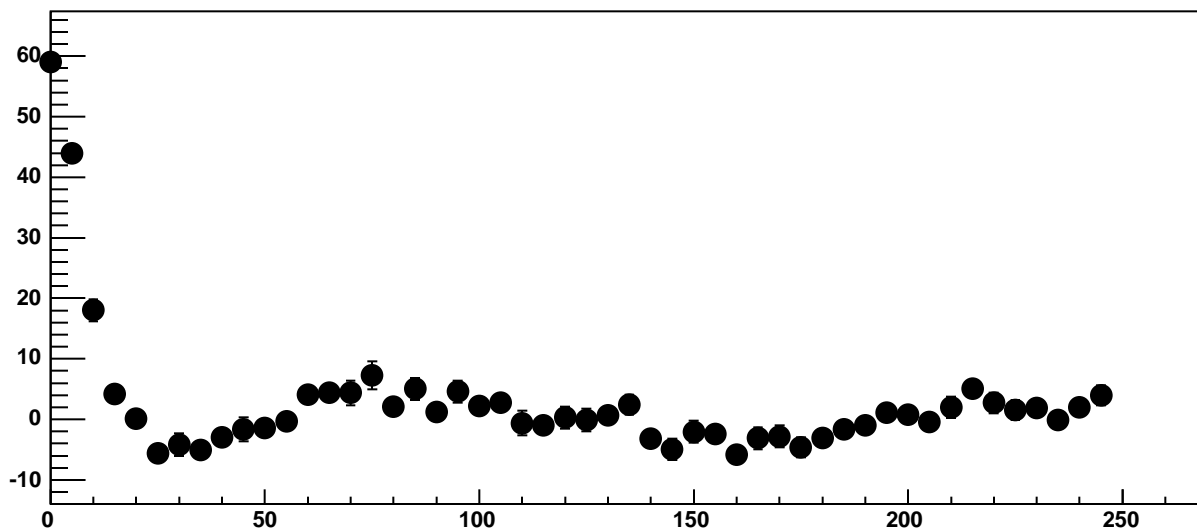


$\chi^2 / \text{ndf}$	185.8 / 41
p0	$390.3 \pm 2.84$
p1	$3.647 \pm 0.5729$
p2	$189.4 \pm 2.96$
p3	$32.62 \pm 0.7155$
p4	$-0.1813 \pm 0.00824$

Chip 1, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold

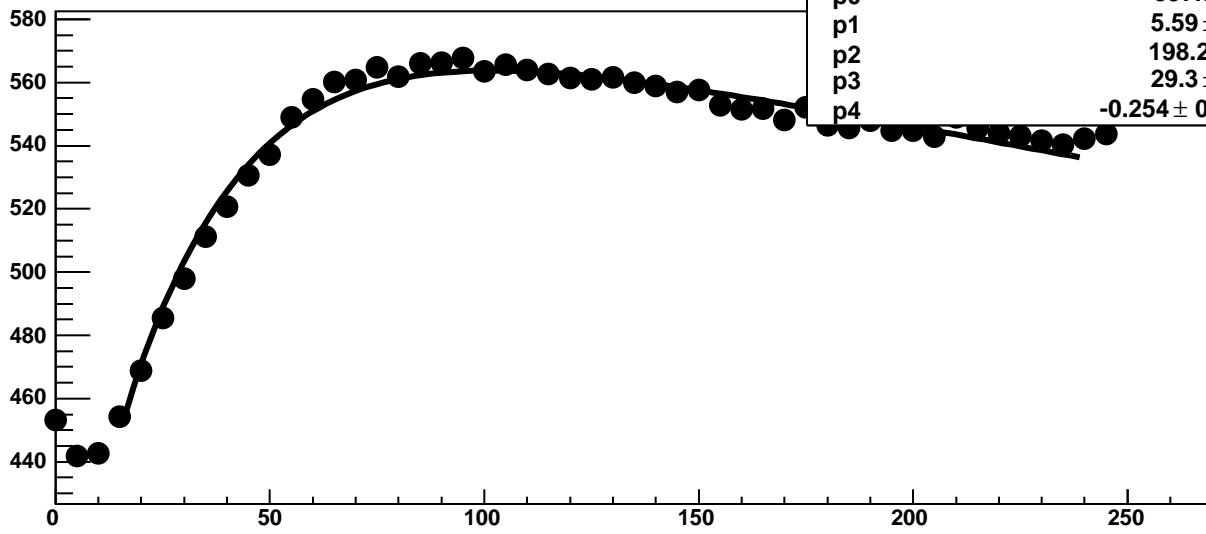


Chip 1, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold



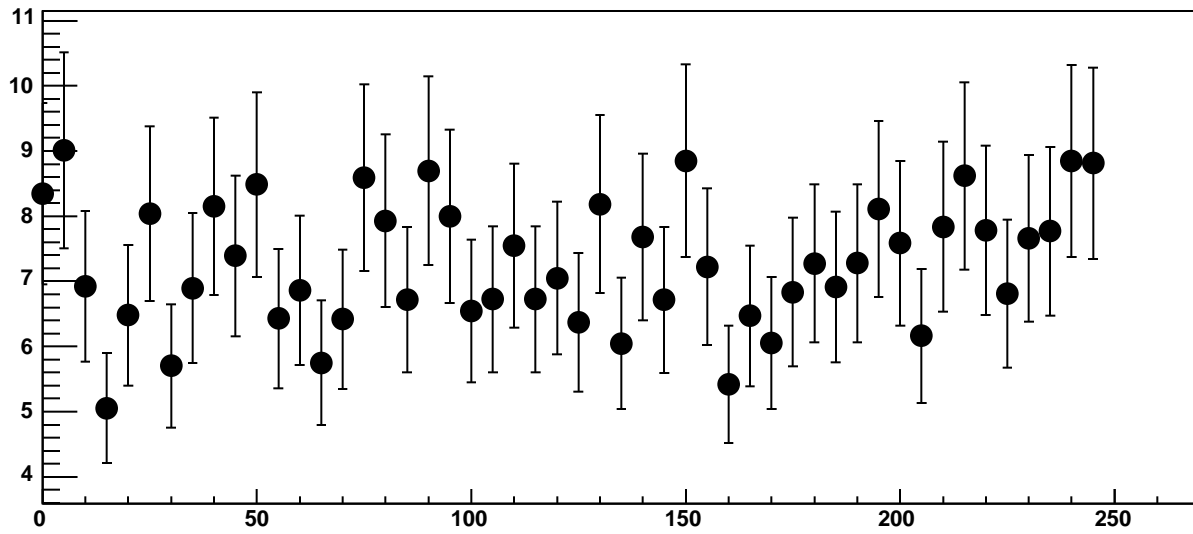


Chip 1, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold

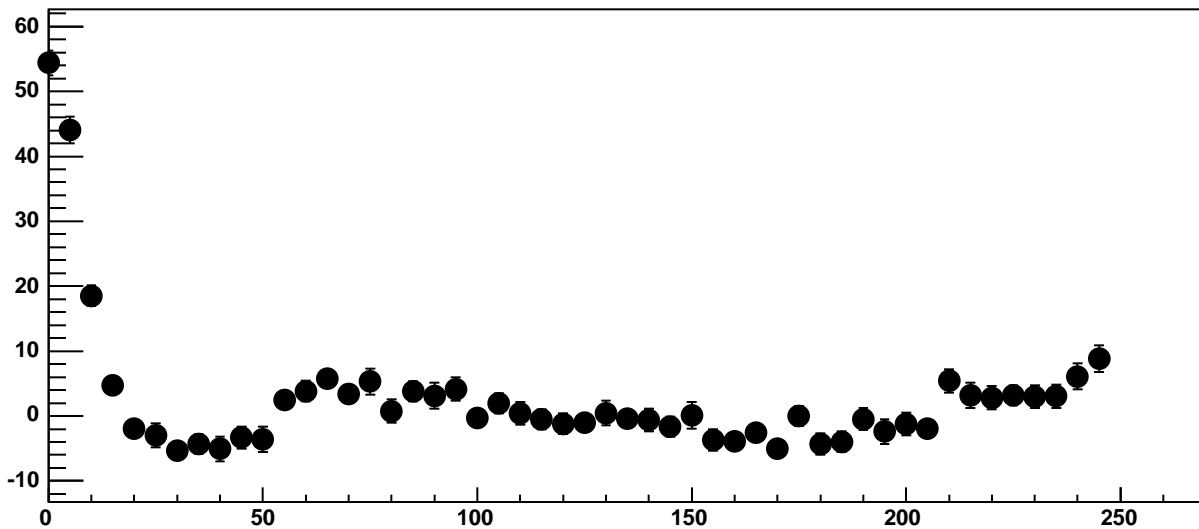


$\chi^2 / \text{ndf}$	199.9 / 41
p0	$397.5 \pm 3.011$
p1	$5.59 \pm 0.5219$
p2	$198.2 \pm 3.149$
p3	$29.3 \pm 0.6027$
p4	$-0.254 \pm 0.008395$

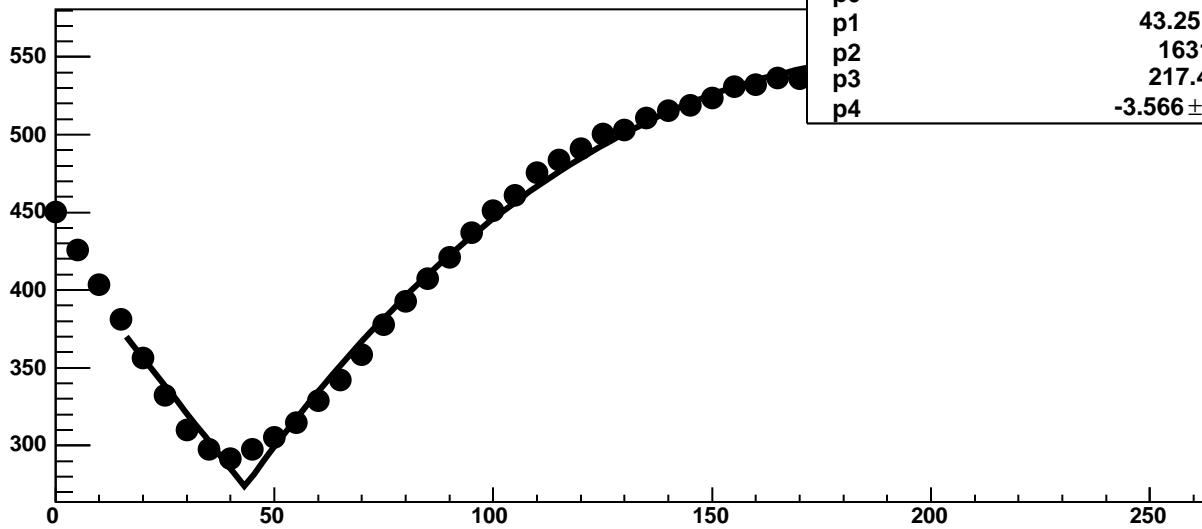
Chip 1, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold

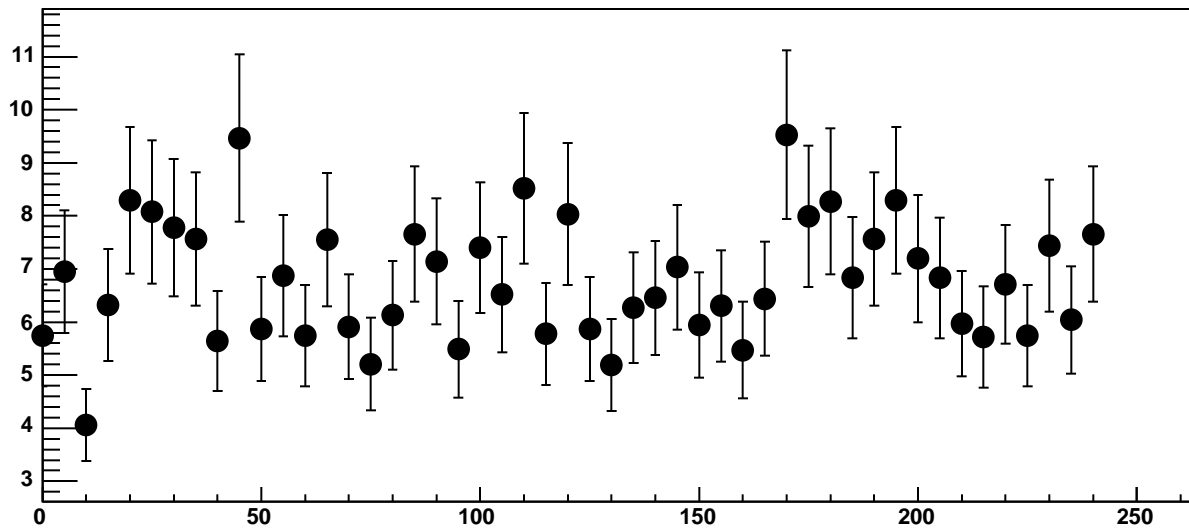


Chip 1, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

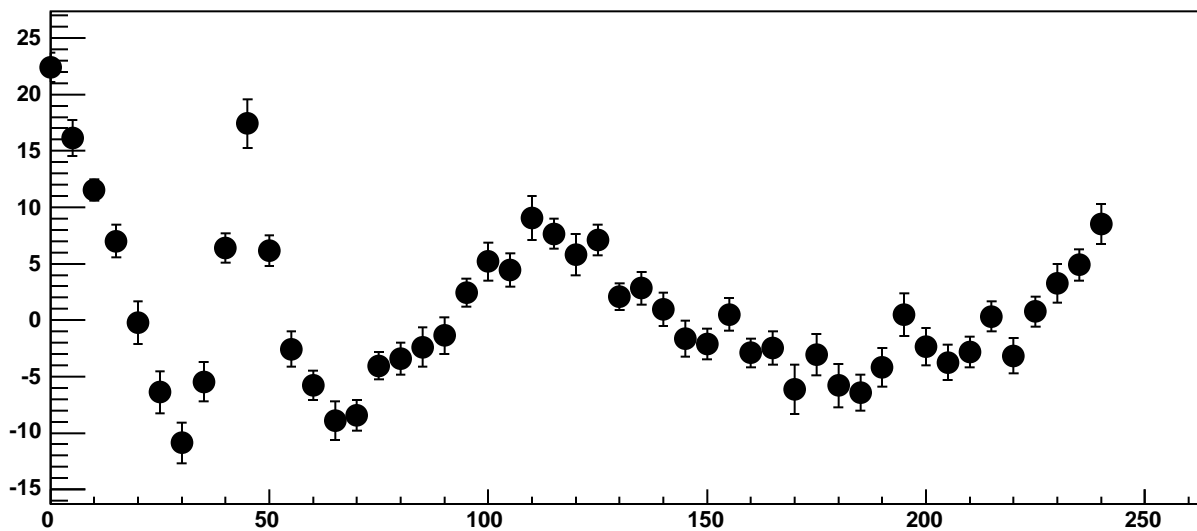


$\chi^2 / \text{ndf}$	531.1 / 41
p0	$273.4 \pm 0.7131$
p1	$43.25 \pm 0.2051$
p2	$1631 \pm 39.14$
p3	$217.4 \pm 3.523$
p4	$-3.566 \pm 0.06972$

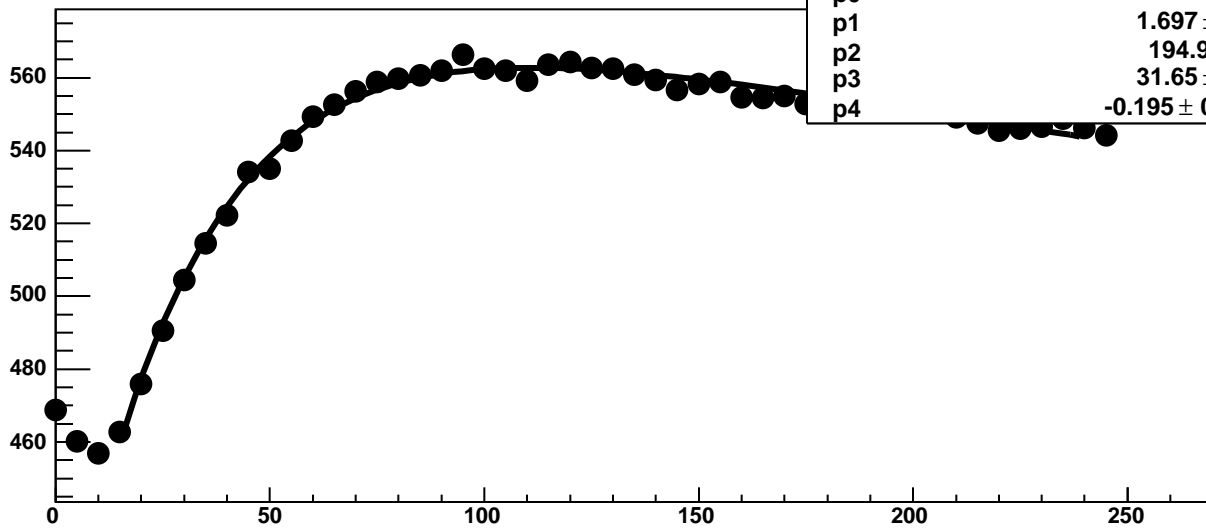
Chip 1, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



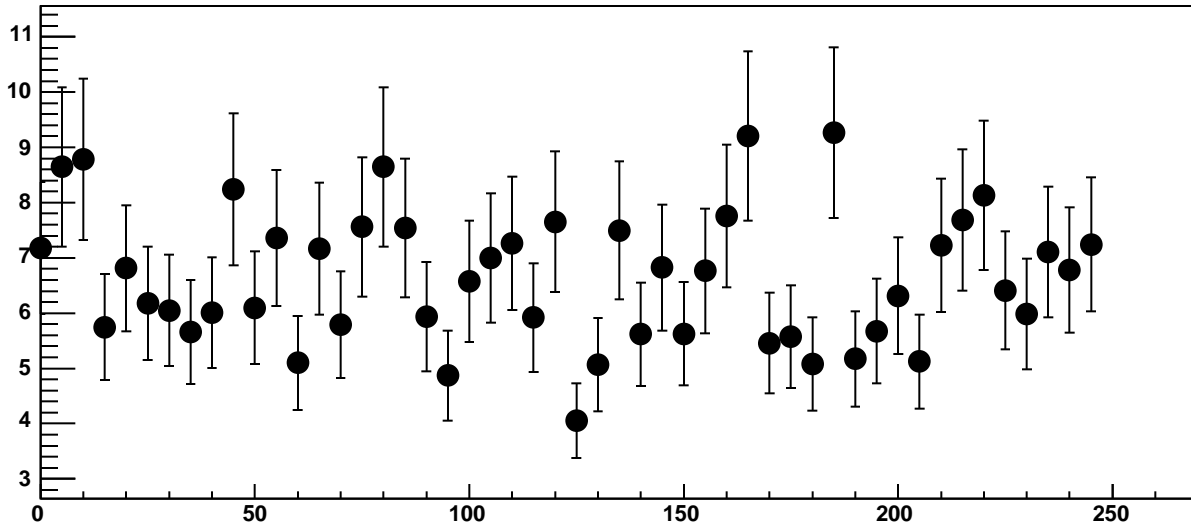
Chip 1, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold



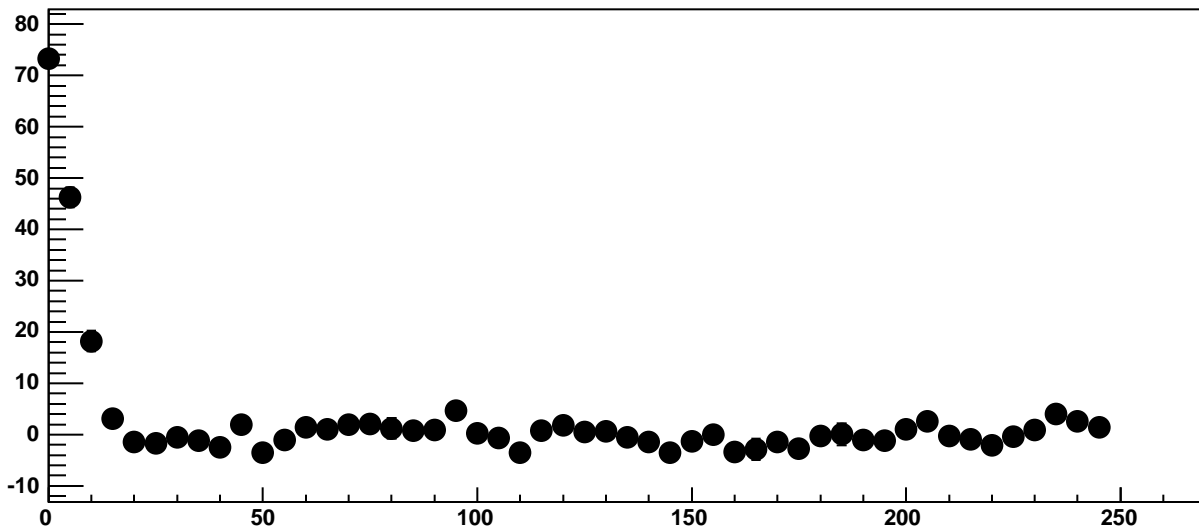
Chip 1, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold



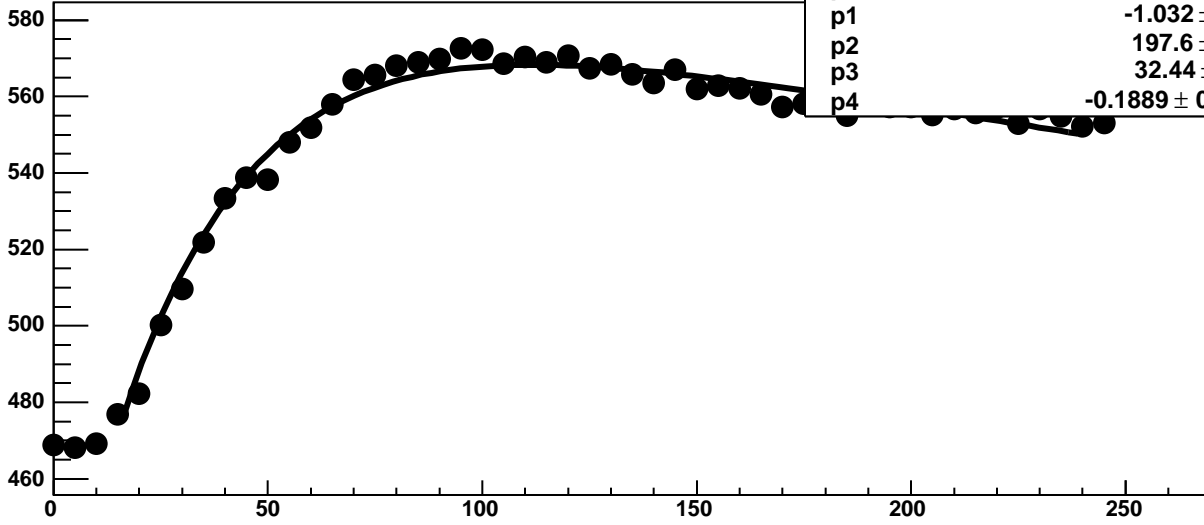
Chip 1, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

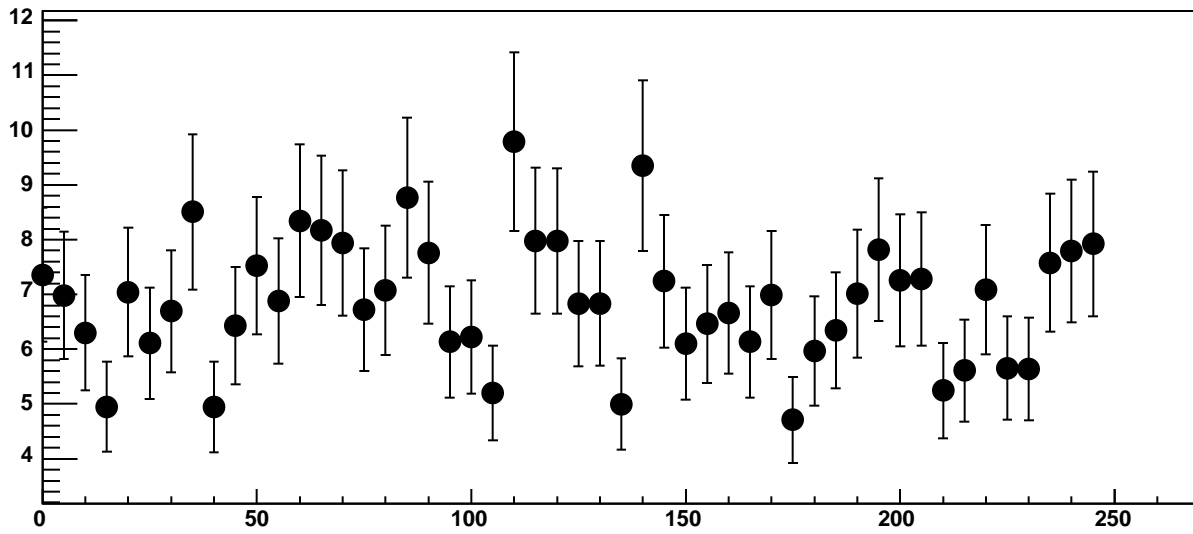


Chip 1, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold

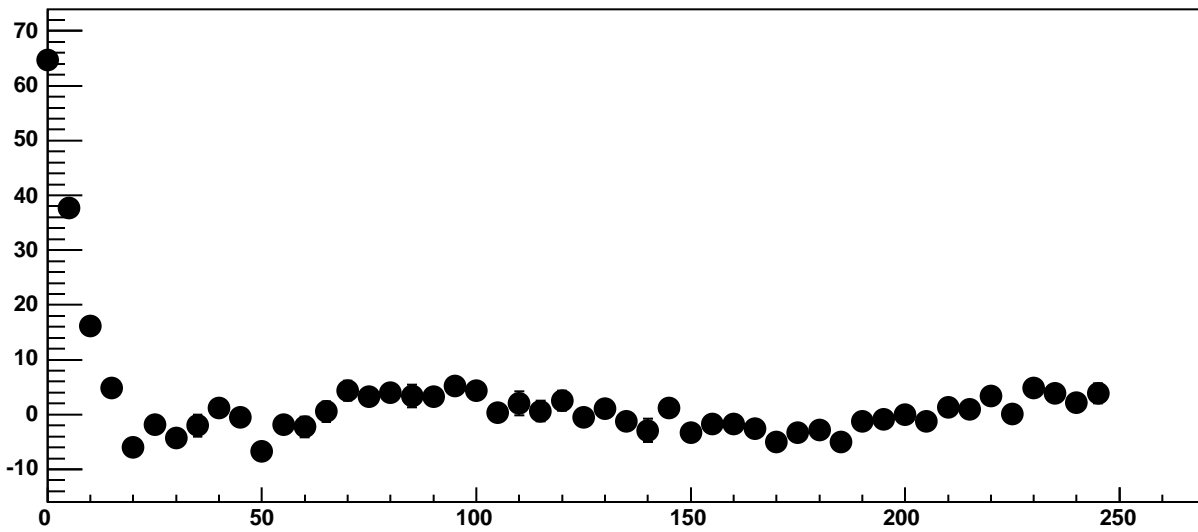


$\chi^2 / \text{ndf}$	189.1 / 41
p0	$398.1 \pm 0.886$
p1	$-1.032 \pm 0.4306$
p2	$197.6 \pm 0.9634$
p3	$32.44 \pm 0.8583$
p4	$-0.1889 \pm 0.009202$

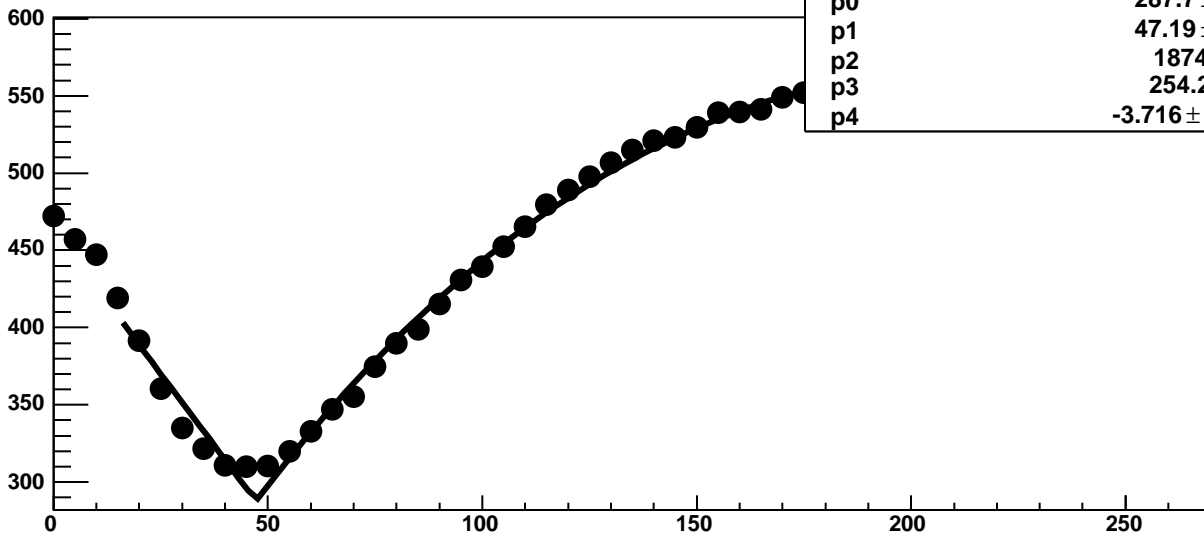
Chip 1, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold

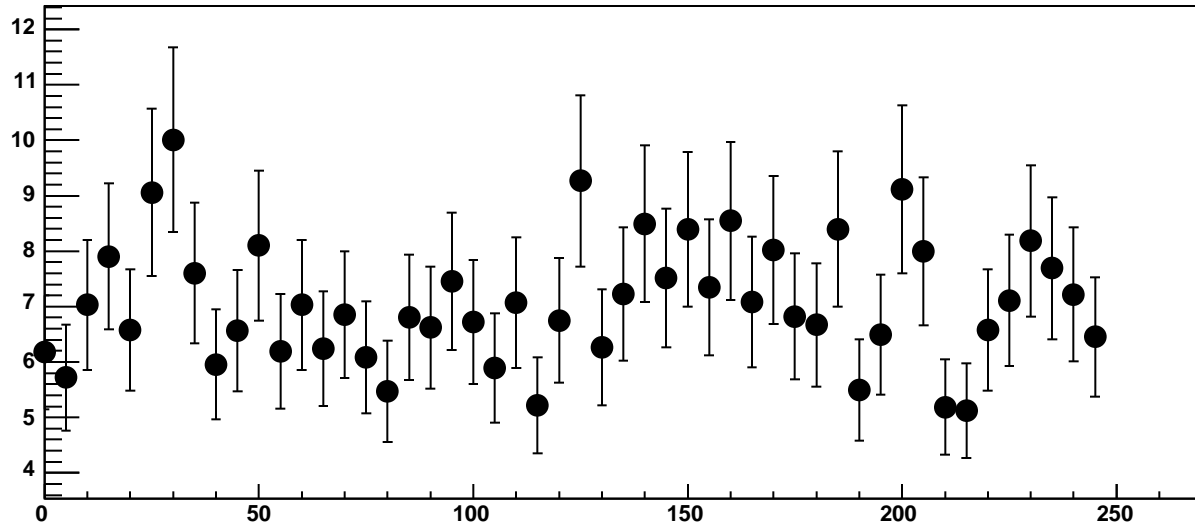


Chip 1, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

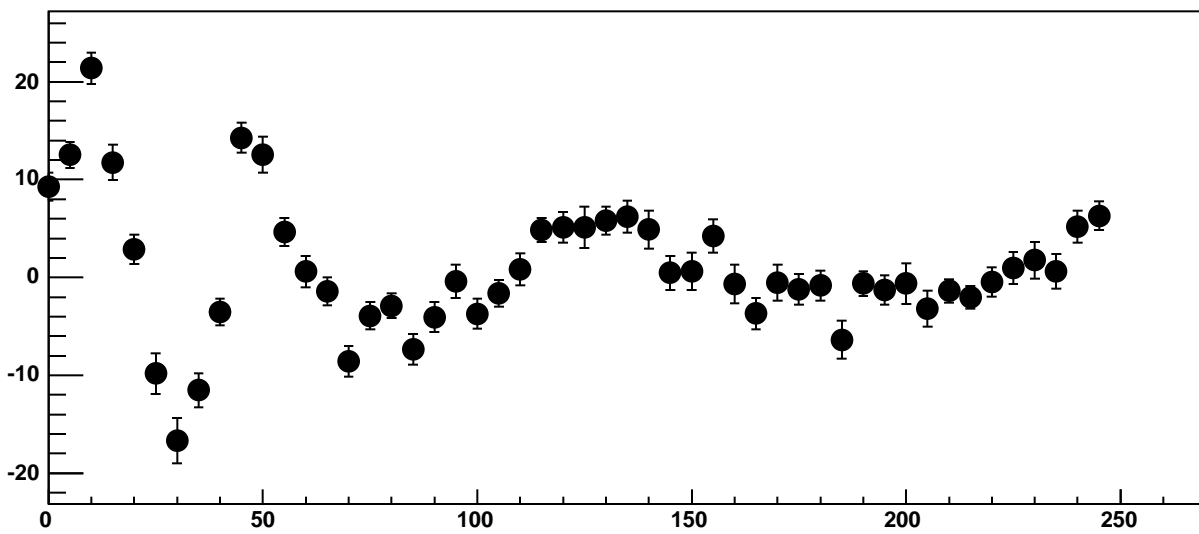


$\chi^2 / \text{ndf}$	511 / 41
p0	$287.7 \pm 0.6798$
p1	$47.19 \pm 0.1953$
p2	$1874 \pm 38.06$
p3	$254.2 \pm 3.661$
p4	$-3.716 \pm 0.05913$

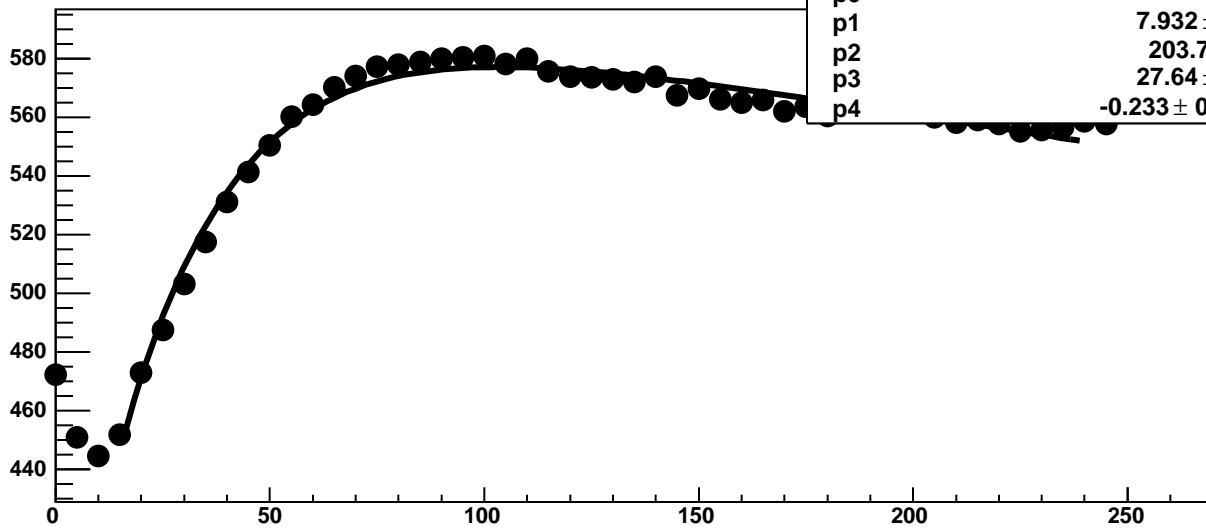
Chip 1, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

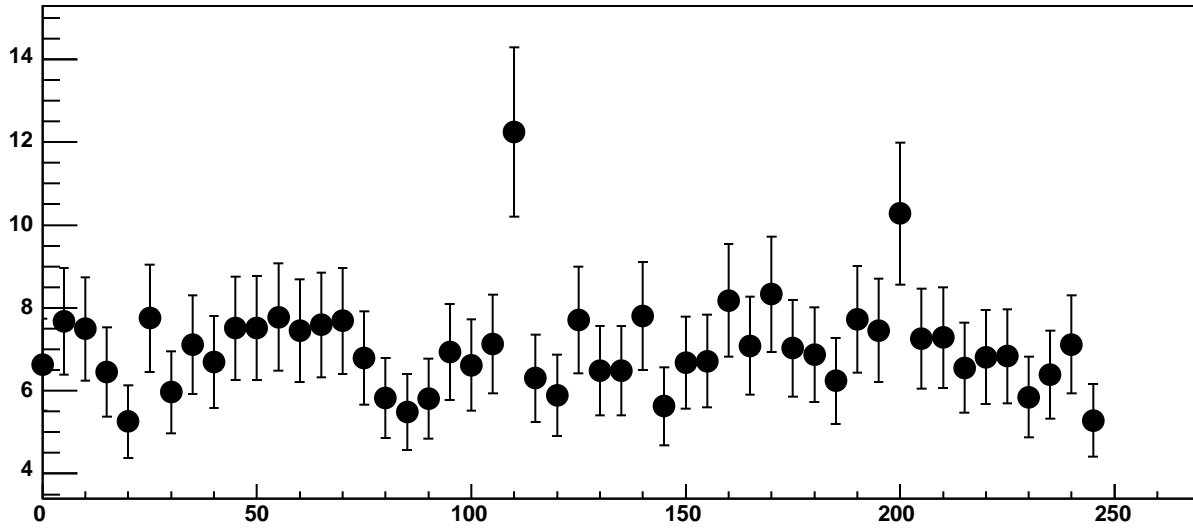


Chip 1, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

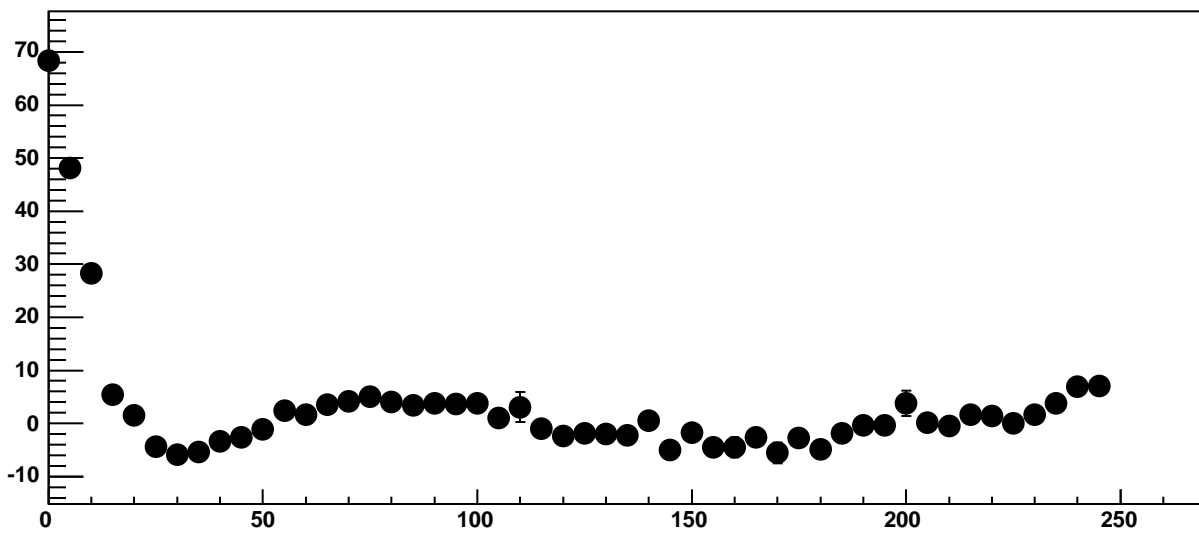


$\chi^2 / \text{ndf}$	214.9 / 41
p0	$402 \pm 2.937$
p1	$7.932 \pm 0.4647$
p2	$203.7 \pm 3.049$
p3	$27.64 \pm 0.5199$
p4	$-0.233 \pm 0.007433$

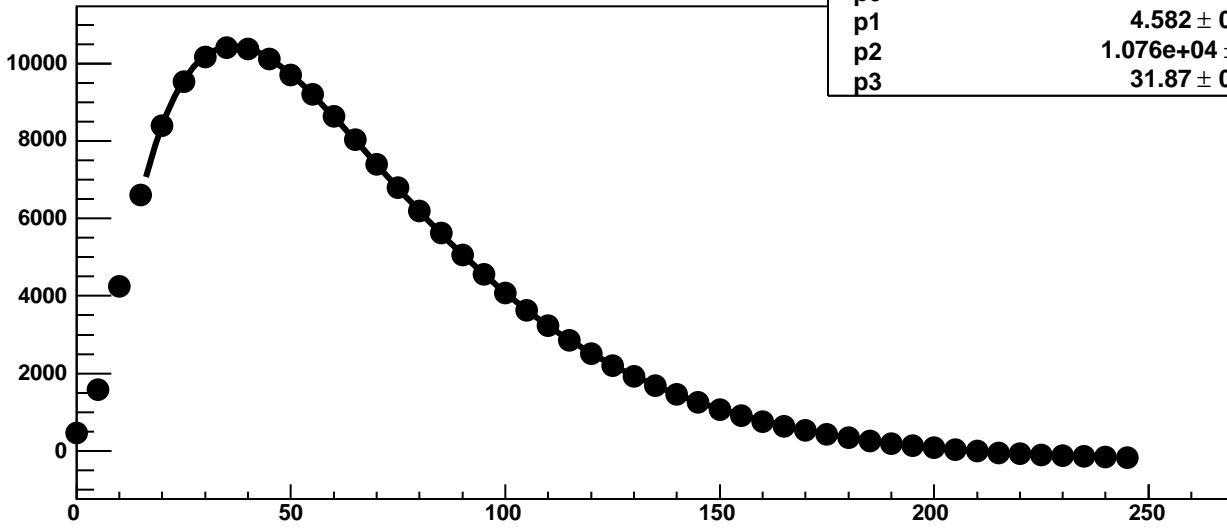
Chip 1, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

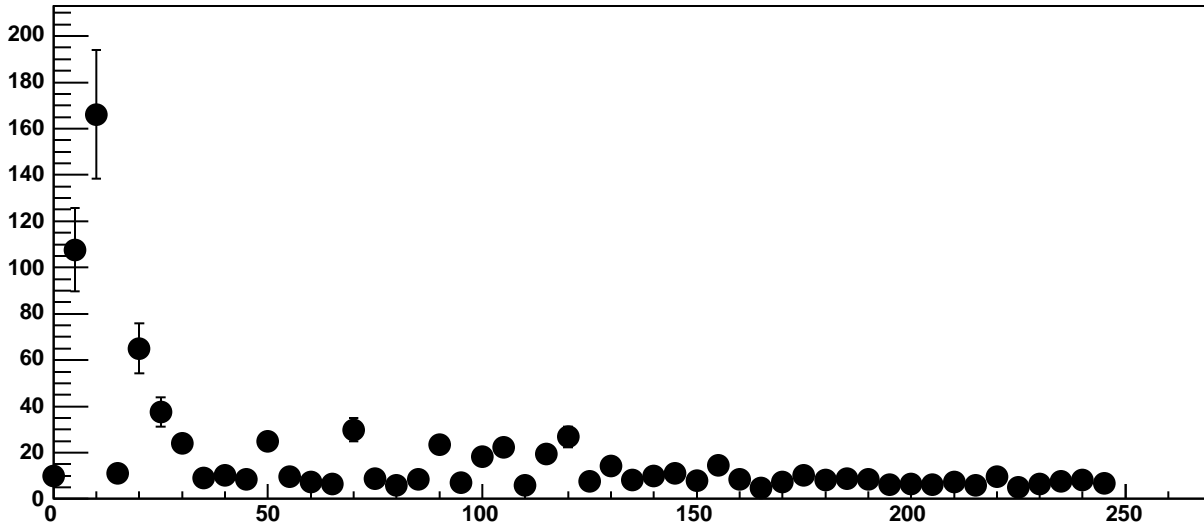


Chip 1, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold

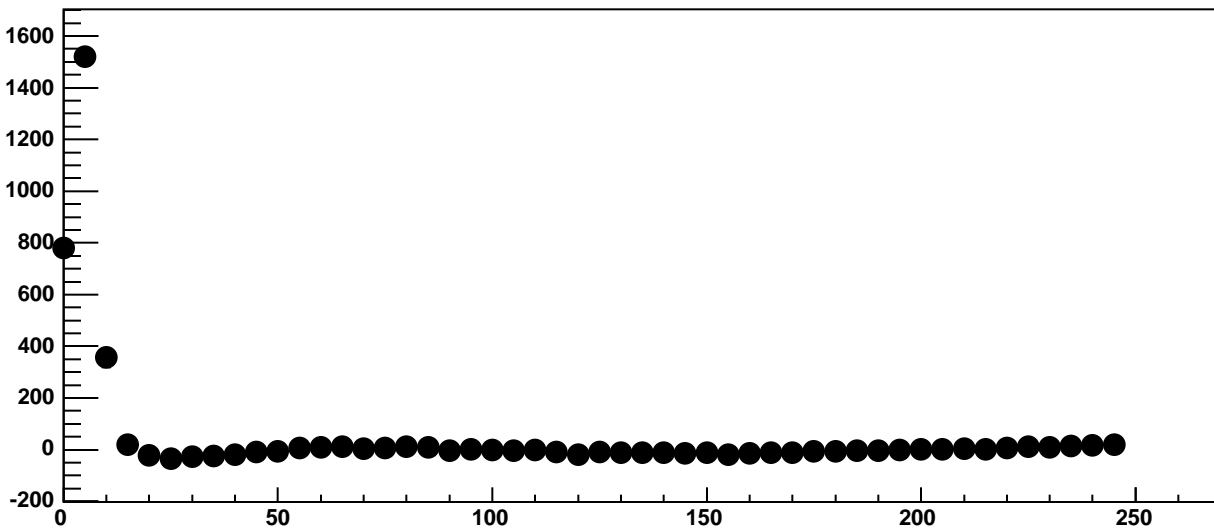


$\chi^2 / \text{ndf}$	1316 / 42
p0	$-311.7 \pm 0.5397$
p1	$4.582 \pm 0.006562$
p2	$1.076\text{e}+04 \pm 0.9767$
p3	$31.87 \pm 0.004962$

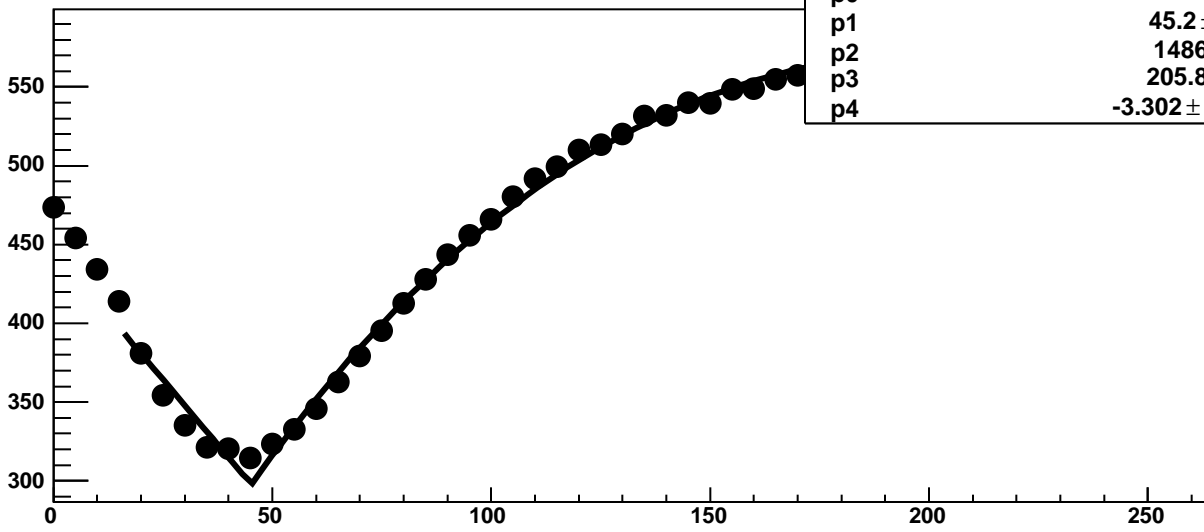
Chip 1, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold

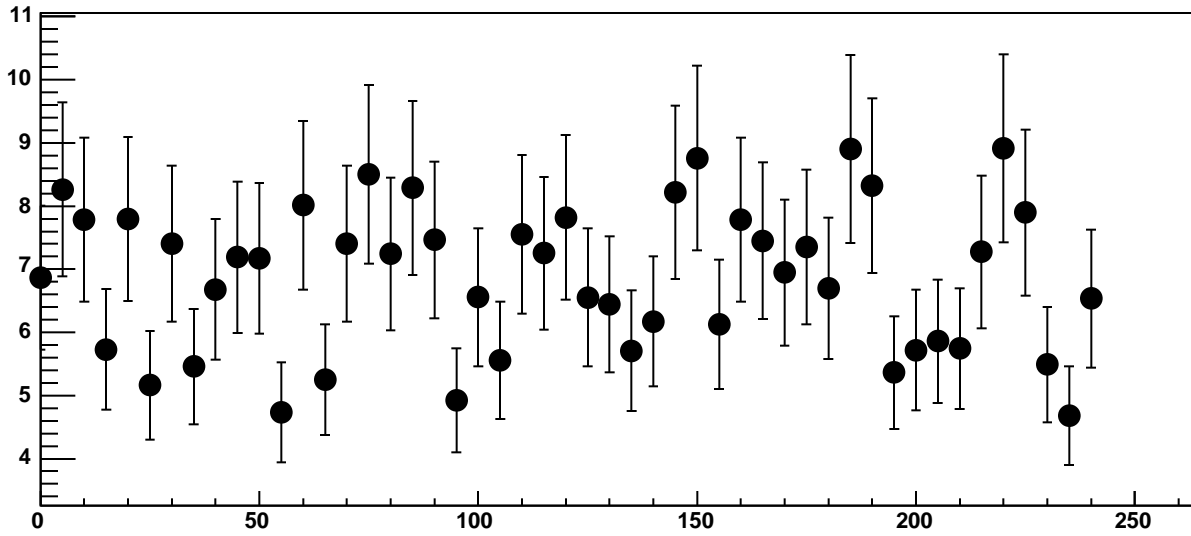


Chip 1, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

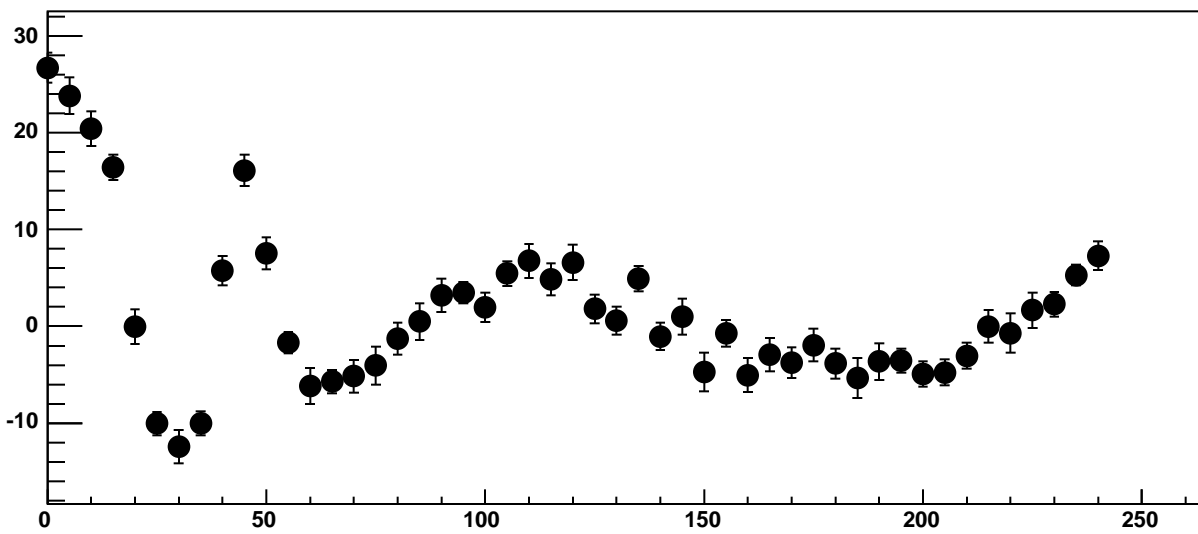


$\chi^2 / \text{ndf}$	745.2 / 41
p0	$297.7 \pm 0.6485$
p1	$45.2 \pm 0.1939$
p2	$1486 \pm 30.76$
p3	$205.8 \pm 3.019$
p4	$-3.302 \pm 0.05647$

Chip 1, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold

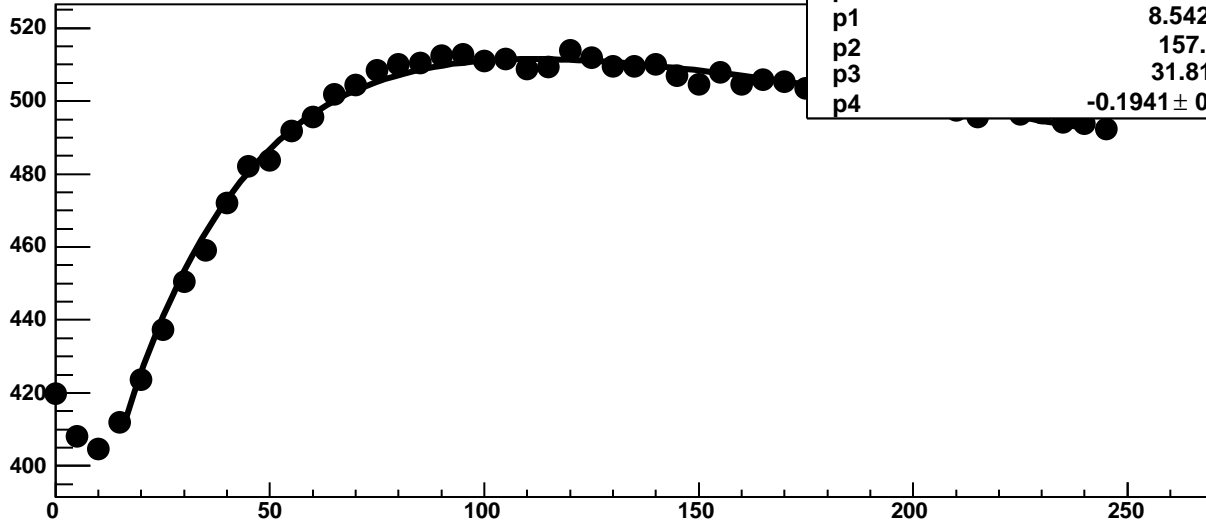


Chip 1, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold



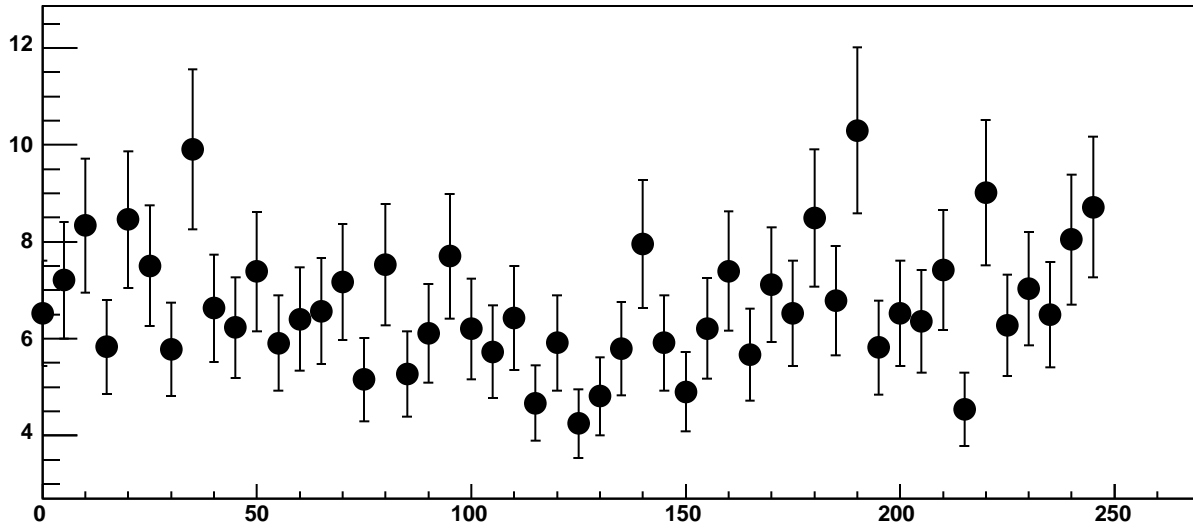


Chip 1, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

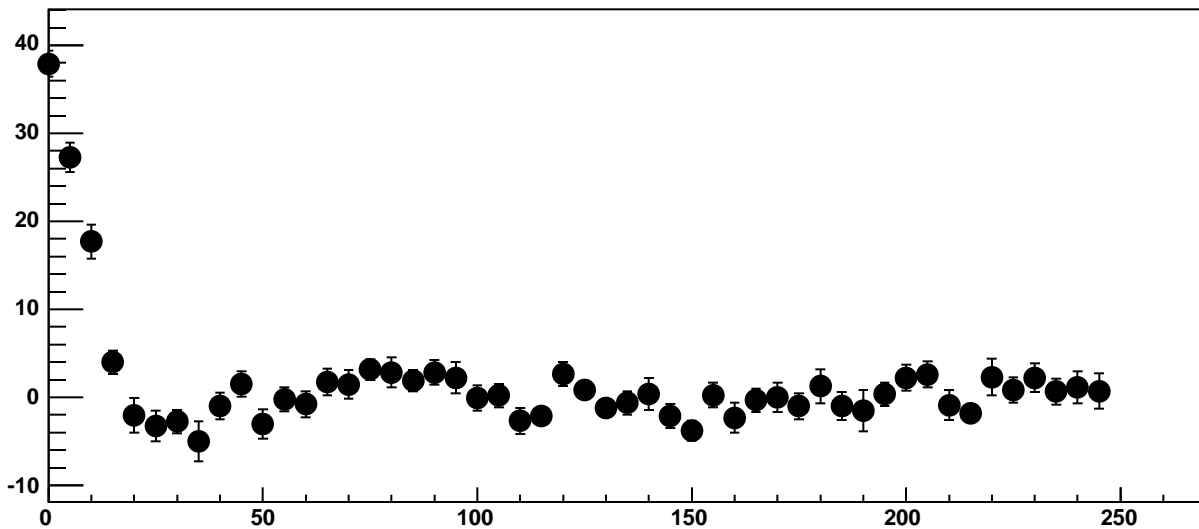


$\chi^2 / \text{ndf}$	91.27 / 41
p0	$380.2 \pm 2.68$
p1	$8.542 \pm 0.626$
p2	$157.5 \pm 2.81$
p3	$31.81 \pm 0.747$
p4	$-0.1941 \pm 0.008059$

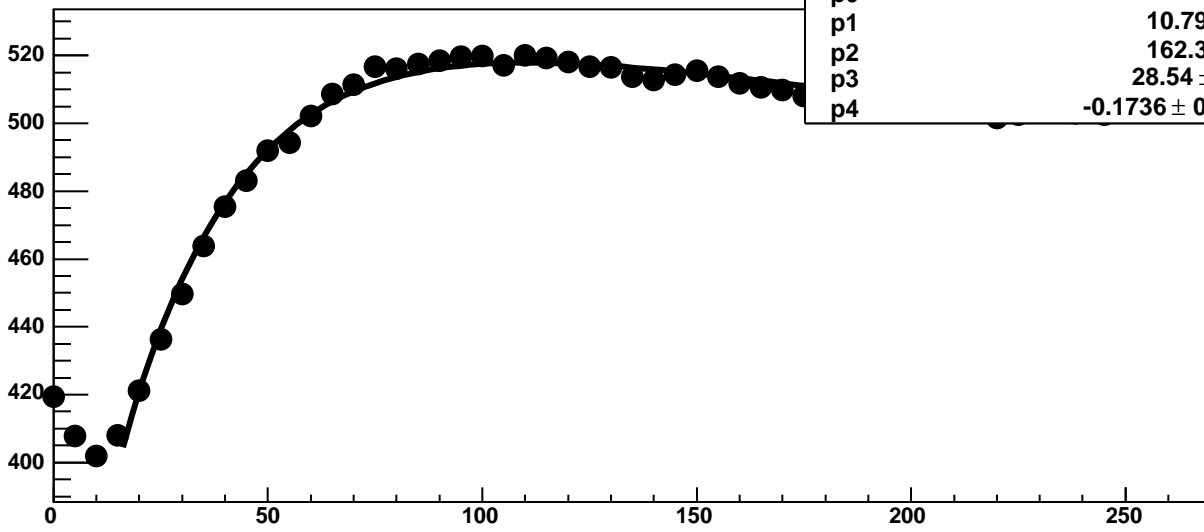
Chip 1, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold

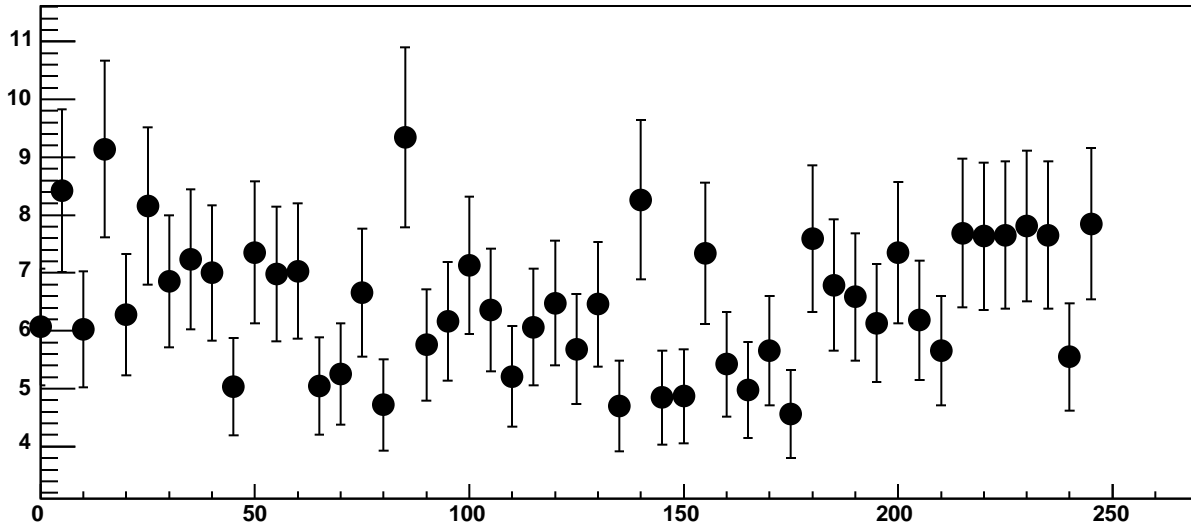


Chip 1, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

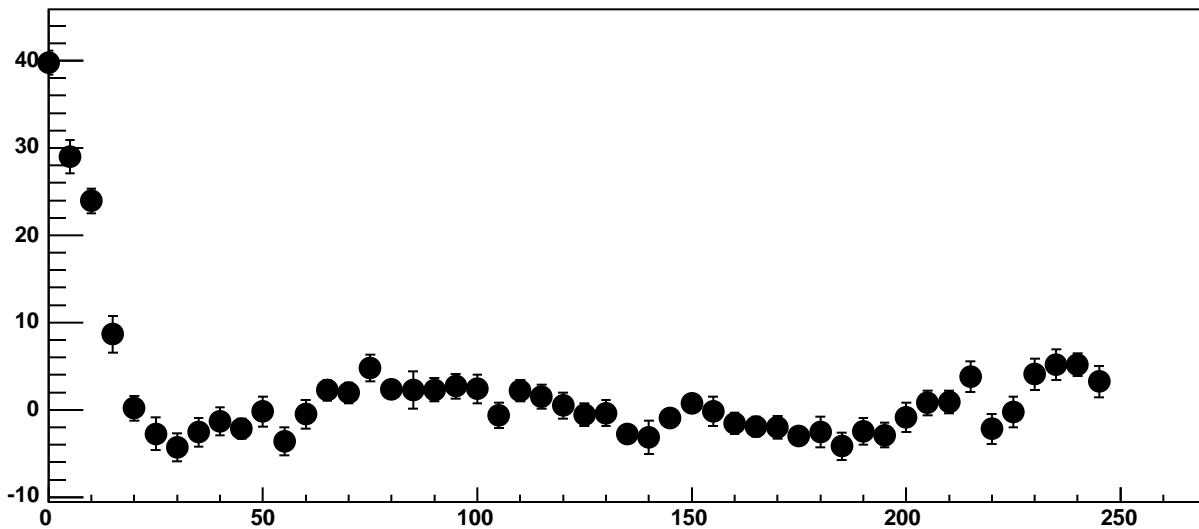


$\chi^2 / \text{ndf}$	153.8 / 41
p0	$377.8 \pm 2.644$
p1	$10.79 \pm 0.548$
p2	$162.3 \pm 2.743$
p3	$28.54 \pm 0.6056$
p4	$-0.1736 \pm 0.007426$

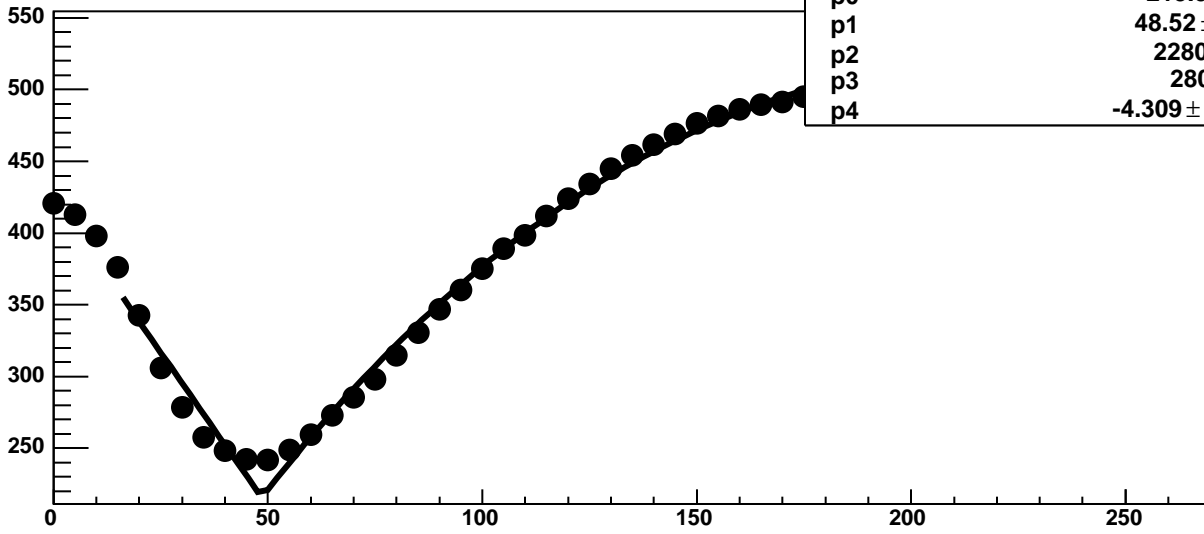
Chip 1, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



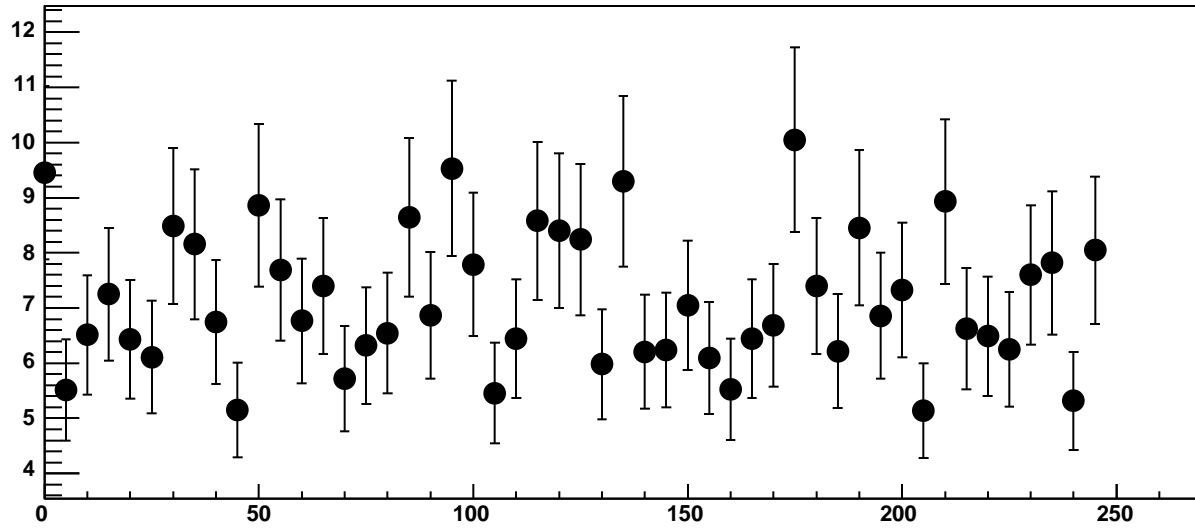
Chip 1, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold



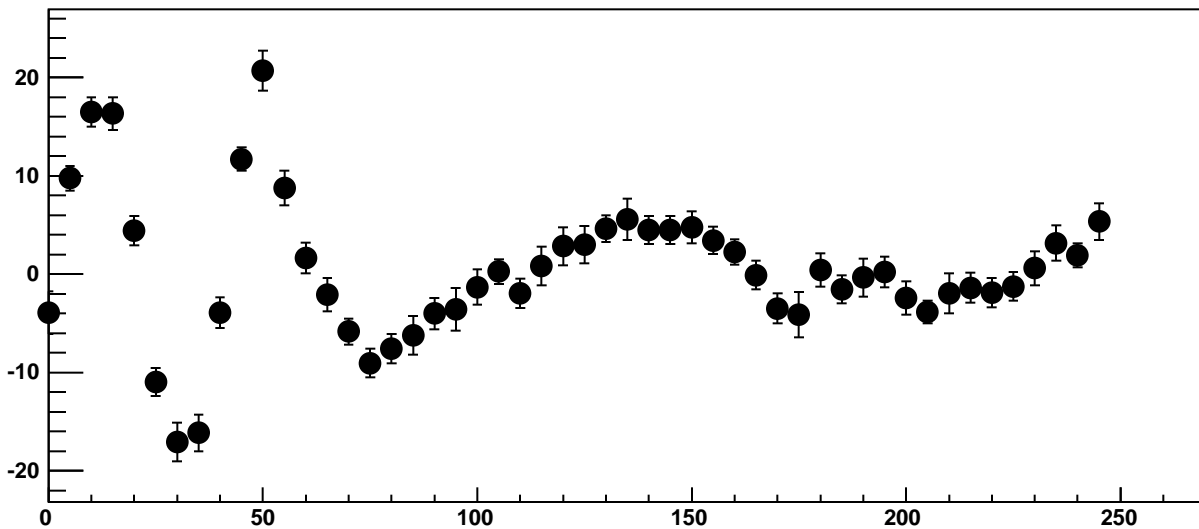
Chip 1, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold



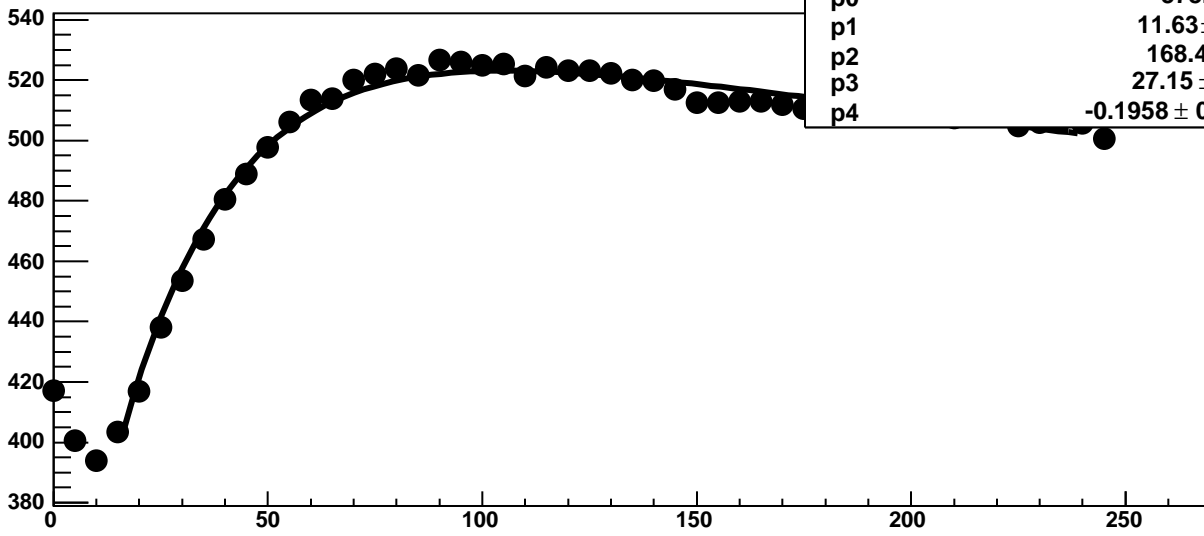
Chip 1, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold

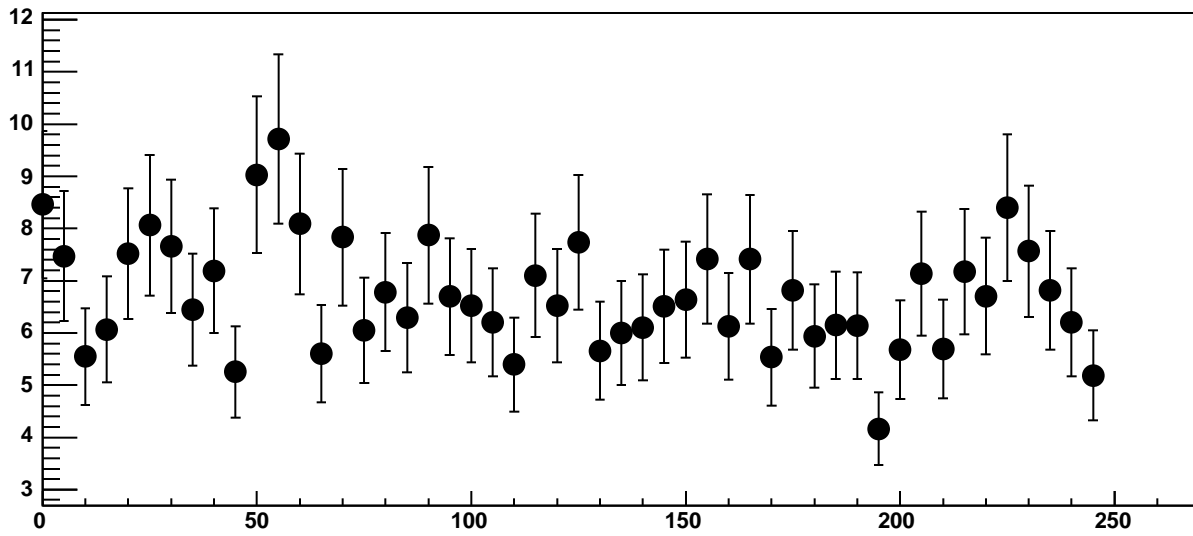


Chip 1, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

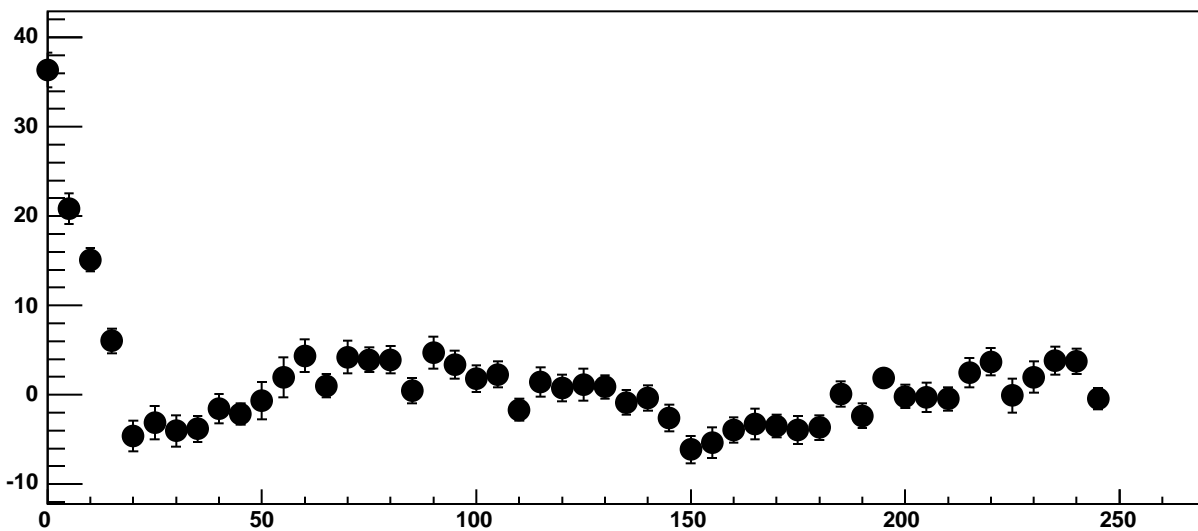


$\chi^2 / \text{ndf}$	182.6 / 41
p0	$378.4 \pm 2.8$
p1	$11.63 \pm 0.5077$
p2	$168.4 \pm 2.906$
p3	$27.15 \pm 0.5304$
p4	$-0.1958 \pm 0.007232$

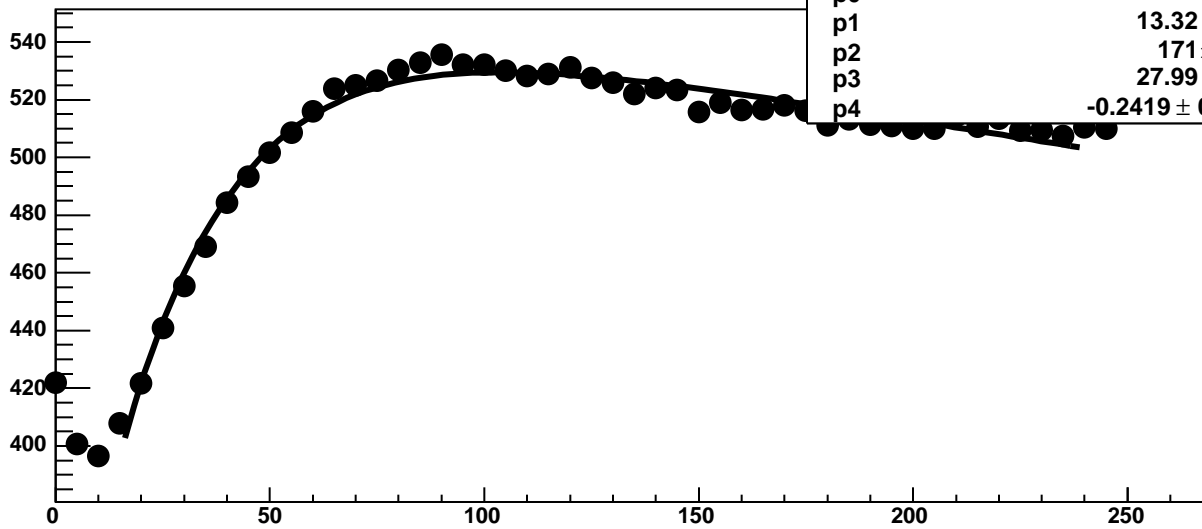
Chip 1, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



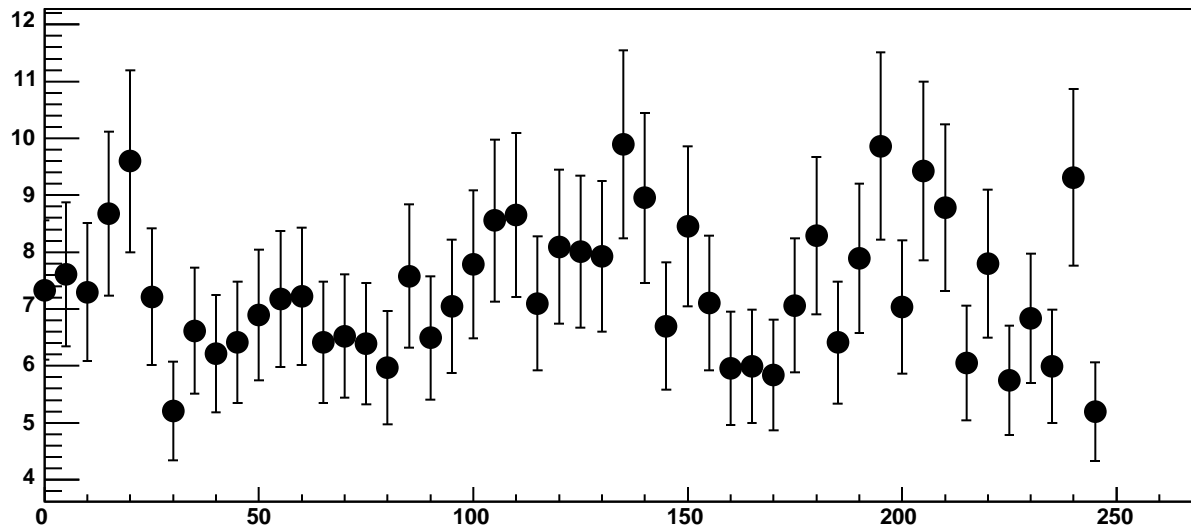
Chip 1, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold



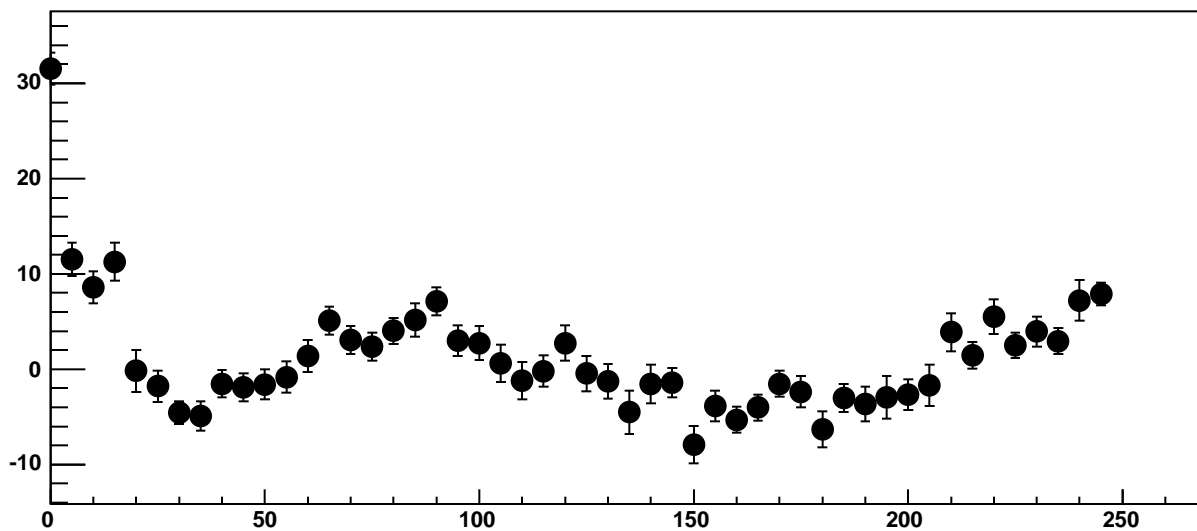
Chip 1, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold



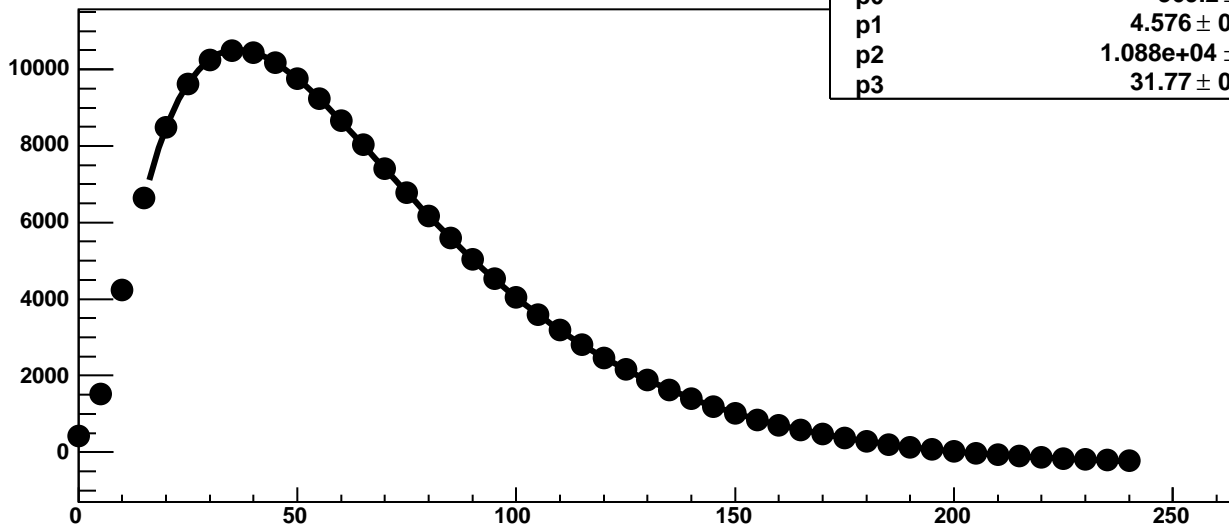
Chip 1, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

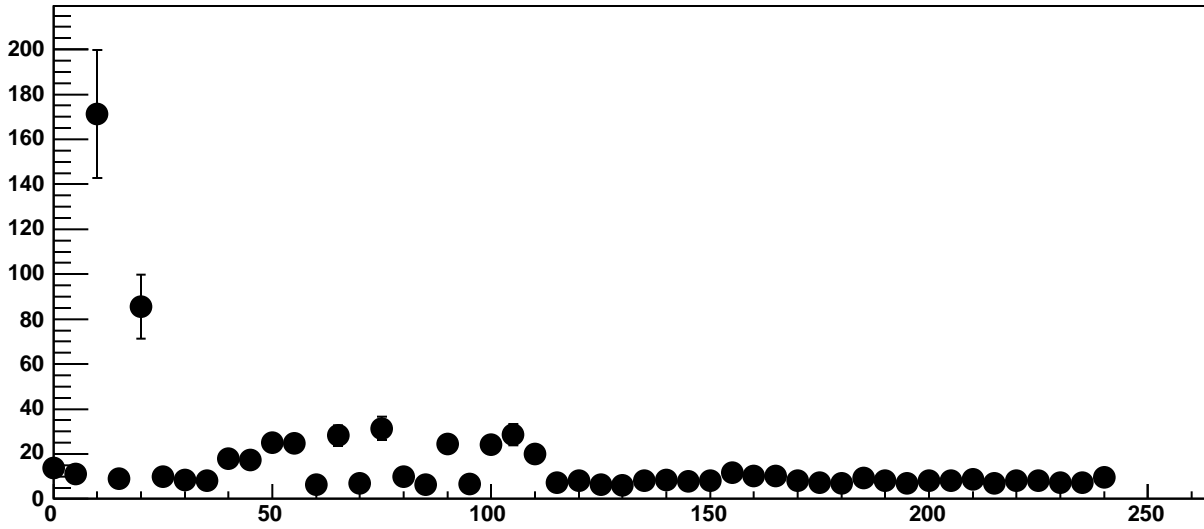


Chip 1, Channel 15, Enable 5!, DAC=4000, ADC Mean vs Hold

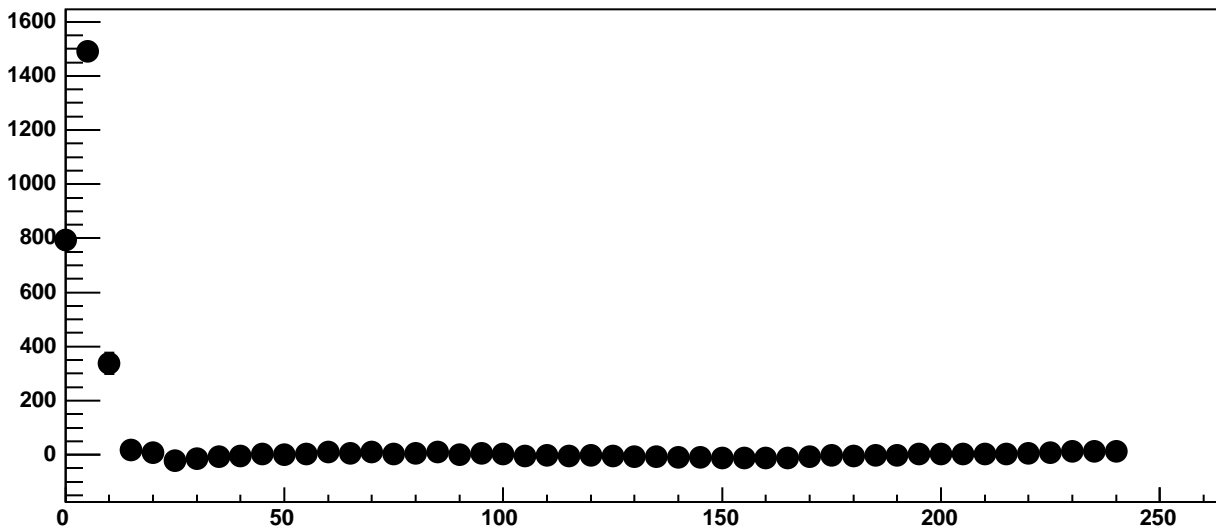


$\chi^2 / \text{ndf}$	816.4 / 42
p0	-369.2 ± 0.5975
p1	4.576 ± 0.005875
p2	1.088e+04 ± 0.9755
p3	31.77 ± 0.004776

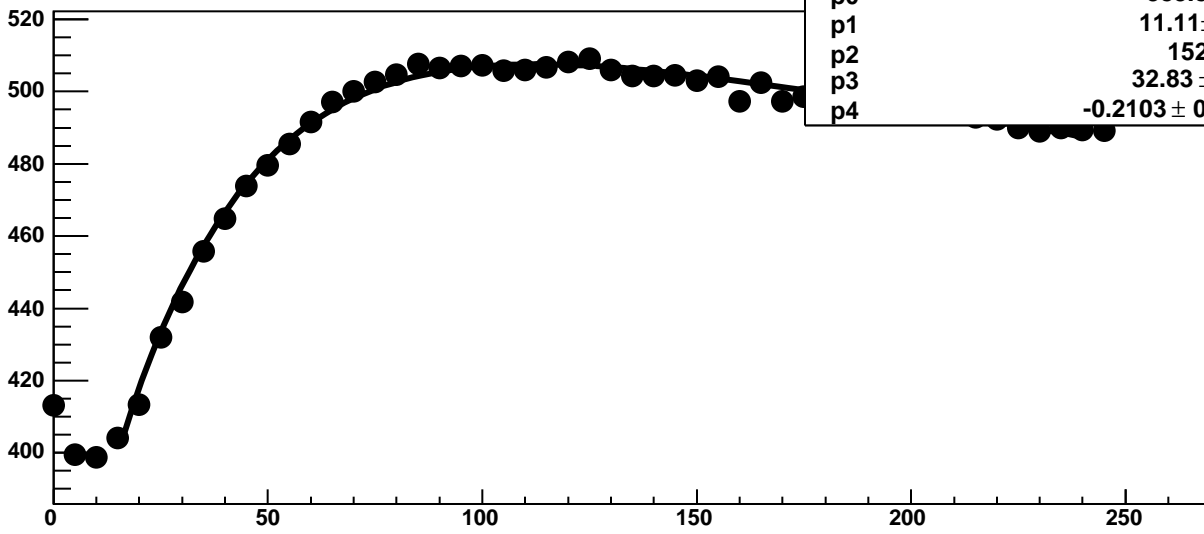
Chip 1, Channel 15, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 15, Enable 5!, DAC=4000, ADC Residuals vs Hold

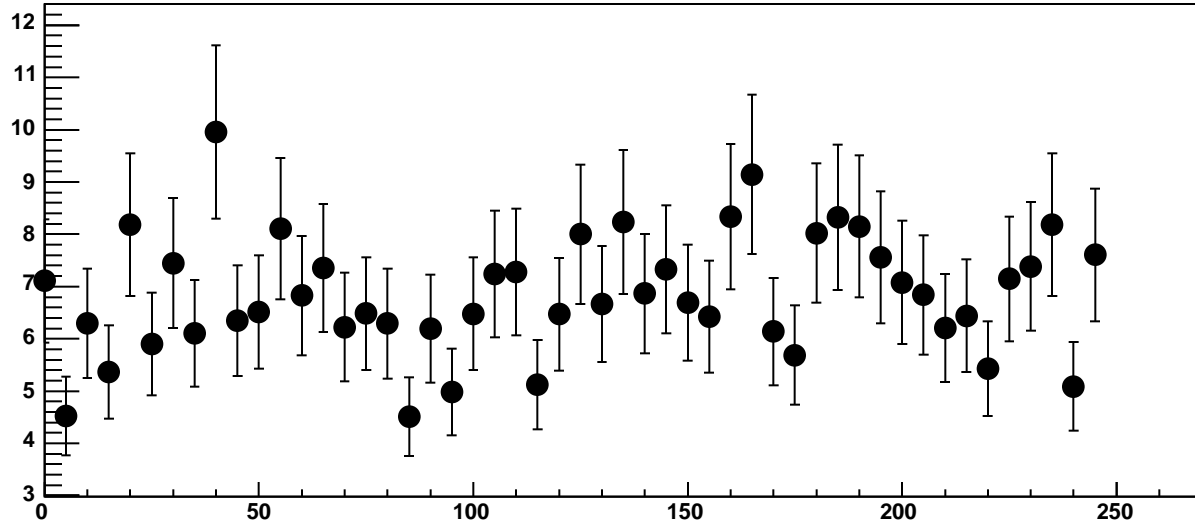


Chip 1, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

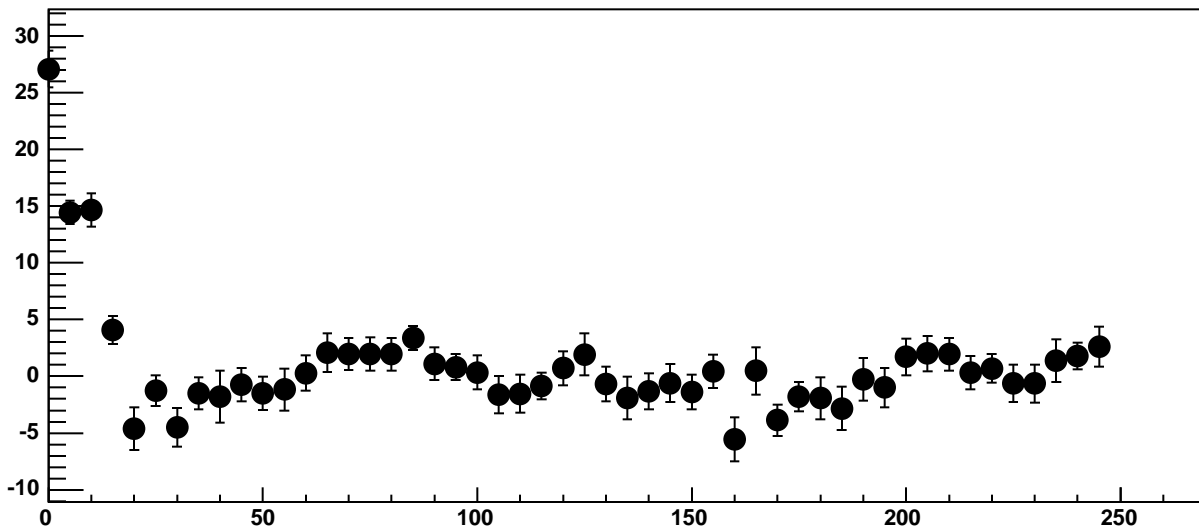


$\chi^2 / \text{ndf}$	83.48 / 41
p0	$383.8 \pm 2.836$
p1	$11.11 \pm 0.6738$
p2	$152 \pm 3.005$
p3	$32.83 \pm 0.7744$
p4	$-0.2103 \pm 0.008543$

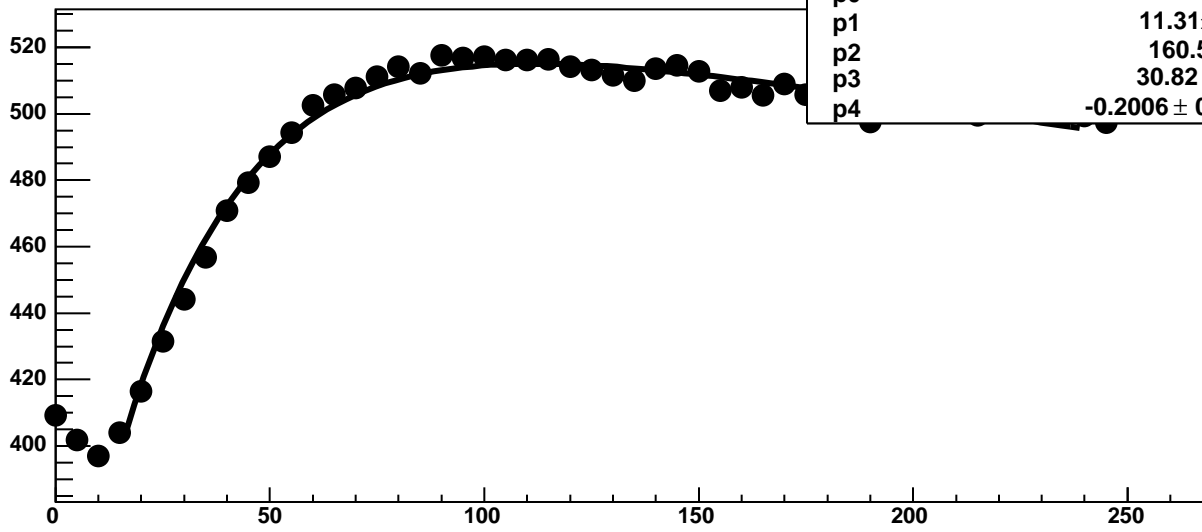
Chip 1, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

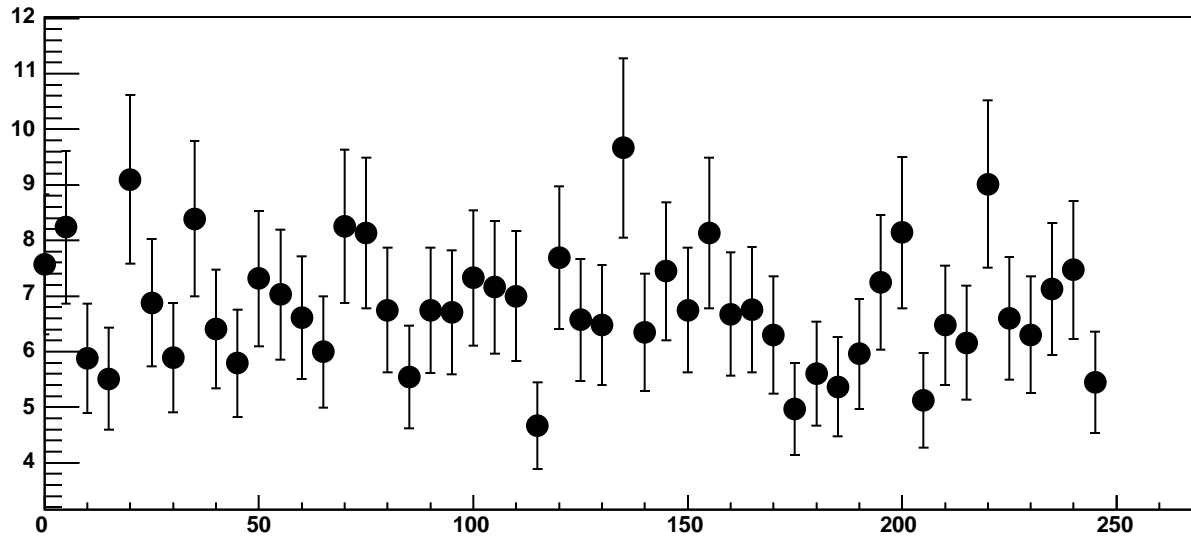


Chip 1, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold

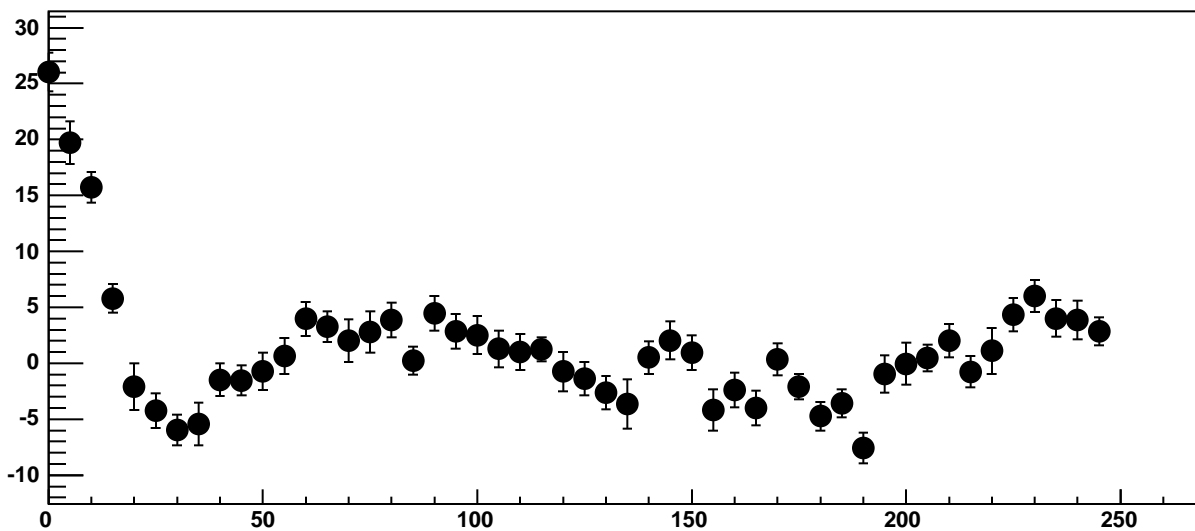


$\chi^2 / \text{ndf}$	216.3 / 41
p0	$380.9 \pm 2.827$
p1	$11.31 \pm 0.6015$
p2	$160.5 \pm 2.967$
p3	$30.82 \pm 0.6433$
p4	$-0.2006 \pm 0.008155$

Chip 1, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold

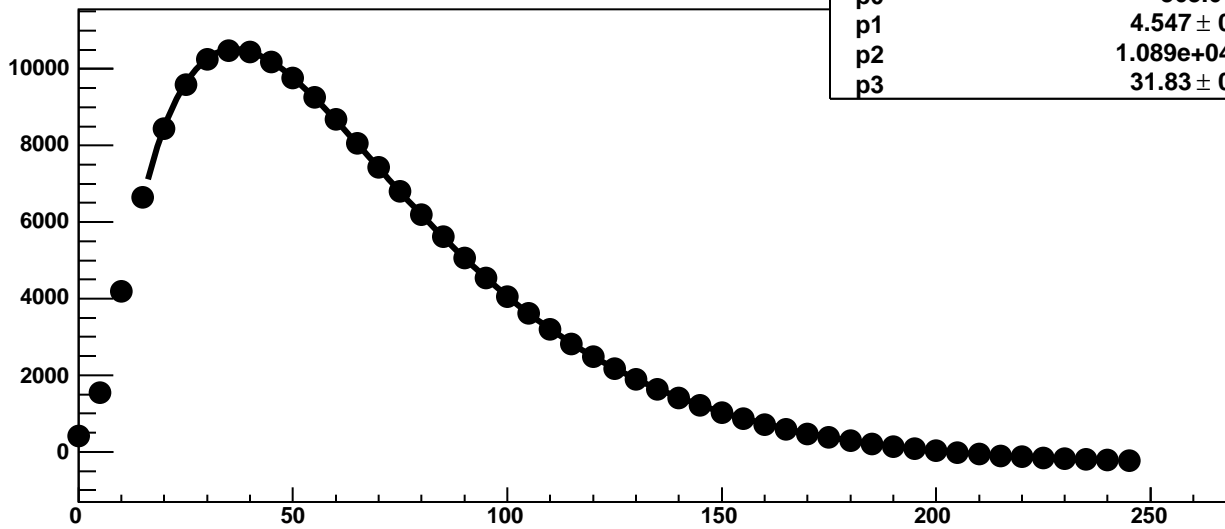


Chip 1, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold



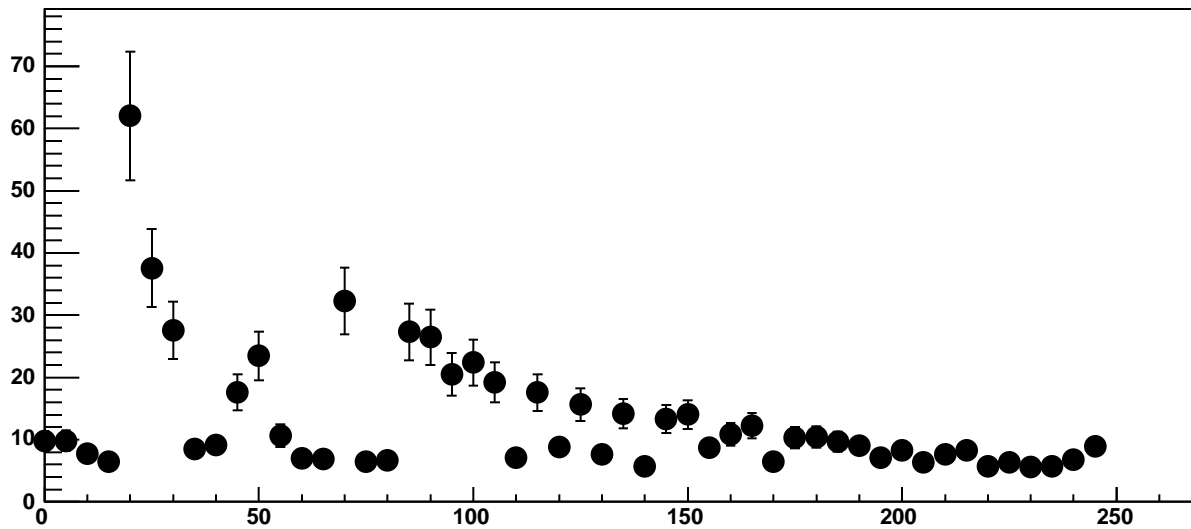


Chip 1, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold

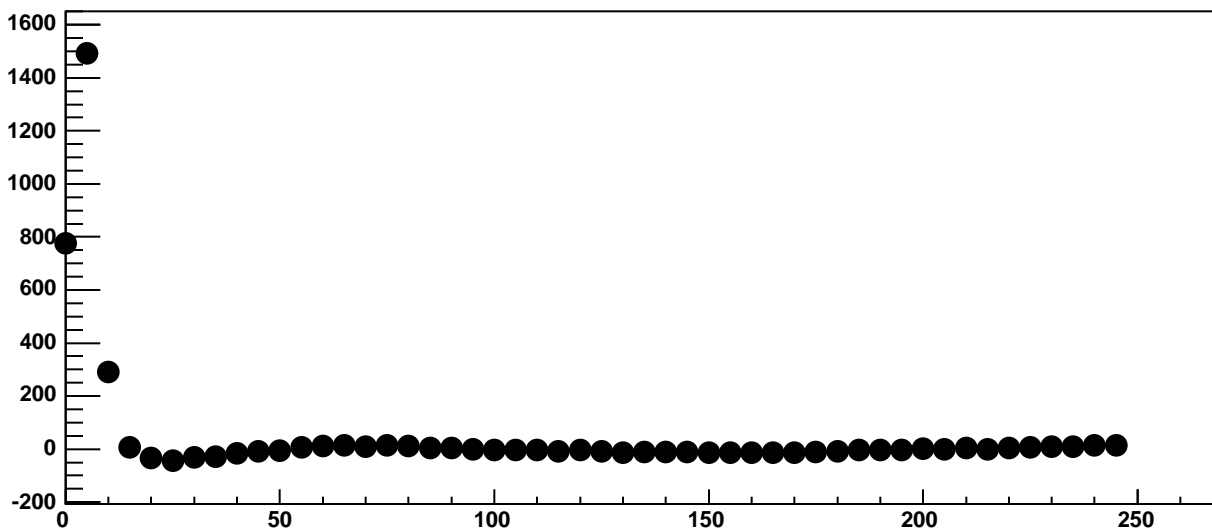


$\chi^2 / \text{ndf}$	1237 / 42
p0	-363.6 ± 0.5453
p1	4.547 ± 0.004745
p2	1.089e+04 ± 1.003
p3	31.83 ± 0.004969

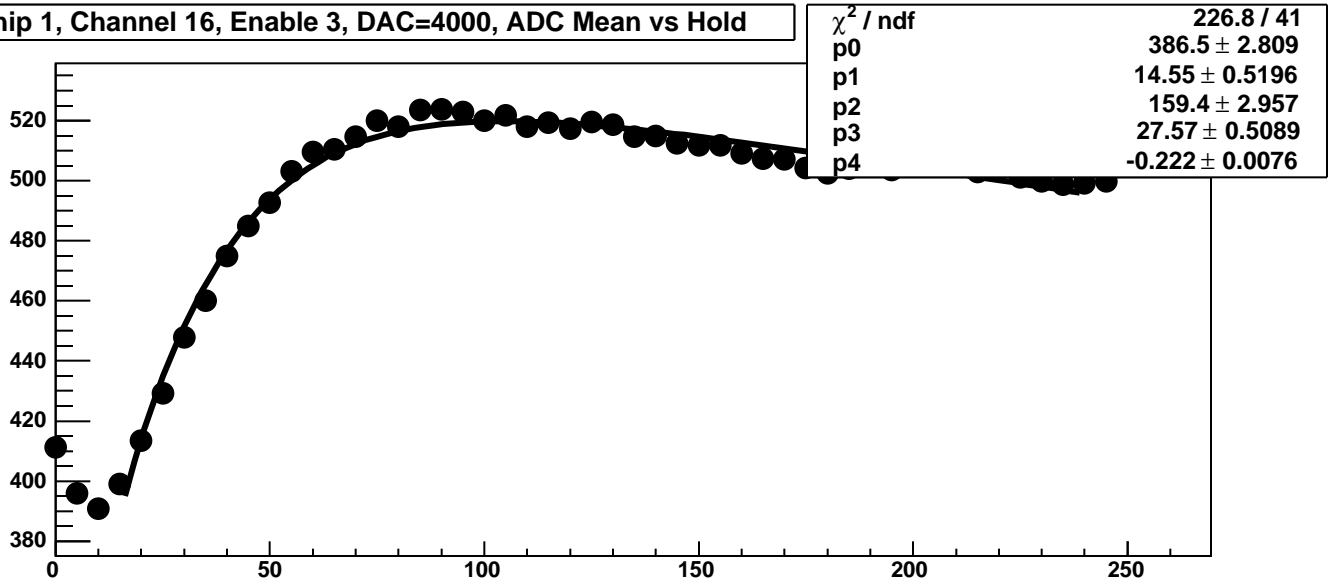
Chip 1, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold



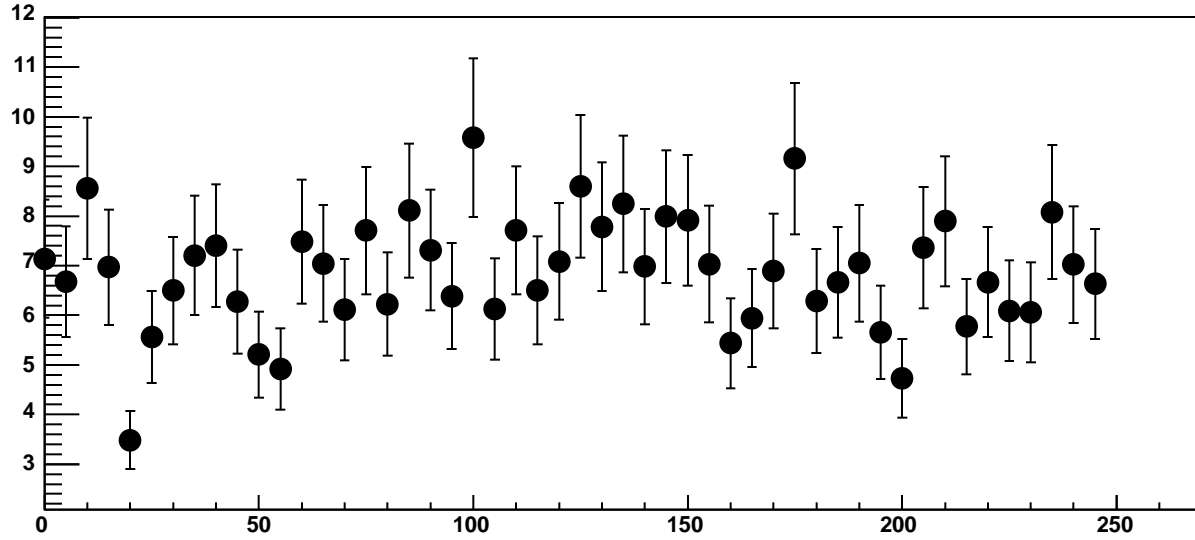
Chip 1, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold



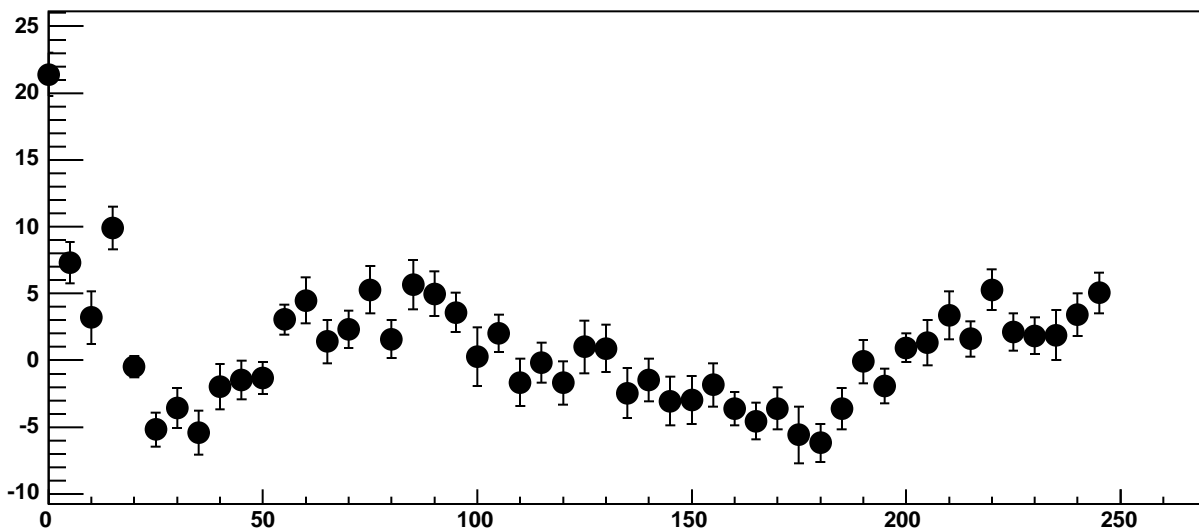
Chip 1, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold



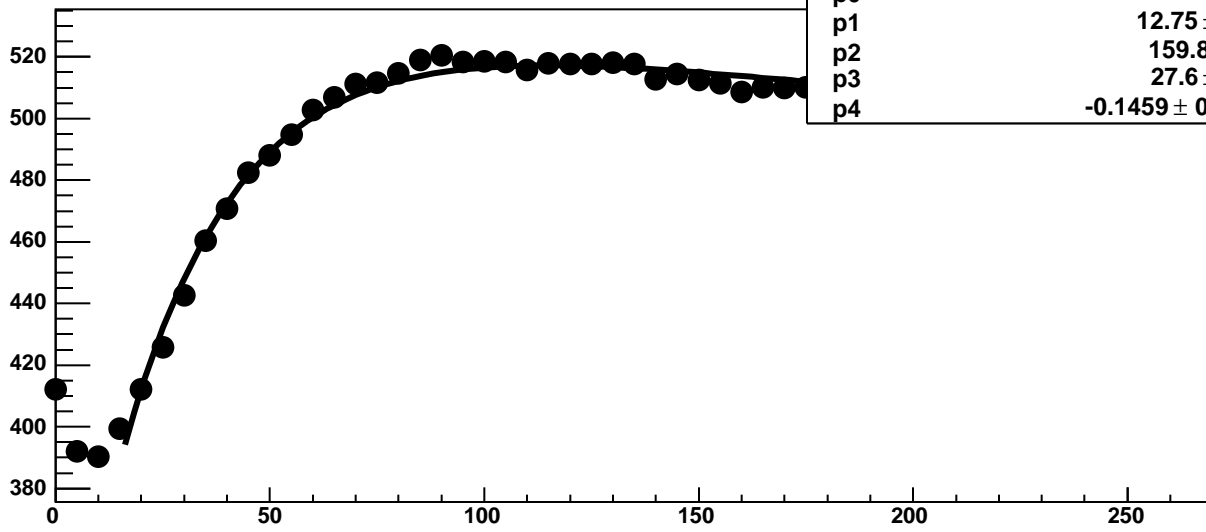
Chip 1, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

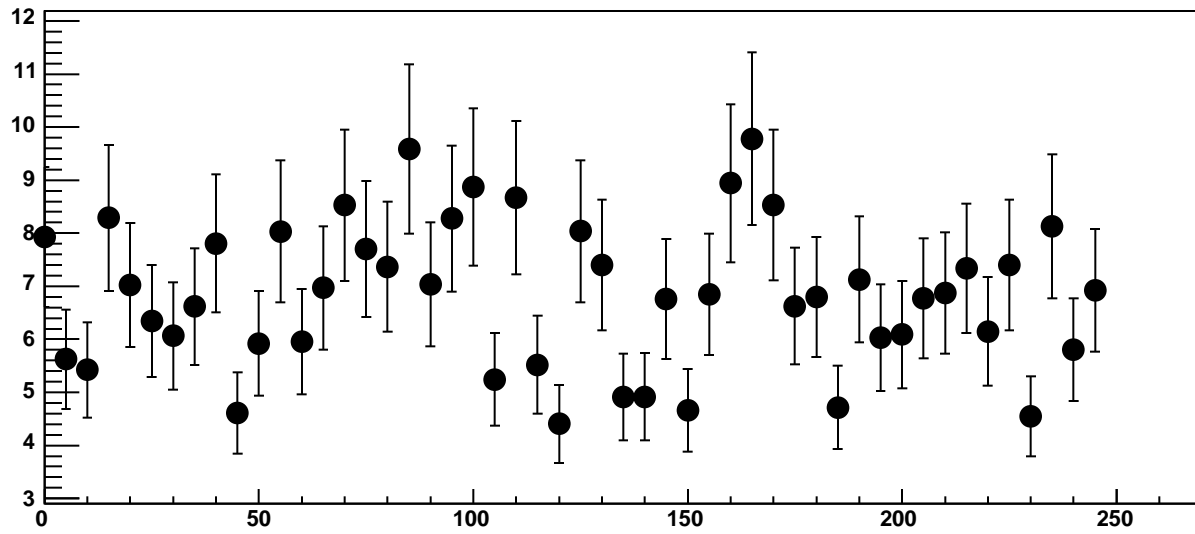


Chip 1, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

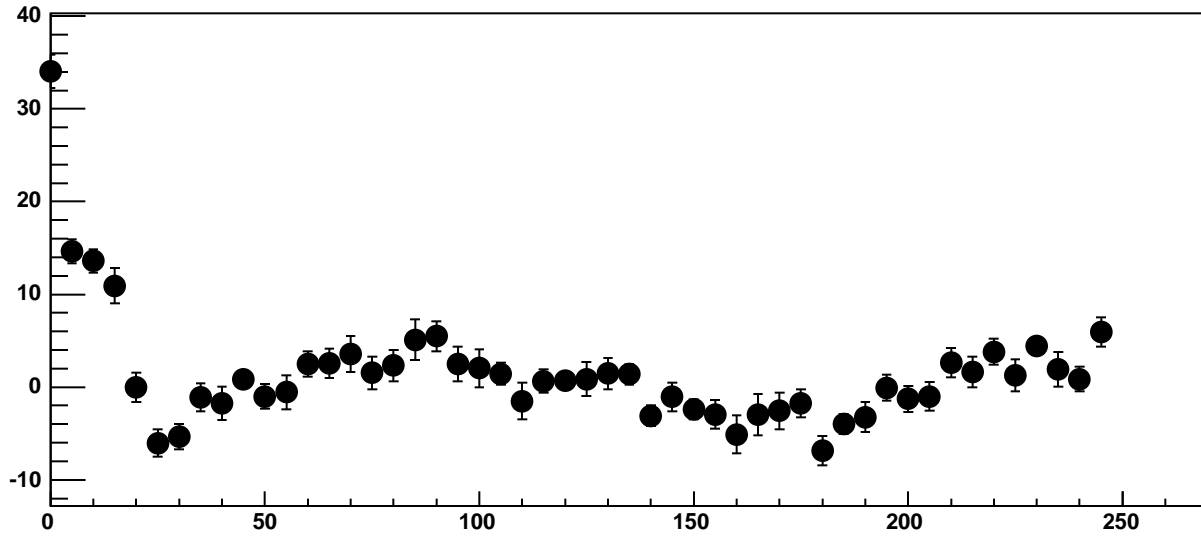


$\chi^2 / \text{ndf}$	200.5 / 41
p0	$376.2 \pm 2.768$
p1	$12.75 \pm 0.5388$
p2	$159.8 \pm 2.858$
p3	$27.6 \pm 0.5458$
p4	$-0.1459 \pm 0.007053$

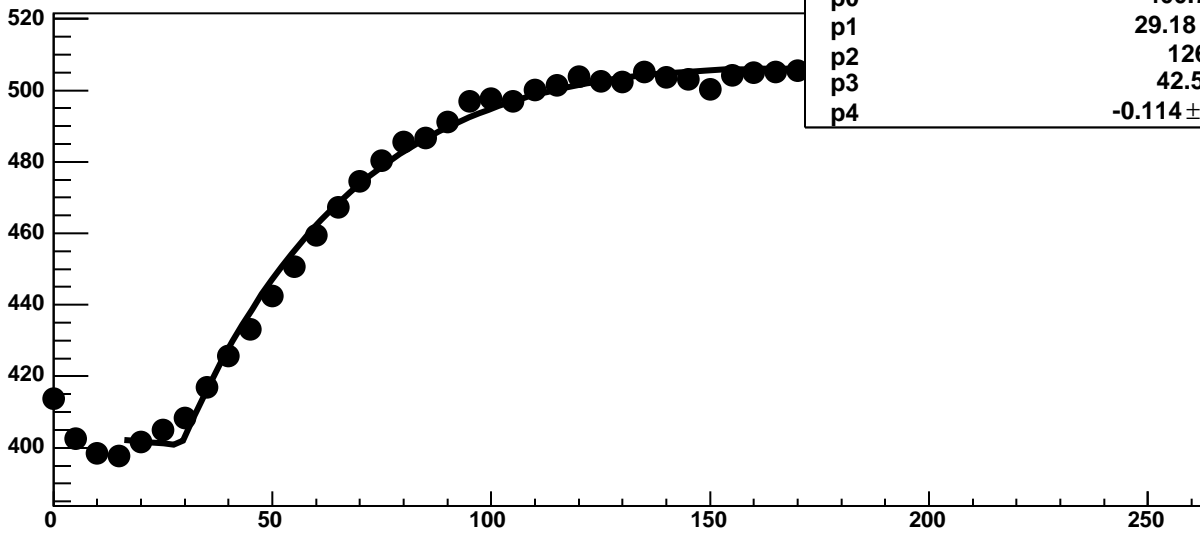
Chip 1, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



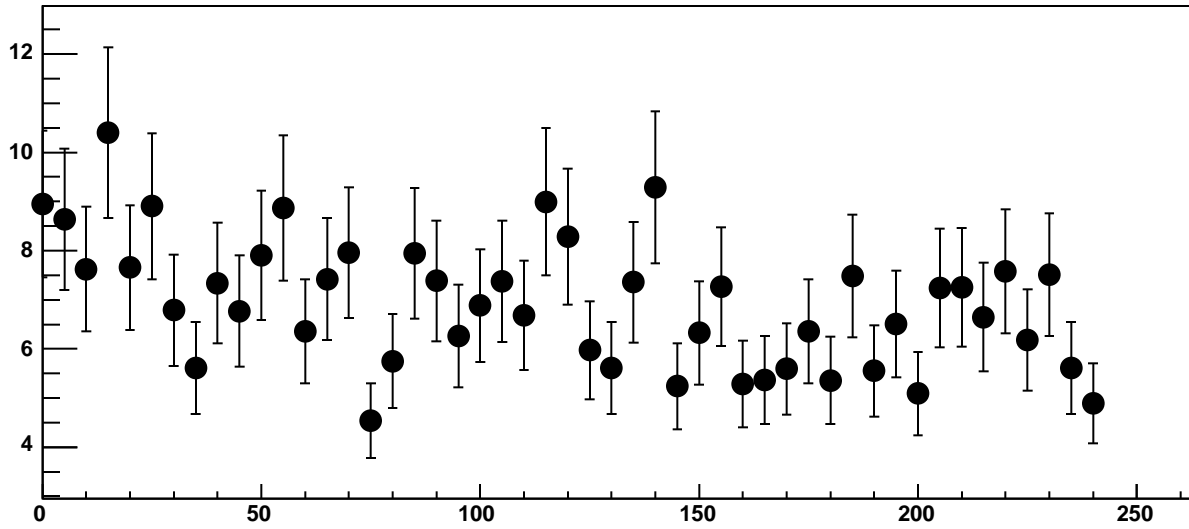
Chip 1, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold



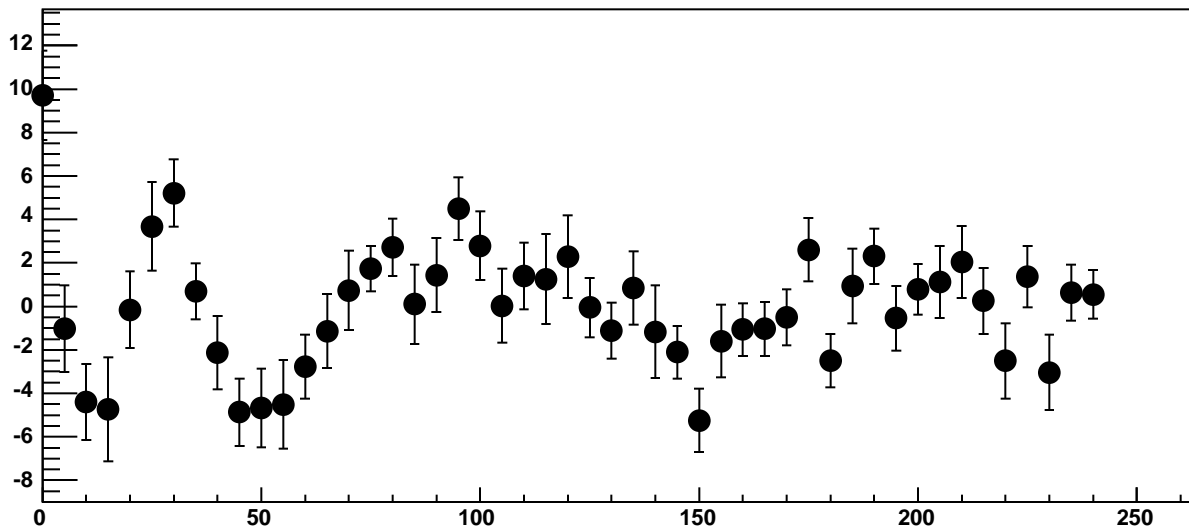
Chip 1, Channel 16, Enable 5, DAC=4000, ADC Mean vs Hold



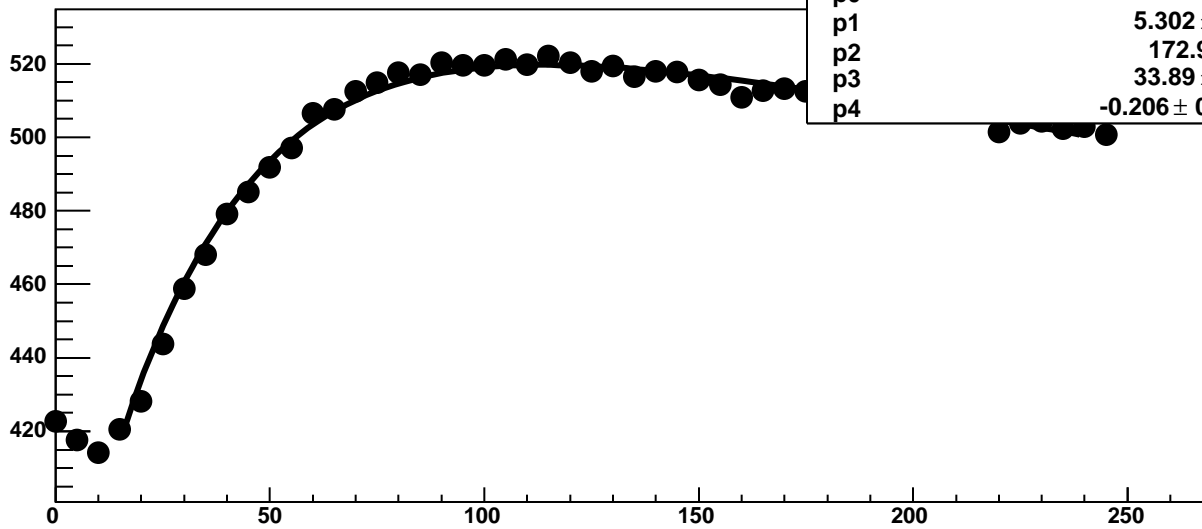
Chip 1, Channel 16, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 16, Enable 5, DAC=4000, ADC Residuals vs Hold

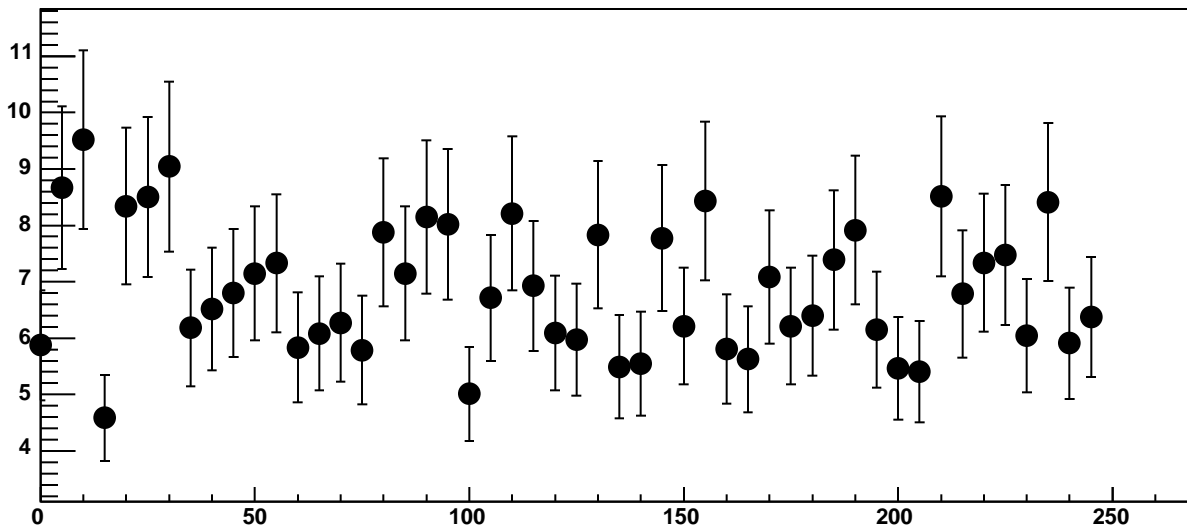


Chip 1, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

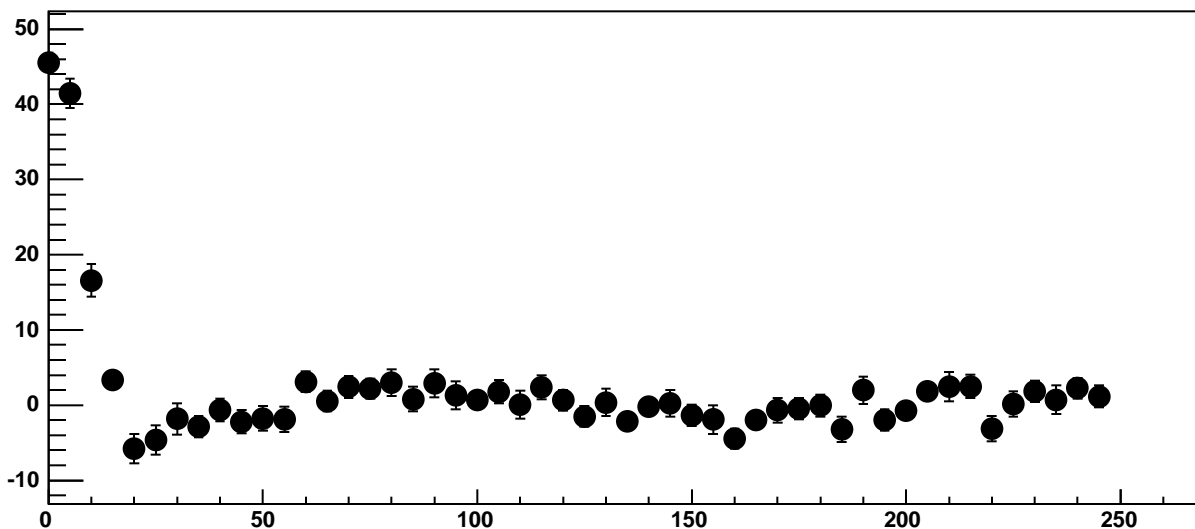


$\chi^2 / \text{ndf}$	96.98 / 41
p0	$376.2 \pm 2.825$
p1	$5.302 \pm 0.6345$
p2	$172.9 \pm 2.973$
p3	$33.89 \pm 0.7876$
p4	$-0.206 \pm 0.008787$

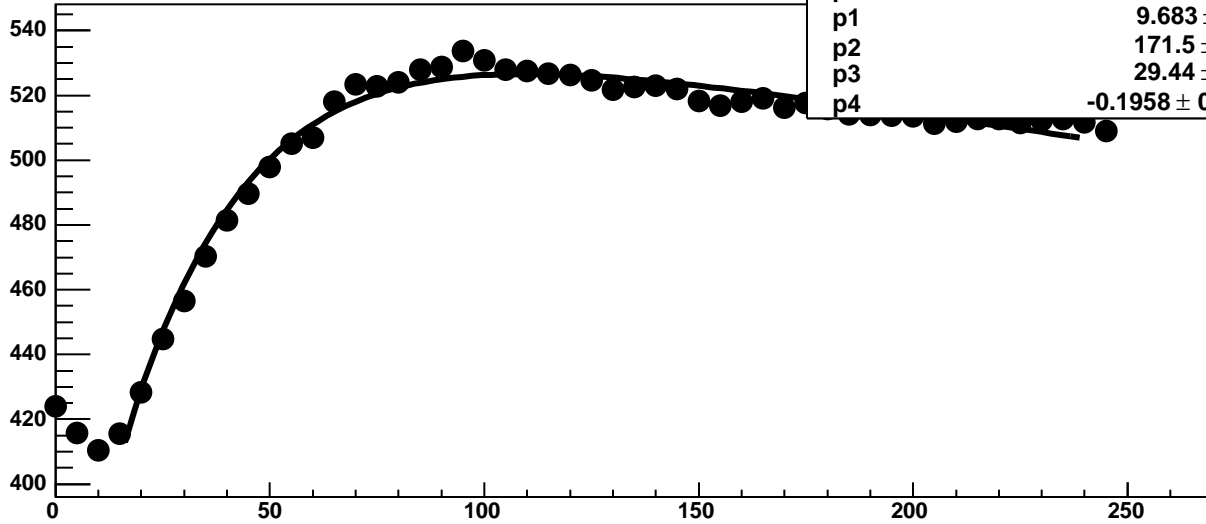
Chip 1, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

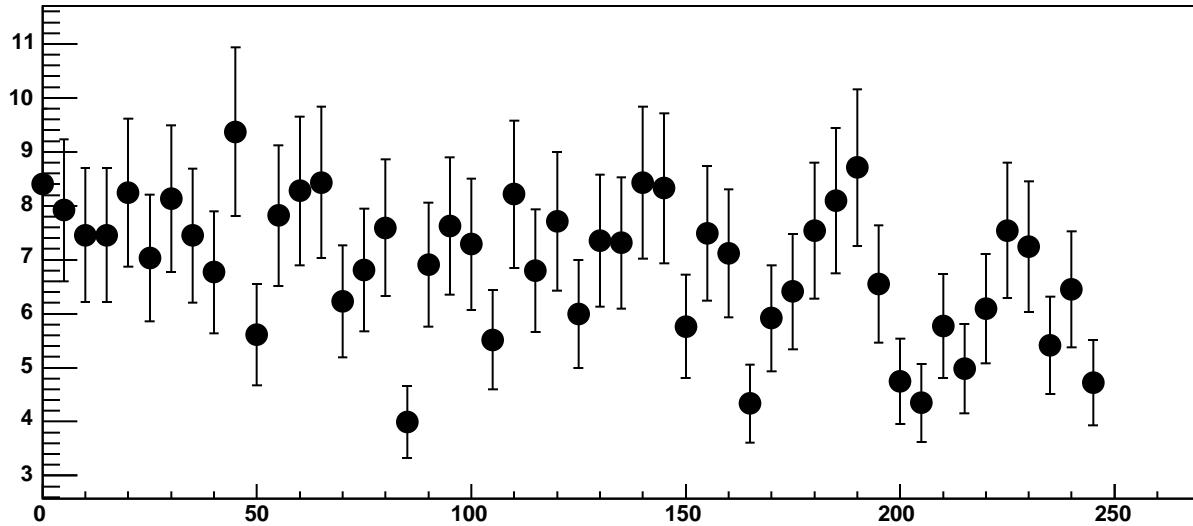


Chip 1, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

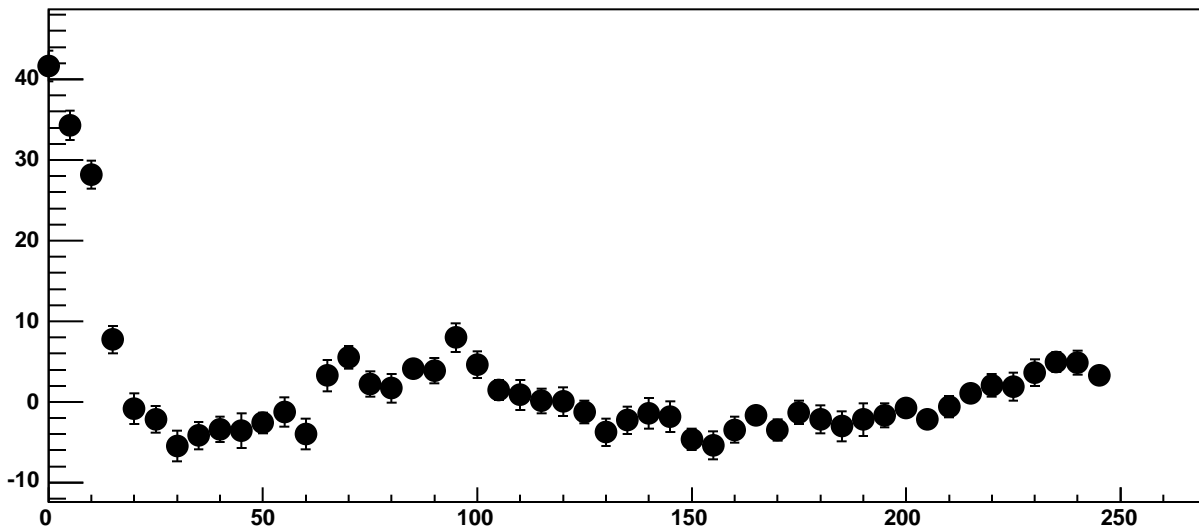


$\chi^2 / \text{ndf}$	224.3 / 41
p0	$380.5 \pm 0.7152$
p1	$9.683 \pm 0.2917$
p2	$171.5 \pm 0.8135$
p3	$29.44 \pm 0.6775$
p4	$-0.1958 \pm 0.008142$

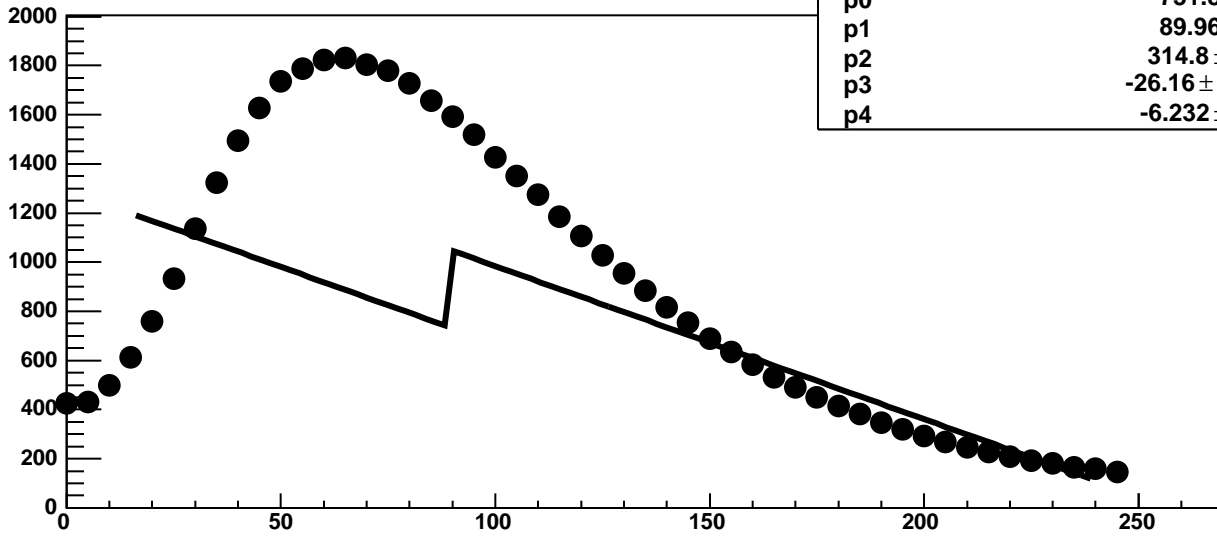
Chip 1, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

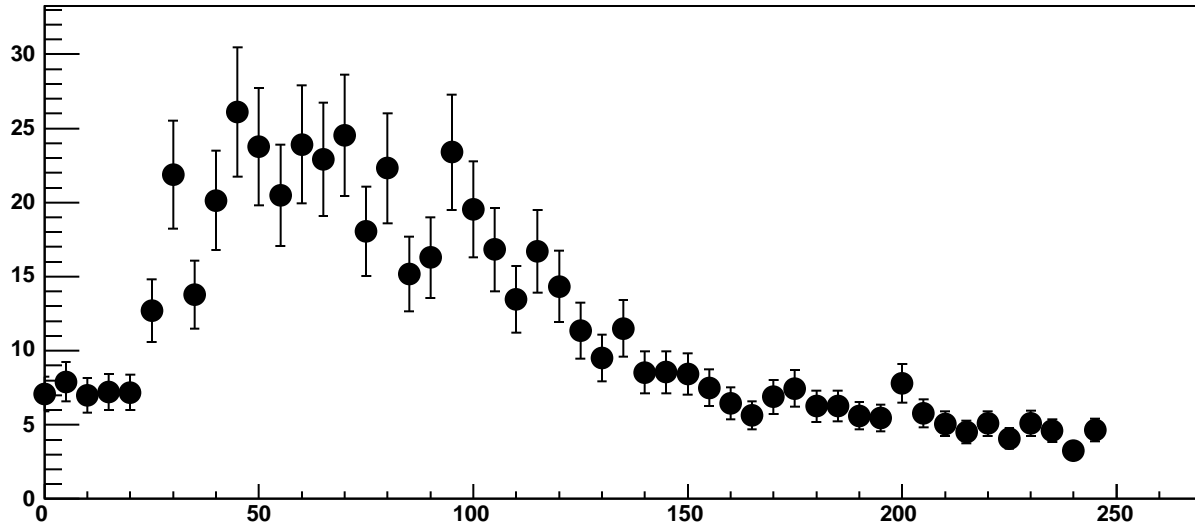


Chip 1, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold

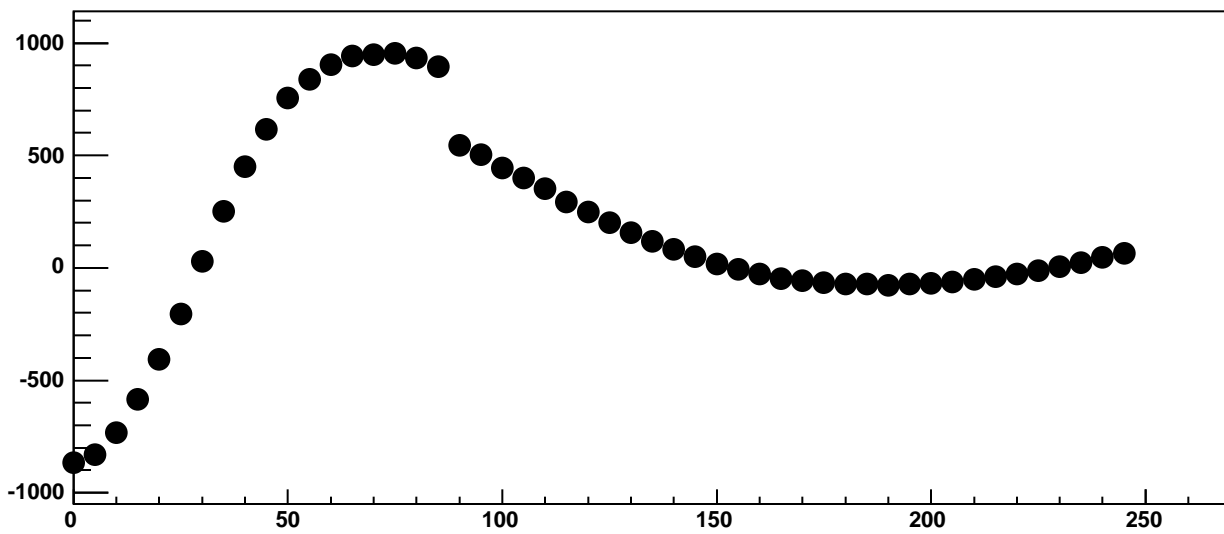


$\chi^2 / \text{ndf}$	6.276e+05 / 41
p0	731.8 ± 1.743
p1	89.96 ± 1.812
p2	314.8 ± 0.9997
p3	-26.16 ± 0.02275
p4	-6.232 ± 0.1869

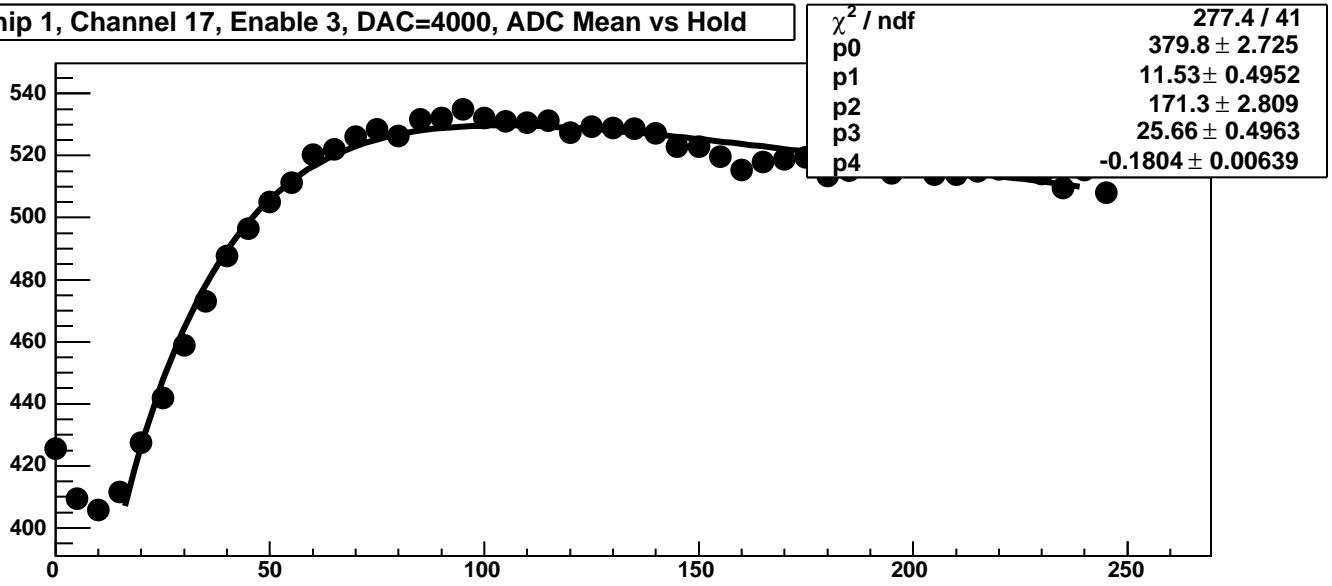
Chip 1, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold



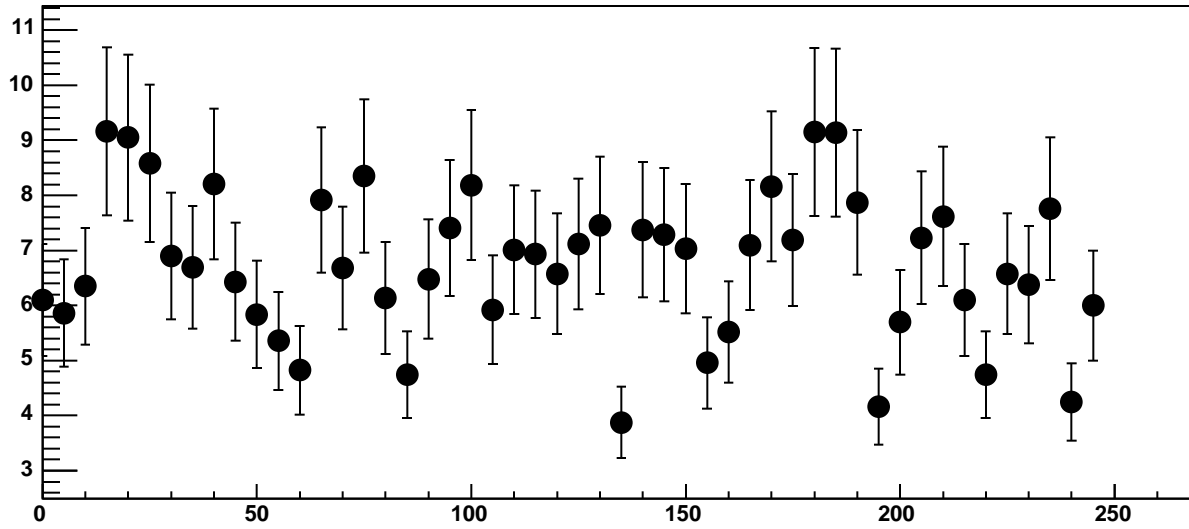
Chip 1, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold



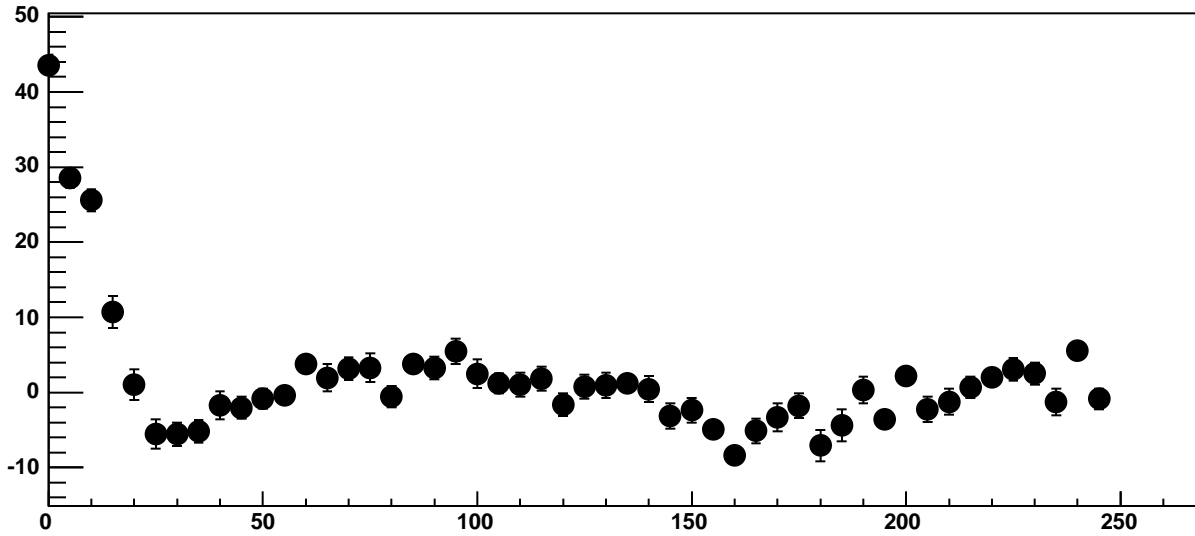
Chip 1, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



Chip 1, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold

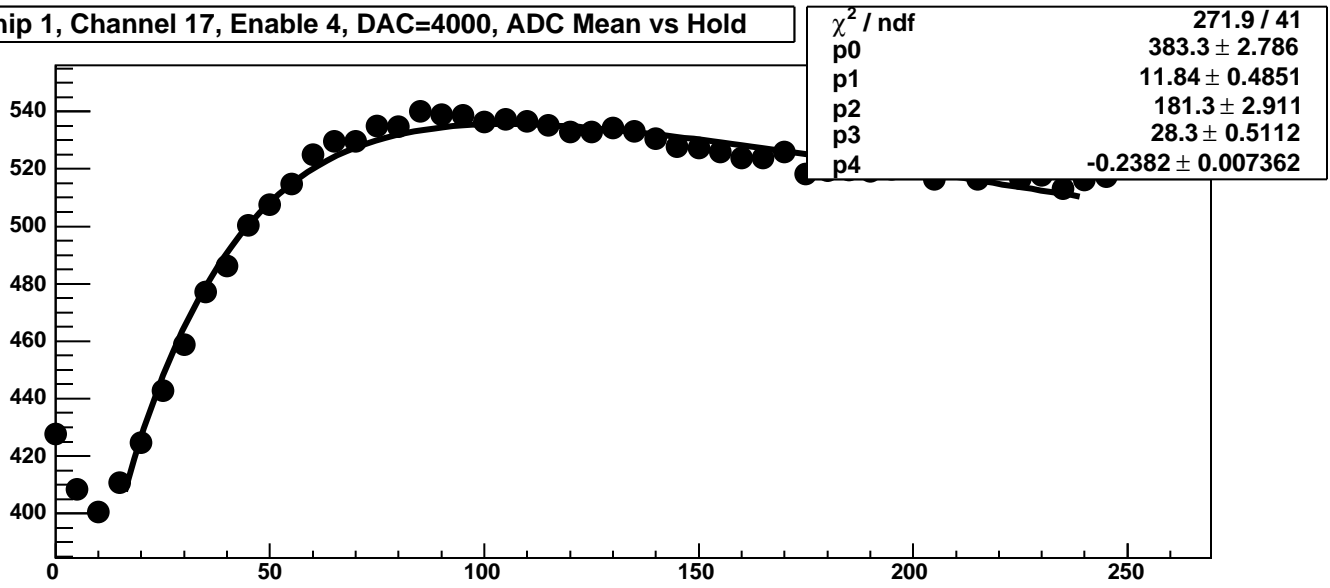


Chip 1, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold

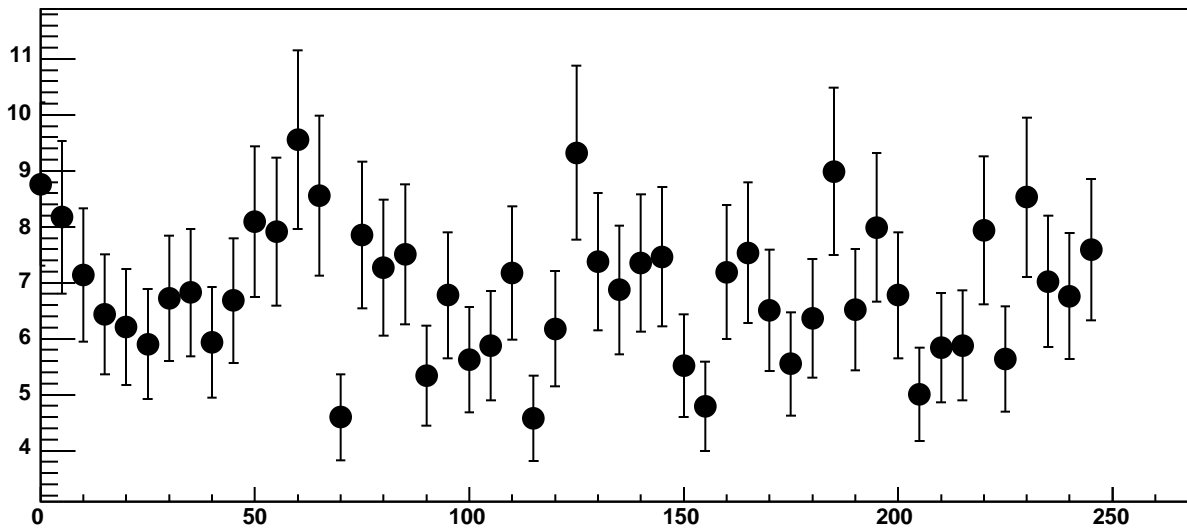




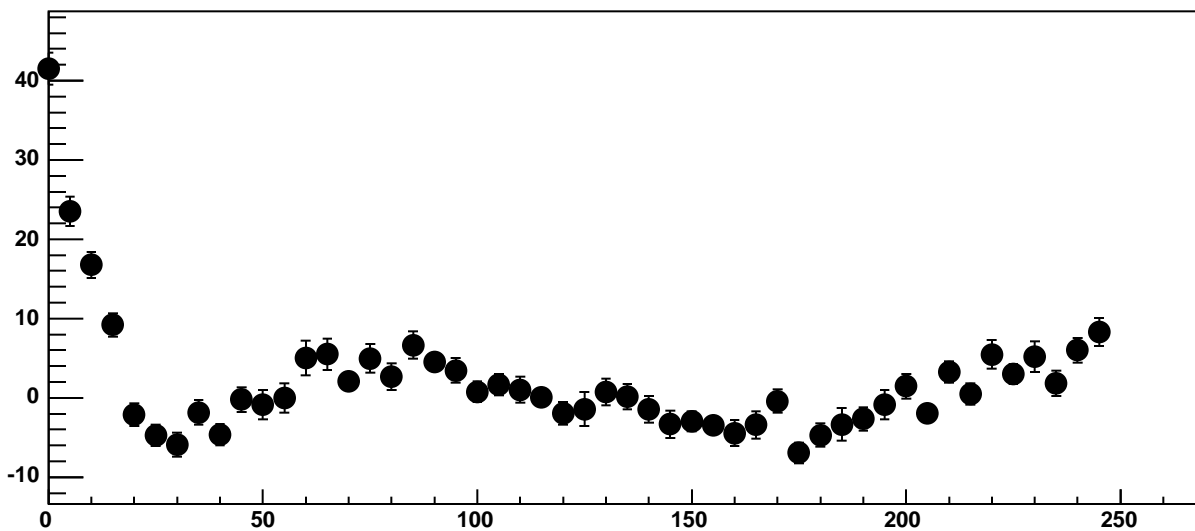
Chip 1, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



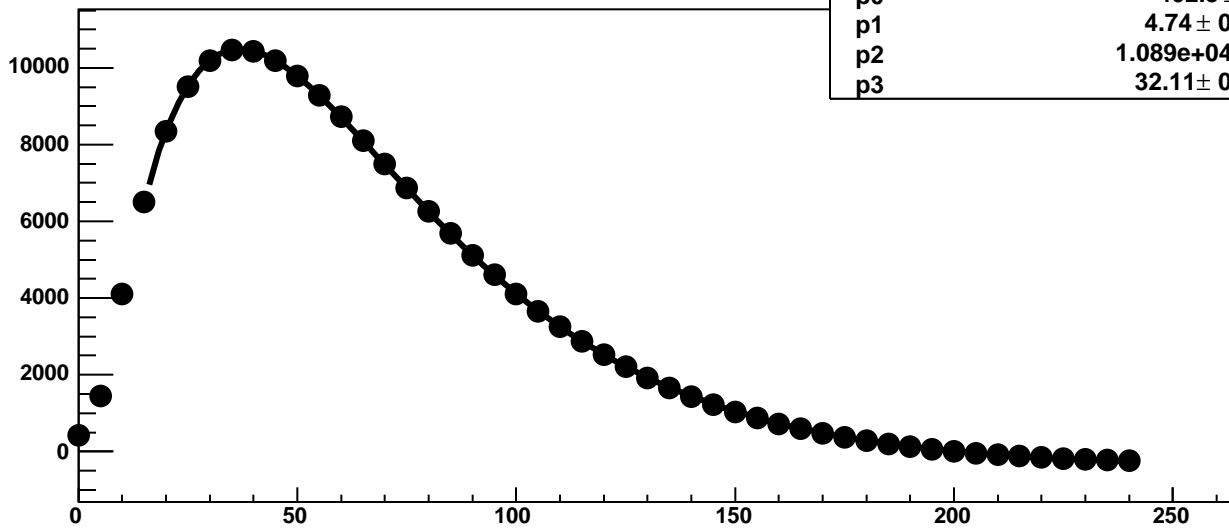
Chip 1, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

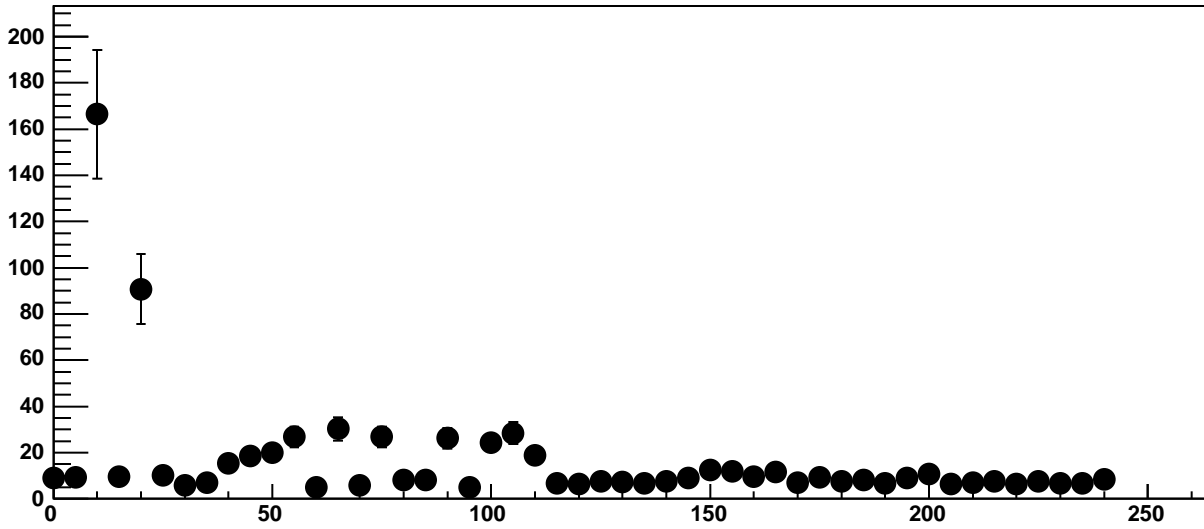


Chip 1, Channel 17, Enable 5!, DAC=4000, ADC Mean vs Hold

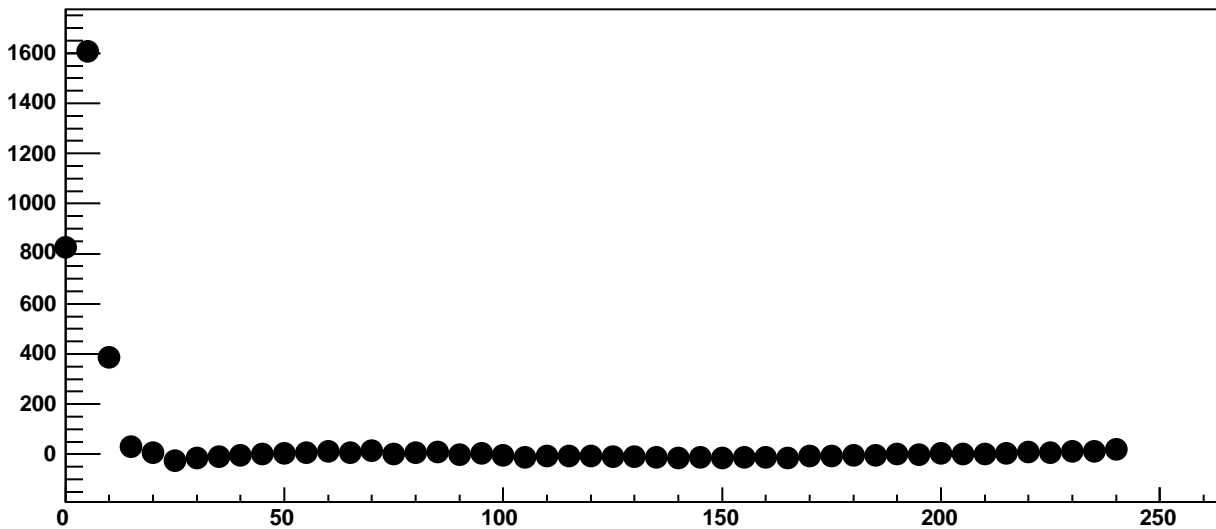


$\chi^2 / \text{ndf}$	1408 / 42
p0	-402.5 ± 0.5726
p1	4.74 ± 0.005825
p2	1.089e+04 ± 0.852
p3	32.11 ± 0.004509

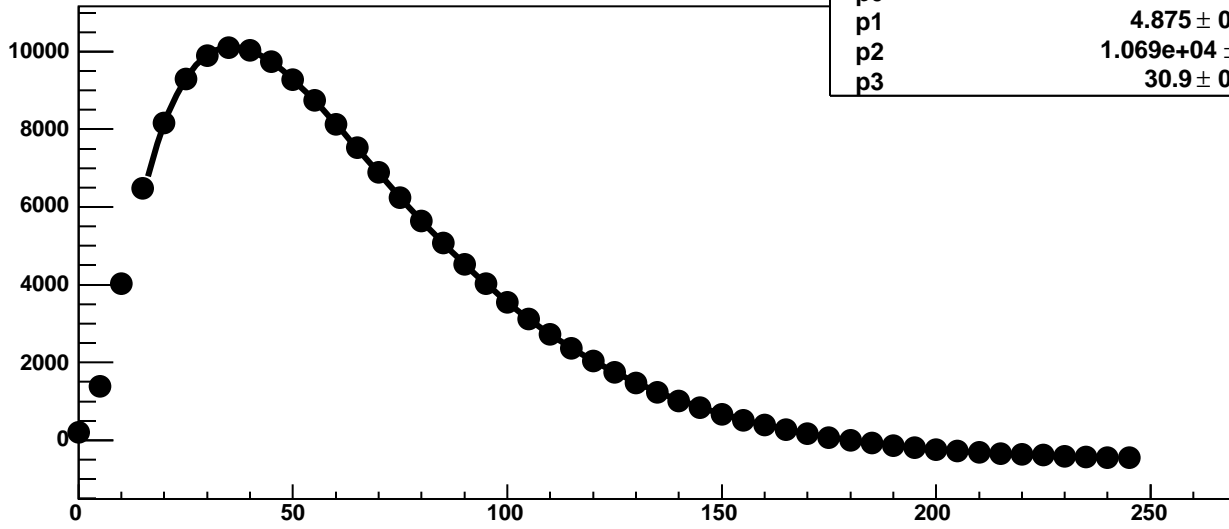
Chip 1, Channel 17, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 17, Enable 5!, DAC=4000, ADC Residuals vs Hold

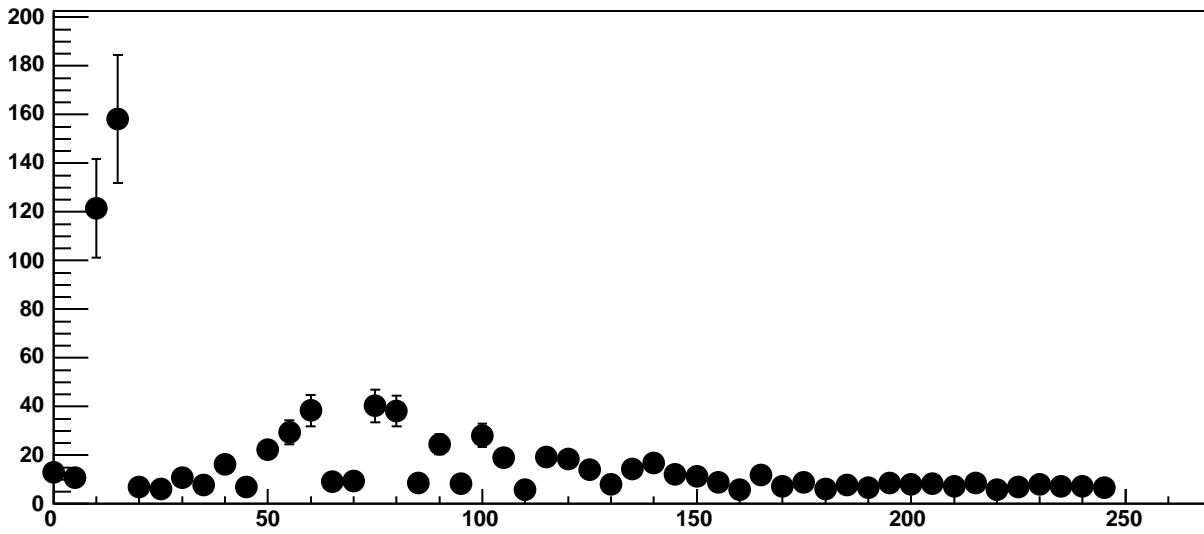


Chip 2, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold

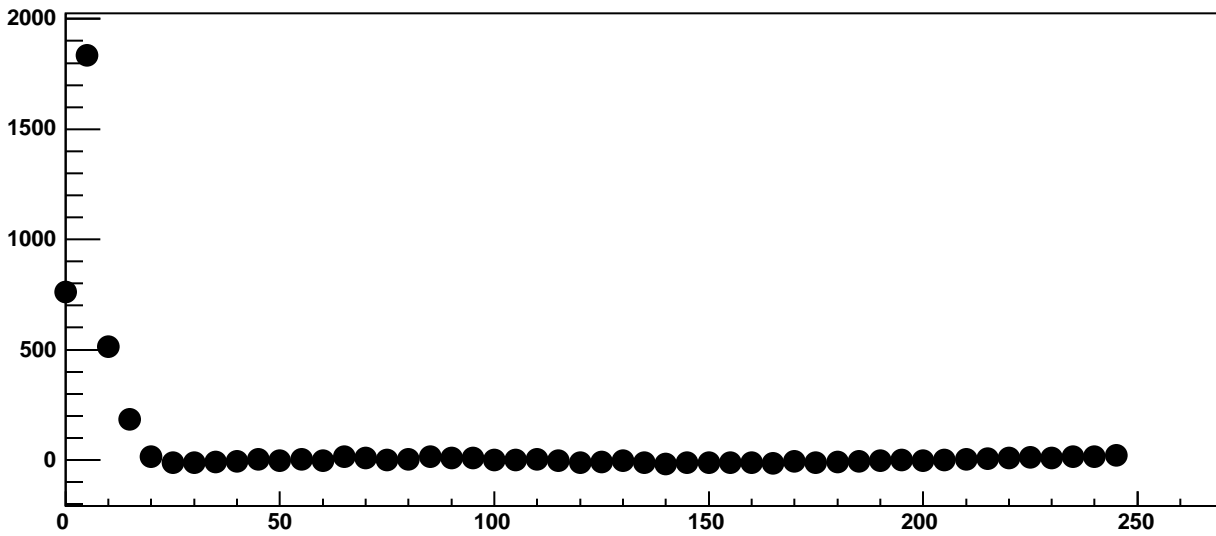


$\chi^2 / \text{ndf}$	1055 / 42
p0	-567.2 ± 0.573
p1	4.875 ± 0.007594
p2	1.069e+04 ± 0.9236
p3	30.9 ± 0.005665

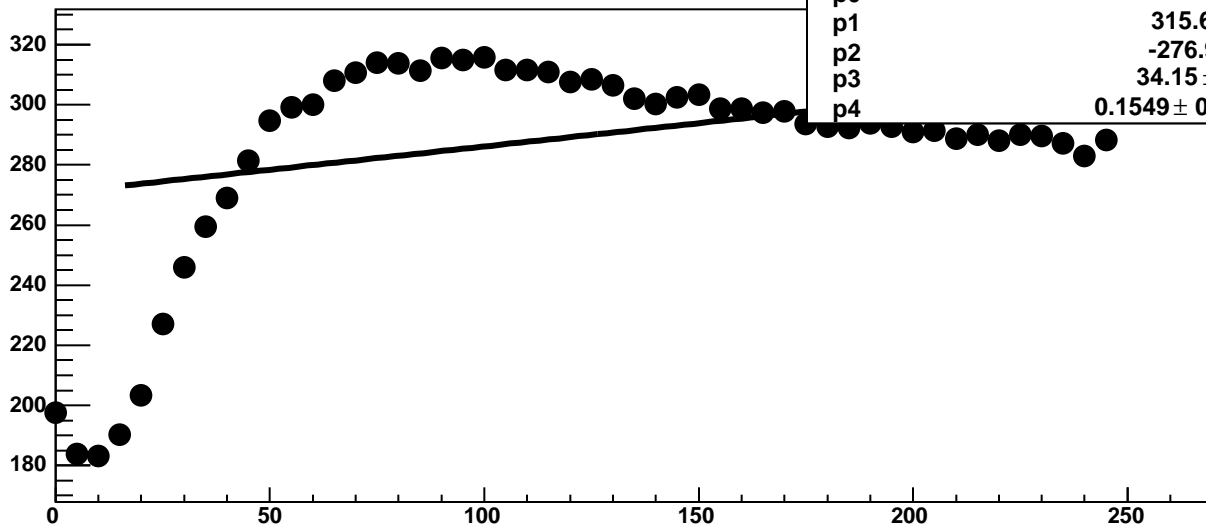
Chip 2, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold

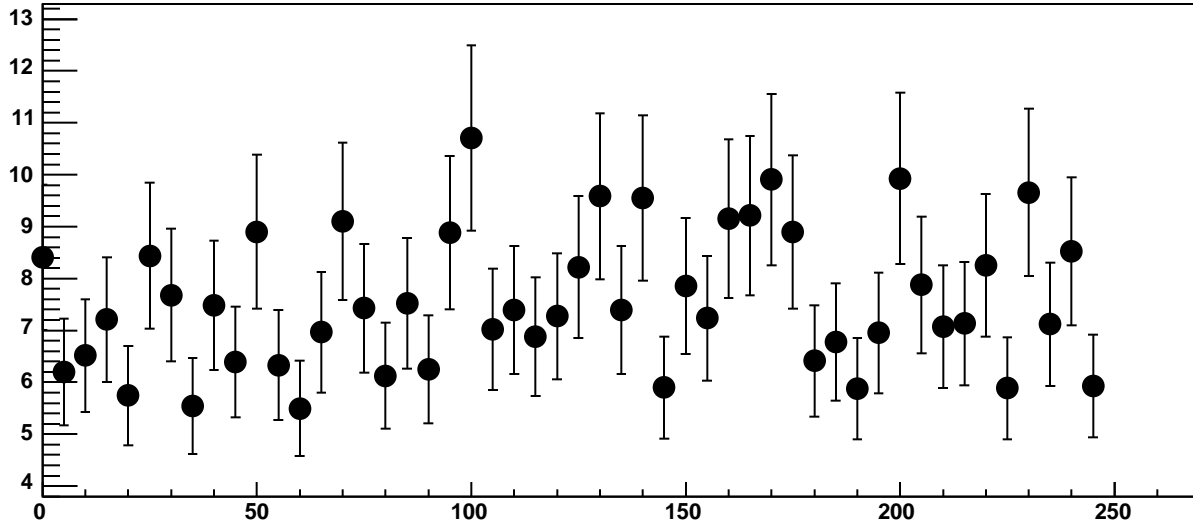


Chip 2, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

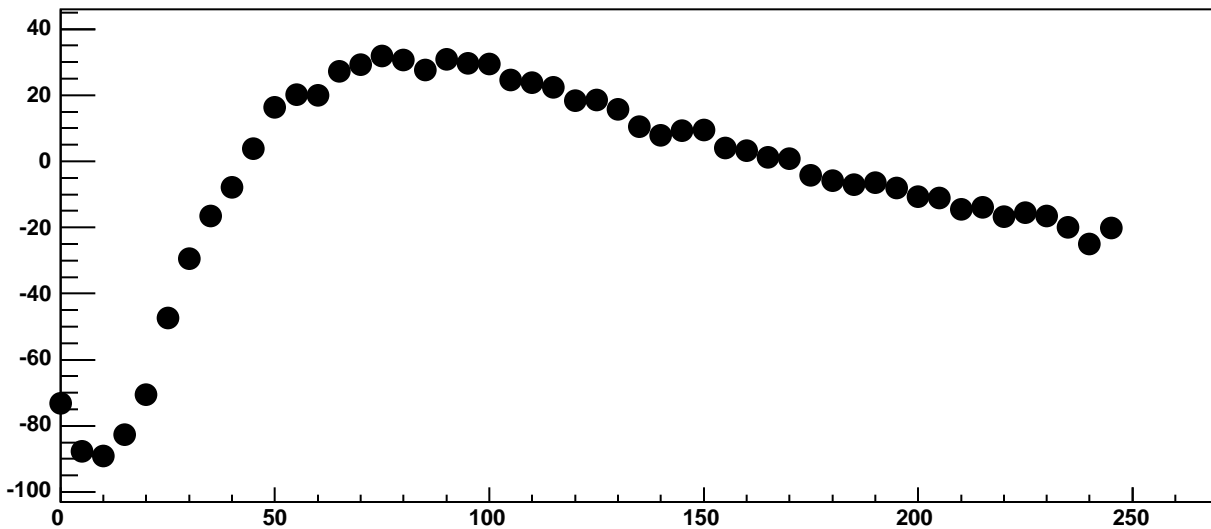


$\chi^2 / \text{ndf}$	1.126e+04 / 41
p0	319.6 ± 0.4857
p1	315.6 ± 1.961
p2	-276.9 ± 2.87
p3	34.15 ± 0.3608
p4	0.1549 ± 0.003639

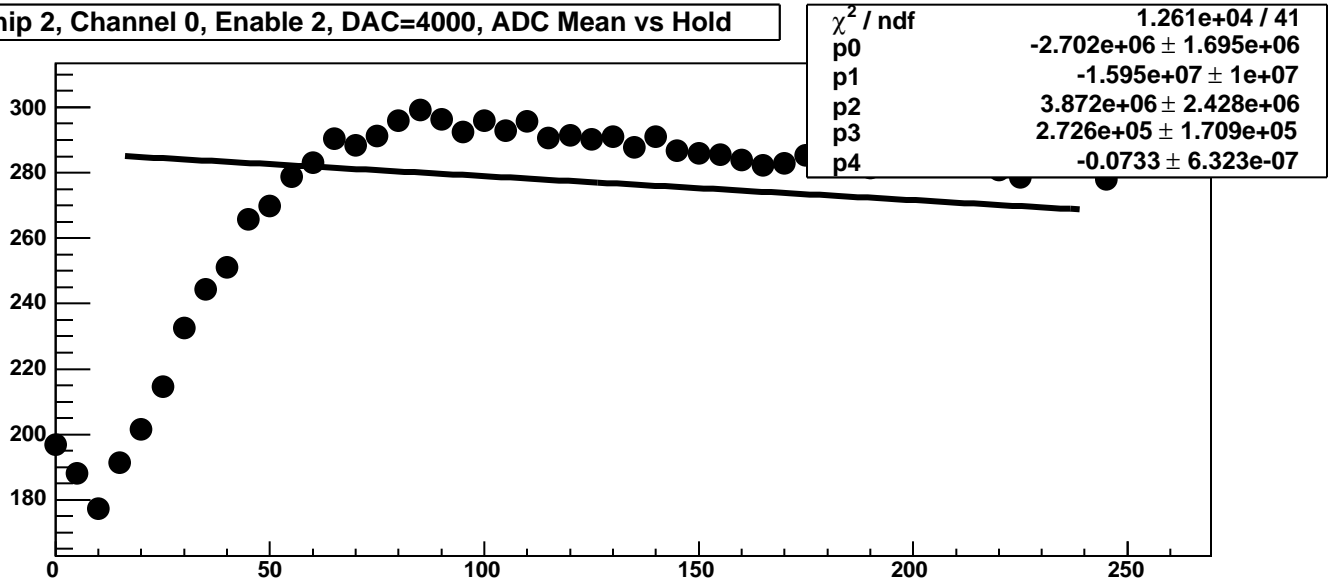
Chip 2, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



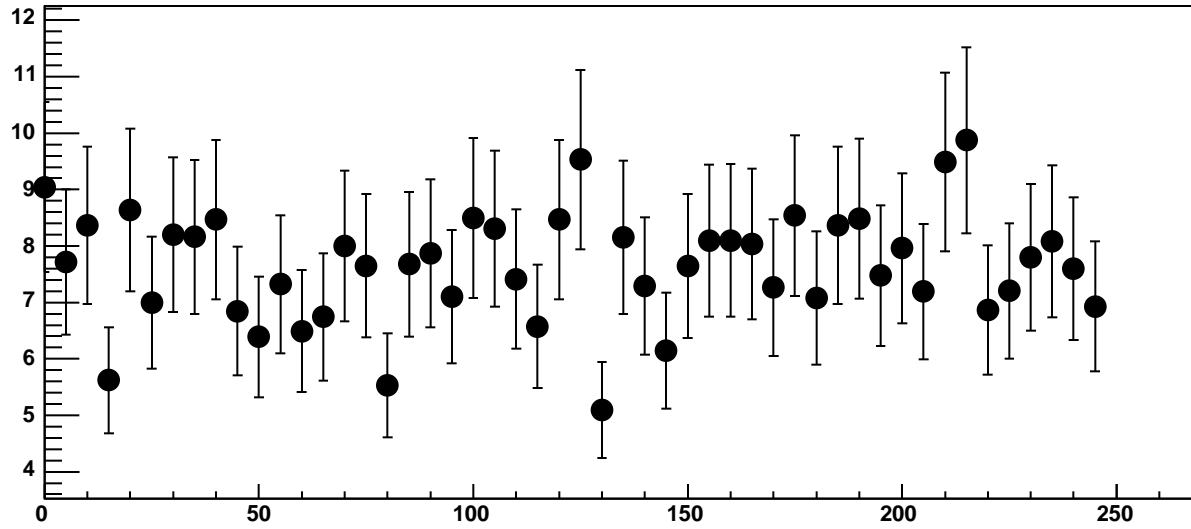
Chip 2, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold



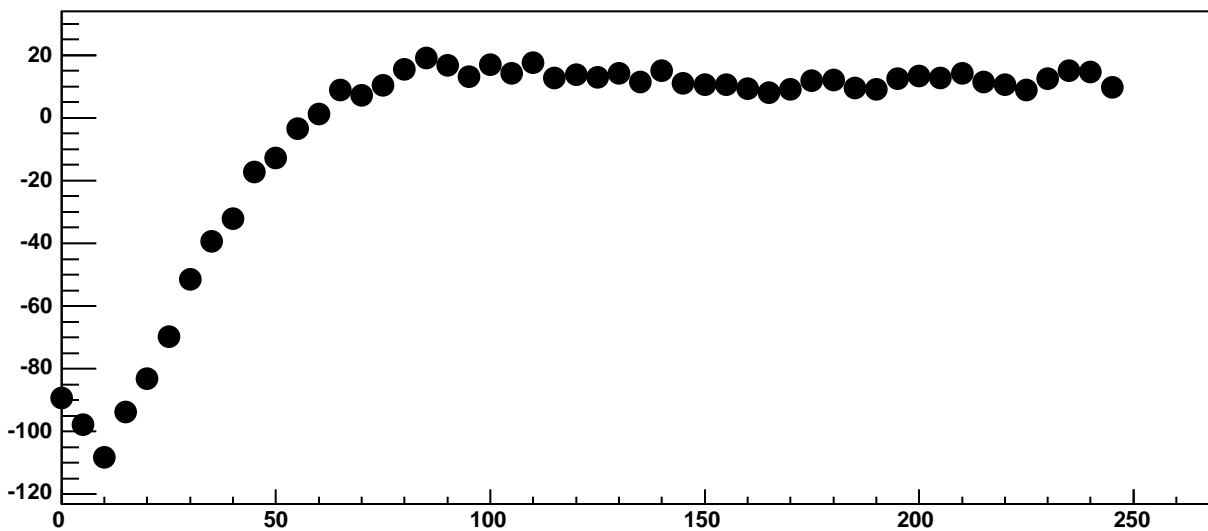
Chip 2, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold



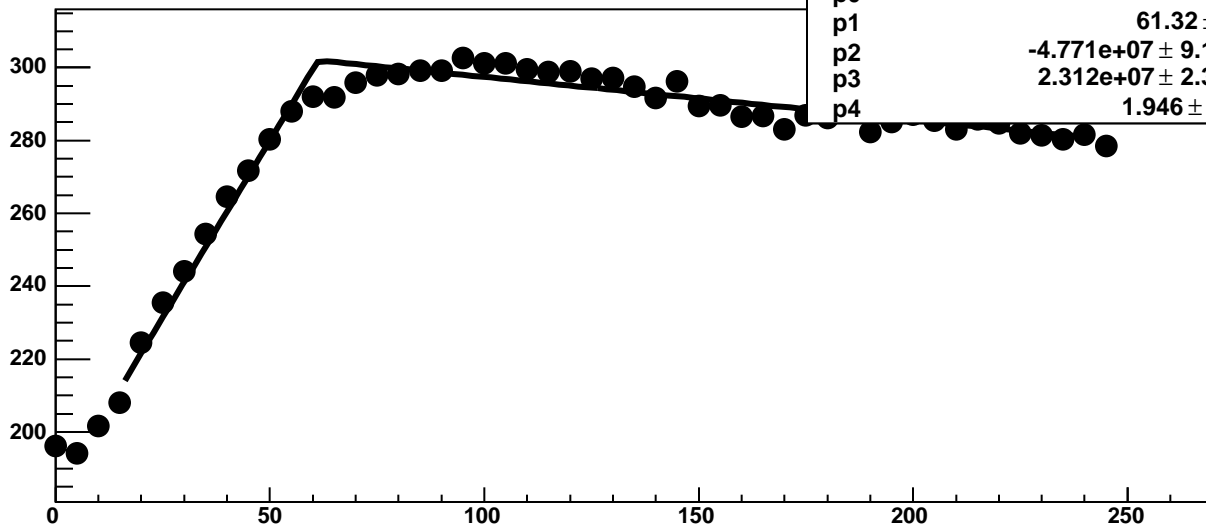
Chip 2, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



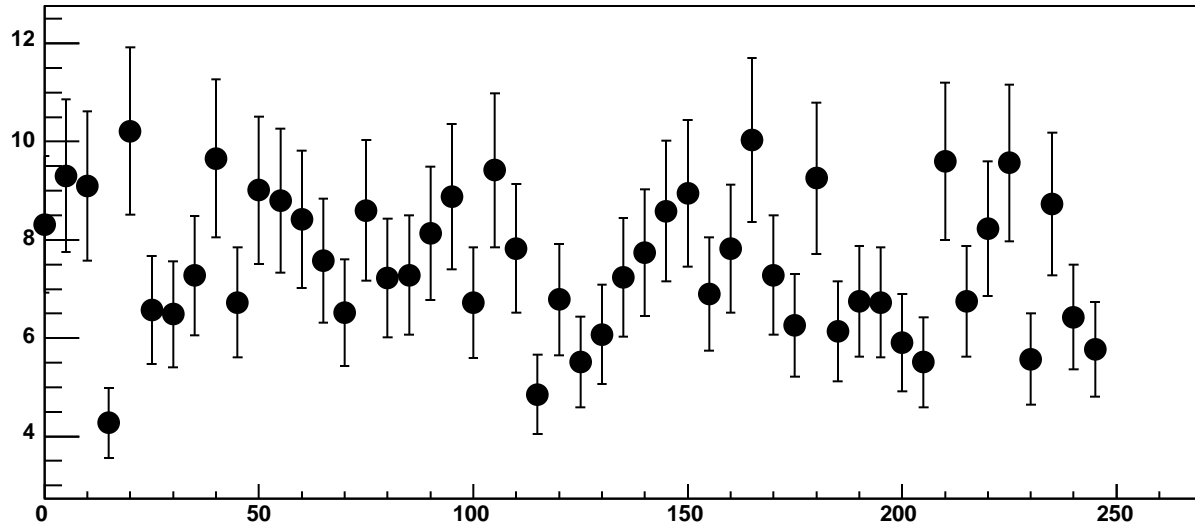
Chip 2, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold



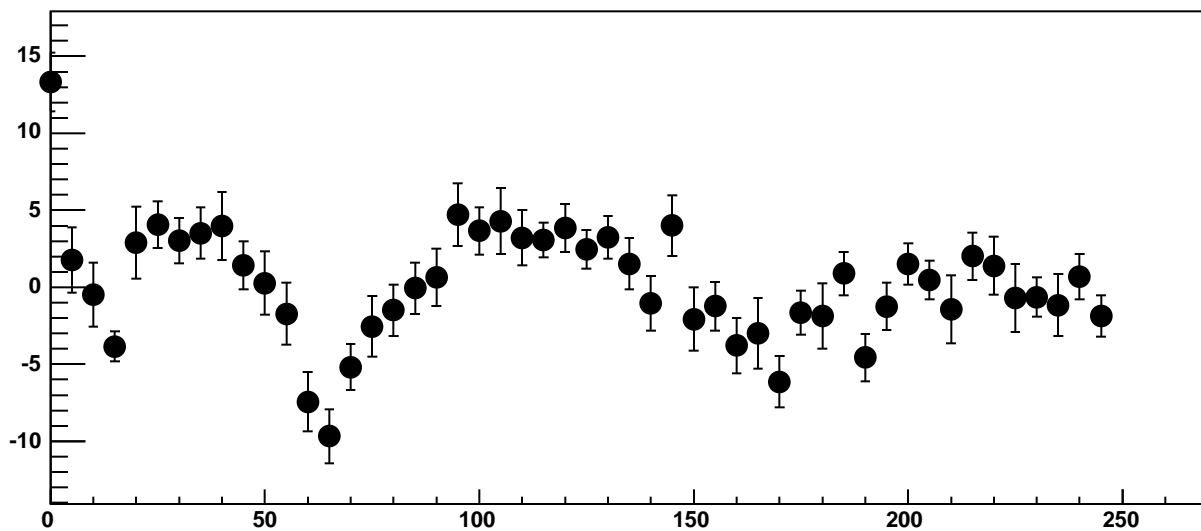
Chip 2, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold



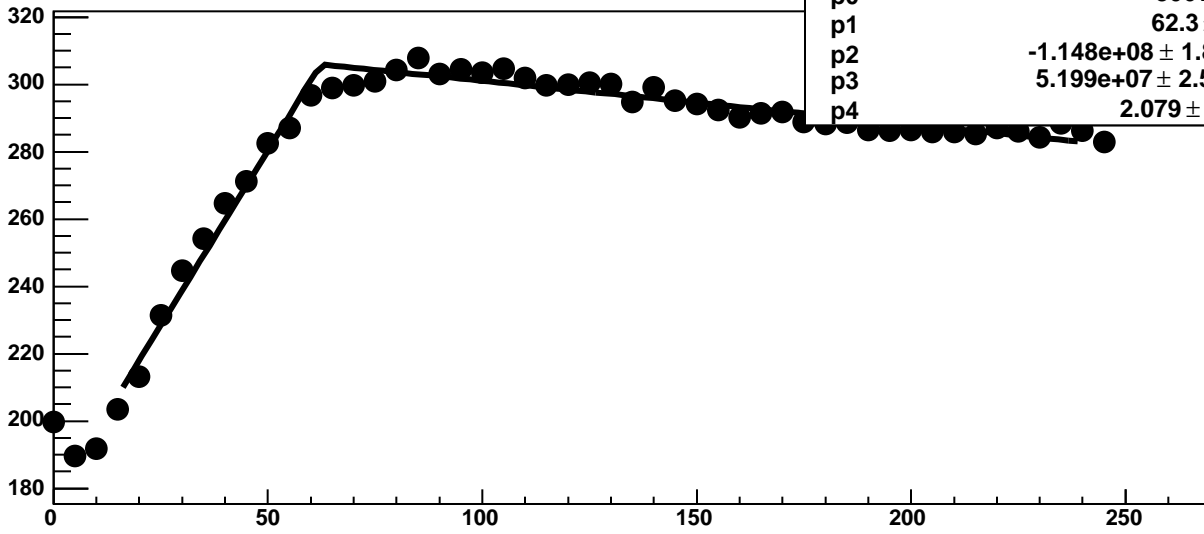
Chip 2, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold

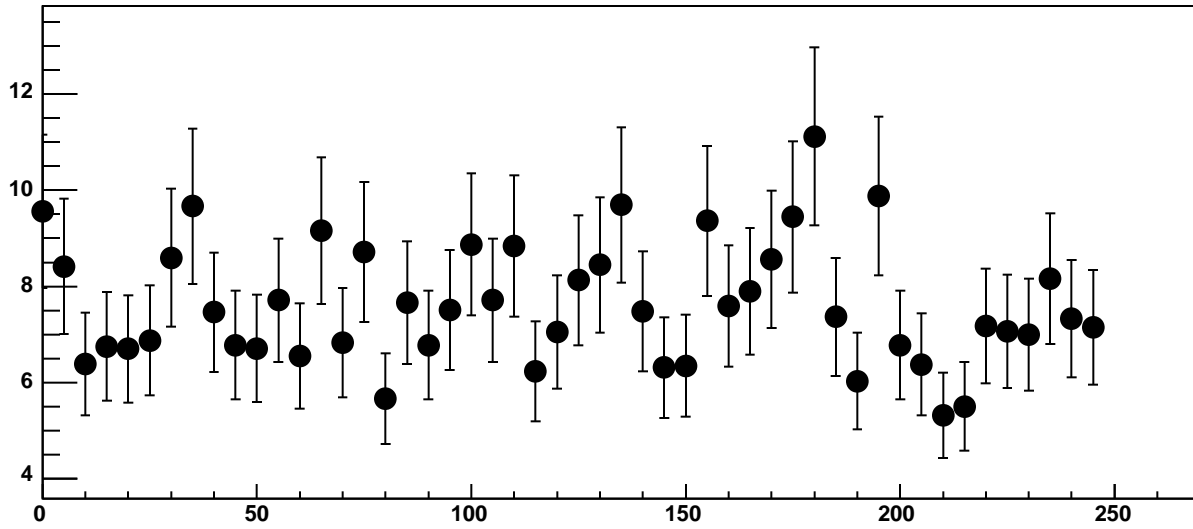


Chip 2, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

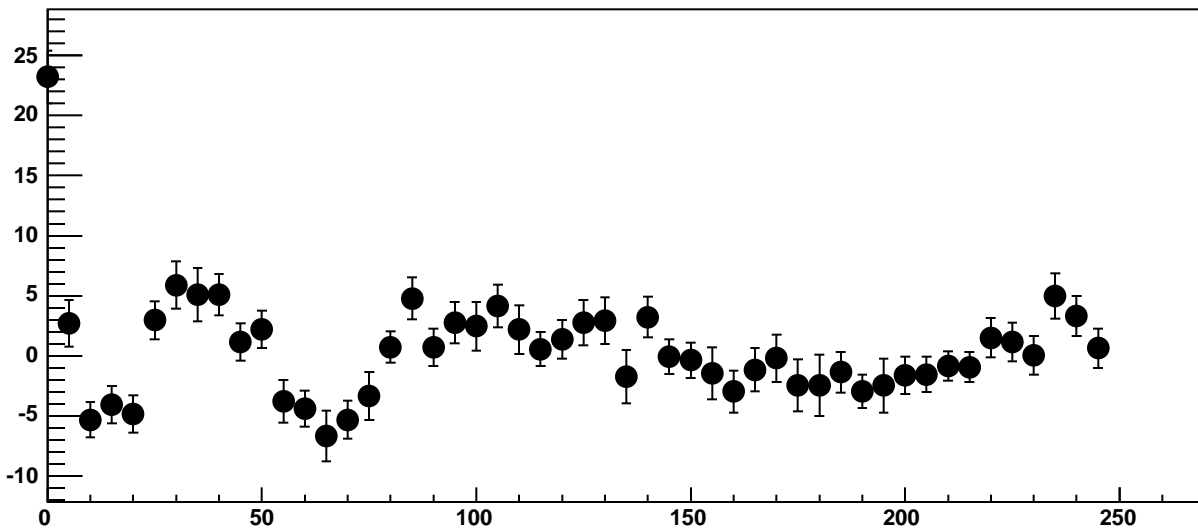


$\chi^2 / \text{ndf}$	139.6 / 41
p0	$306 \pm 0.5403$
p1	$62.3 \pm 0.5281$
p2	$-1.148\text{e}+08 \pm 1.886\text{e}+06$
p3	$5.199\text{e}+07 \pm 2.538\text{e}+05$
p4	$2.079 \pm 0.03468$

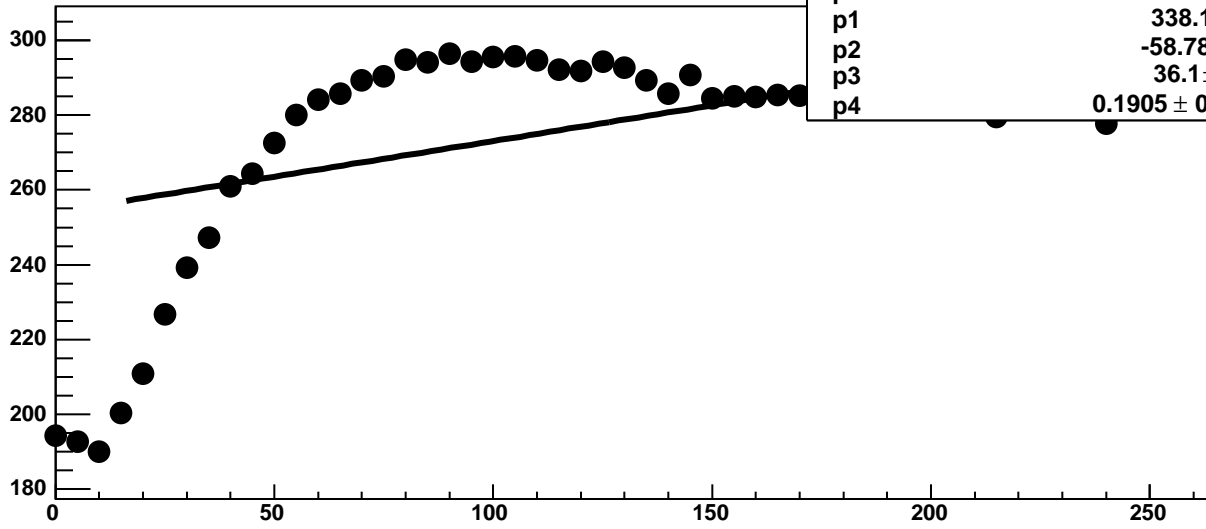
Chip 2, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

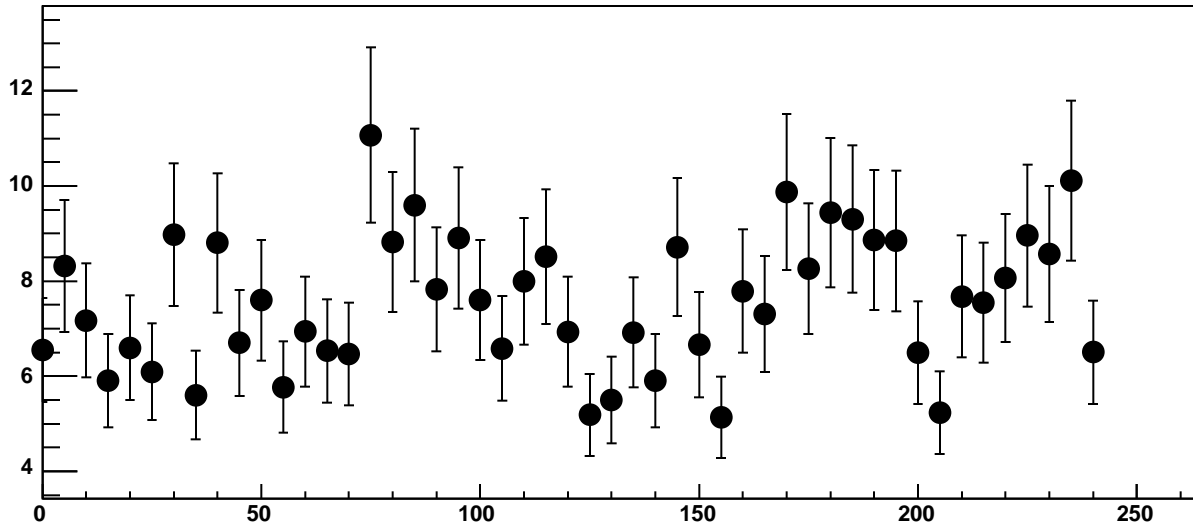


Chip 2, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold

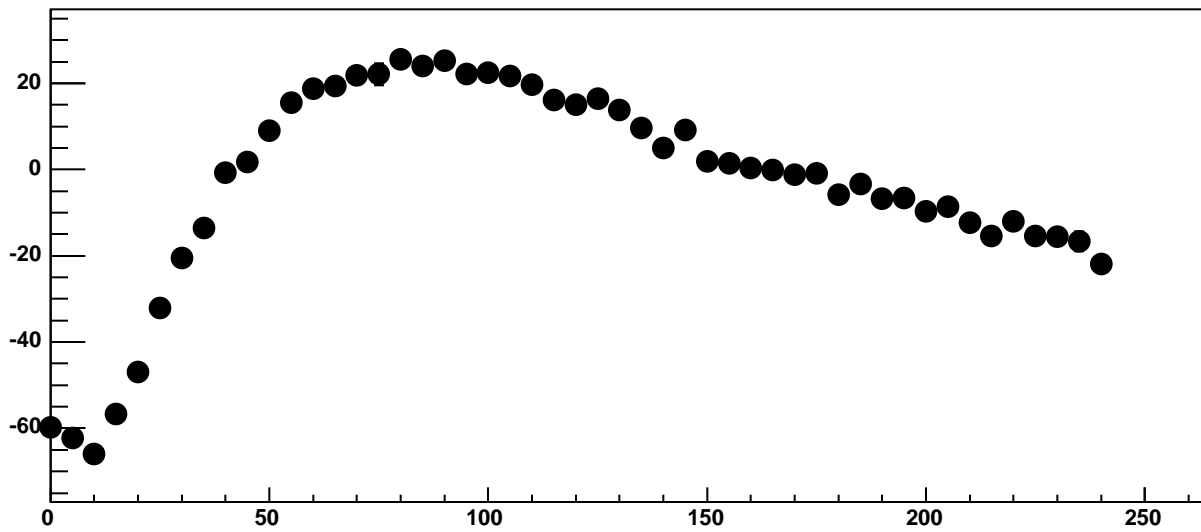


$\chi^2 / \text{ndf}$	6516 / 41
p0	$318.4 \pm 0.5125$
p1	$338.1 \pm 1.855$
p2	$-58.78 \pm 4.766$
p3	$36.1 \pm 0.3109$
p4	$0.1905 \pm 0.003663$

Chip 2, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold

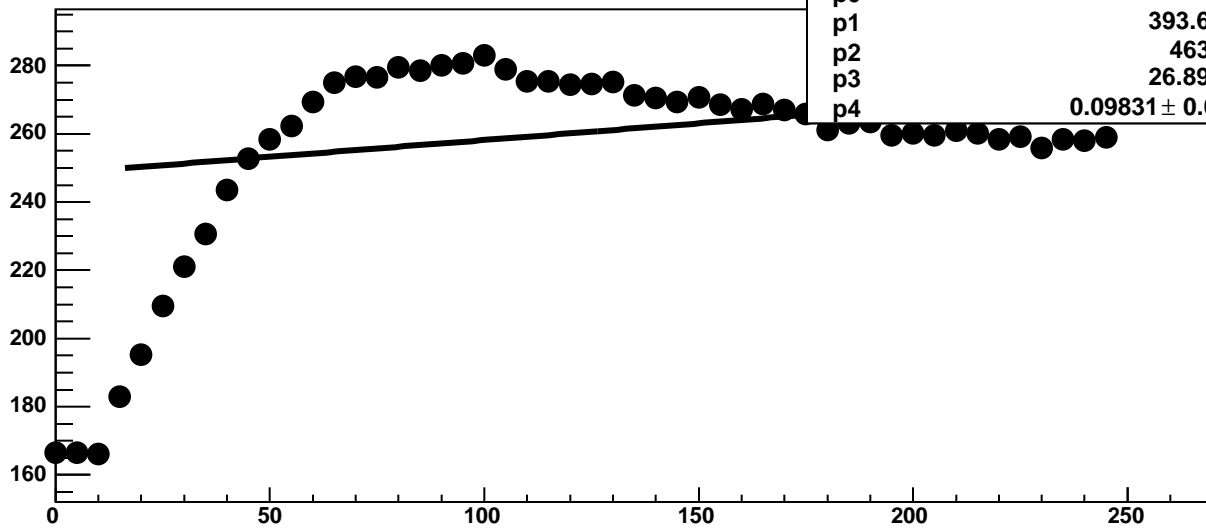


Chip 2, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold

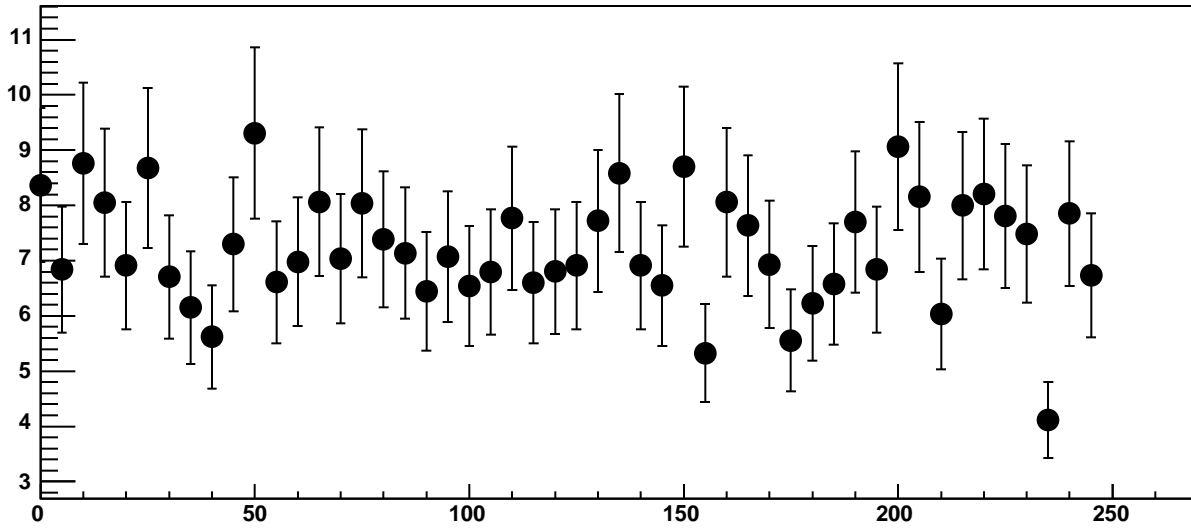




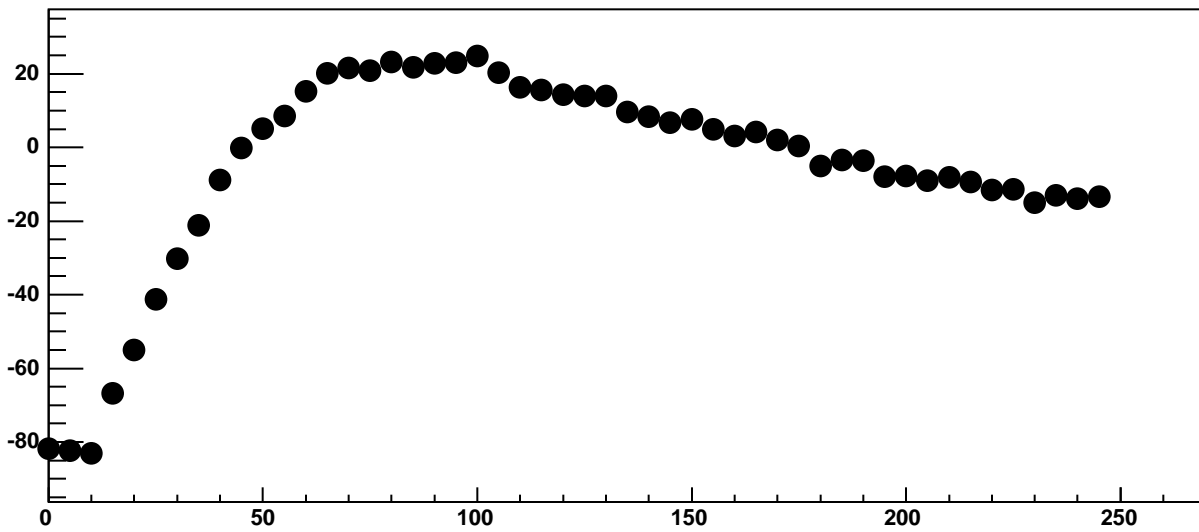
Chip 2, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold



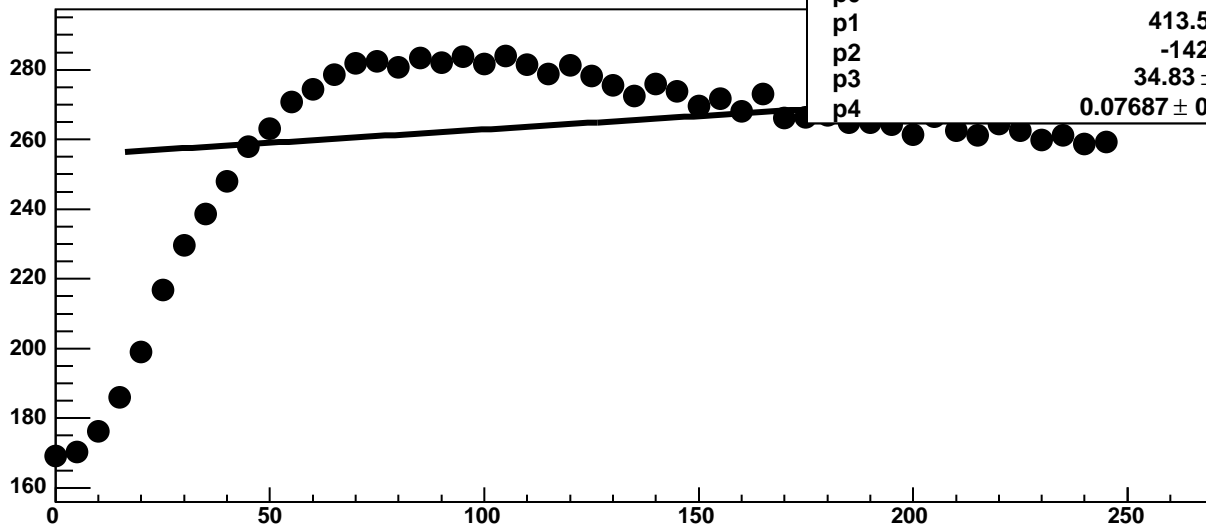
Chip 2, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold



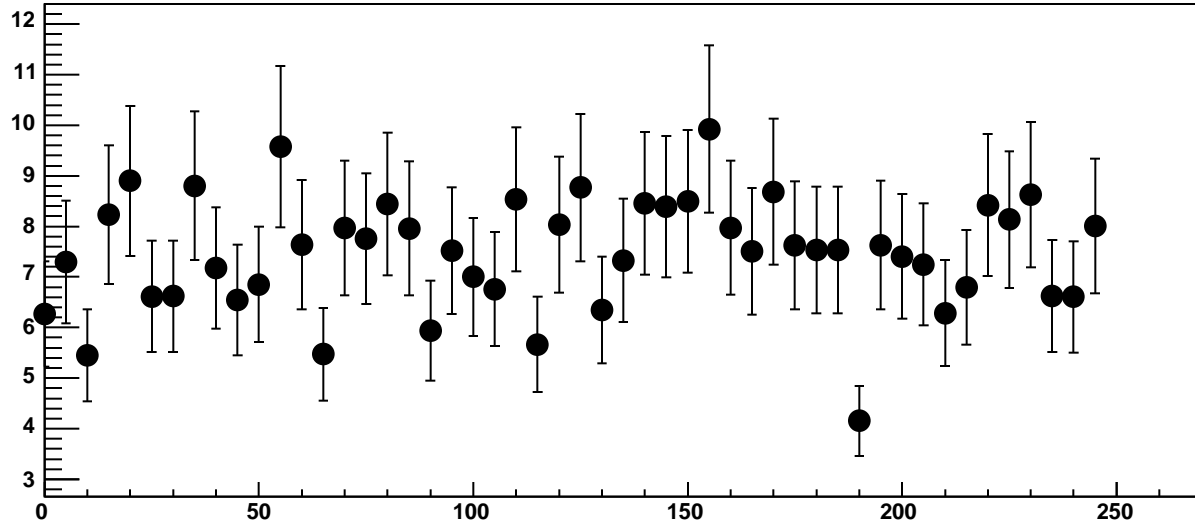
Chip 2, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold



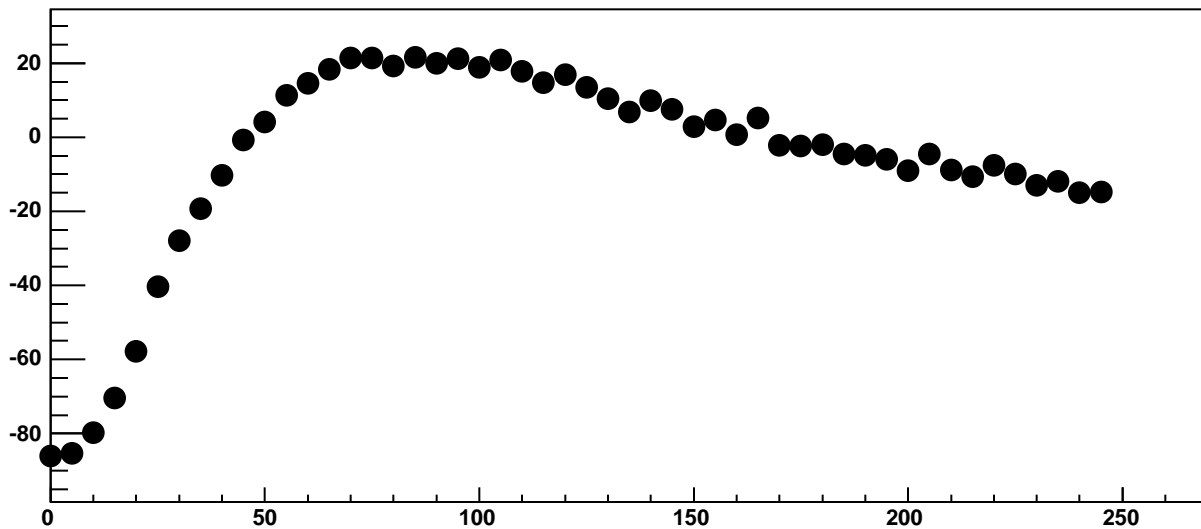
Chip 2, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold



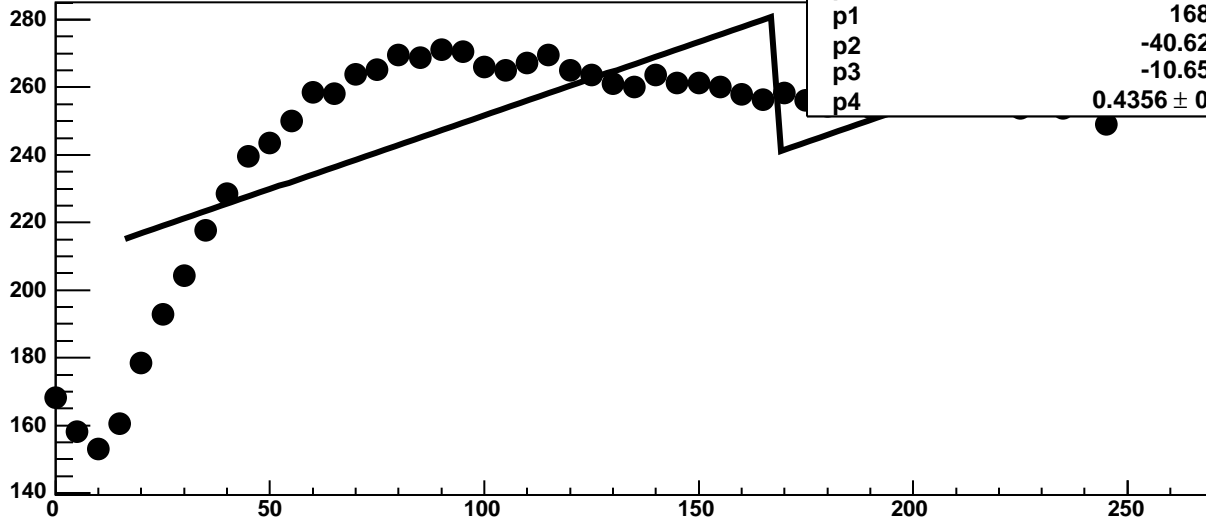
Chip 2, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

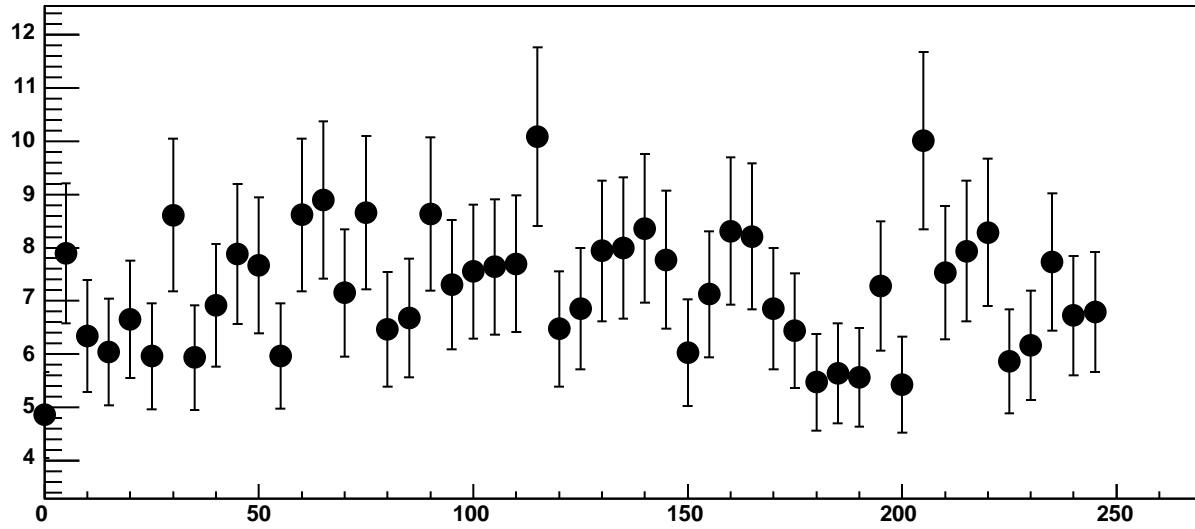


Chip 2, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

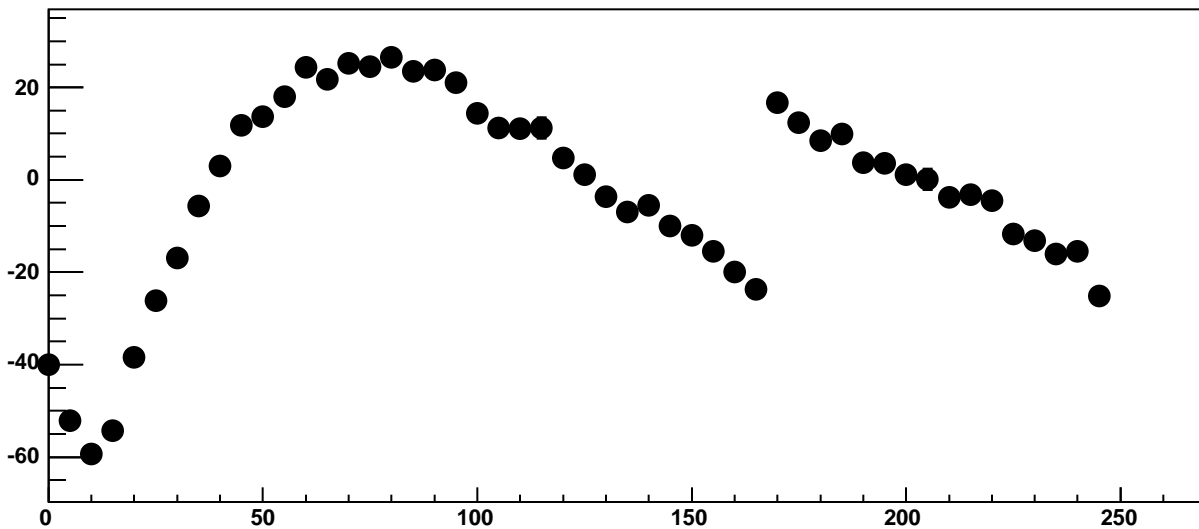


$\chi^2 / \text{ndf}$	5750 / 41
p0	$281.3 \pm 1.369$
p1	$168 \pm 2.952$
p2	$-40.62 \pm 2.479$
p3	$-10.65 \pm 41.66$
p4	$0.4356 \pm 0.002356$

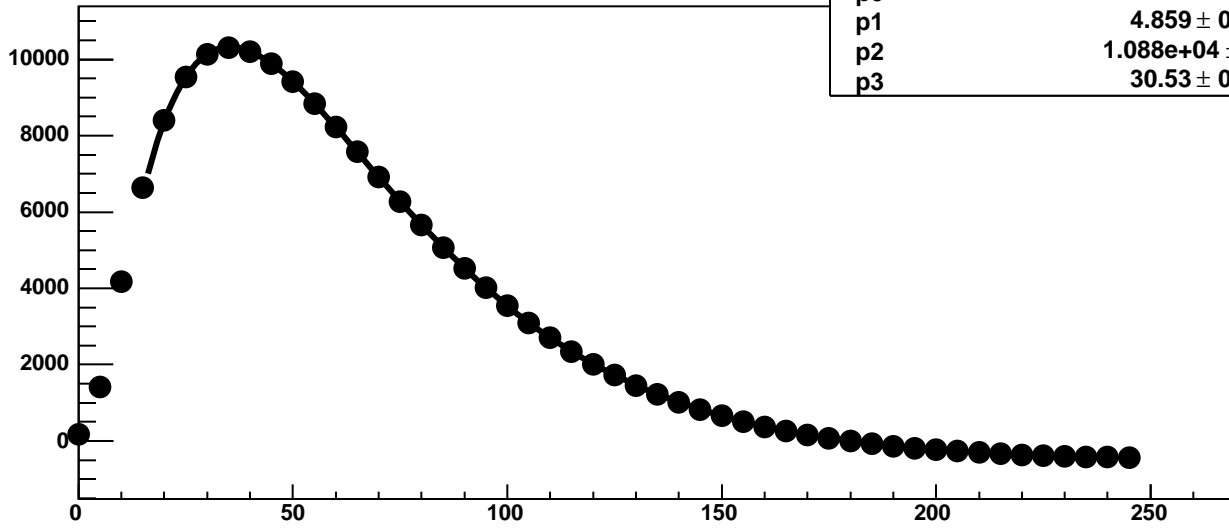
Chip 2, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

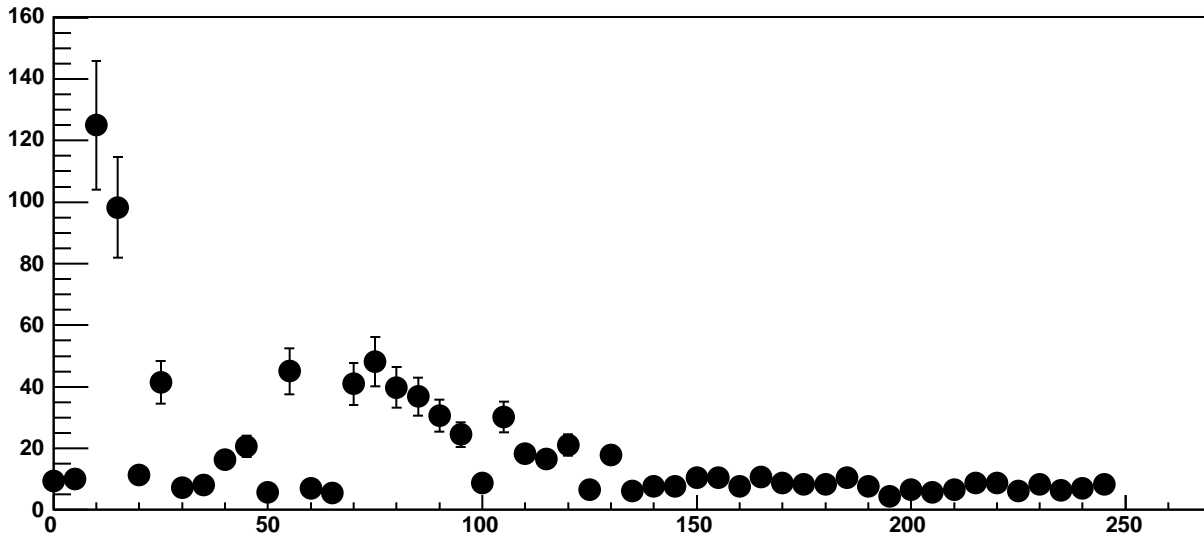


Chip 2, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold

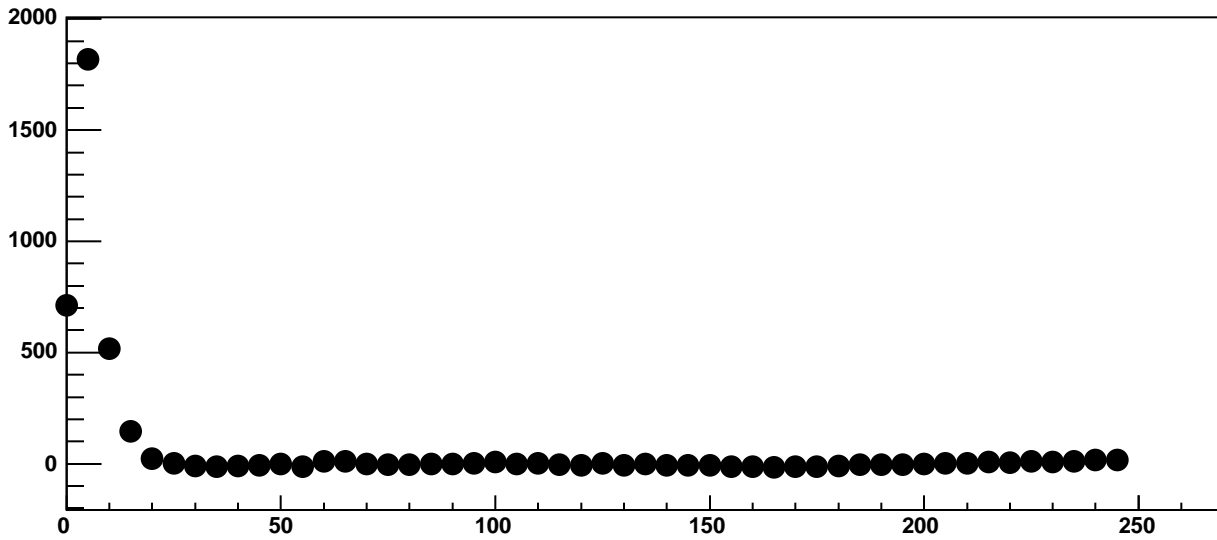


$\chi^2 / \text{ndf}$	877.5 / 42
p0	-547.3 ± 0.5598
p1	4.859 ± 0.009514
p2	1.088e+04 ± 0.8843
p3	30.53 ± 0.006143

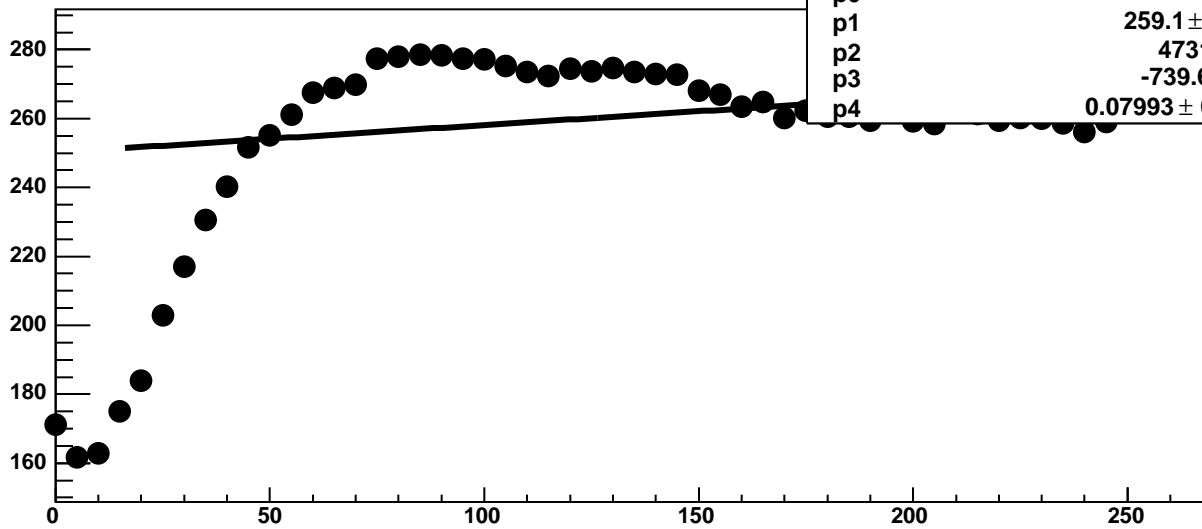
Chip 2, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold



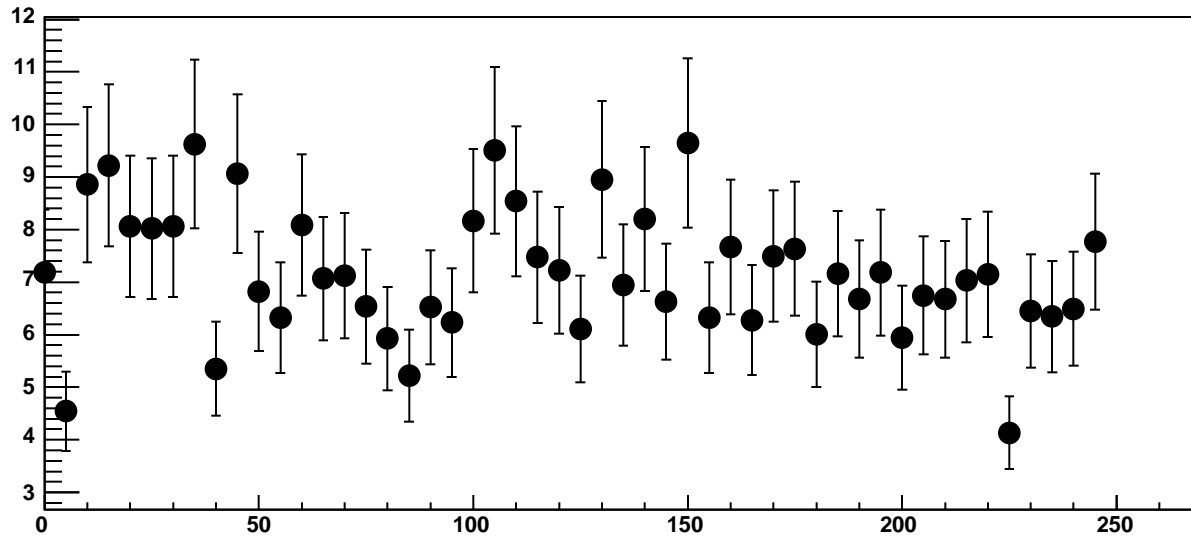
Chip 2, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold



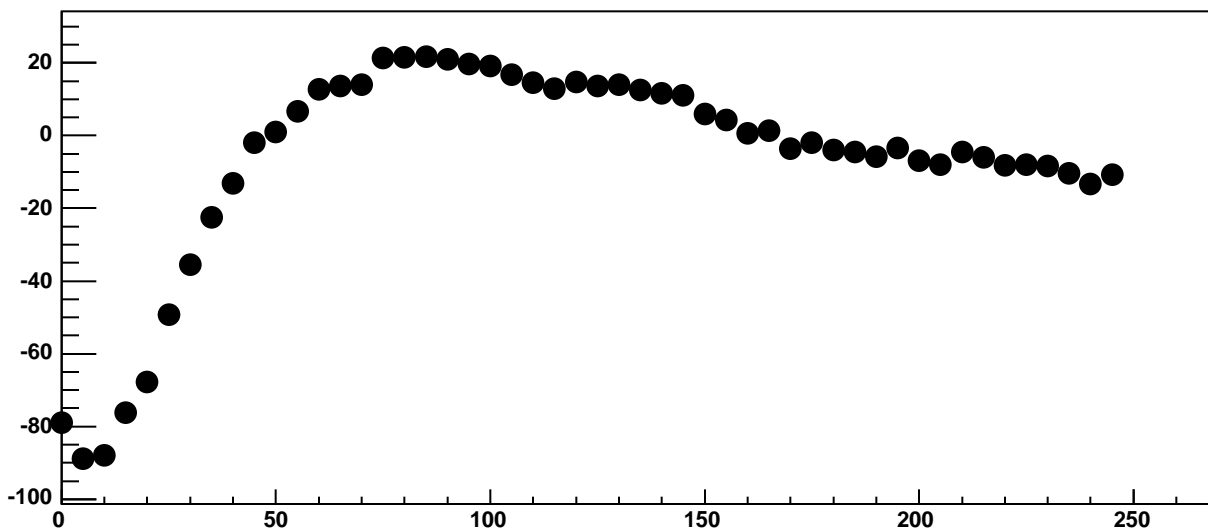
Chip 2, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold



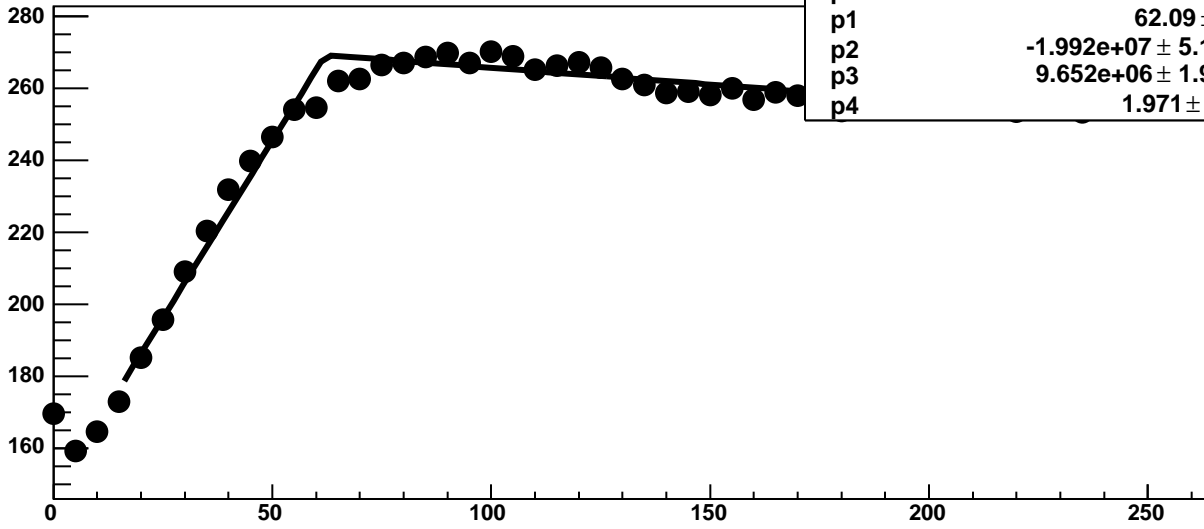
Chip 2, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

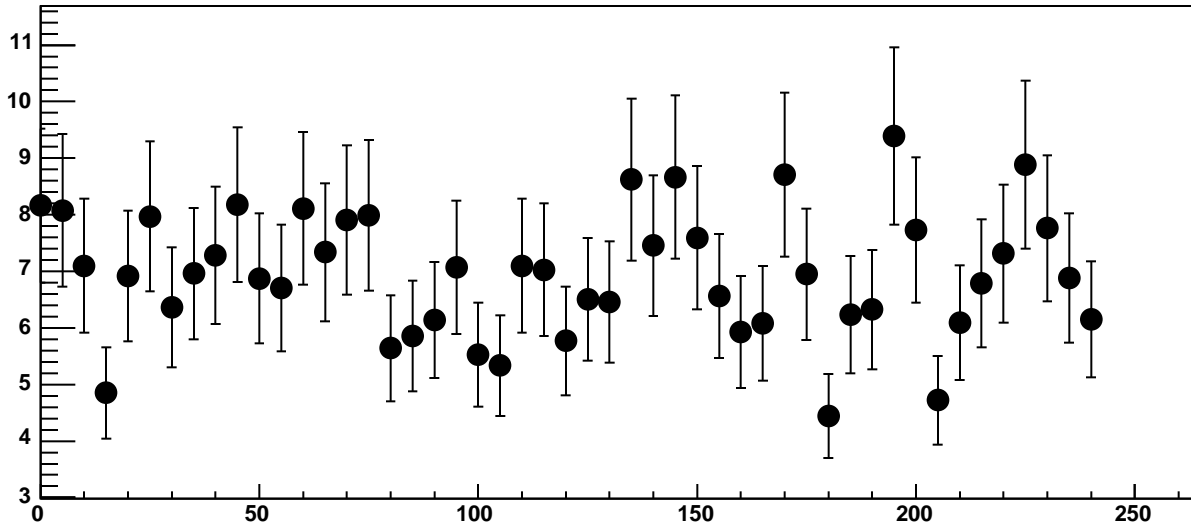


Chip 2, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

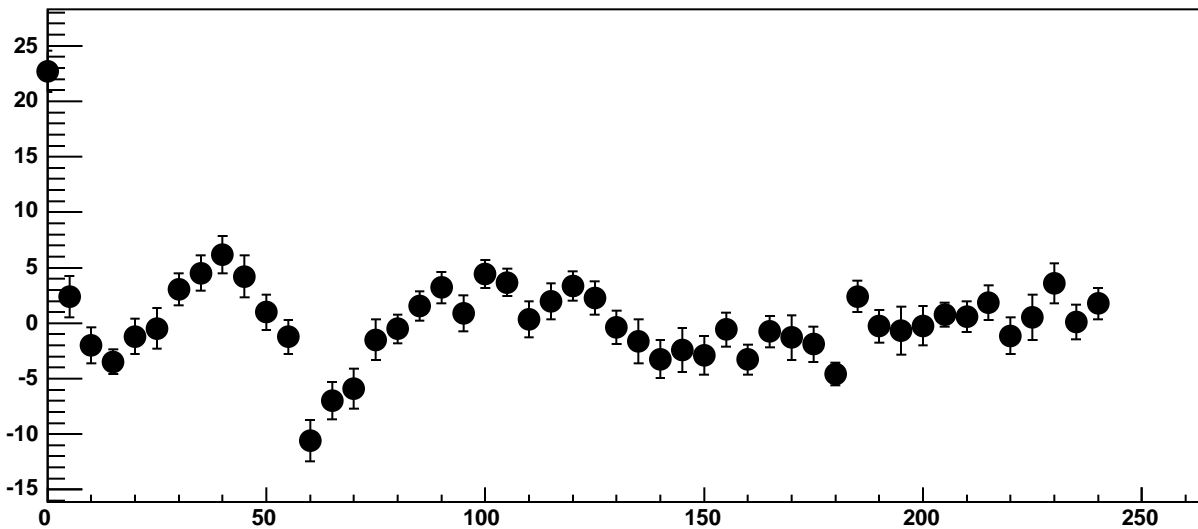


$\chi^2 / \text{ndf}$	189.5 / 41
p0	$269.3 \pm 0.4863$
p1	$62.09 \pm 0.5529$
p2	$-1.992\text{e}+07 \pm 5.121\text{e}+05$
p3	$9.652\text{e}+06 \pm 1.969\text{e}+05$
p4	$1.971 \pm 0.03277$

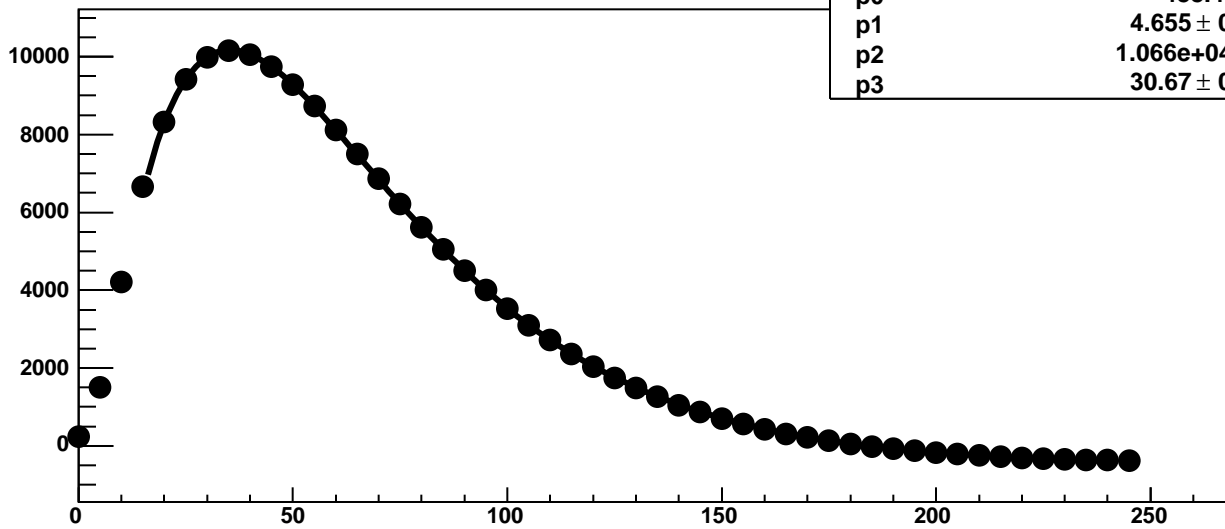
Chip 2, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold

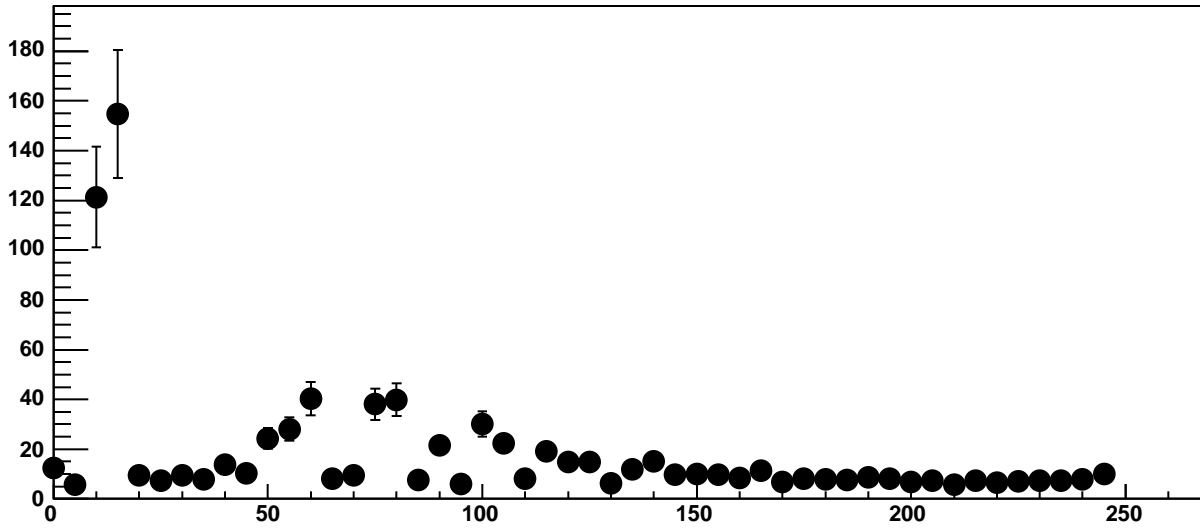


Chip 2, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold

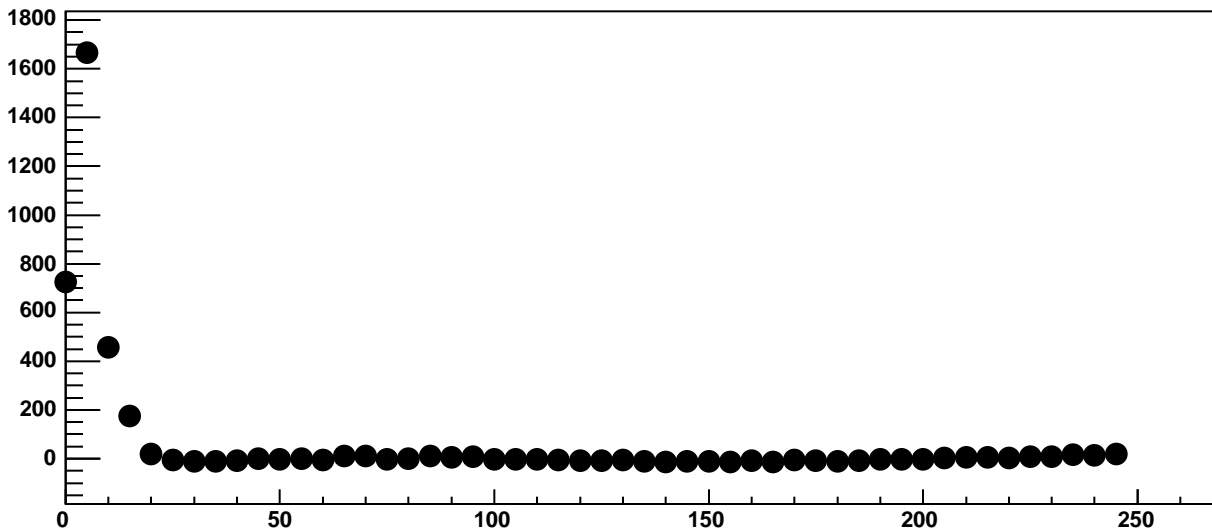


$\chi^2 / \text{ndf}$	866.3 / 42
p0	$-488.4 \pm 0.5621$
p1	$4.655 \pm 0.009216$
p2	$1.066\text{e}+04 \pm 0.973$
p3	$30.67 \pm 0.005964$

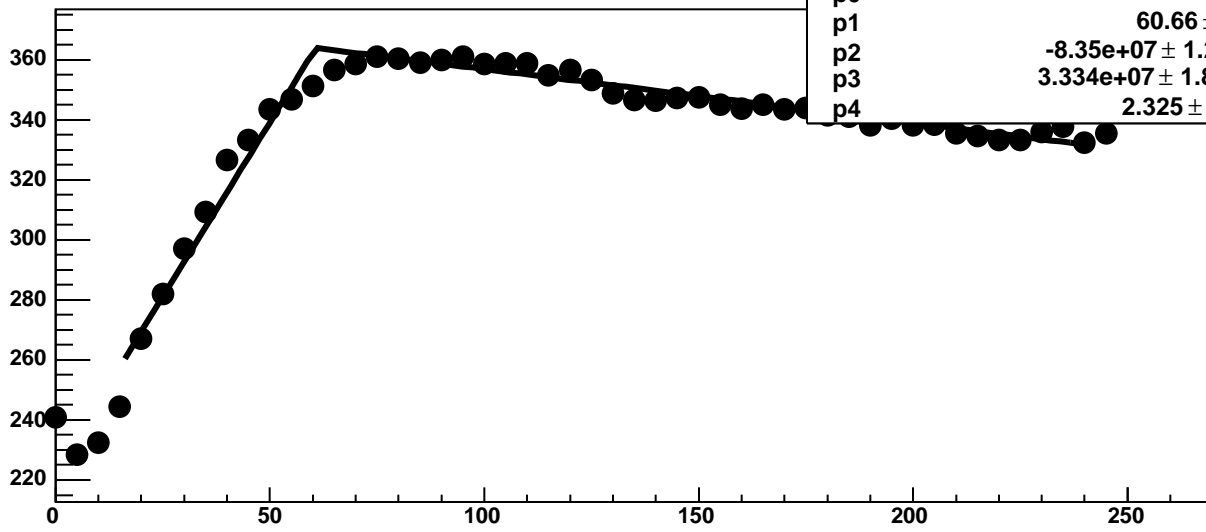
Chip 2, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold

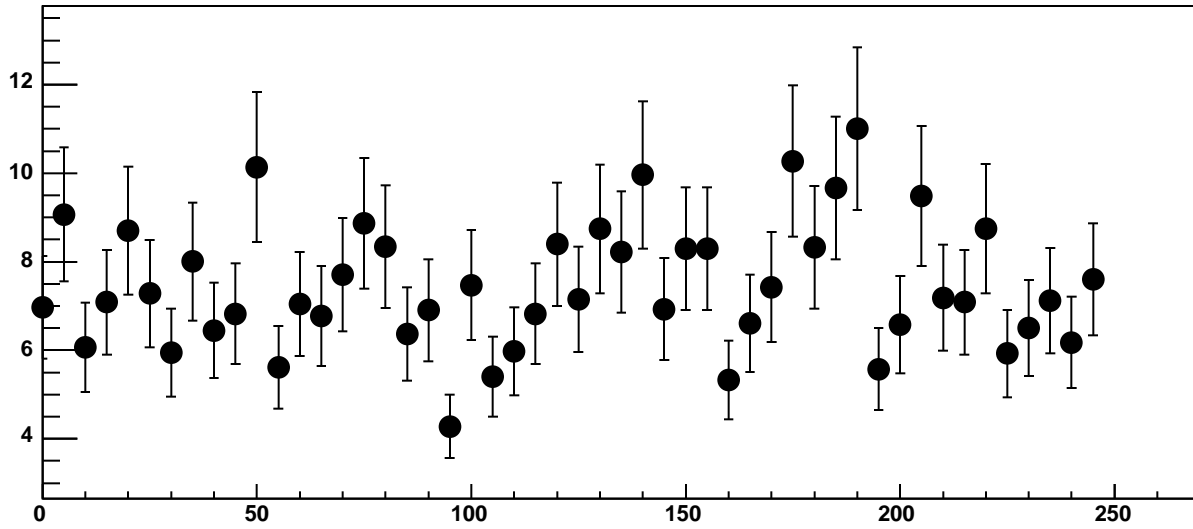


Chip 2, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

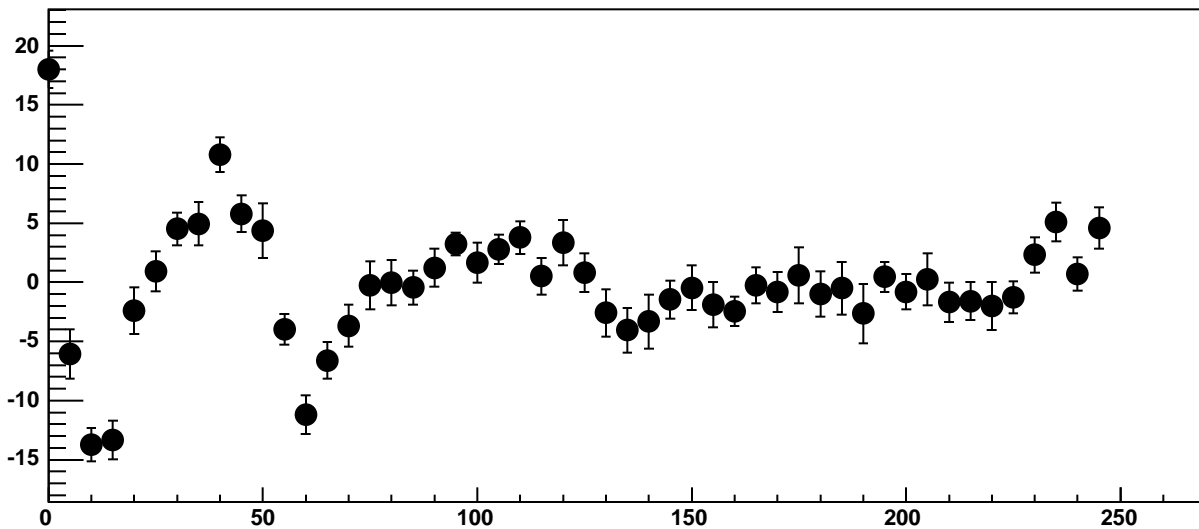


$\chi^2 / \text{ndf}$	298.9 / 41
p0	$363.9 \pm 0.4882$
p1	$60.66 \pm 0.4357$
p2	$-8.35e+07 \pm 1.242e+06$
p3	$3.334e+07 \pm 1.858e+05$
p4	$2.325 \pm 0.03539$

Chip 2, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold

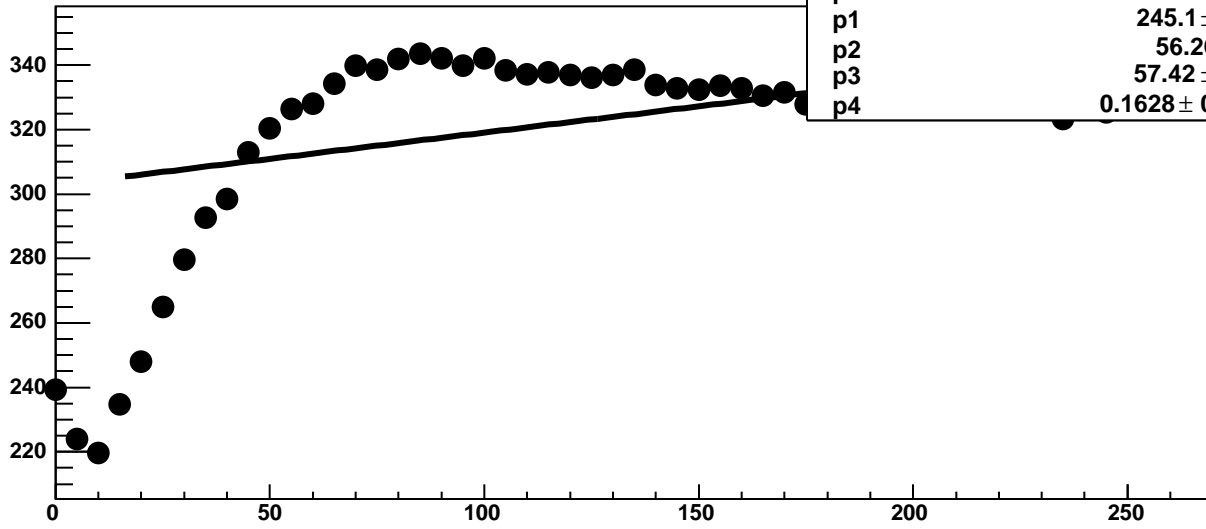


Chip 2, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold



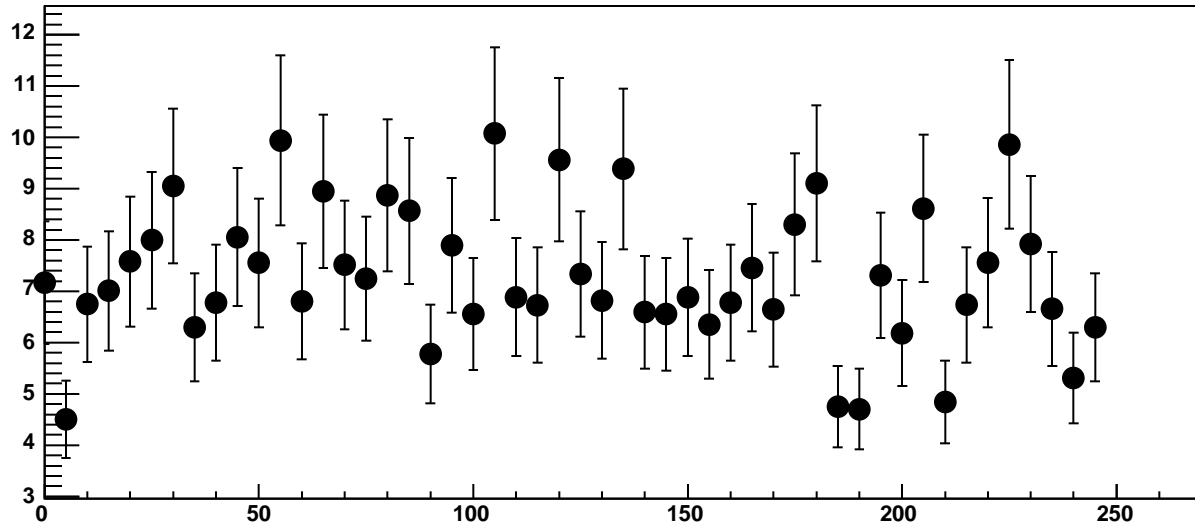


Chip 2, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

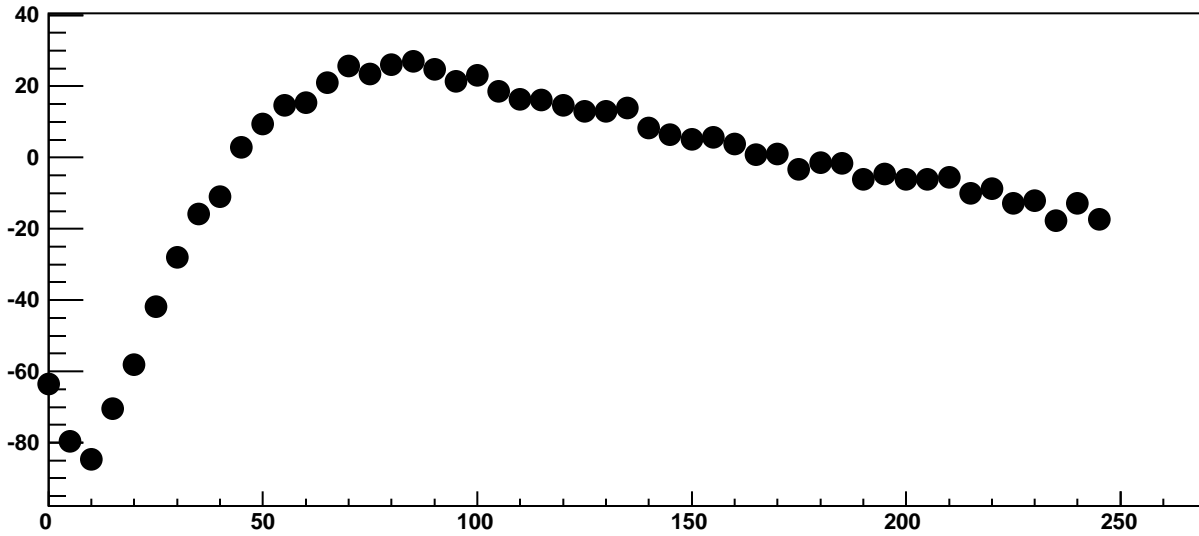


$\chi^2 / \text{ndf}$	6758 / 41
p0	$342.7 \pm 0.4461$
p1	$245.1 \pm 0.2005$
p2	$56.26 \pm 24.8$
p3	$57.42 \pm 0.9495$
p4	$0.1628 \pm 0.003571$

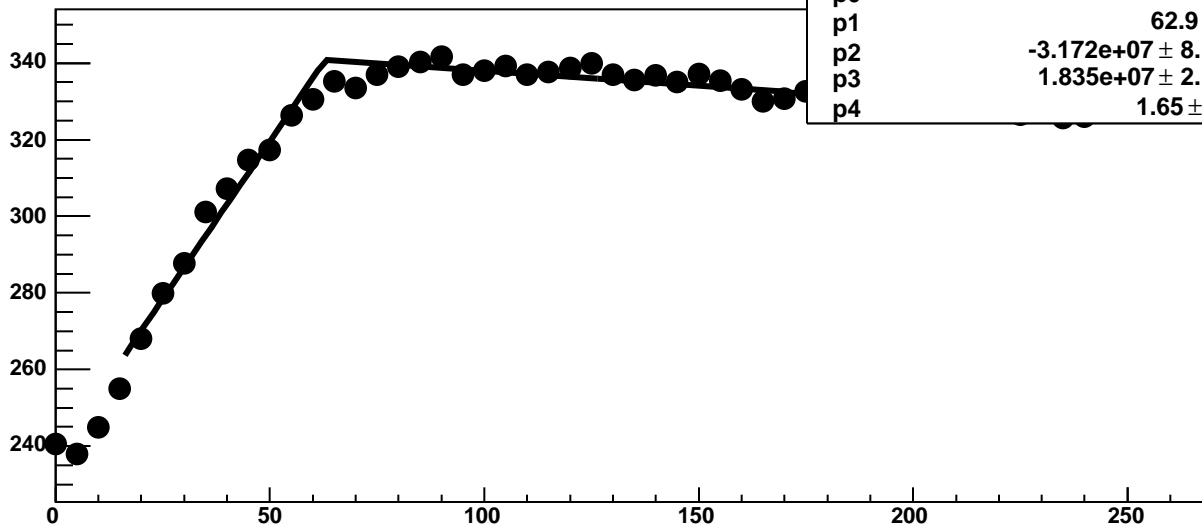
Chip 2, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

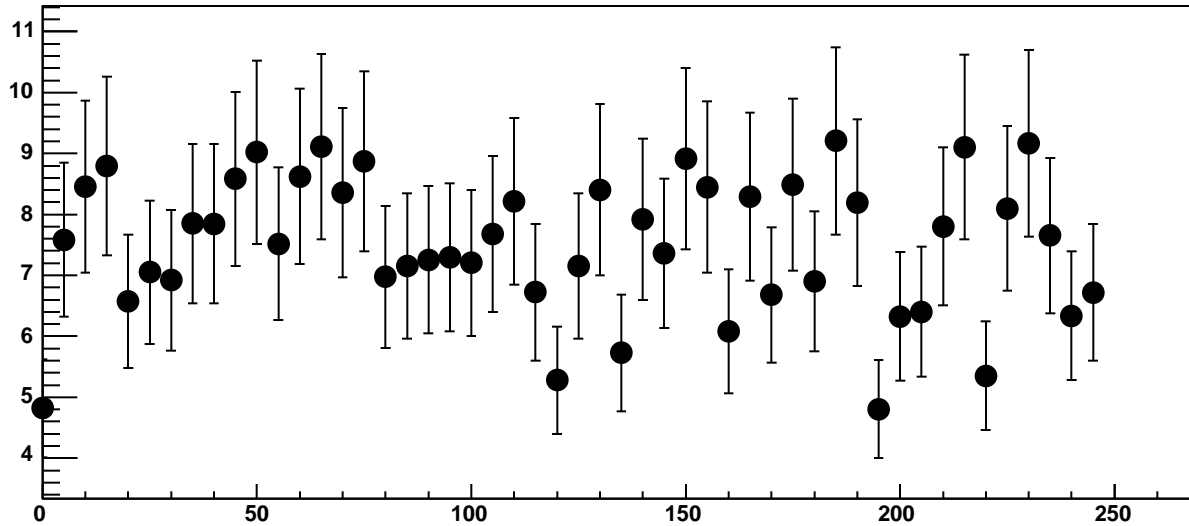


Chip 2, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold

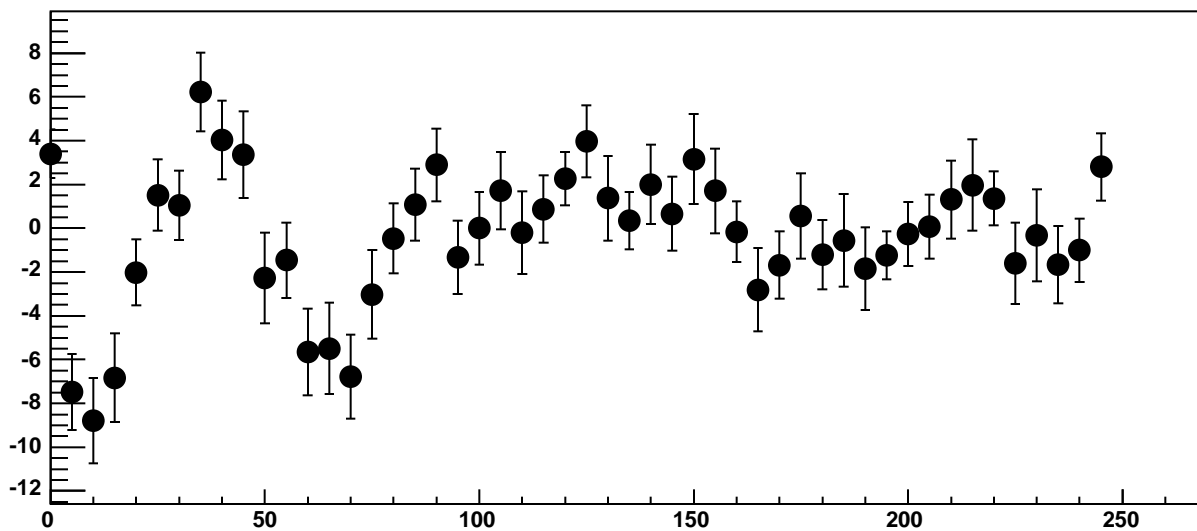


$\chi^2 / \text{ndf}$	97.31 / 41
p0	$340.9 \pm 0.5472$
p1	$62.9 \pm 0.7742$
p2	$-3.172\text{e}+07 \pm 8.193\text{e}+05$
p3	$1.835\text{e}+07 \pm 2.154\text{e}+05$
p4	$1.65 \pm 0.04005$

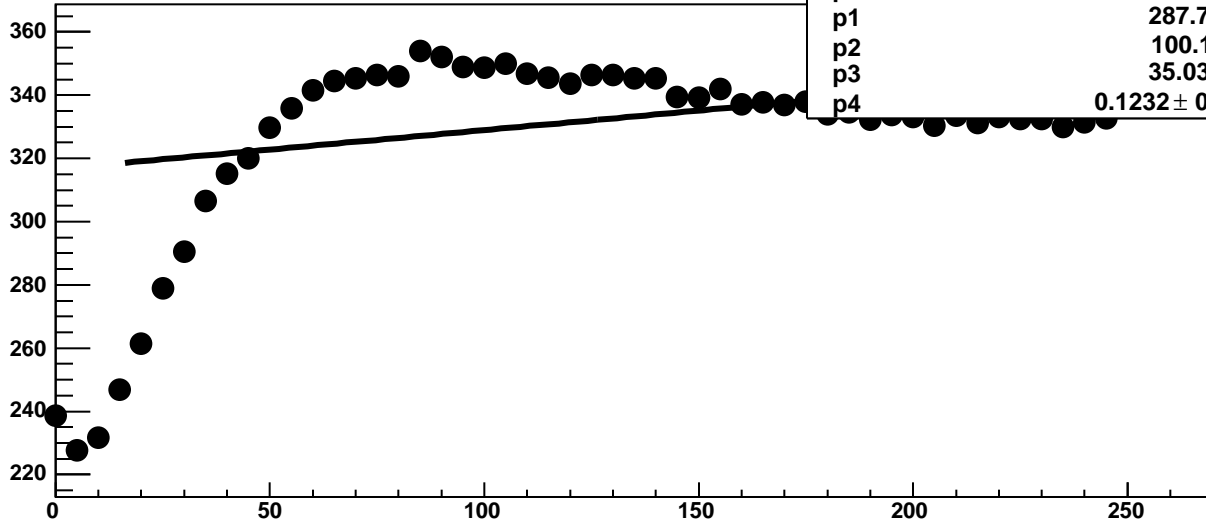
Chip 2, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold

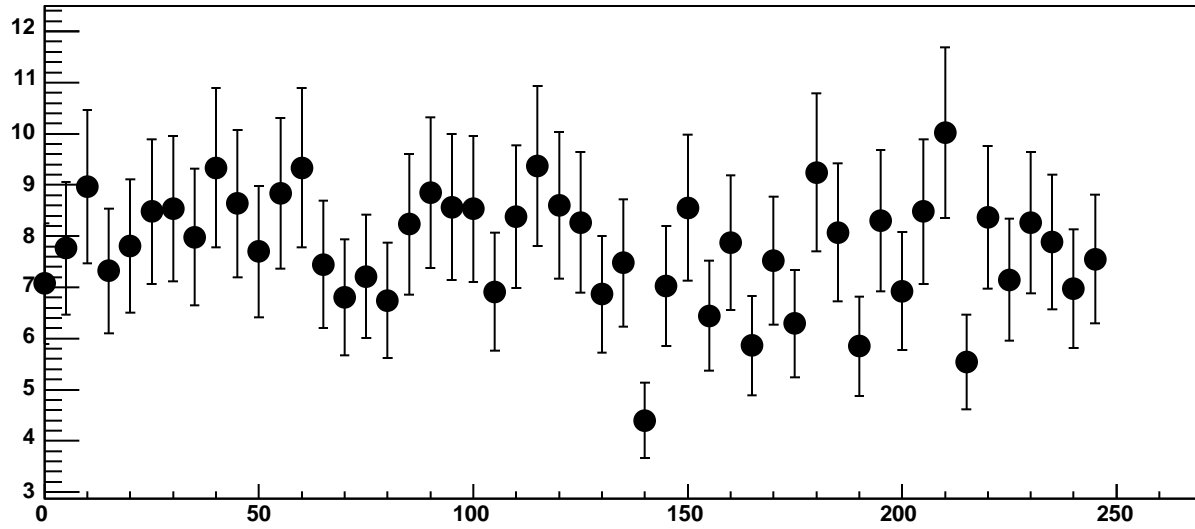


Chip 2, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold

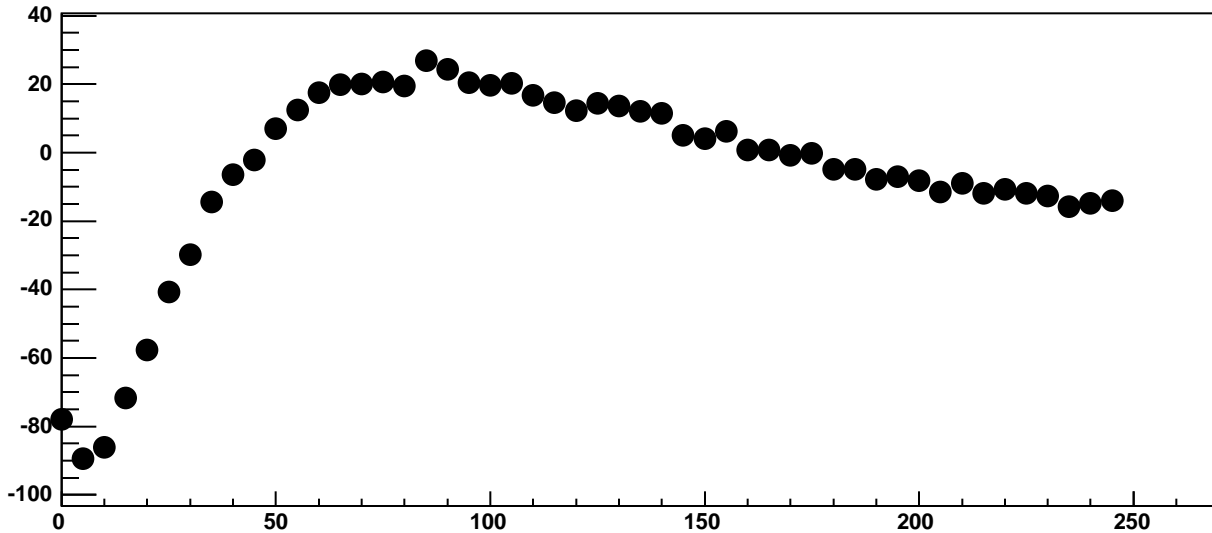


$\chi^2 / \text{ndf}$	6073 / 41
p0	$352.1 \pm 0.2526$
p1	$287.7 \pm 2.049$
p2	$100.1 \pm 84.85$
p3	$35.03 \pm 1.414$
p4	$0.1232 \pm 0.001507$

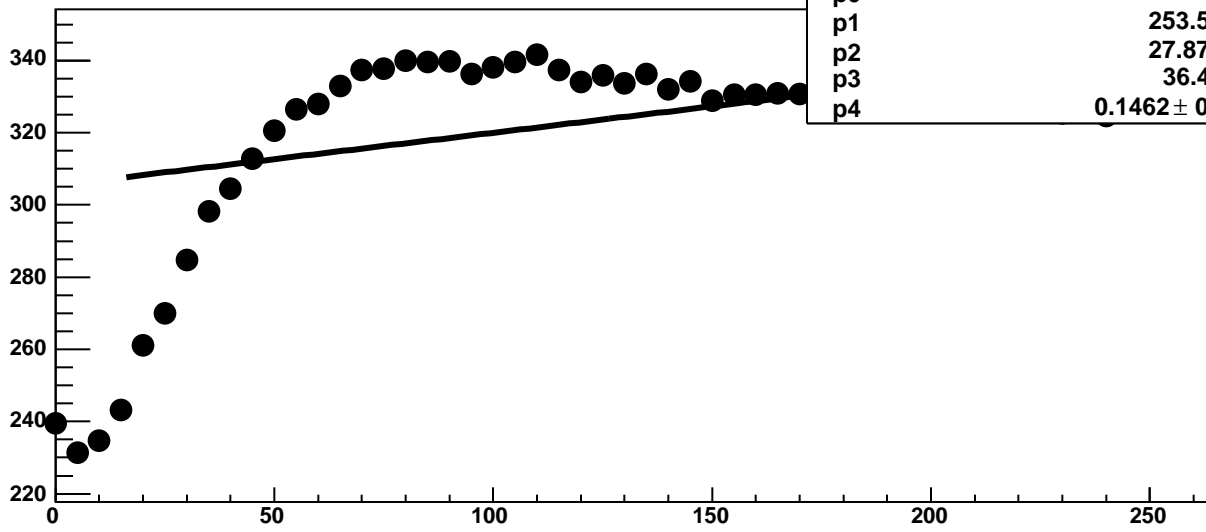
Chip 2, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

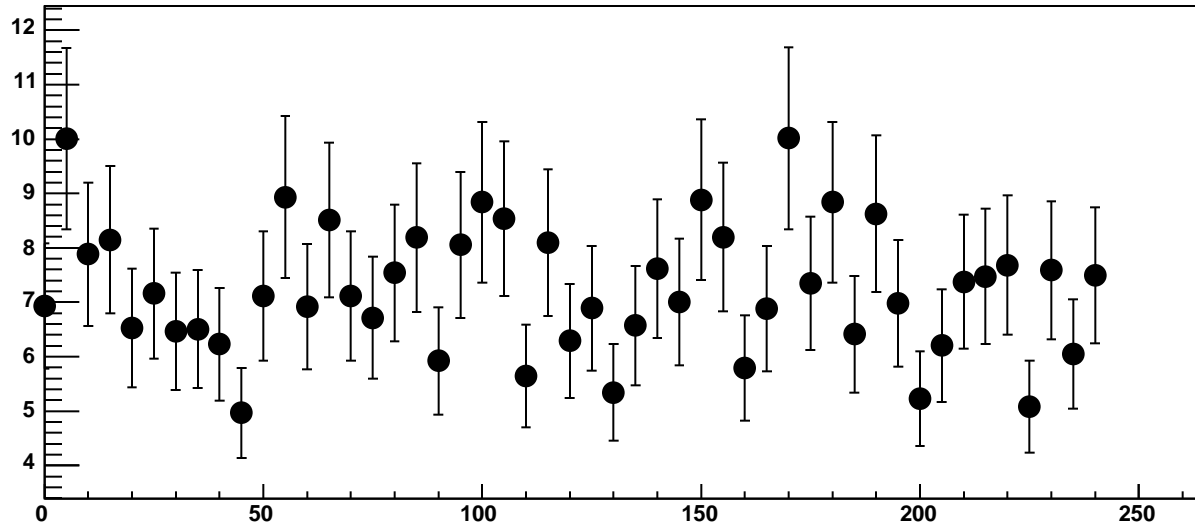


Chip 2, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

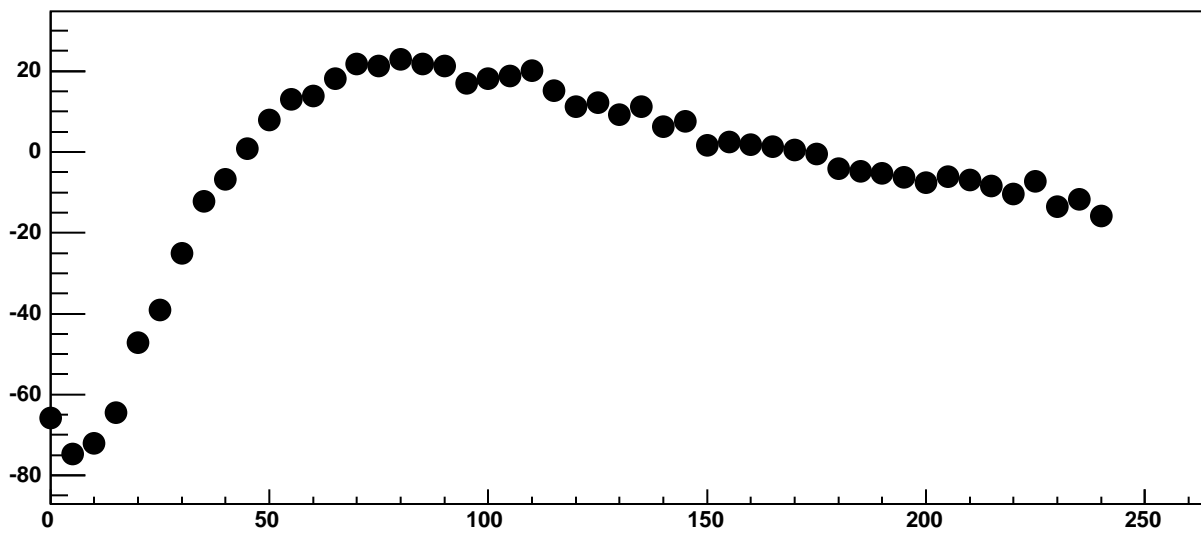


$\chi^2 / \text{ndf}$	5530 / 41
p0	$342.4 \pm 0.2333$
p1	$253.5 \pm 1.595$
p2	$27.87 \pm 84.85$
p3	$36.4 \pm 1.414$
p4	$0.1462 \pm 0.001638$

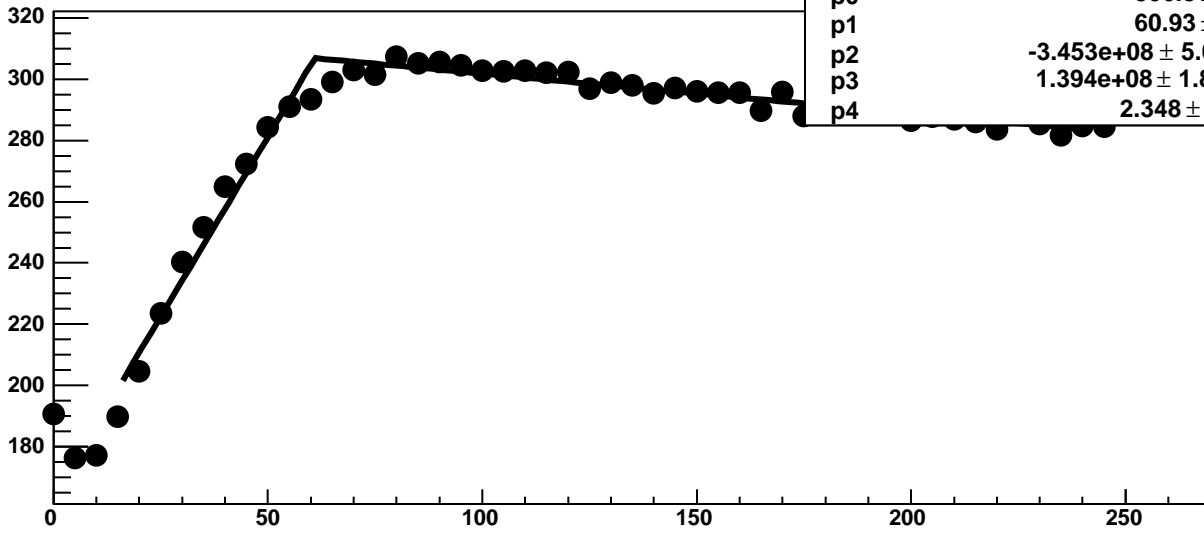
Chip 2, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

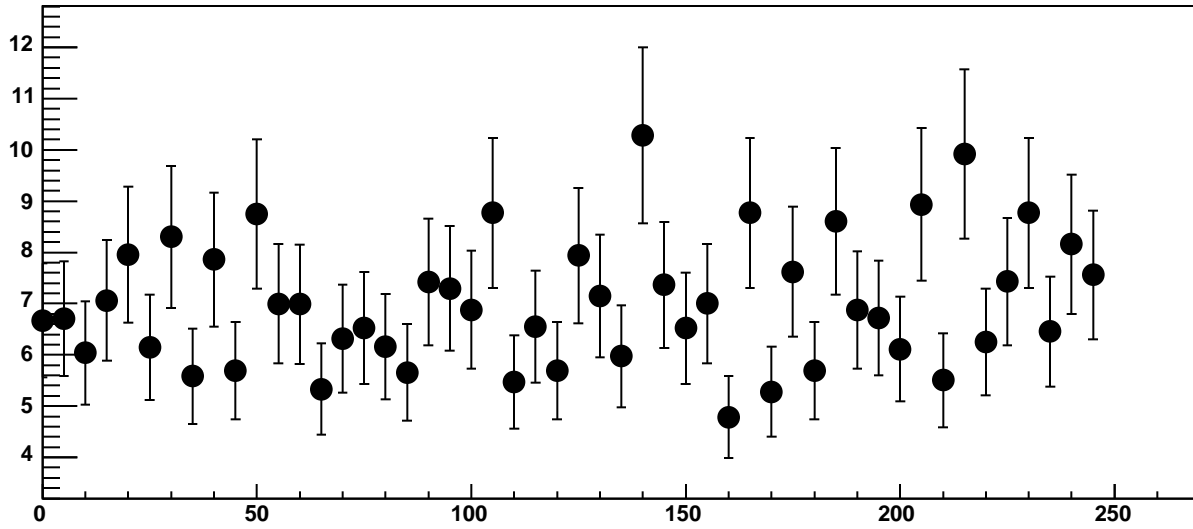


Chip 2, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold

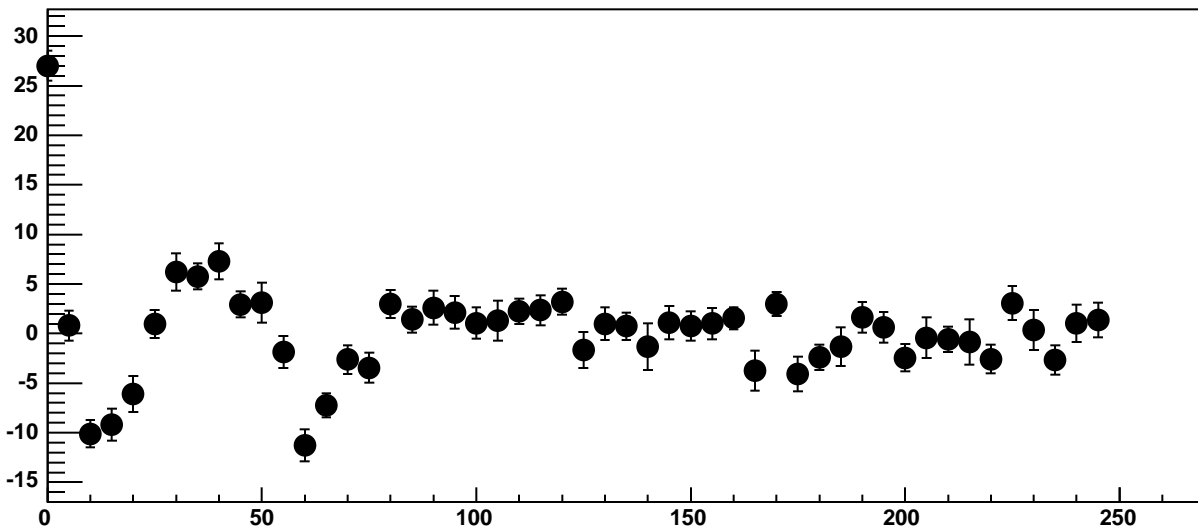


$\chi^2 / \text{ndf}$	253.3 / 41
p0	$306.8 \pm 0.4752$
p1	$60.93 \pm 0.4399$
p2	$-3.453\text{e}+08 \pm 5.032\text{e}+06$
p3	$1.394\text{e}+08 \pm 1.859\text{e}+05$
p4	$2.348 \pm 0.03568$

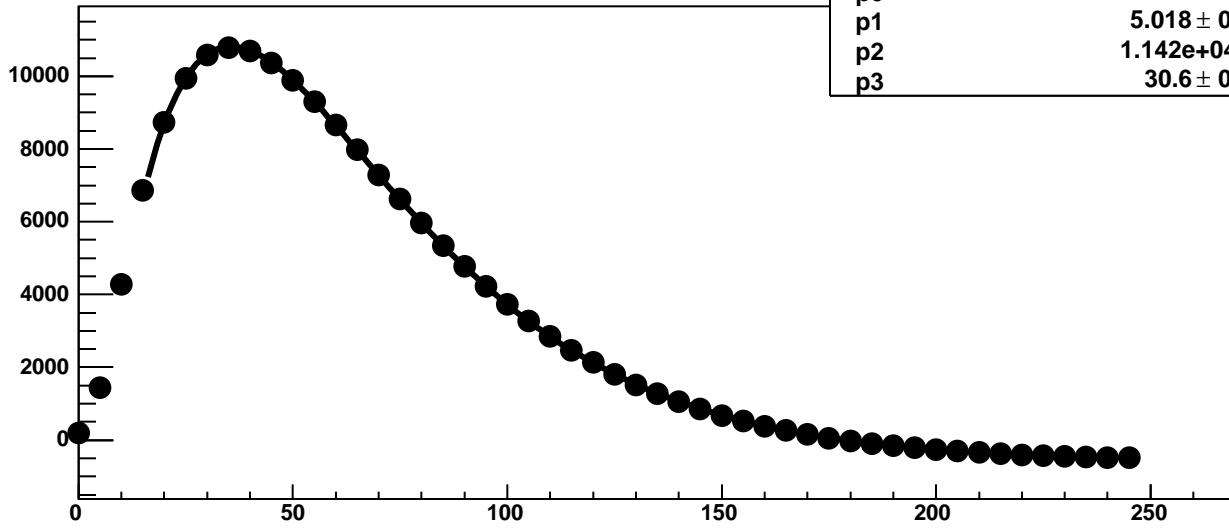
Chip 2, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

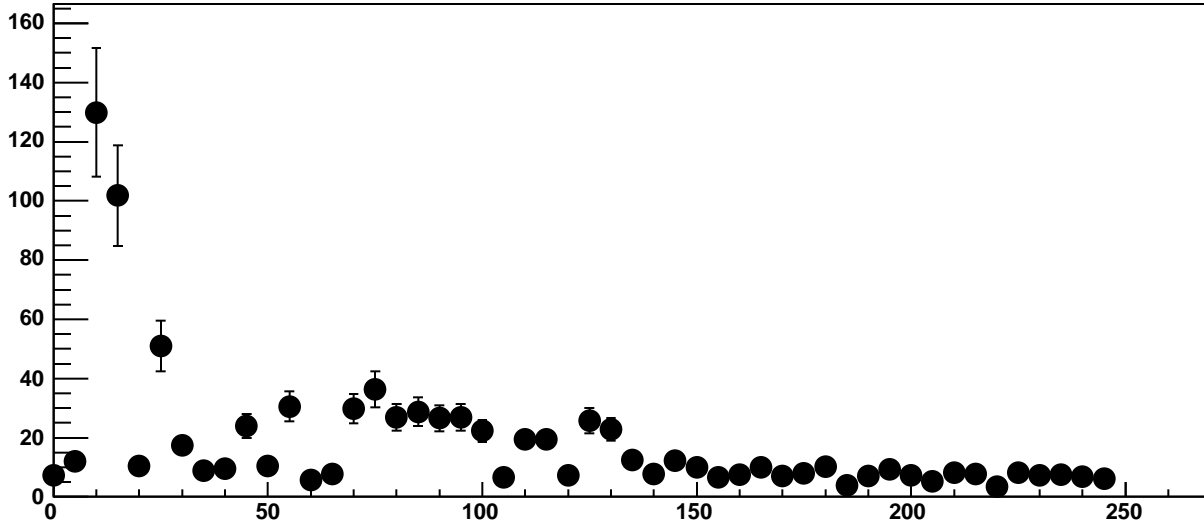


Chip 2, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold

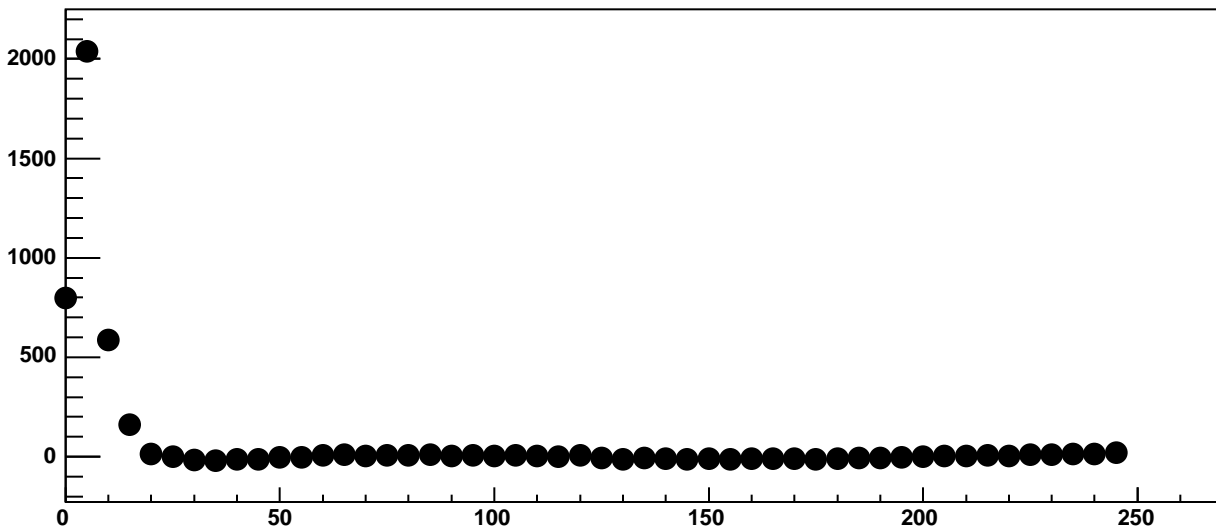


$\chi^2 / \text{ndf}$	1032 / 42
p0	-600.5 ± 0.5193
p1	5.018 ± 0.008859
p2	1.142e+04 ± 1.011
p3	30.6 ± 0.005828

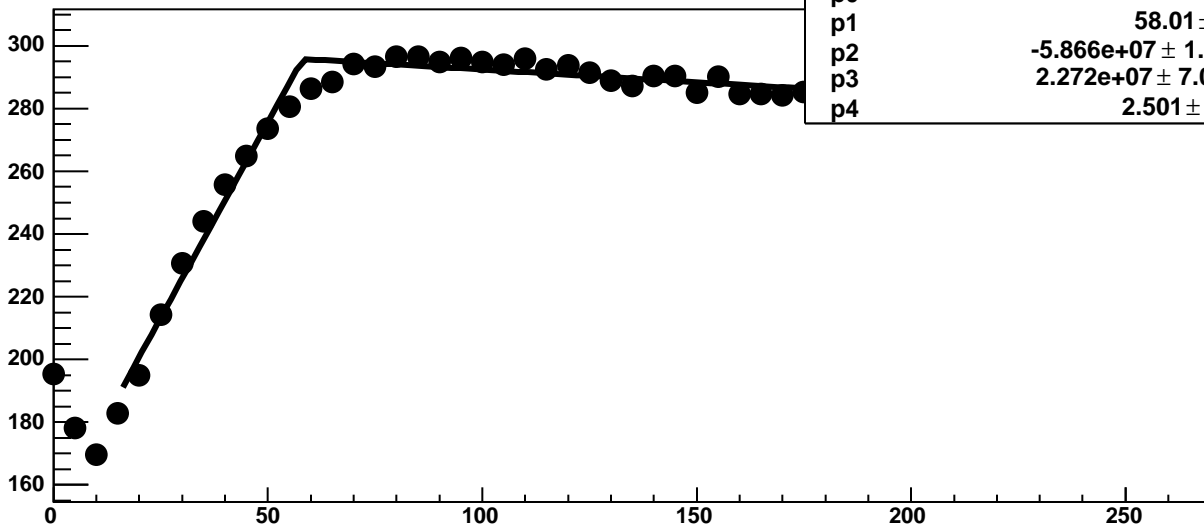
Chip 2, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold

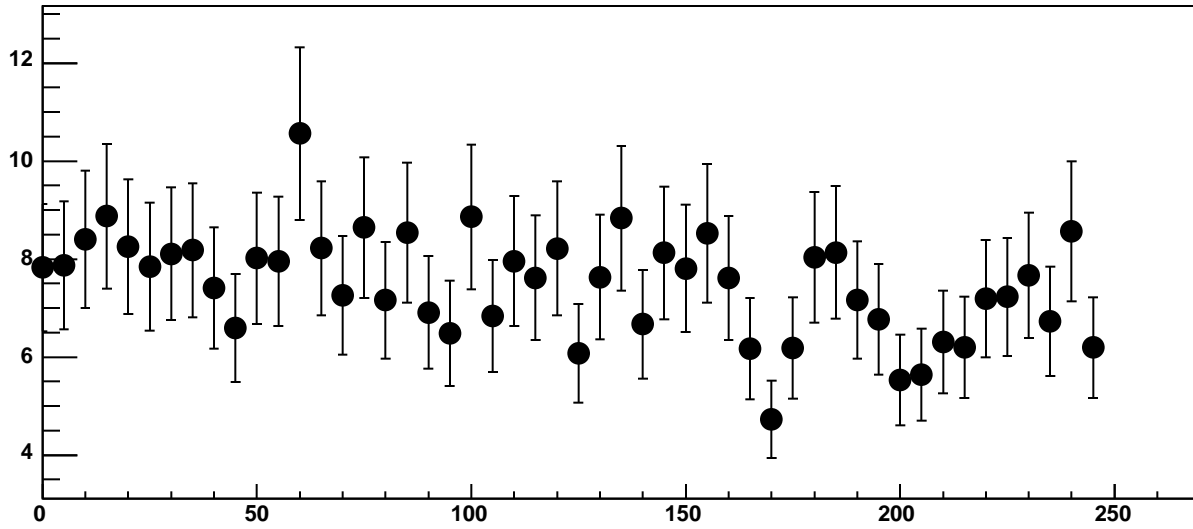


Chip 2, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

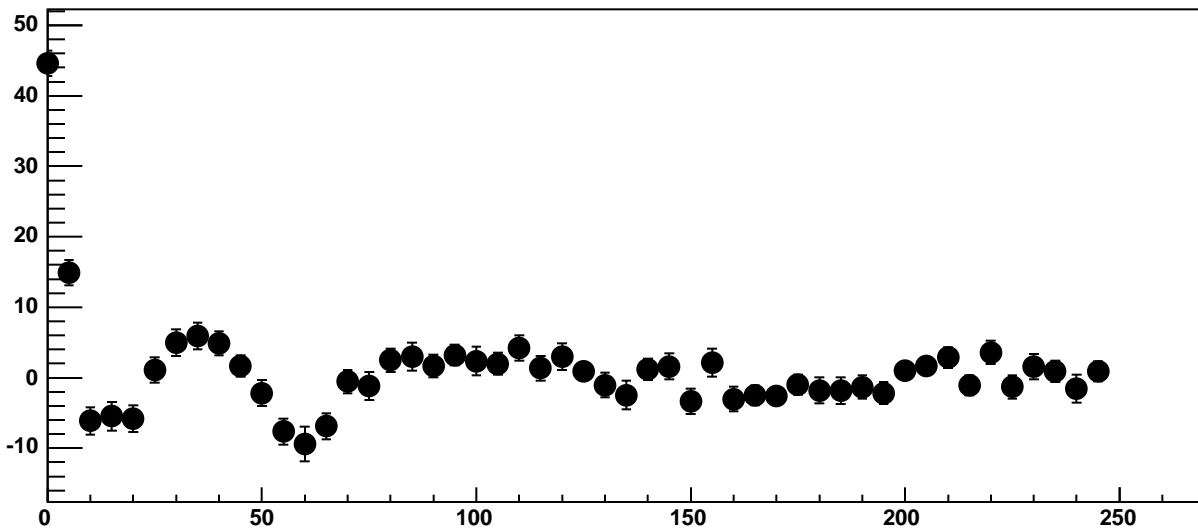


$\chi^2 / \text{ndf}$	152.5 / 41
p0	$295.8 \pm 0.5638$
p1	$58.01 \pm 0.5264$
p2	$-5.866\text{e}+07 \pm 1.111\text{e}+06$
p3	$2.272\text{e}+07 \pm 7.003\text{e}+04$
p4	$2.501 \pm 0.04806$

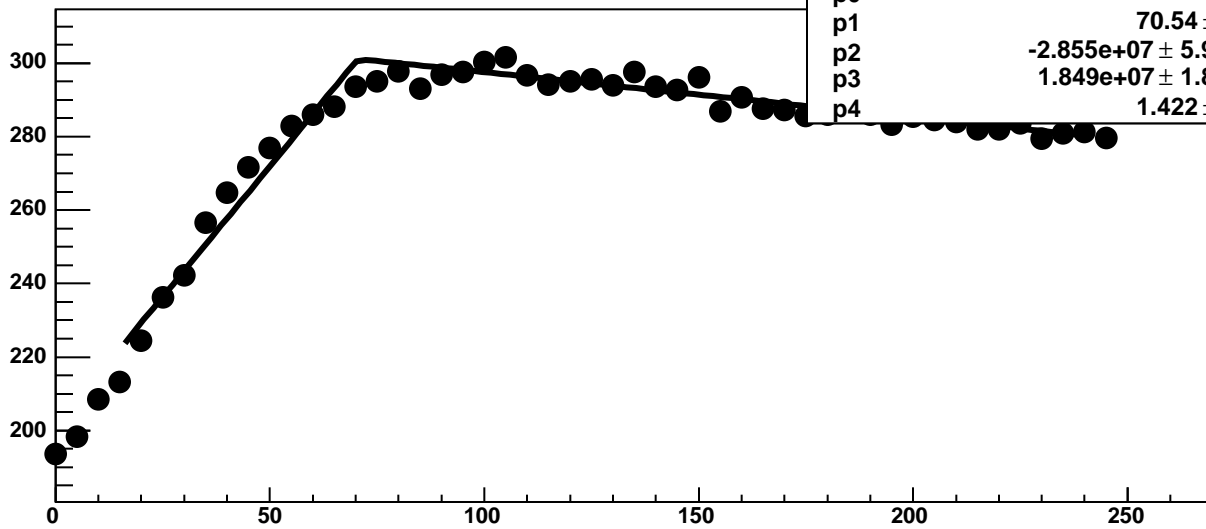
Chip 2, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

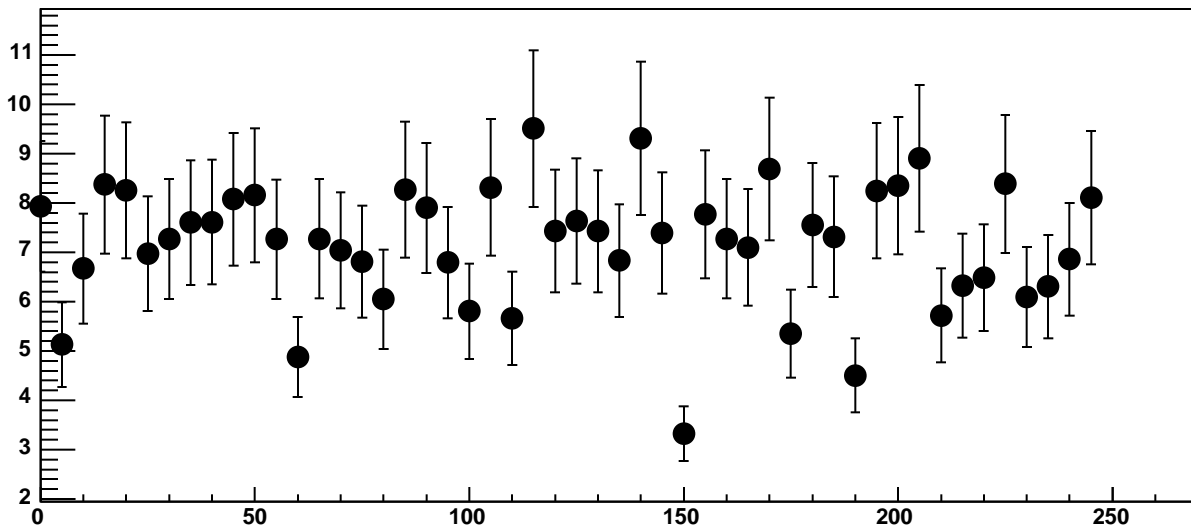


Chip 2, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

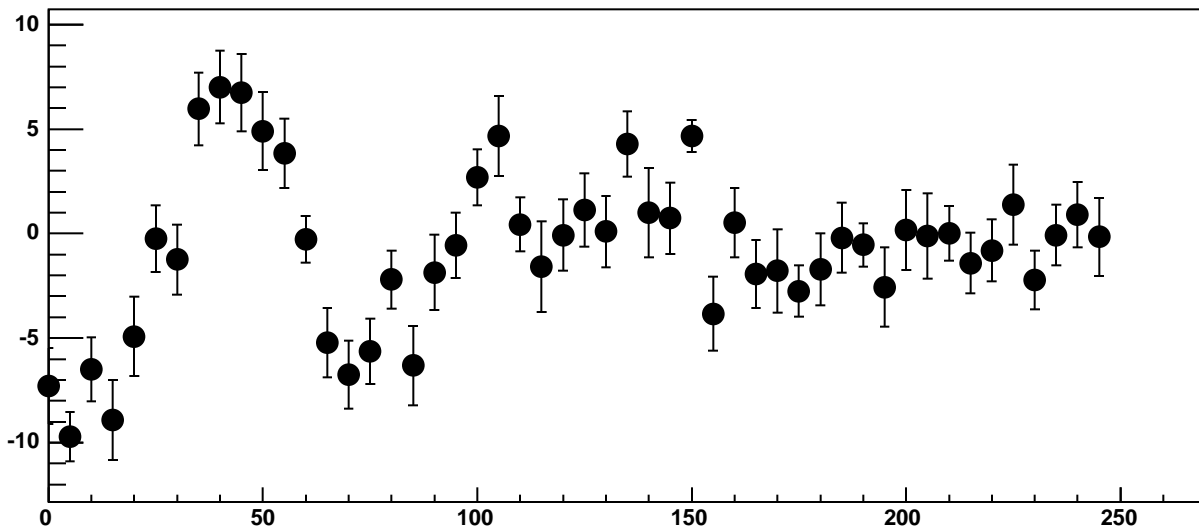


$\chi^2 / \text{ndf}$	213.1 / 41
p0	$301.1 \pm 0.5083$
p1	$70.54 \pm 0.6538$
p2	$-2.855e+07 \pm 5.919e+05$
p3	$1.849e+07 \pm 1.817e+05$
p4	$1.422 \pm 0.0277$

Chip 2, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold

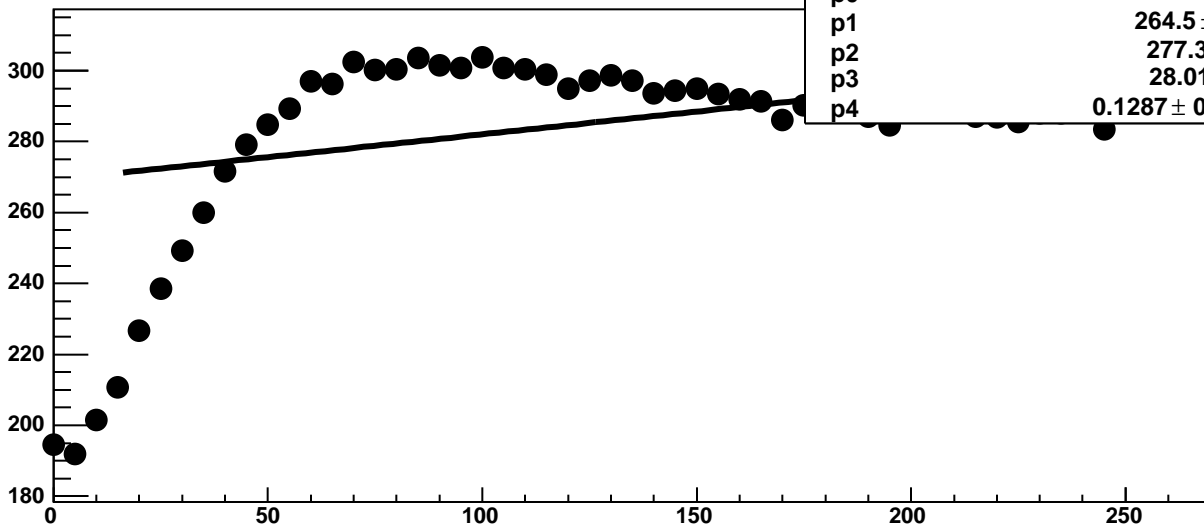


Chip 2, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold



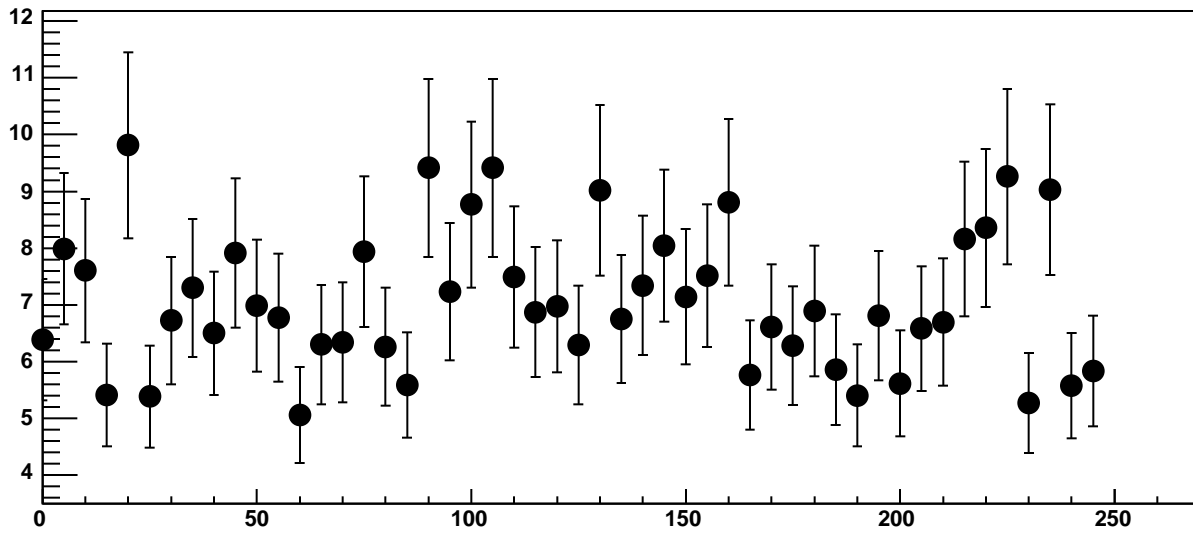


Chip 2, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold

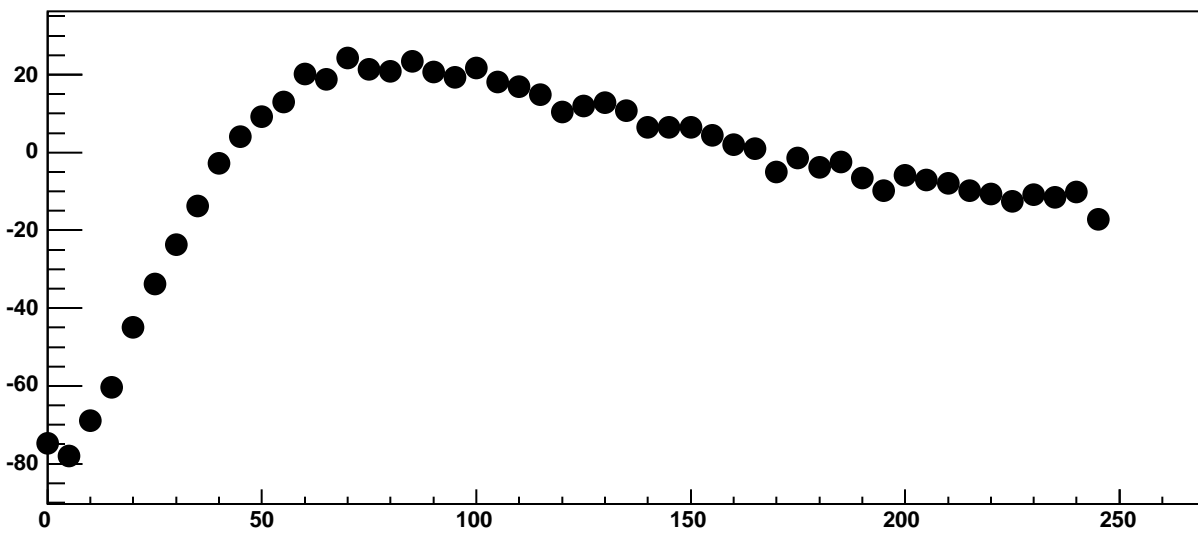


$\chi^2 / \text{ndf}$	6628 / 41
p0	$303.2 \pm 0.5079$
p1	$264.5 \pm 0.2877$
p2	$277.3 \pm 3.833$
p3	$28.01 \pm 0.233$
p4	$0.1287 \pm 0.003348$

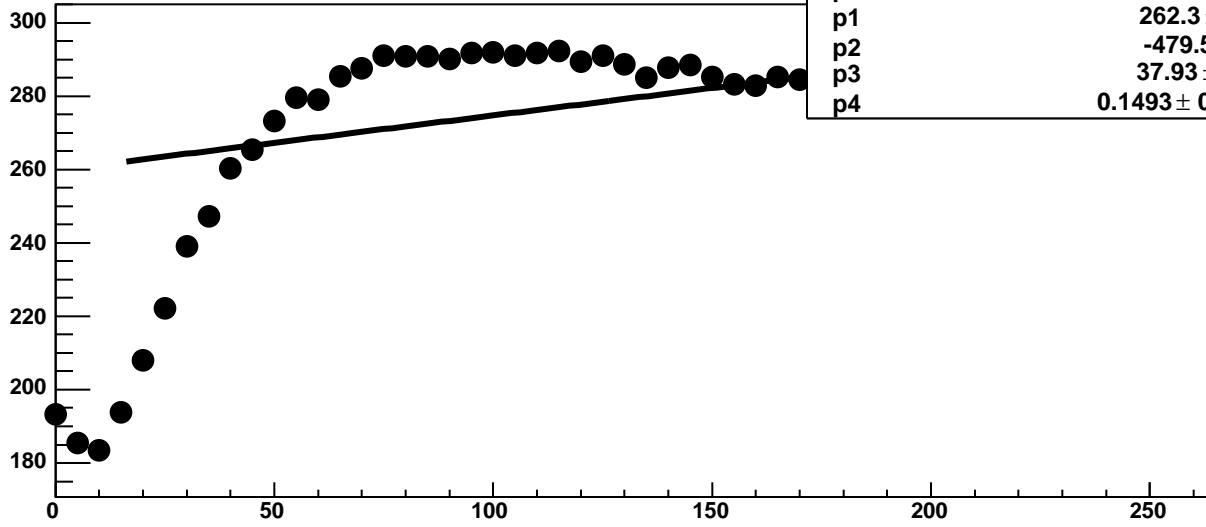
Chip 2, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold



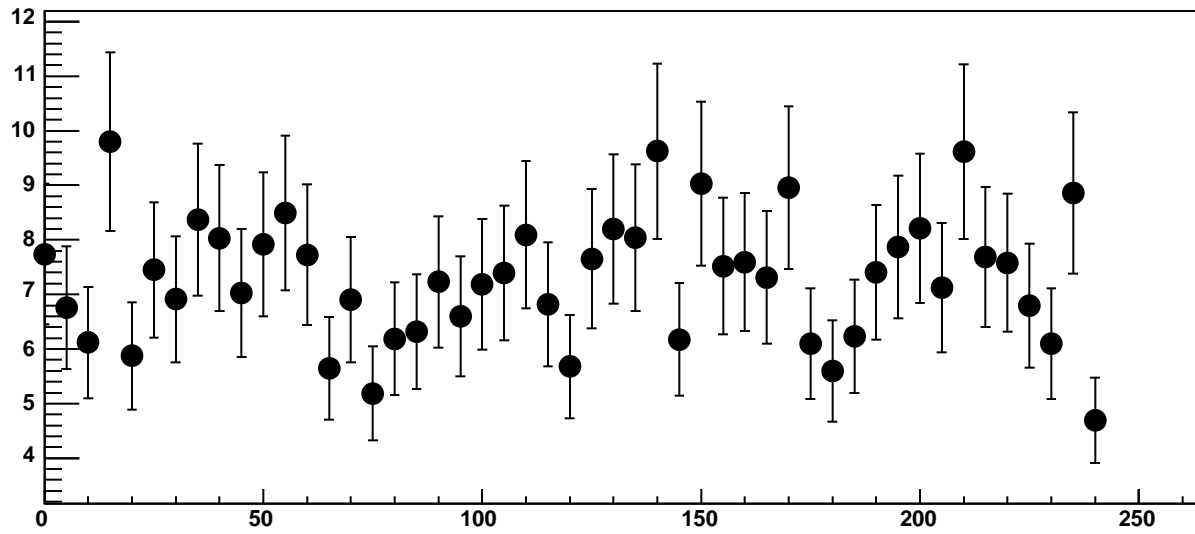
Chip 2, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold



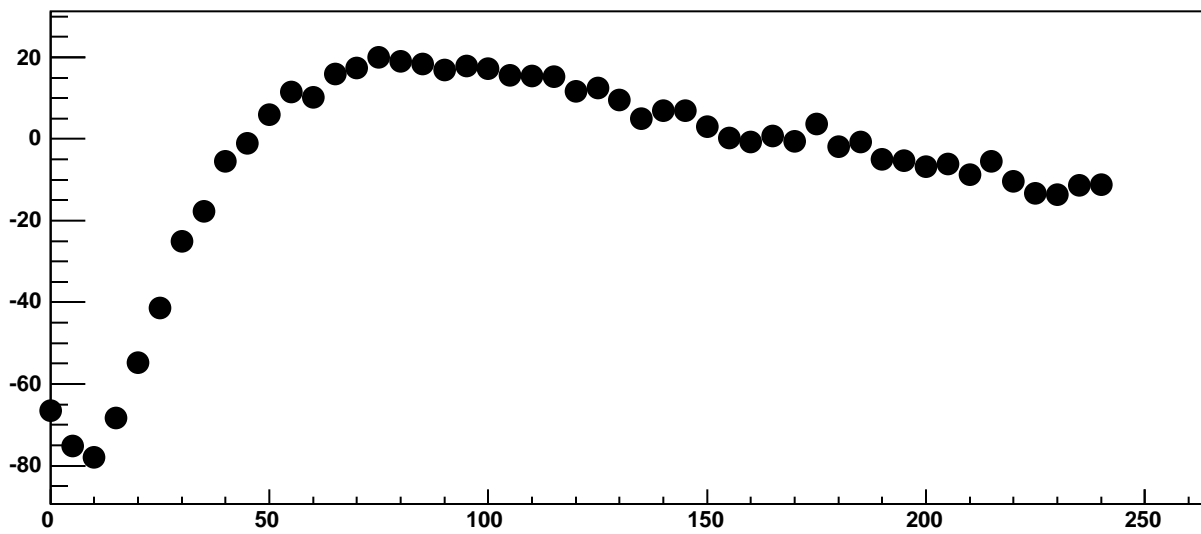
Chip 2, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold



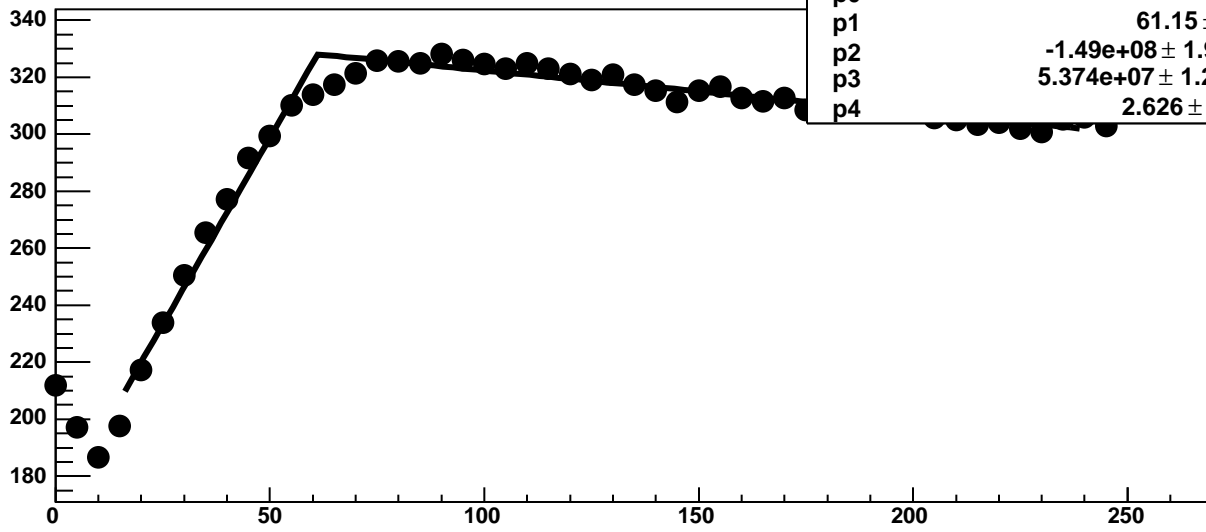
Chip 2, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold

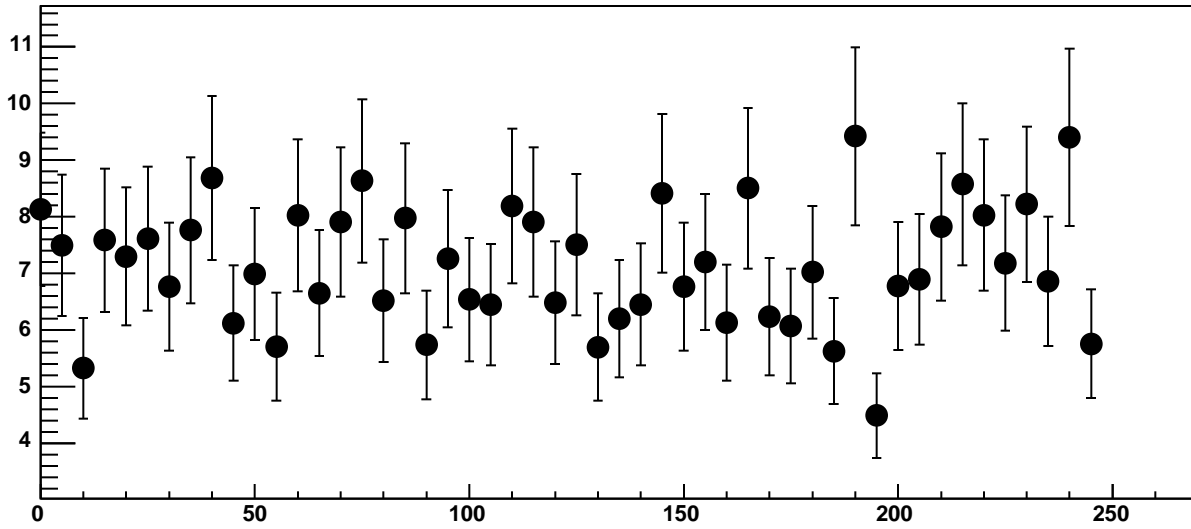


Chip 2, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

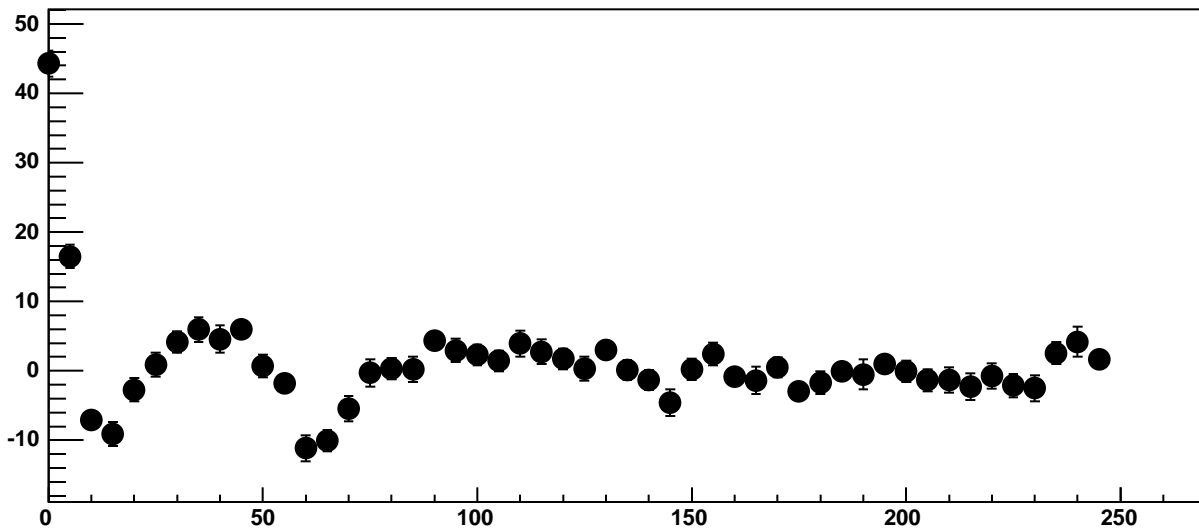


$\chi^2 / \text{ndf}$	222.7 / 41
p0	$328.1 \pm 0.5219$
p1	$61.15 \pm 0.3983$
p2	$-1.49\text{e}+08 \pm 1.992\text{e}+06$
p3	$5.374\text{e}+07 \pm 1.257\text{e}+05$
p4	$2.626 \pm 0.03609$

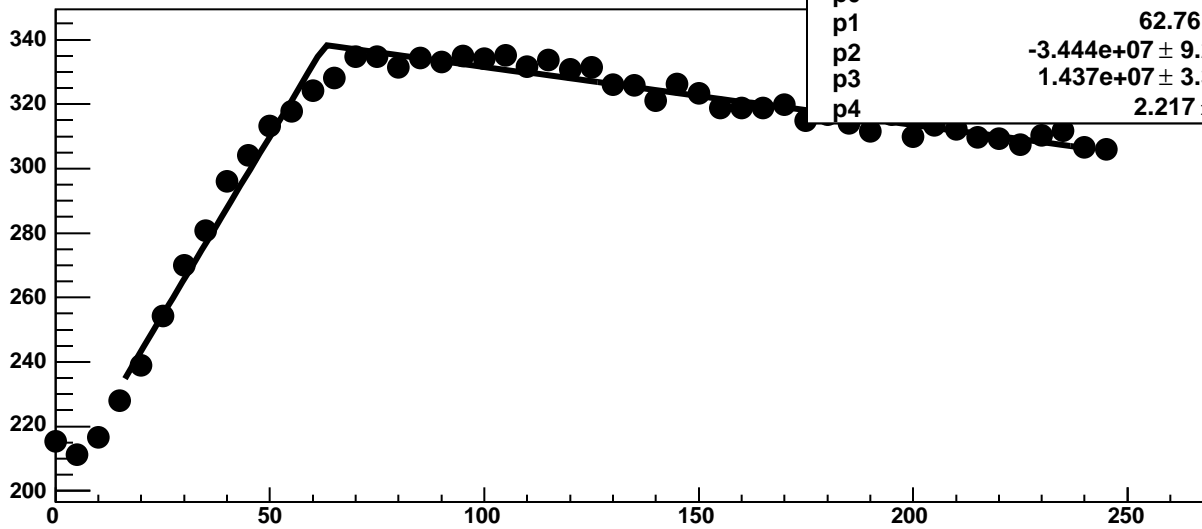
Chip 2, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

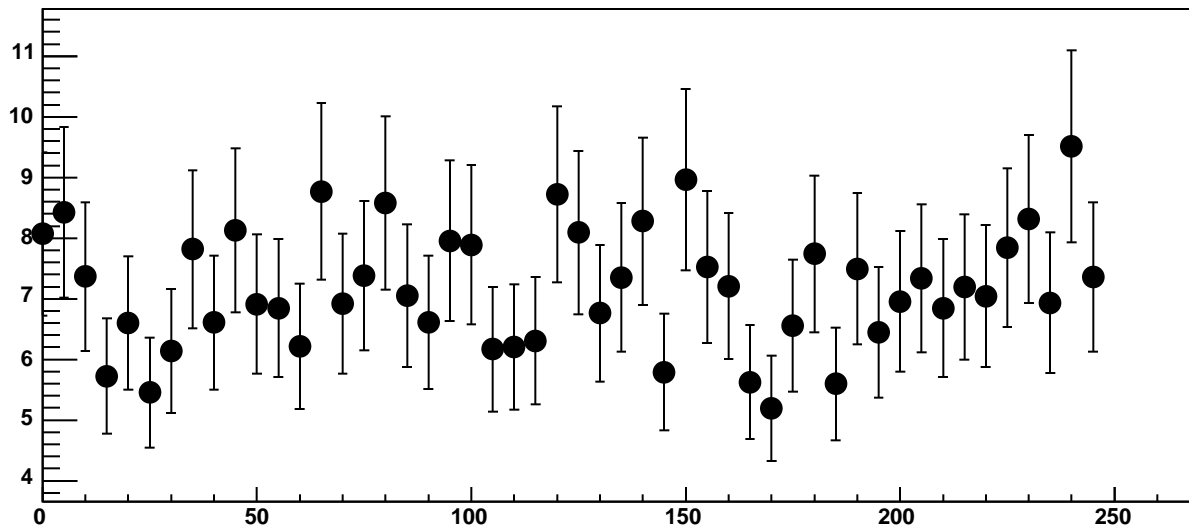


Chip 2, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold

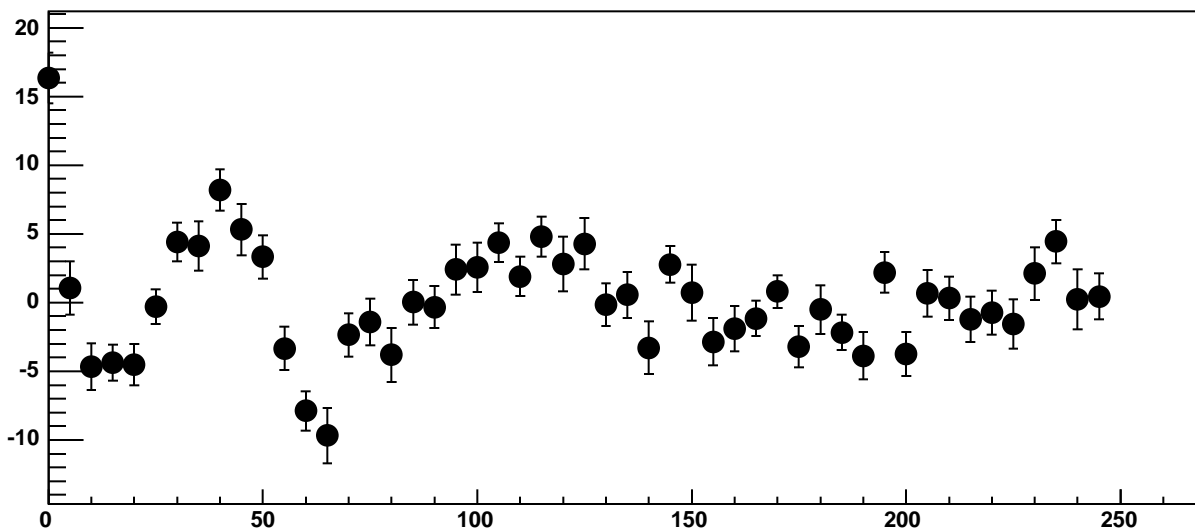


$\chi^2 / \text{ndf}$	219 / 41
p0	$338.3 \pm 0.5249$
p1	$62.76 \pm 0.4671$
p2	$-3.444\text{e}+07 \pm 9.271\text{e}+05$
p3	$1.437\text{e}+07 \pm 3.359\text{e}+05$
p4	$2.217 \pm 0.0316$

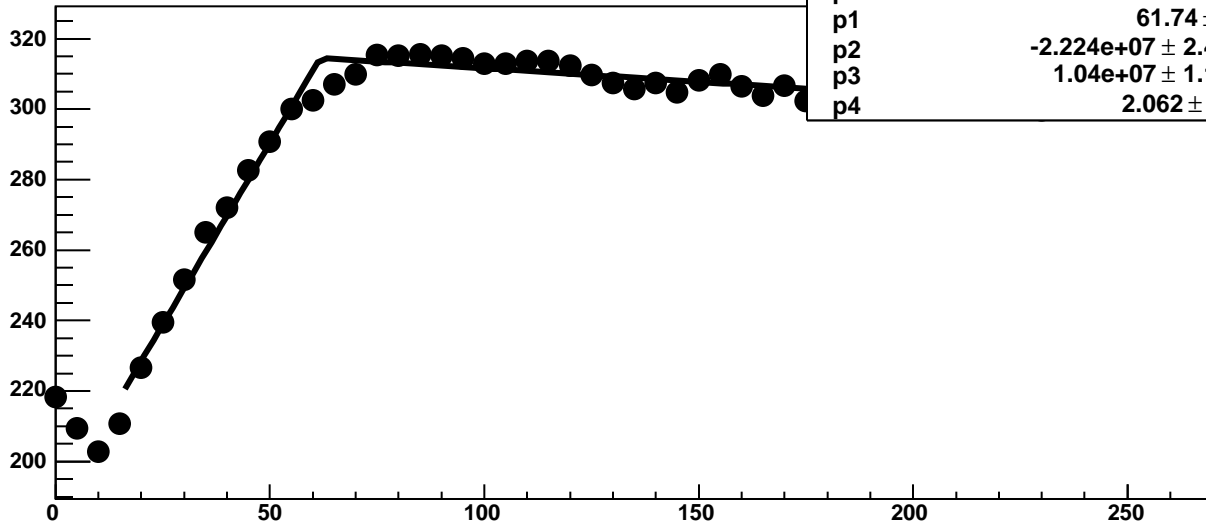
Chip 2, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold

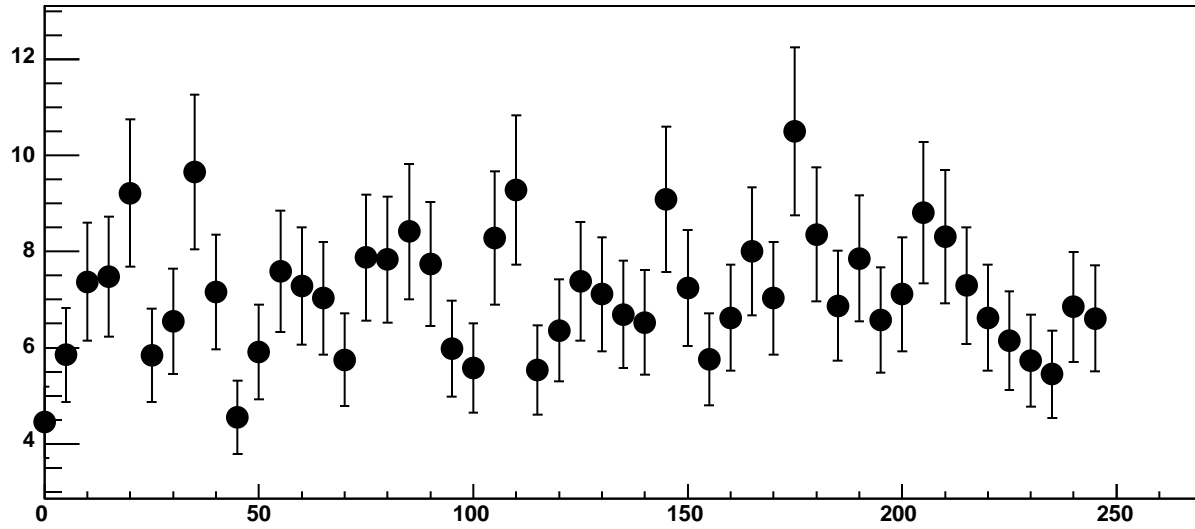


Chip 2, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

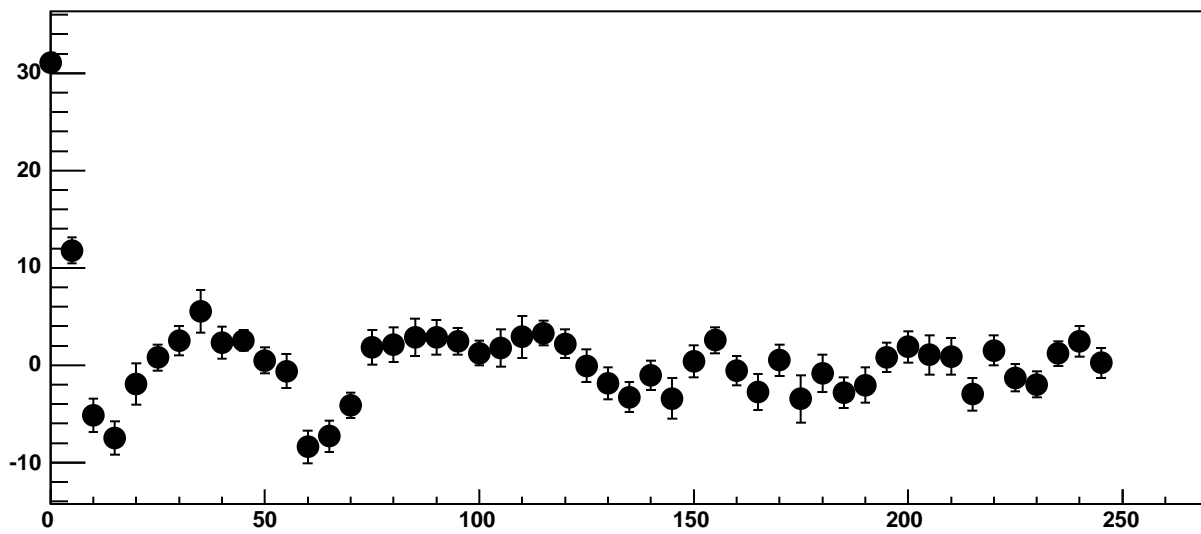


$\chi^2 / \text{ndf}$	150.7 / 41
p0	$314.5 \pm 0.509$
p1	$61.74 \pm 0.5115$
p2	$-2.224\text{e}+07 \pm 2.487\text{e}+06$
p3	$1.04\text{e}+07 \pm 1.161\text{e}+06$
p4	$2.062 \pm 0.03618$

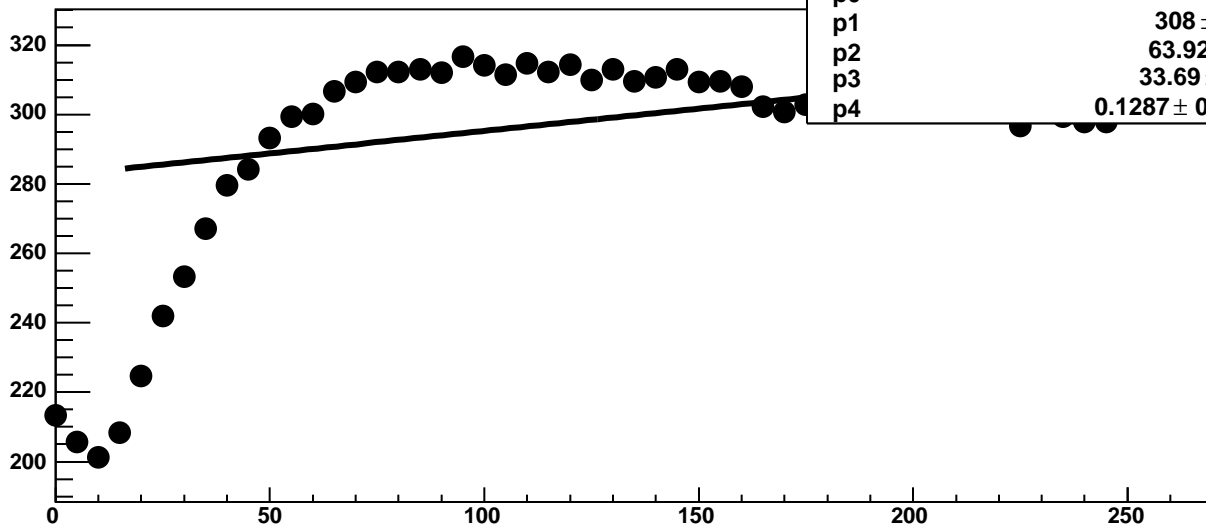
Chip 2, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

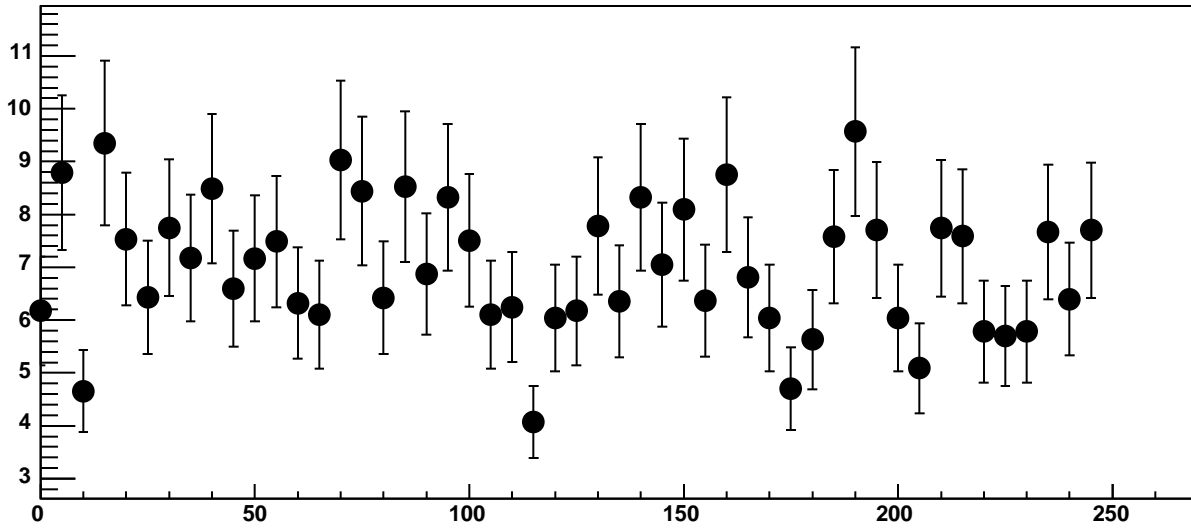


Chip 2, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

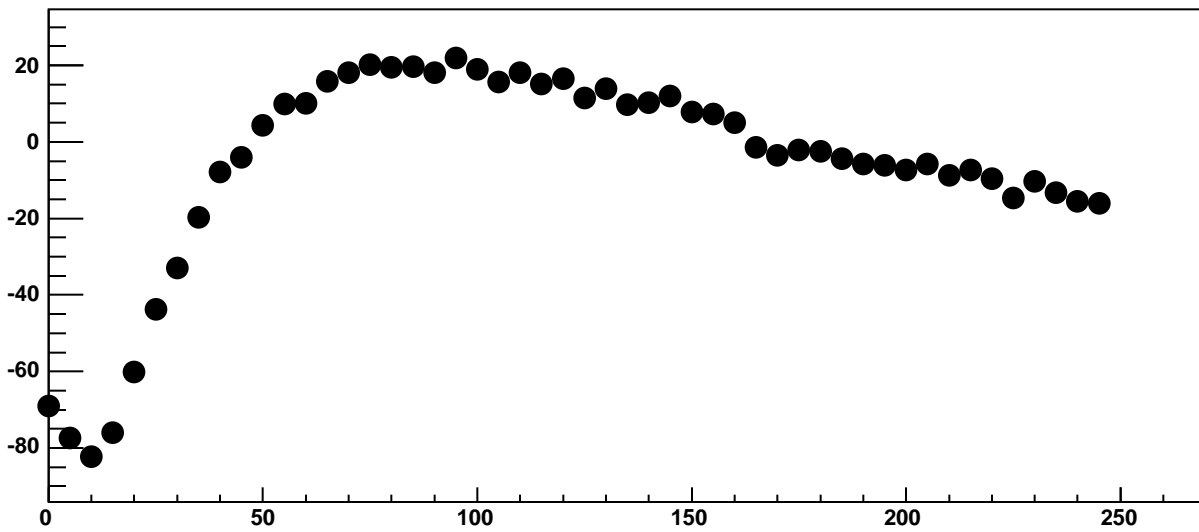


$\chi^2 / \text{ndf}$	6453 / 41
p0	$322 \pm 0.5486$
p1	$308 \pm 0.9646$
p2	$63.92 \pm 3.657$
p3	$33.69 \pm 0.2521$
p4	$0.1287 \pm 0.003494$

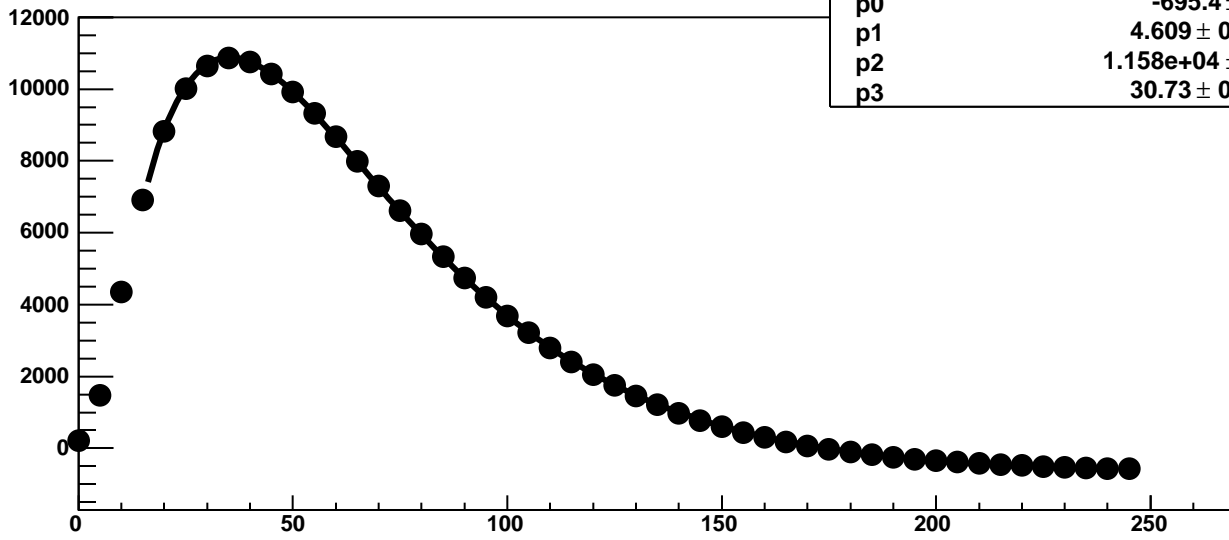
Chip 2, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

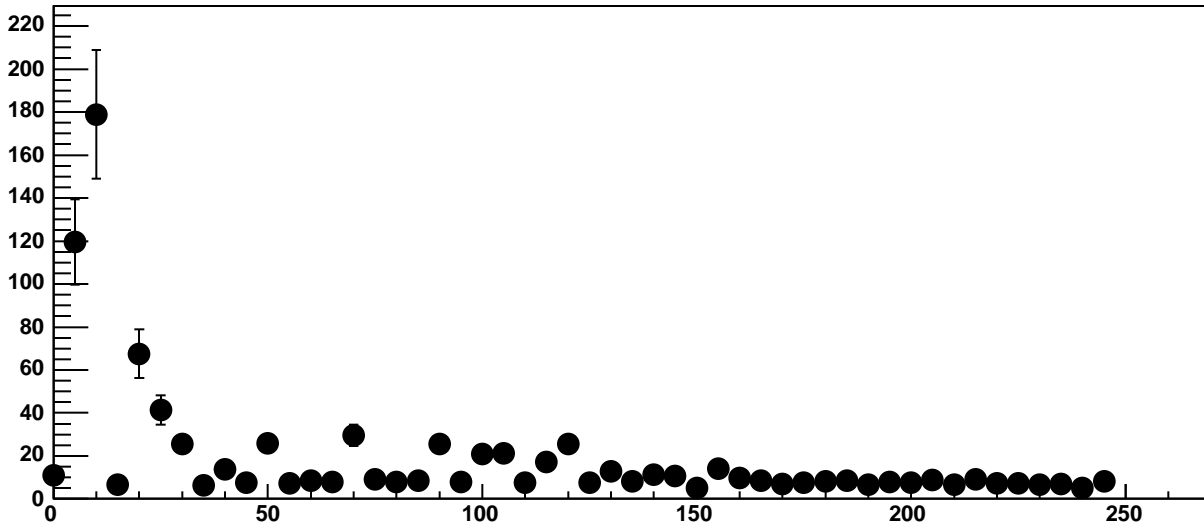


Chip 2, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold

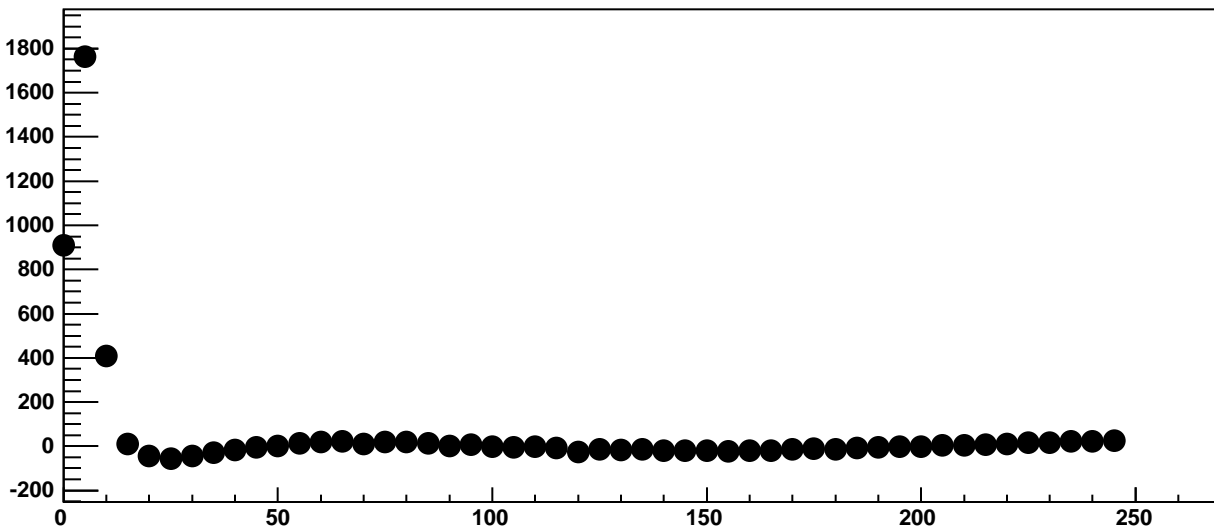


$\chi^2 / \text{ndf}$	2924 / 42
p0	-695.4 ± 0.5278
p1	4.609 ± 0.004246
p2	1.158e+04 ± 0.9189
p3	30.73 ± 0.004249

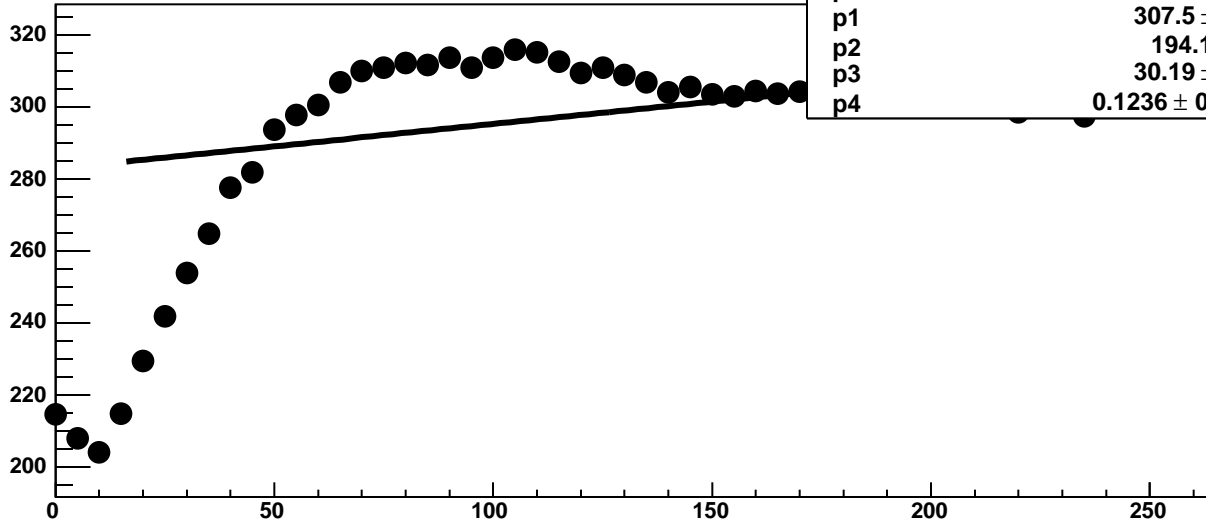
Chip 2, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold

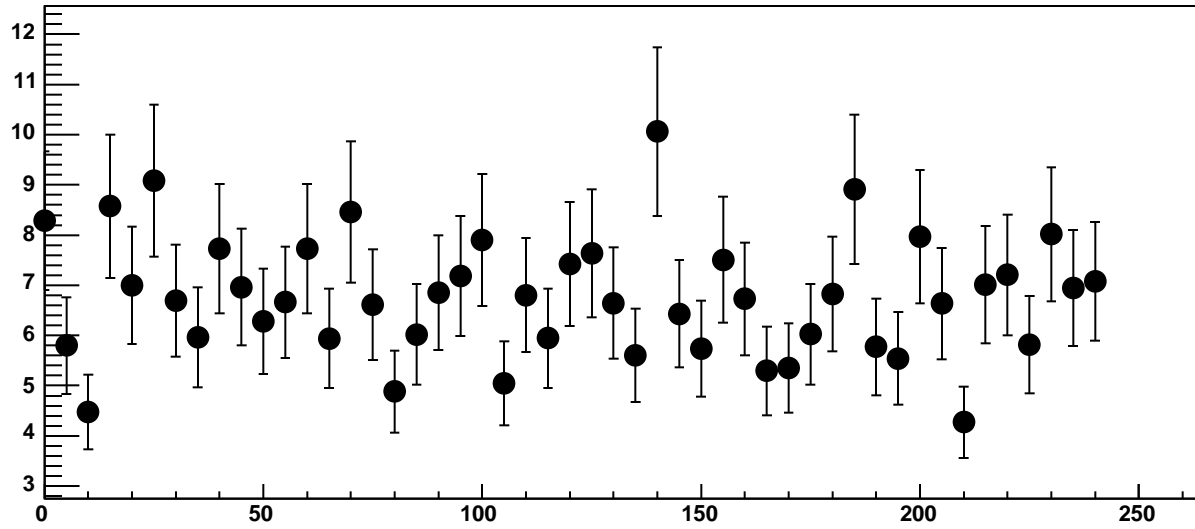


Chip 2, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold

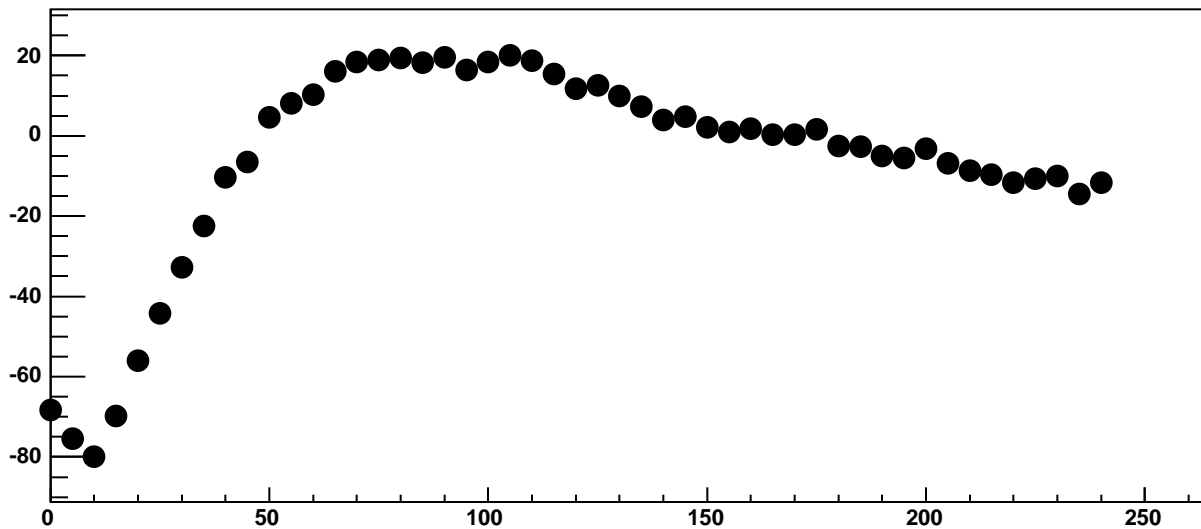


$\chi^2 / \text{ndf}$	6203 / 41
p0	$320.9 \pm 0.5572$
p1	$307.5 \pm 0.8976$
p2	$194.1 \pm 3.309$
p3	$30.19 \pm 0.2264$
p4	$0.1236 \pm 0.003423$

Chip 2, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold

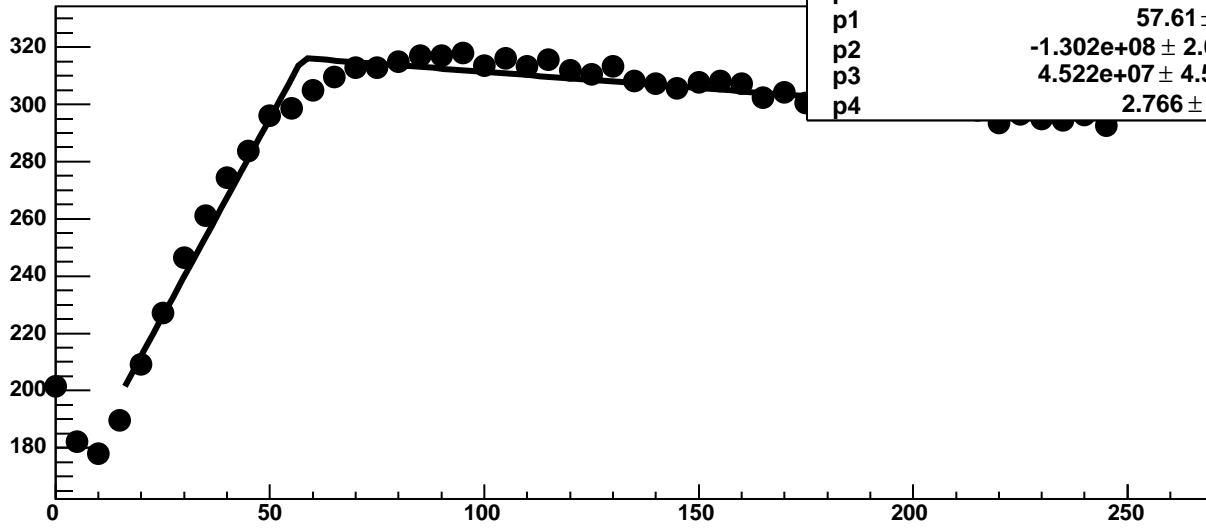


Chip 2, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold



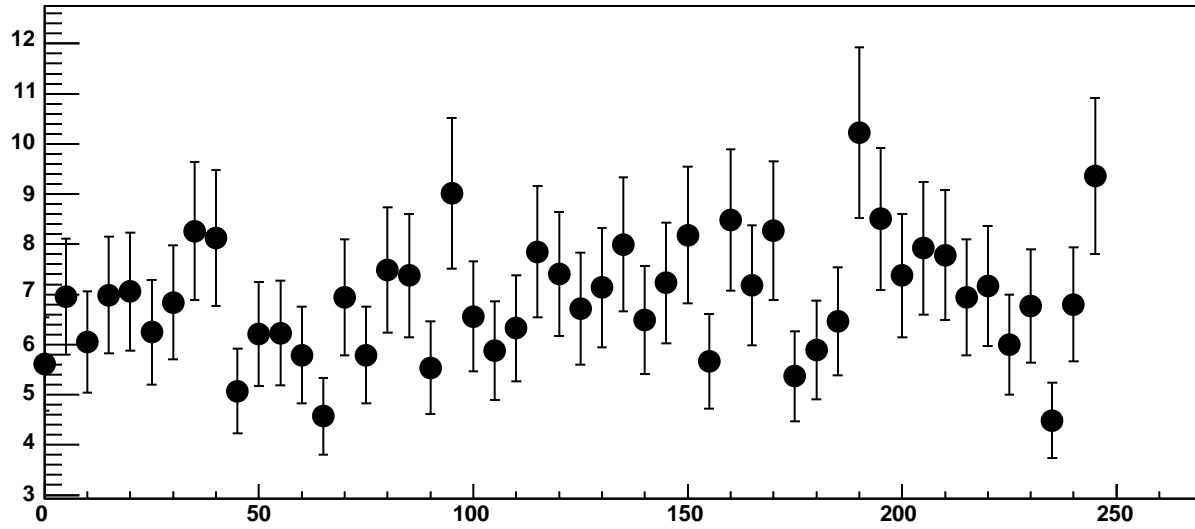


Chip 2, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold

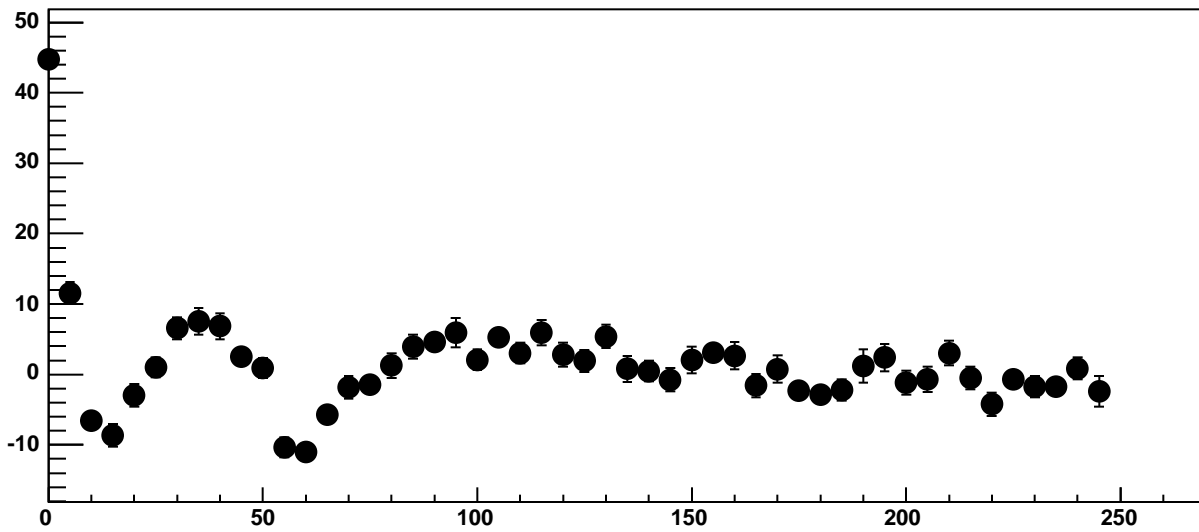


$\chi^2 / \text{ndf}$	349.7 / 41
p0	$316.2 \pm 0.4514$
p1	$57.61 \pm 0.3675$
p2	$-1.302\text{e}+08 \pm 2.095\text{e}+06$
p3	$4.522\text{e}+07 \pm 4.548\text{e}+05$
p4	$2.766 \pm 0.03828$

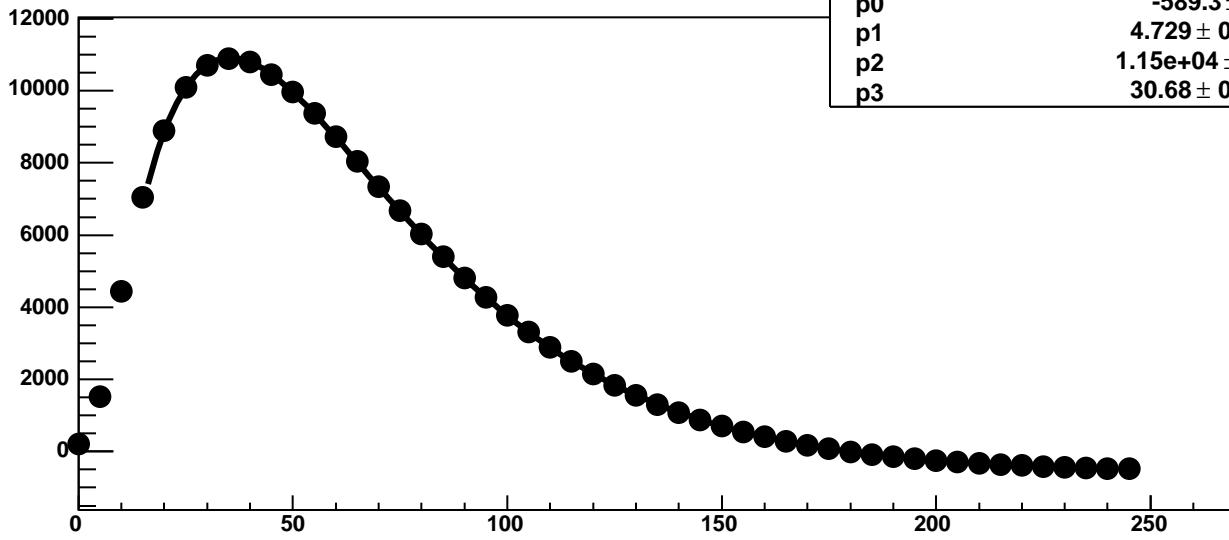
Chip 2, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

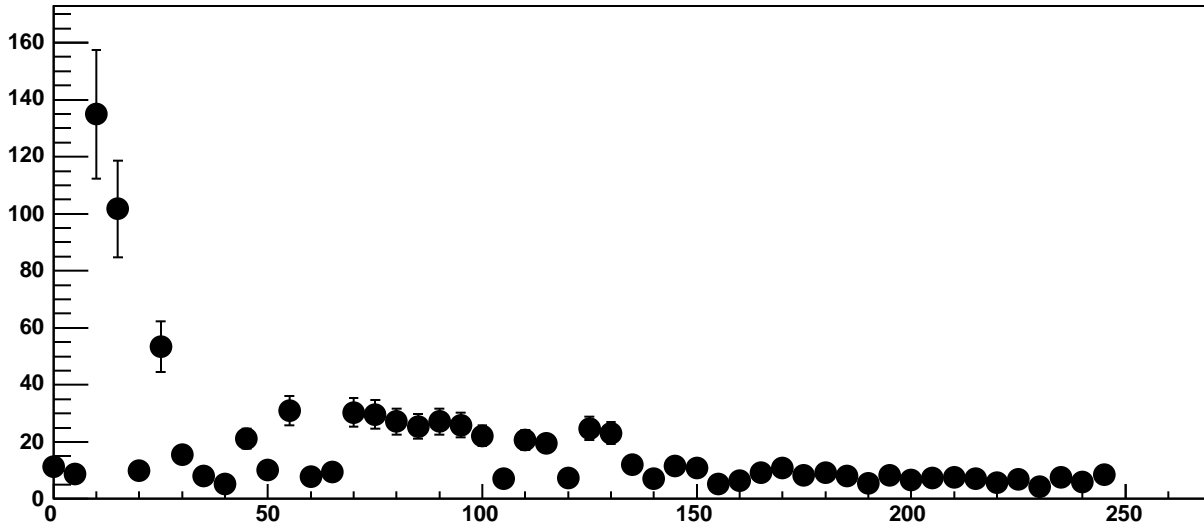


Chip 2, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold

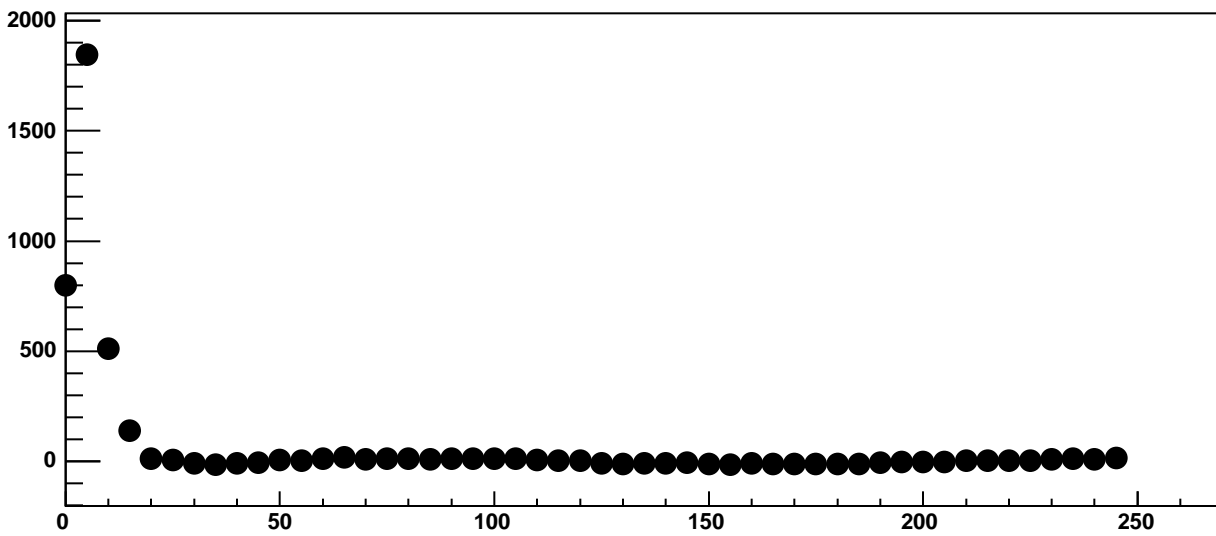


$\chi^2 / \text{ndf}$	1007 / 42
p0	-589.3 ± 0.5217
p1	4.729 ± 0.008458
p2	1.15e+04 ± 0.8925
p3	30.68 ± 0.005417

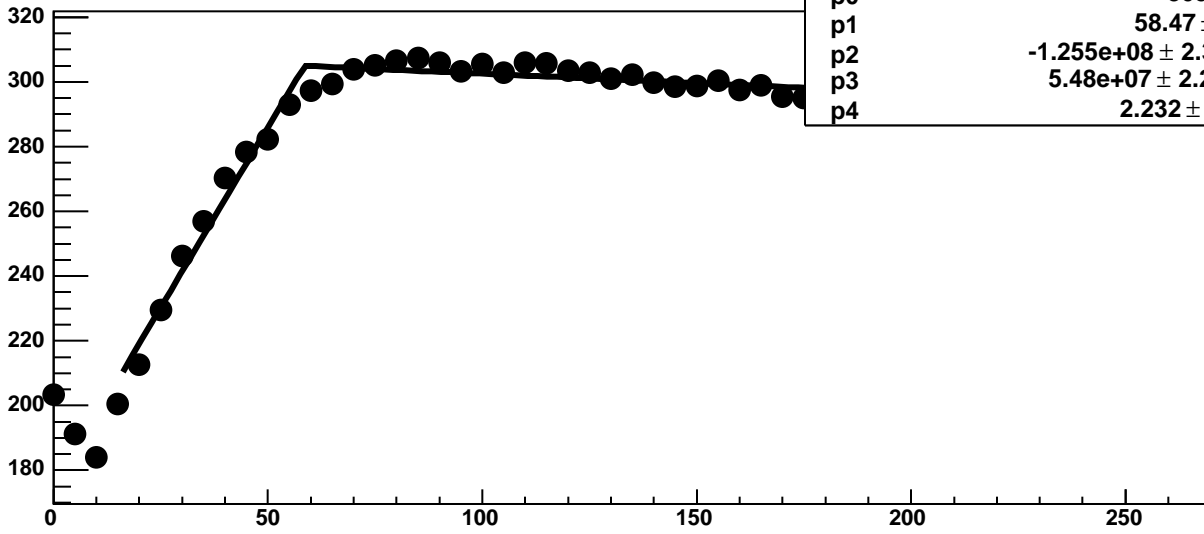
Chip 2, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold

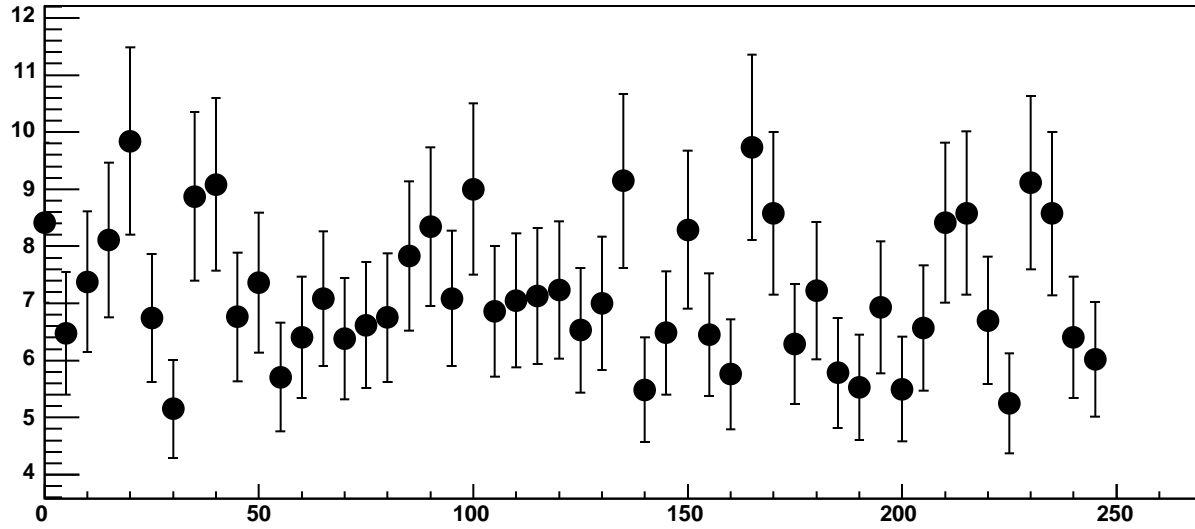


Chip 2, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

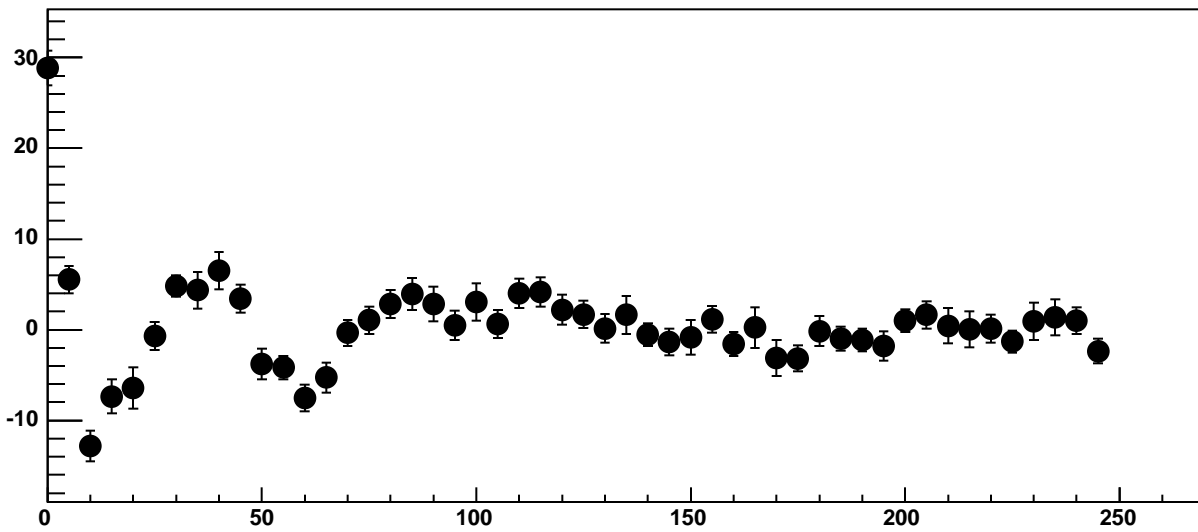


$\chi^2 / \text{ndf}$	158.9 / 41
p0	$305 \pm 0.513$
p1	$58.47 \pm 0.5238$
p2	$-1.255\text{e}+08 \pm 2.366\text{e}+06$
p3	$5.48\text{e}+07 \pm 2.245\text{e}+05$
p4	$2.232 \pm 0.04246$

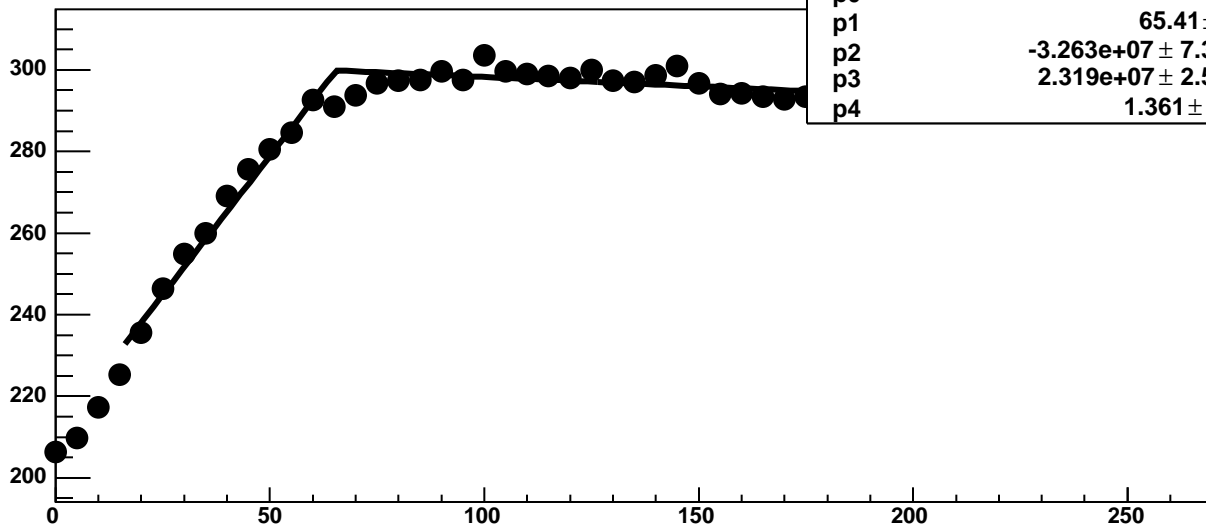
Chip 2, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

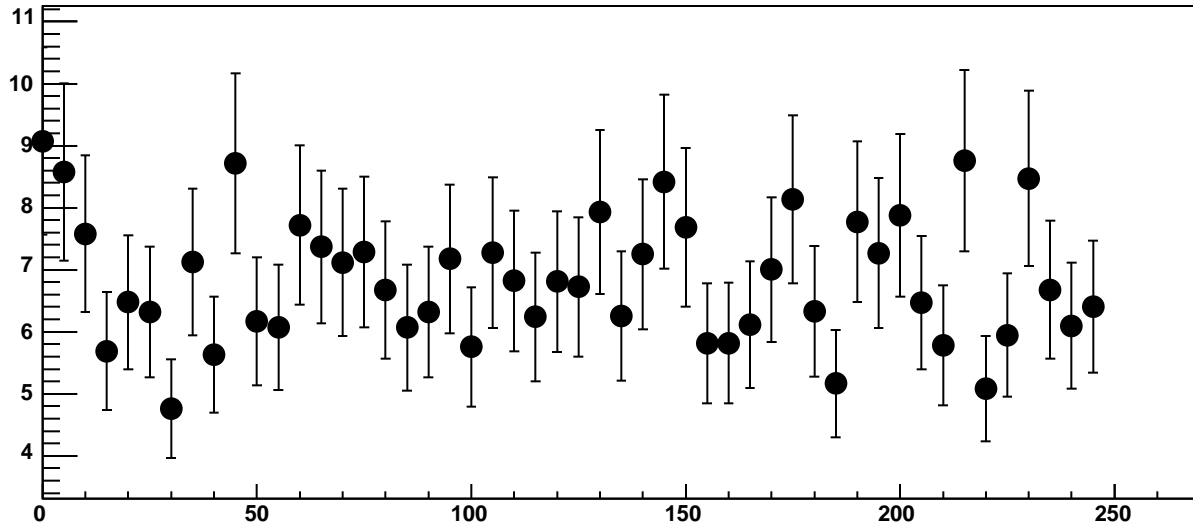


Chip 2, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

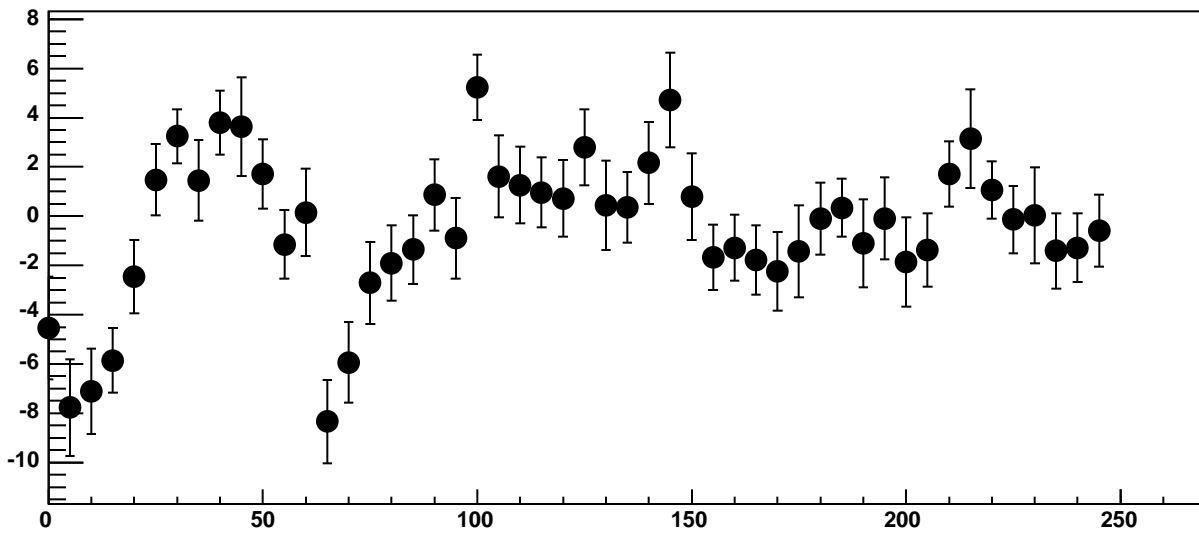


$\chi^2 / \text{ndf}$	135.8 / 41
p0	$299.8 \pm 0.5097$
p1	$65.41 \pm 0.7403$
p2	$-3.263\text{e}+07 \pm 7.345\text{e}+05$
p3	$2.319\text{e}+07 \pm 2.538\text{e}+05$
p4	$1.361 \pm 0.02839$

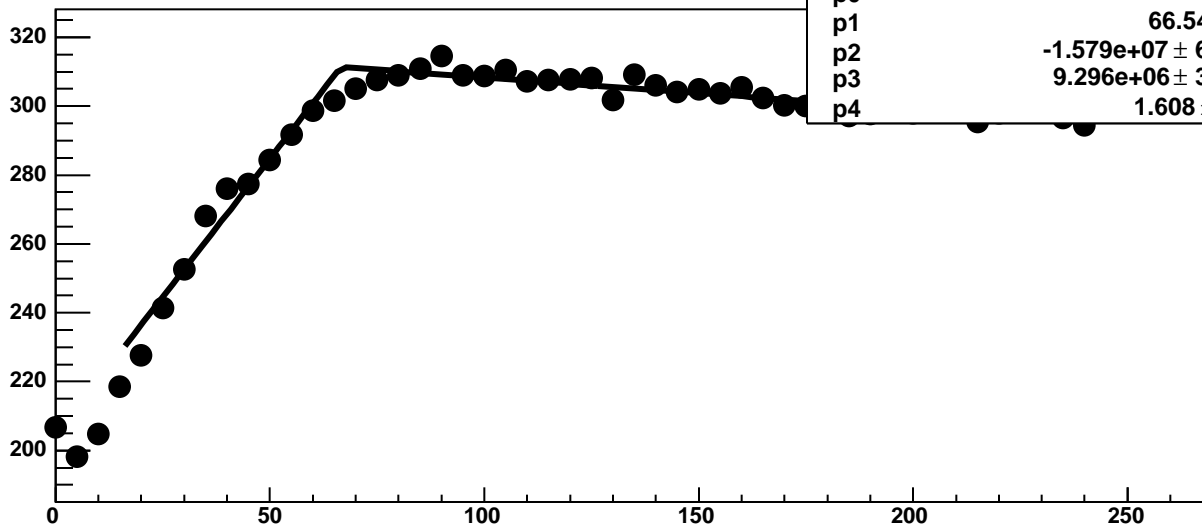
Chip 2, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

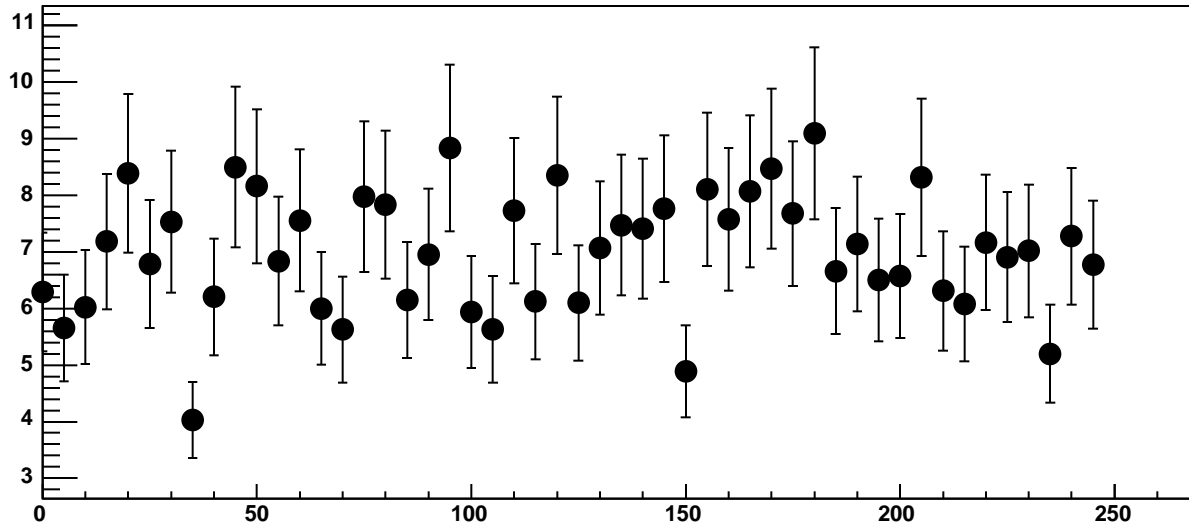


Chip 2, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold

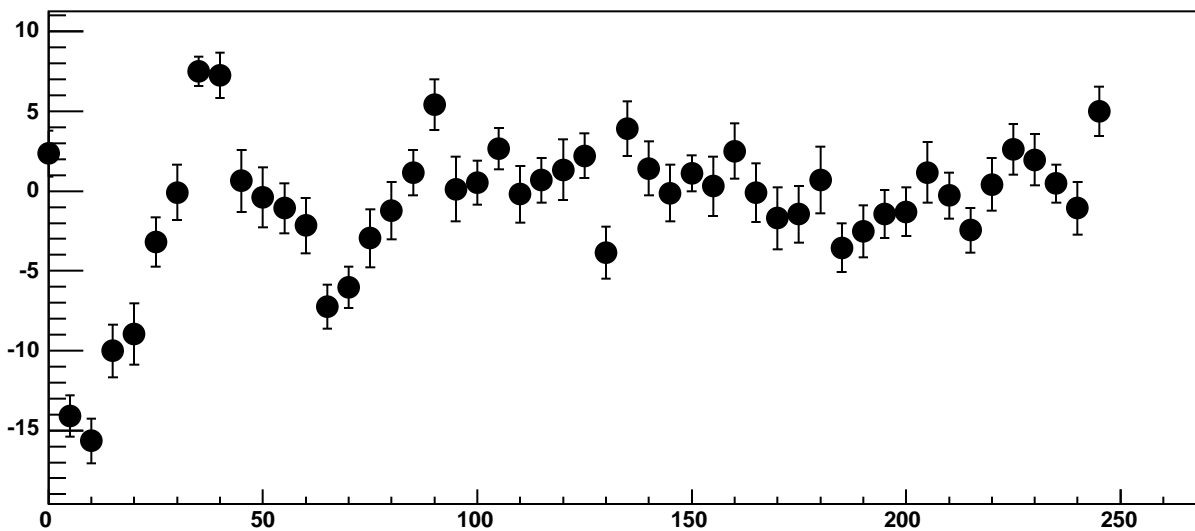


$\chi^2 / \text{ndf}$	263.4 / 41
p0	$311.4 \pm 0.4986$
p1	$66.54 \pm 0.626$
p2	$-1.579\text{e}+07 \pm 6.98\text{e}+05$
p3	$9.296\text{e}+06 \pm 3.78\text{e}+05$
p4	$1.608 \pm 0.0304$

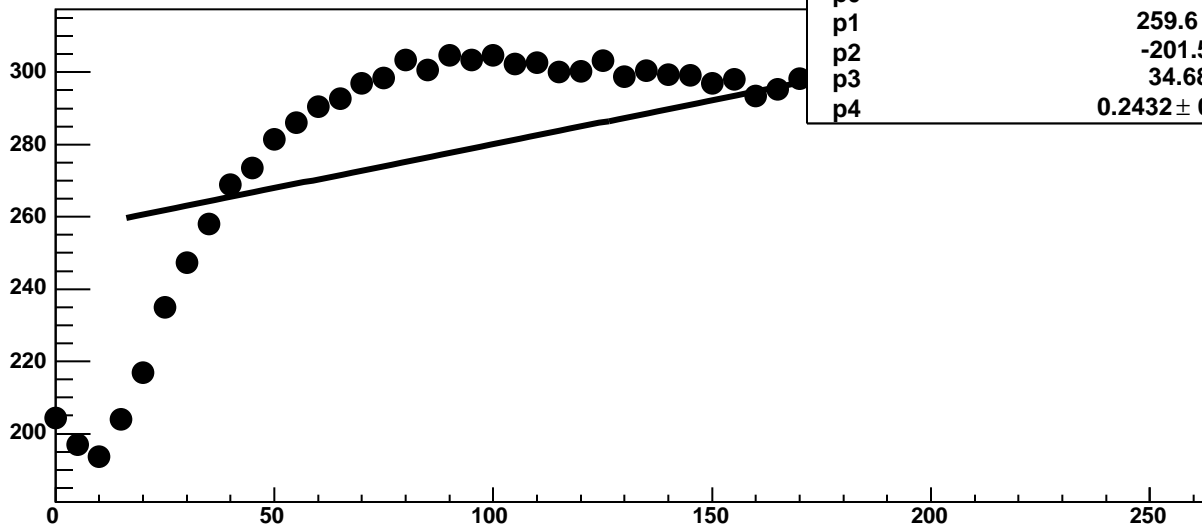
Chip 2, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold

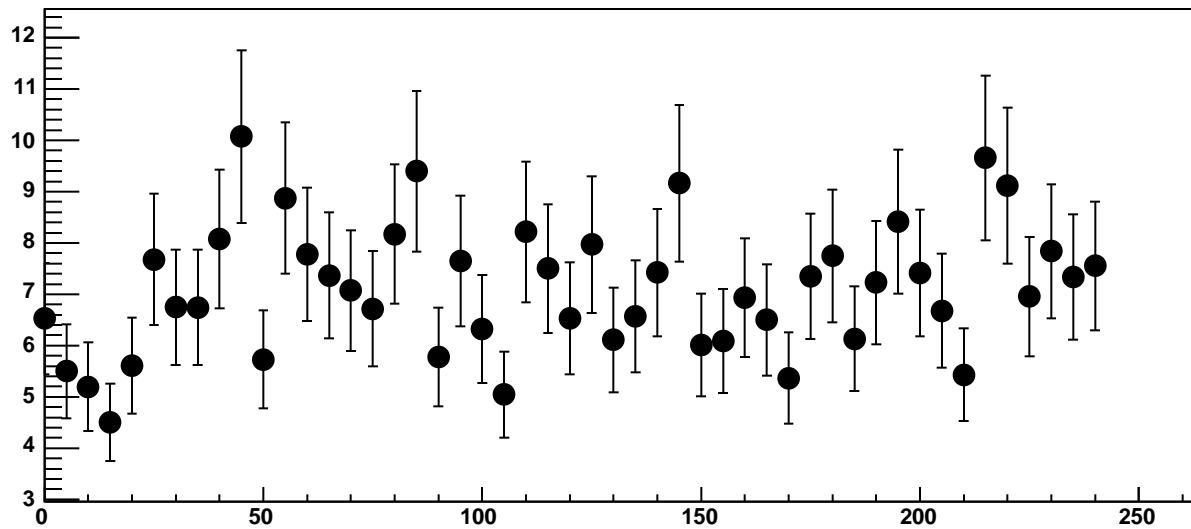


Chip 2, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

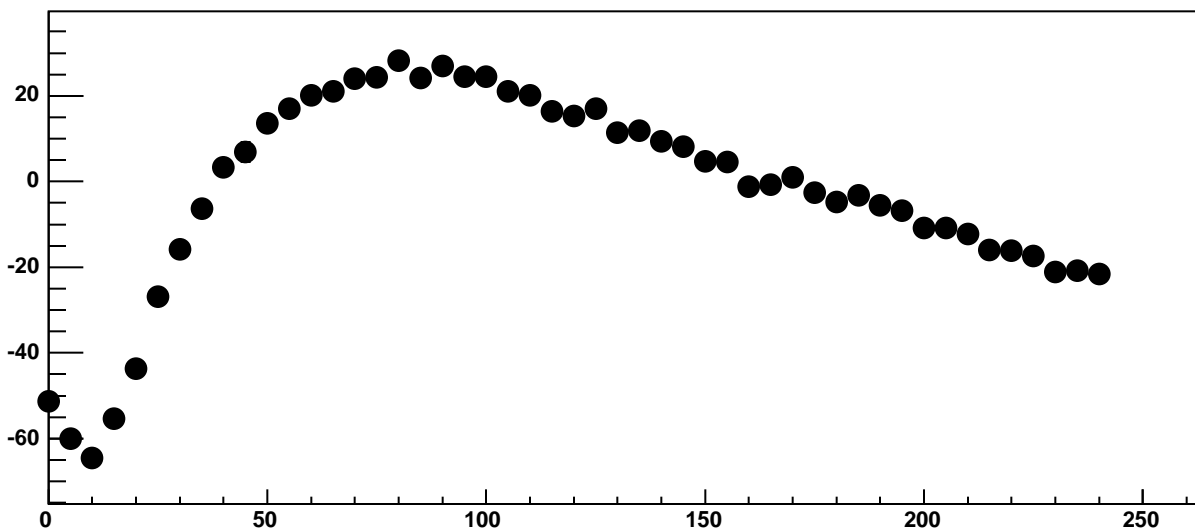


$\chi^2 / \text{ndf}$	8387 / 41
p0	$318.9 \pm 0.2322$
p1	$259.6 \pm 0.9547$
p2	$-201.5 \pm 84.85$
p3	$34.68 \pm 1.414$
p4	$0.2432 \pm 0.001532$

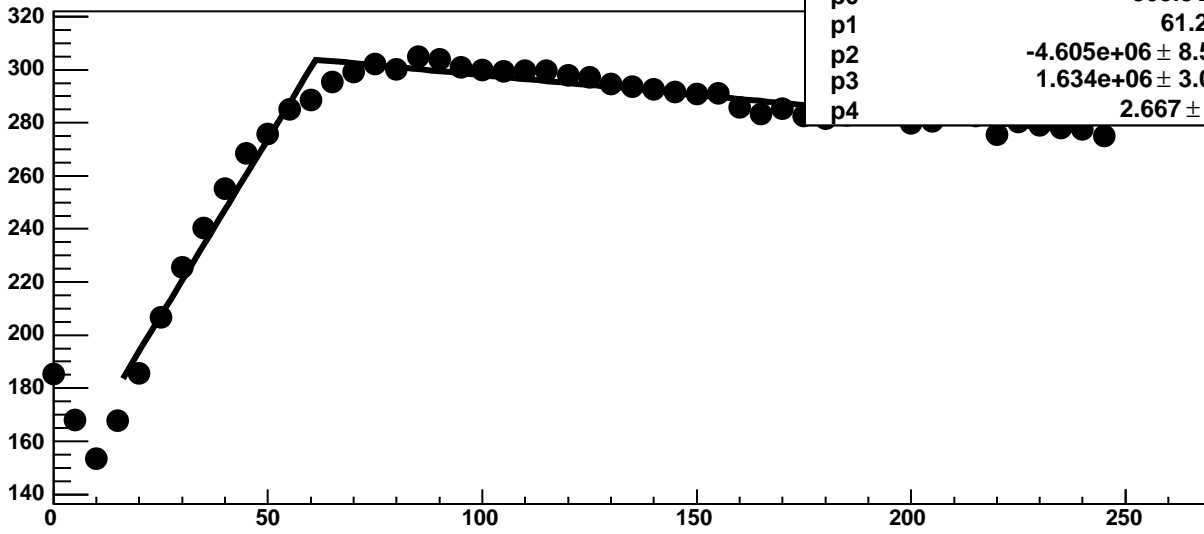
Chip 2, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

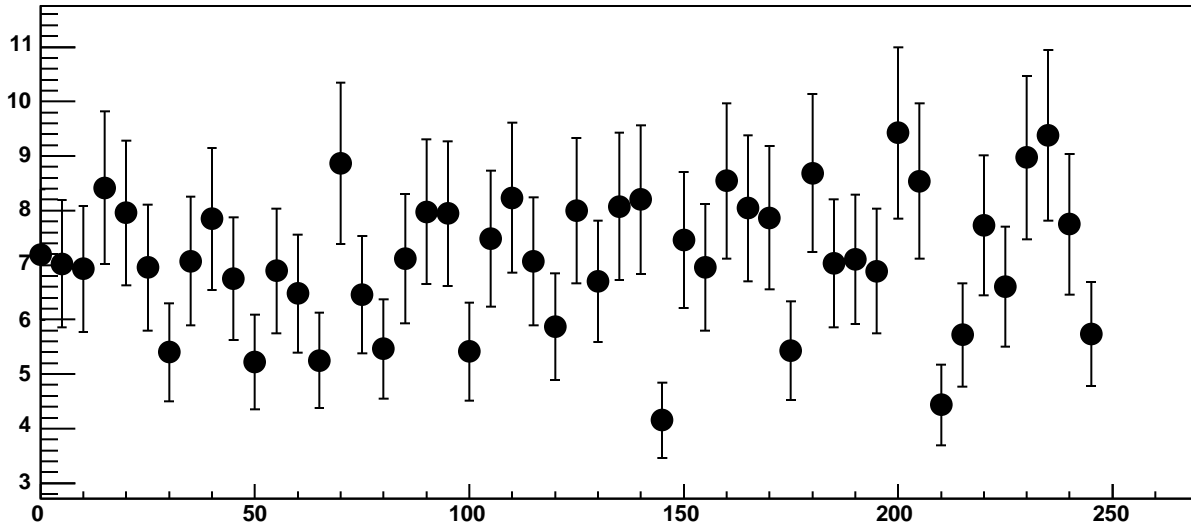


Chip 2, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

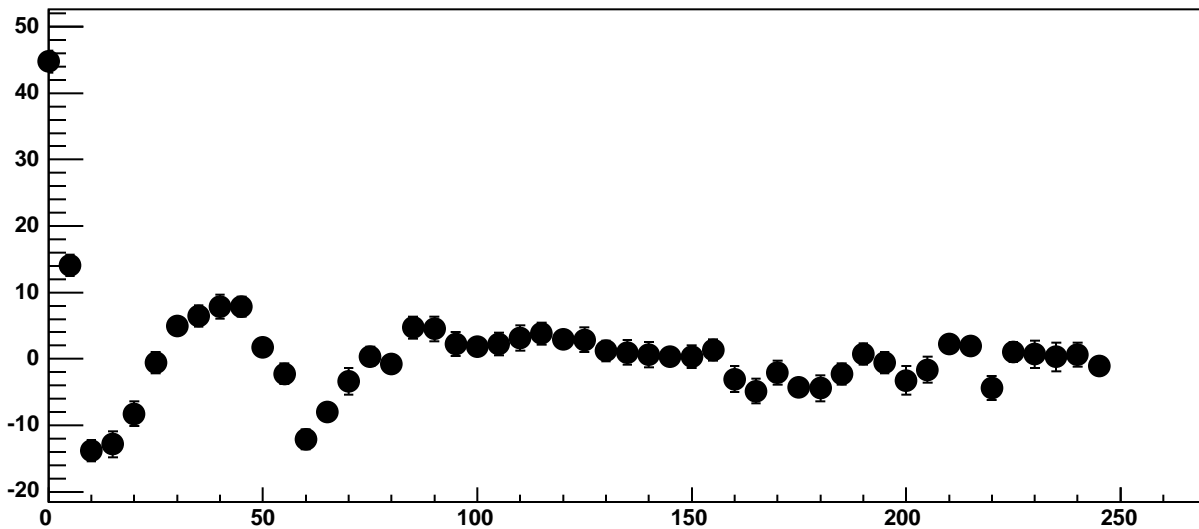


$\chi^2 / \text{ndf}$	342.7 / 41
p0	$303.8 \pm 0.4896$
p1	$61.2 \pm 0.372$
p2	$-4.605e+06 \pm 8.528e+05$
p3	$1.634e+06 \pm 3.029e+05$
p4	$2.667 \pm 0.03539$

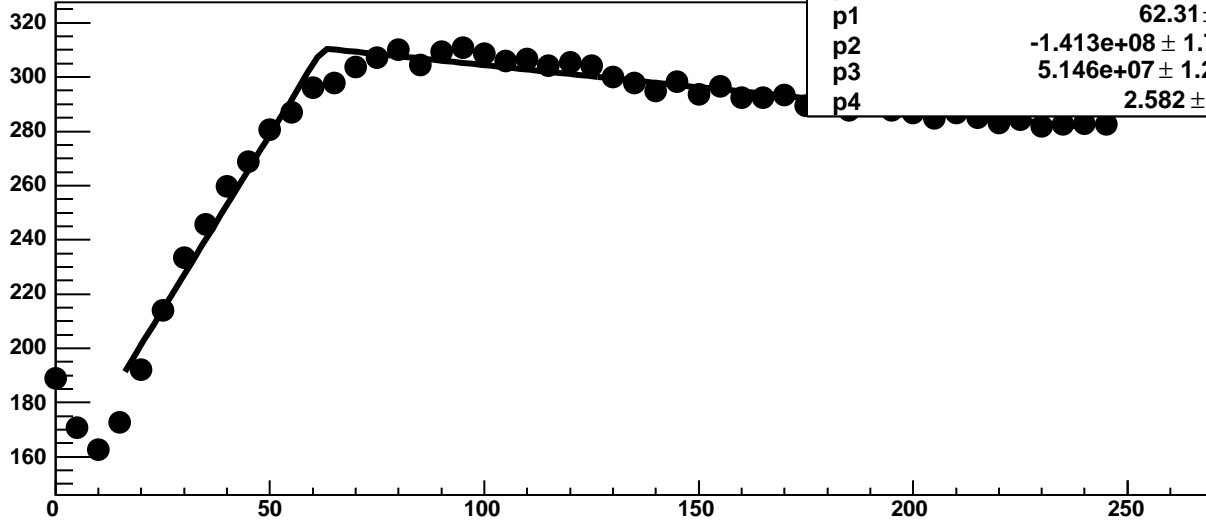
Chip 2, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold

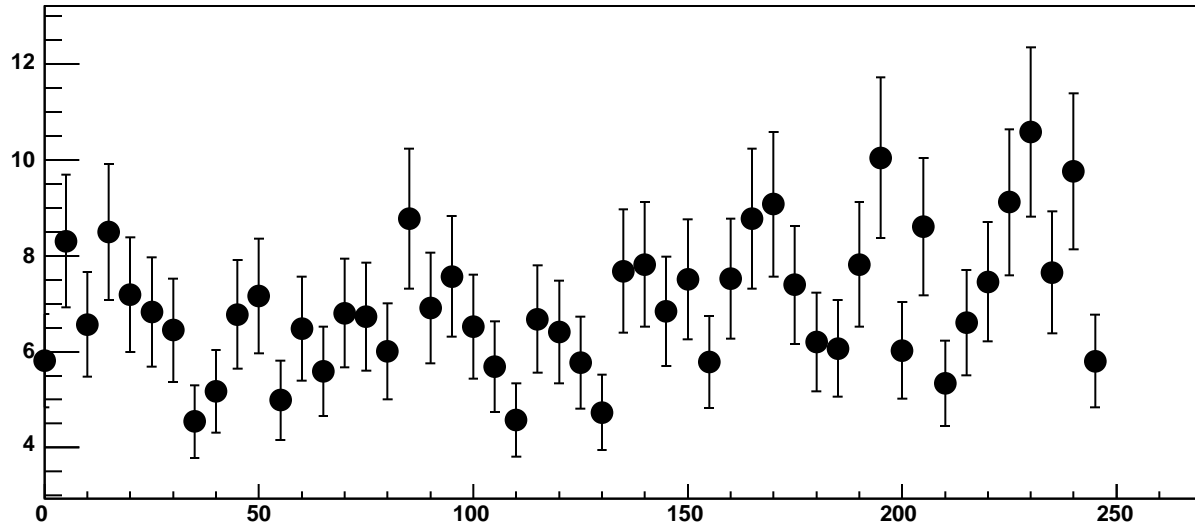


Chip 2, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold

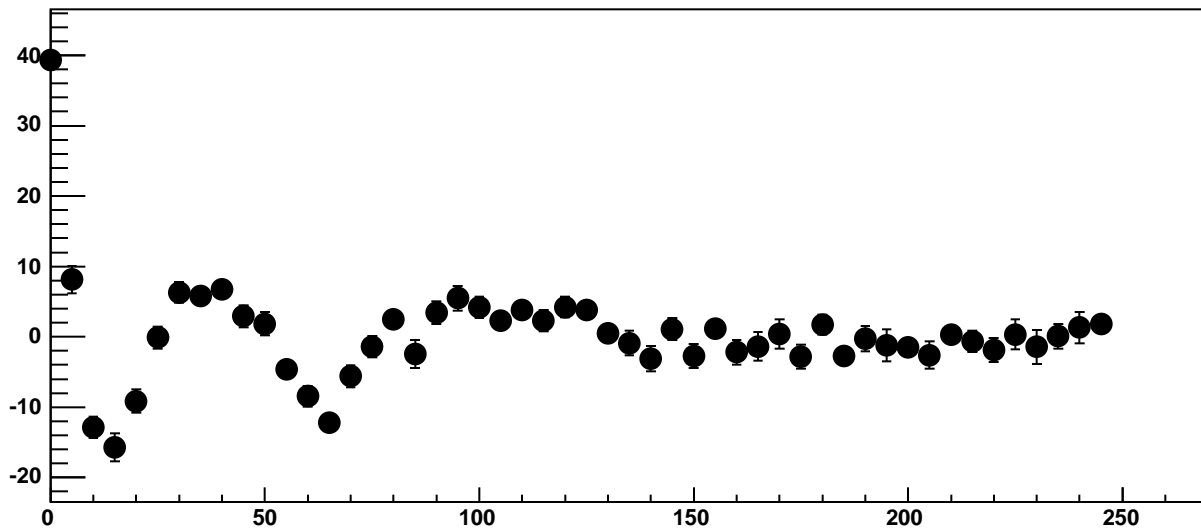


$\chi^2 / \text{ndf}$	418.6 / 41
p0	$310.6 \pm 0.4725$
p1	$62.31 \pm 0.3729$
p2	$-1.413\text{e}+08 \pm 1.775\text{e}+06$
p3	$5.146\text{e}+07 \pm 1.272\text{e}+05$
p4	$2.582 \pm 0.03391$

Chip 2, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold

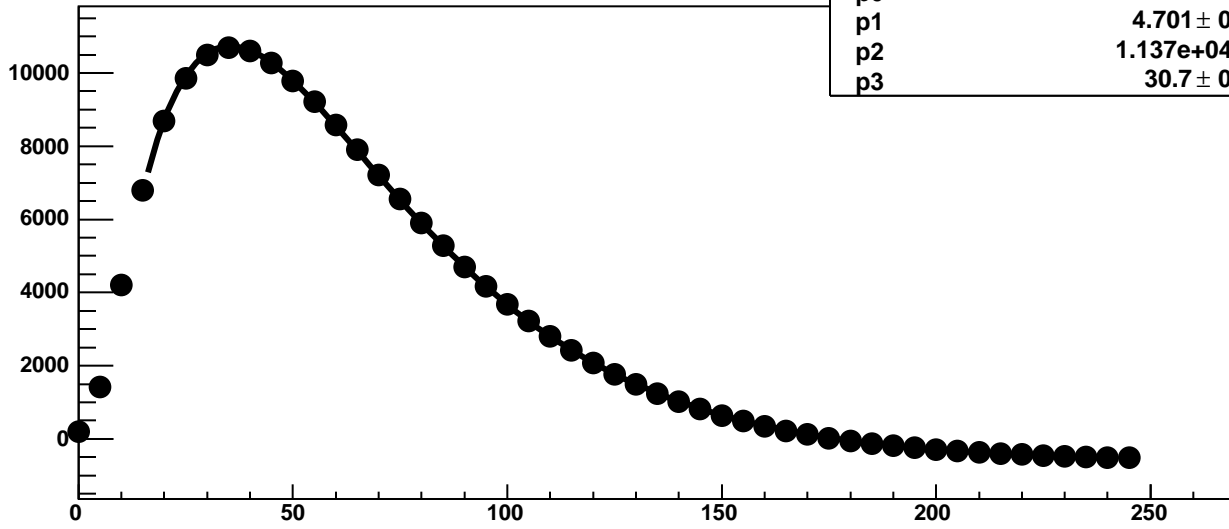


Chip 2, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold



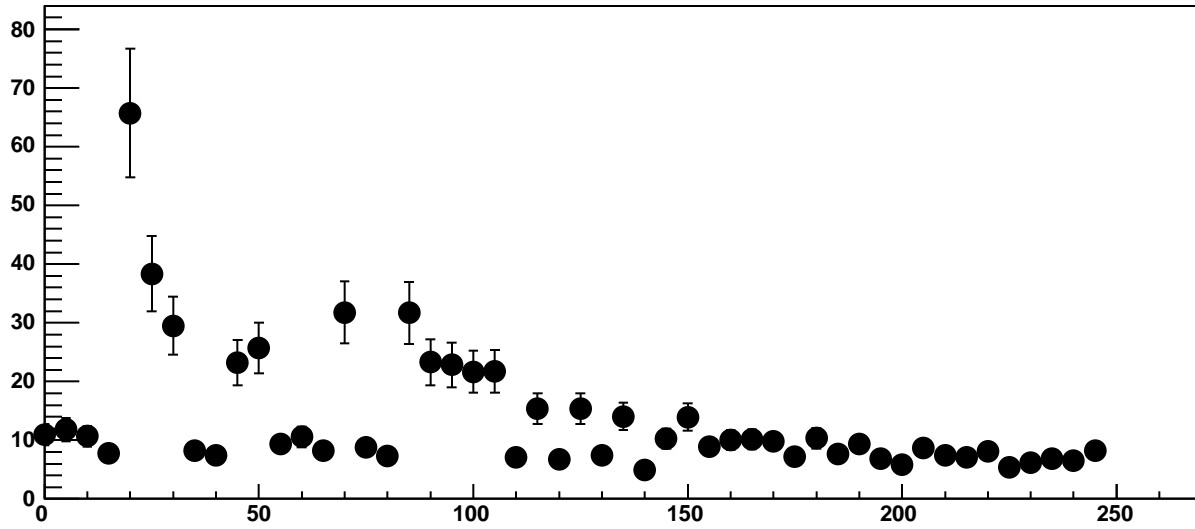


Chip 2, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold

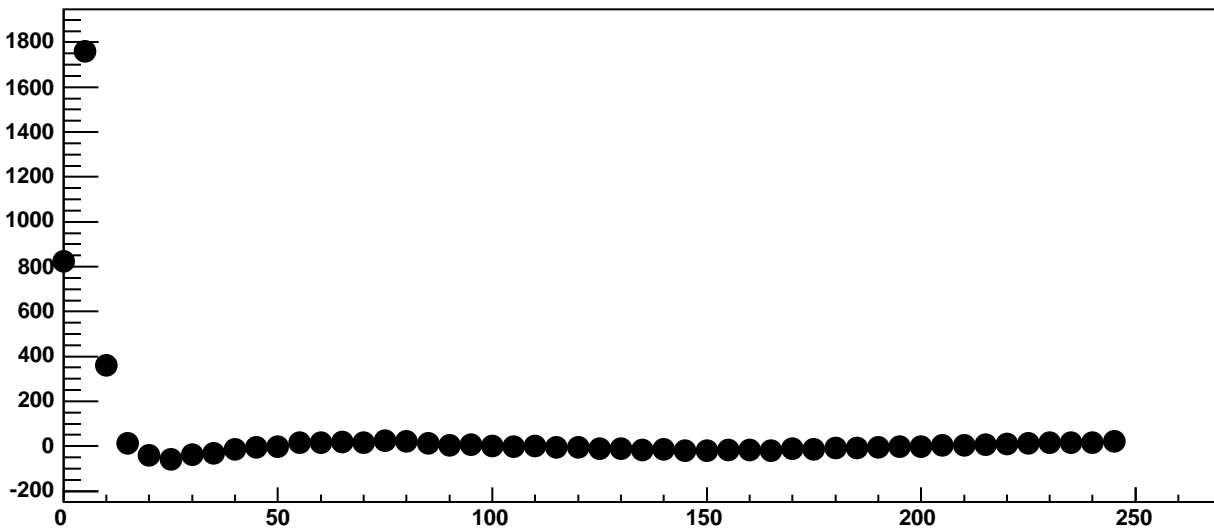


$\chi^2 / \text{ndf}$	2096 / 42
p0	$-633.6 \pm 0.5296$
p1	$4.701 \pm 0.004828$
p2	$1.137\text{e}+04 \pm 1.025$
p3	$30.7 \pm 0.004516$

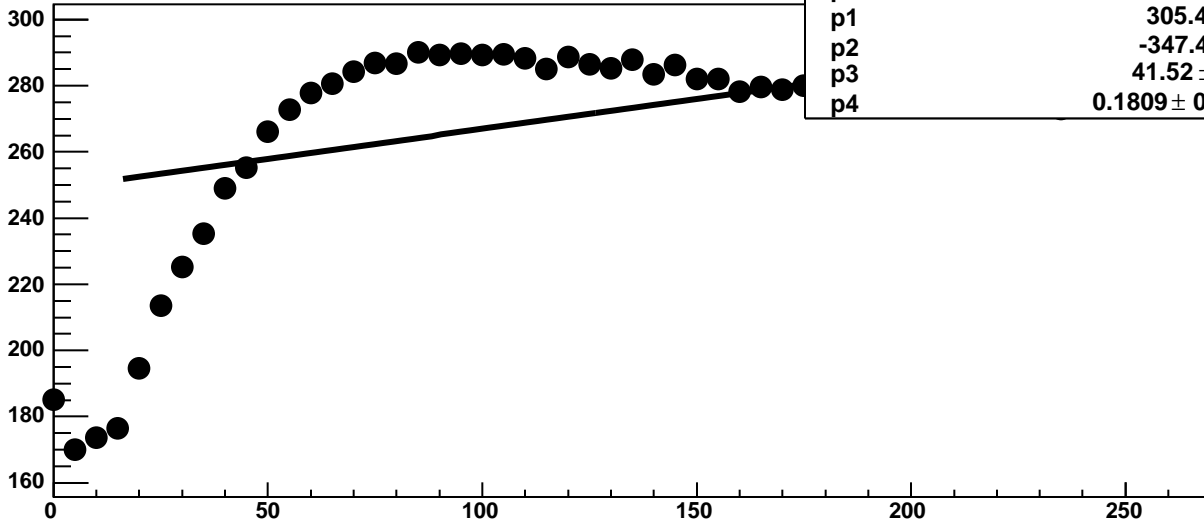
Chip 2, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold

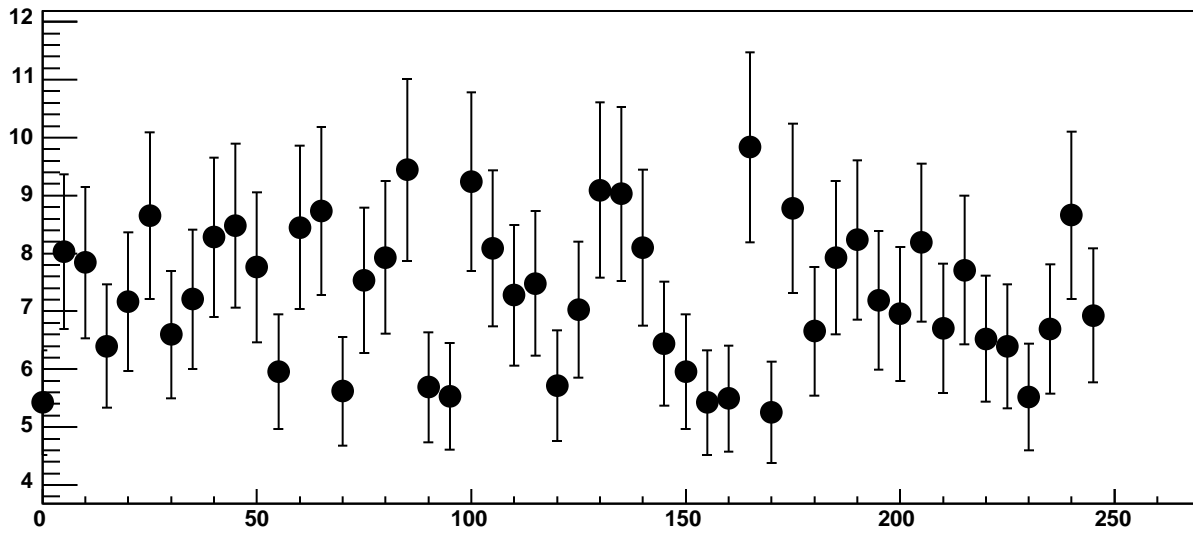


Chip 2, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

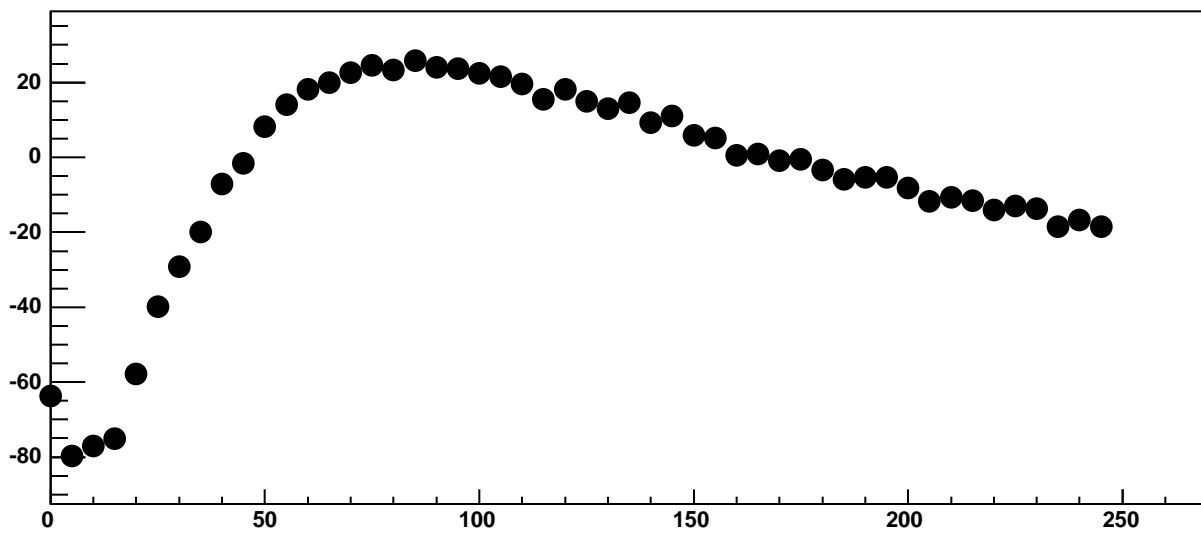


$\chi^2 / \text{ndf}$	8244 / 41
p0	$304.1 \pm 0.4843$
p1	$305.4 \pm 1.267$
p2	$-347.4 \pm 2.893$
p3	$41.52 \pm 0.3794$
p4	$0.1809 \pm 0.003595$

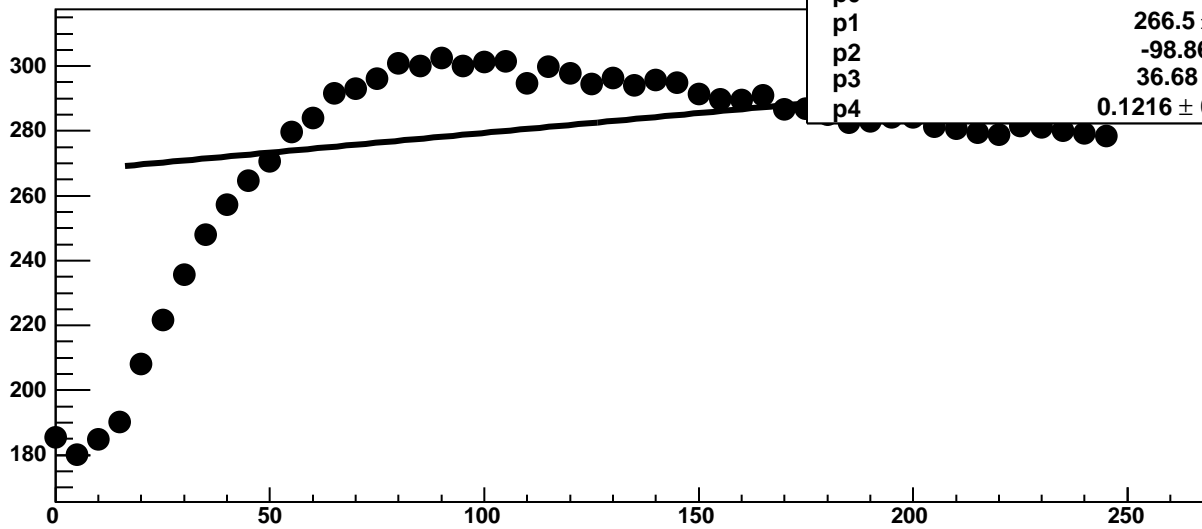
Chip 2, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

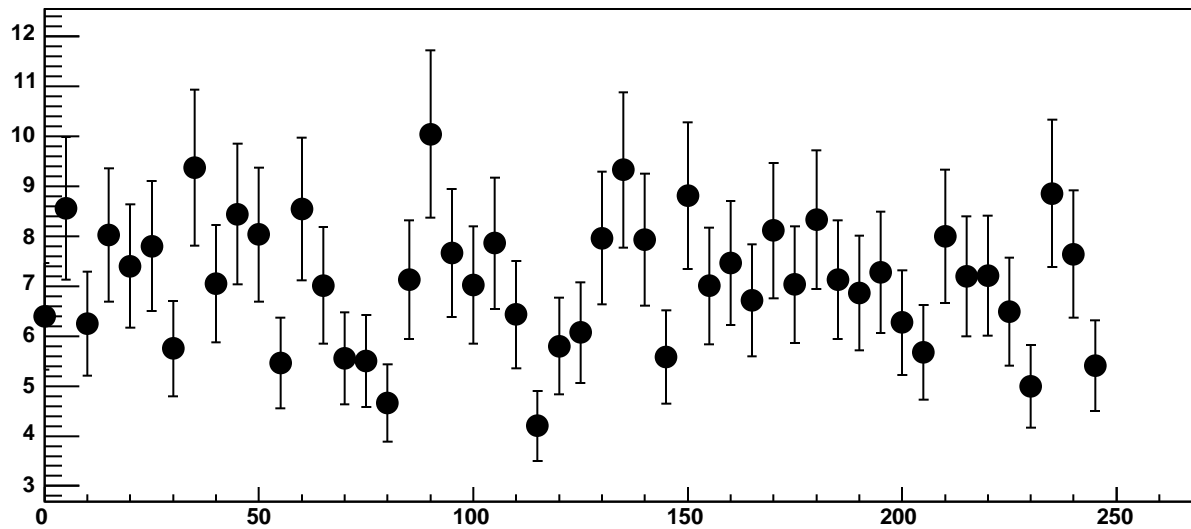


Chip 2, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

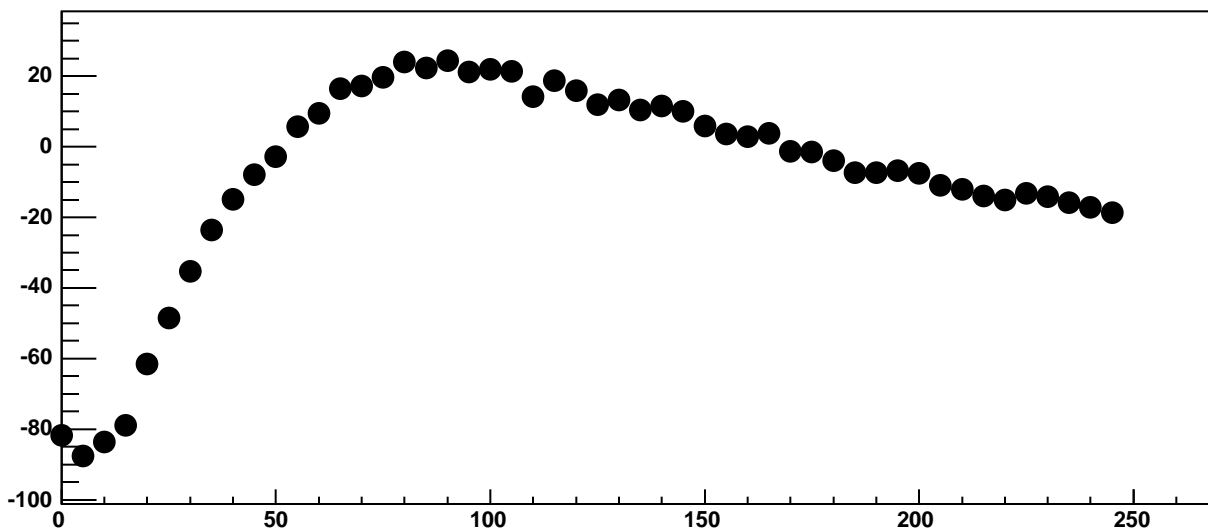


$\chi^2 / \text{ndf}$	8314 / 41
p0	$299.6 \pm 0.5143$
p1	$266.5 \pm 0.3954$
p2	$-98.86 \pm 5.528$
p3	$36.68 \pm 0.3467$
p4	$0.1216 \pm 0.003531$

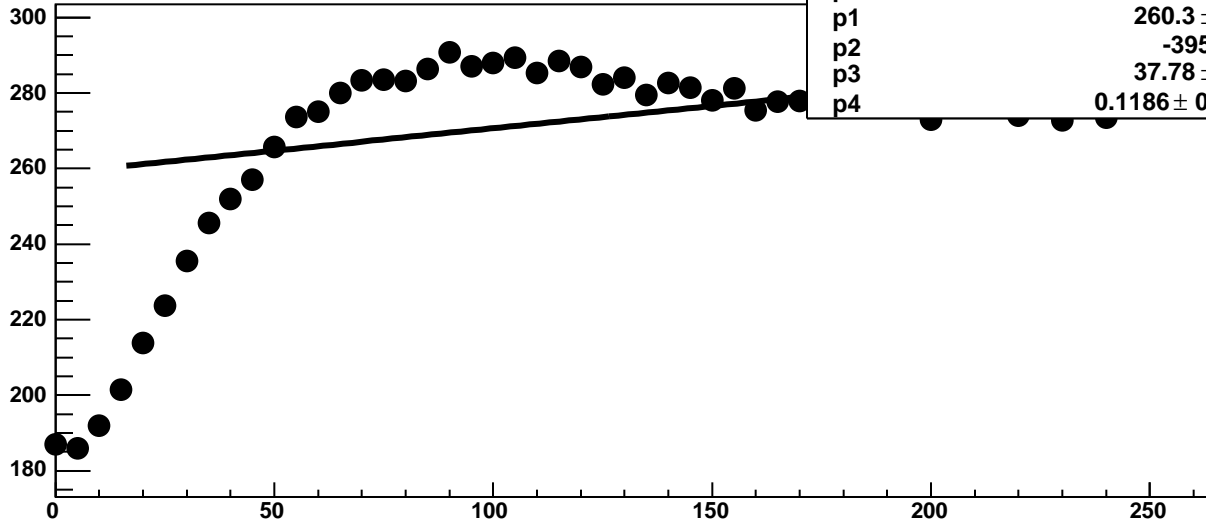
Chip 2, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

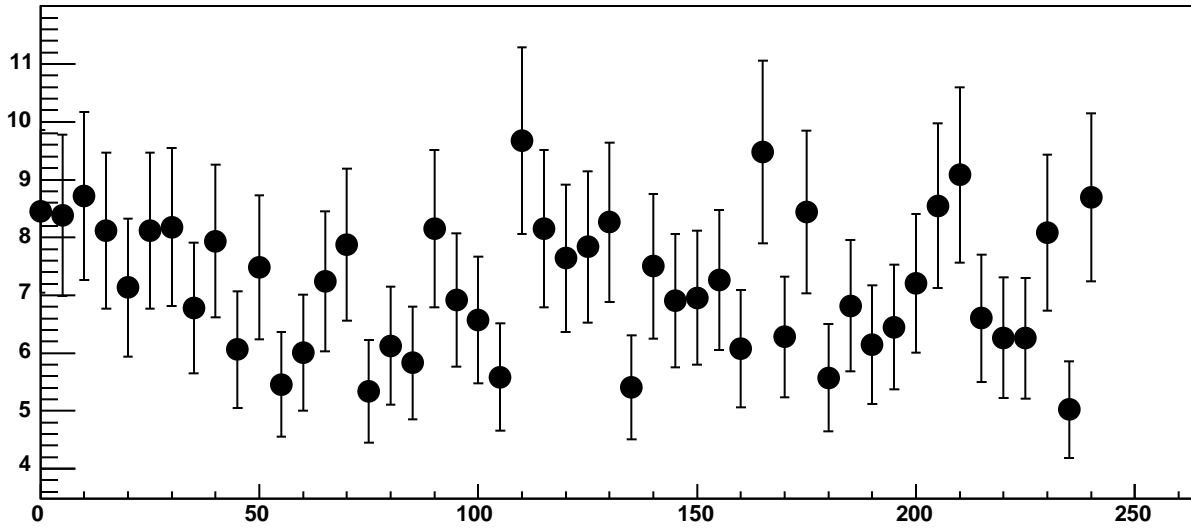


Chip 2, Channel 6, Enable 5, DAC=4000, ADC Mean vs Hold

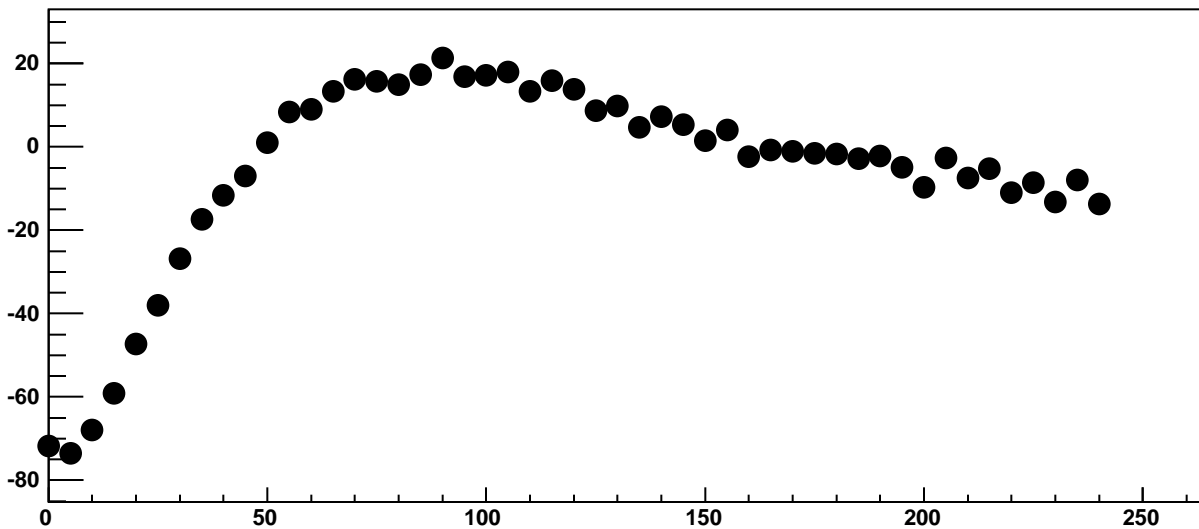


$\chi^2 / \text{ndf}$	4500 / 41
p0	$289.6 \pm 0.4897$
p1	$260.3 \pm 0.4024$
p2	$-395 \pm 11.52$
p3	$37.78 \pm 0.5316$
p4	$0.1186 \pm 0.003517$

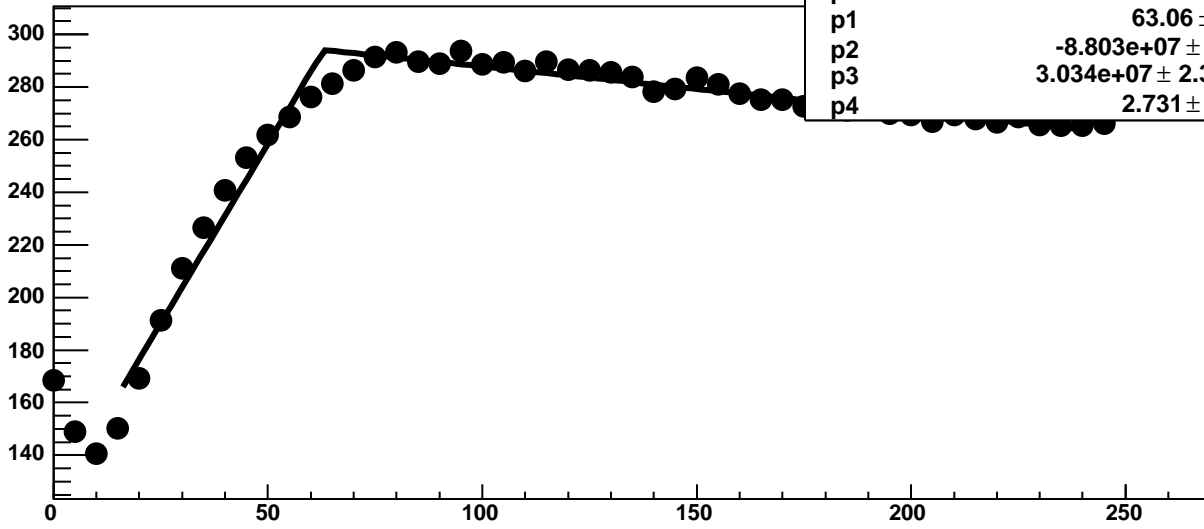
Chip 2, Channel 6, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 6, Enable 5, DAC=4000, ADC Residuals vs Hold

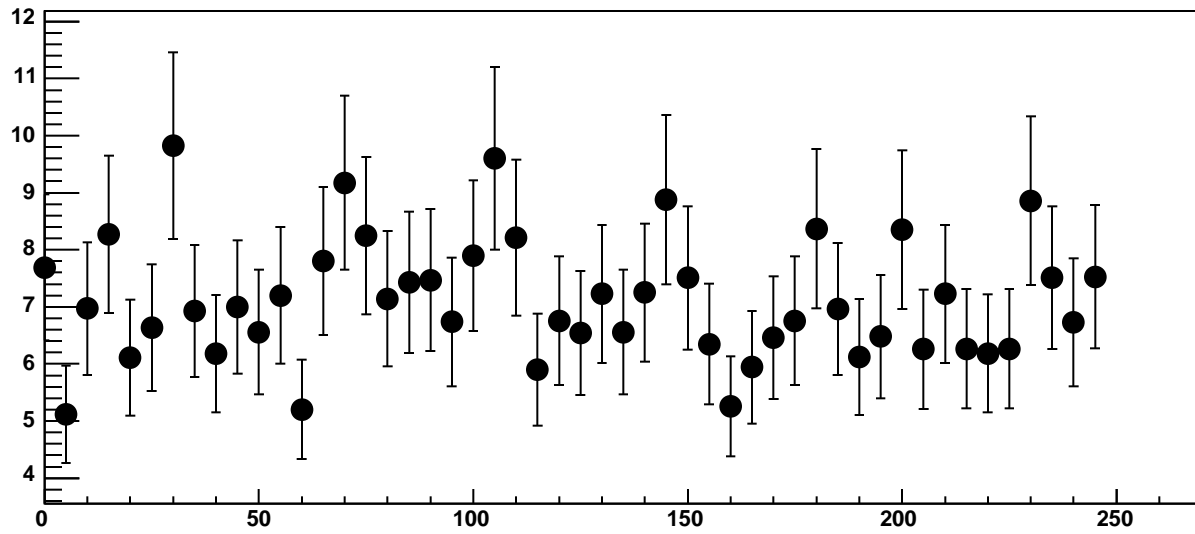


Chip 2, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

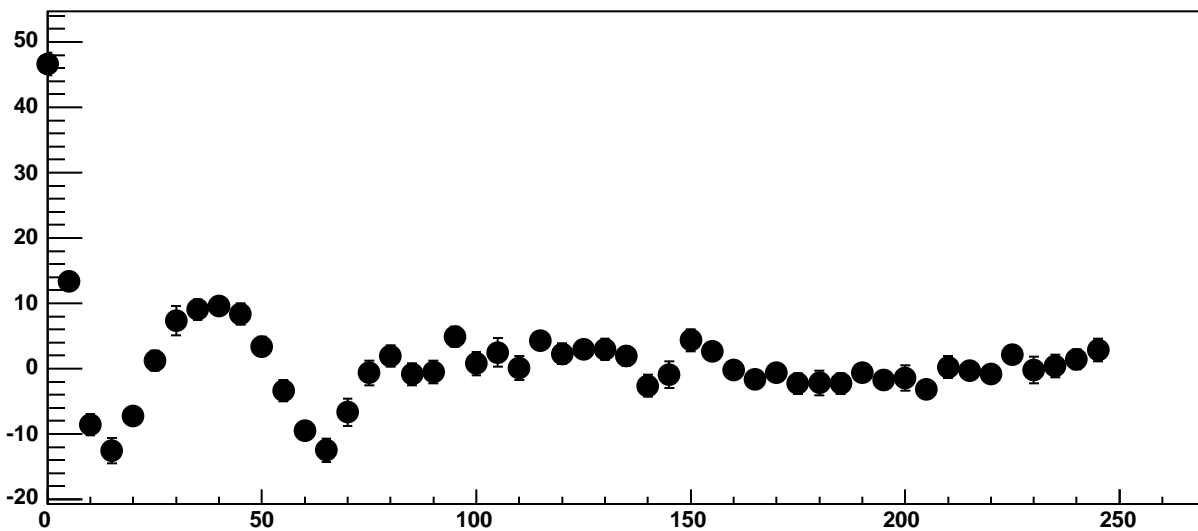


$\chi^2 / \text{ndf}$	381.5 / 41
p0	294.1 ± 0.5391
p1	63.06 ± 0.3705
p2	-8.803e+07 ± 1.2e+06
p3	3.034e+07 ± 2.377e+05
p4	2.731 ± 0.03289

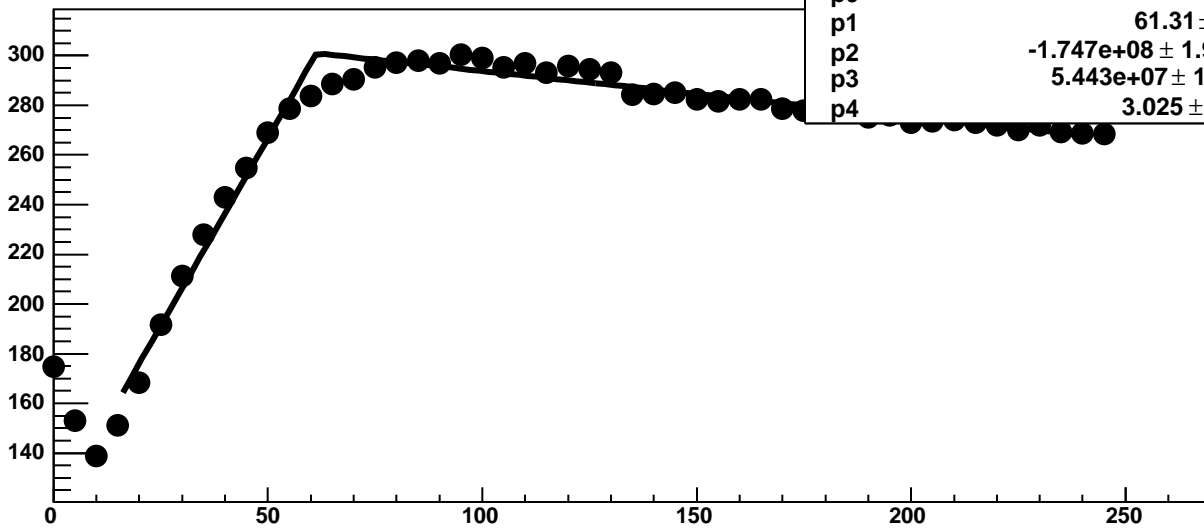
Chip 2, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

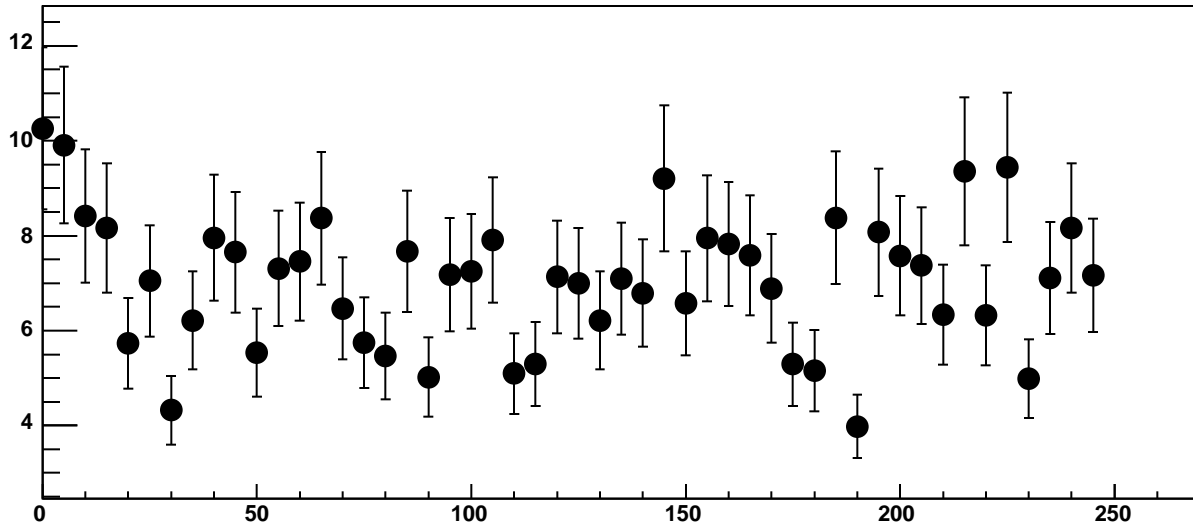


Chip 2, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold

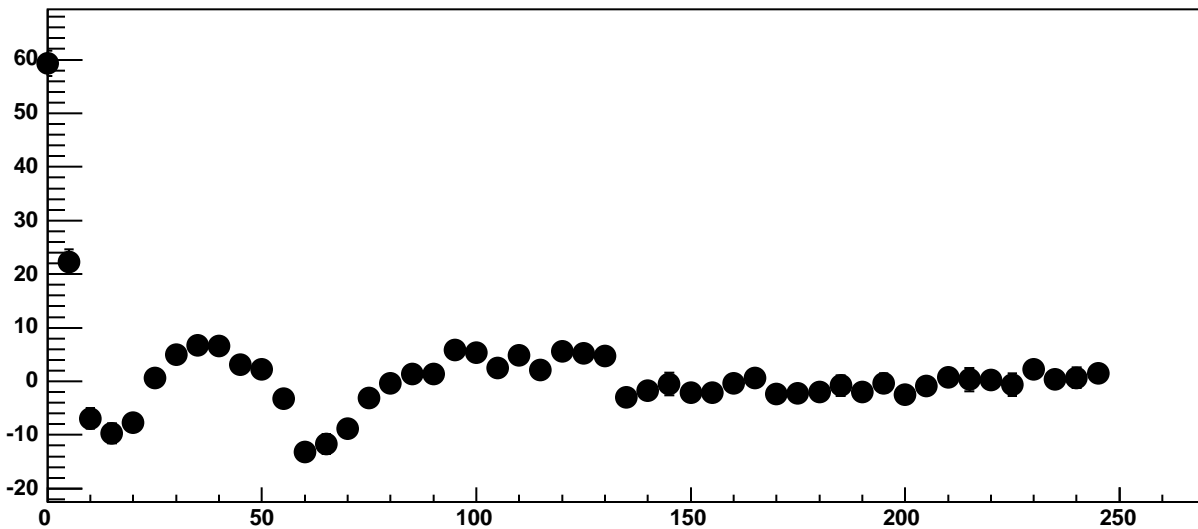


$\chi^2 / \text{ndf}$	375.4 / 41
p0	$301 \pm 0.4728$
p1	$61.31 \pm 0.3404$
p2	$-1.747\text{e}+08 \pm 1.952\text{e}+06$
p3	$5.443\text{e}+07 \pm 1.73\text{e}+05$
p4	$3.025 \pm 0.03451$

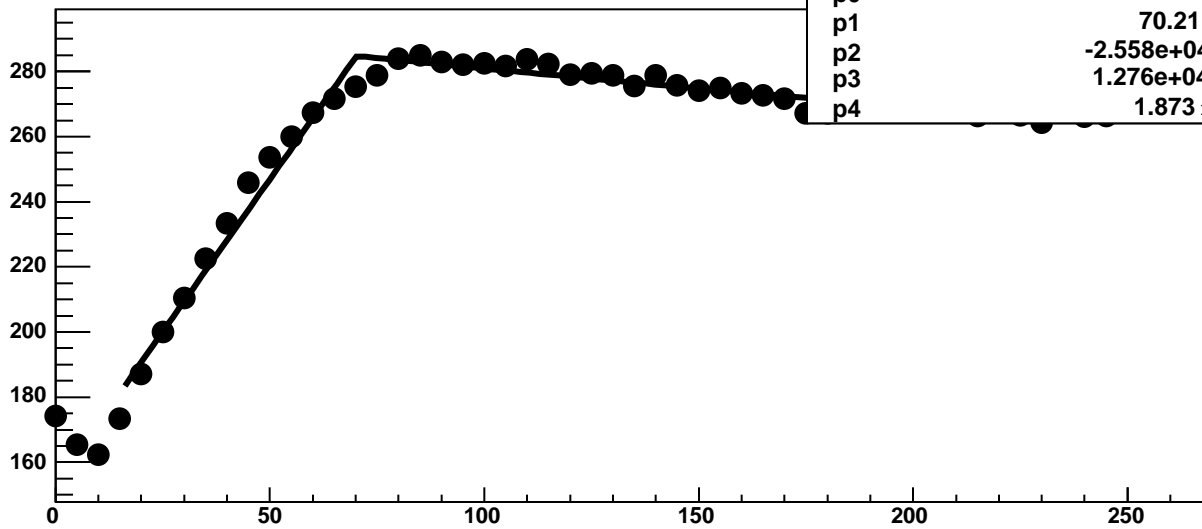
Chip 2, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

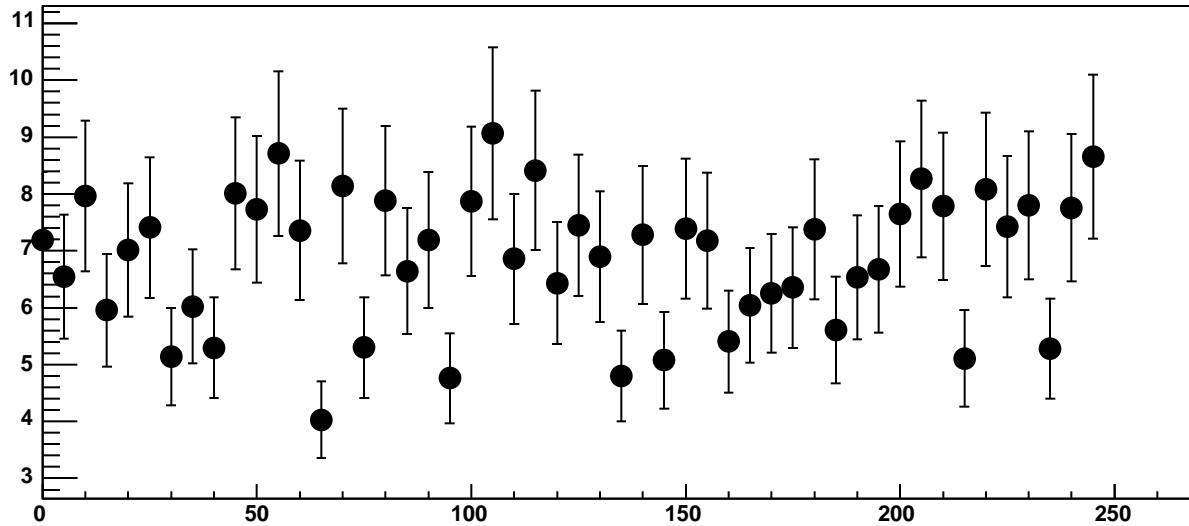


Chip 2, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold

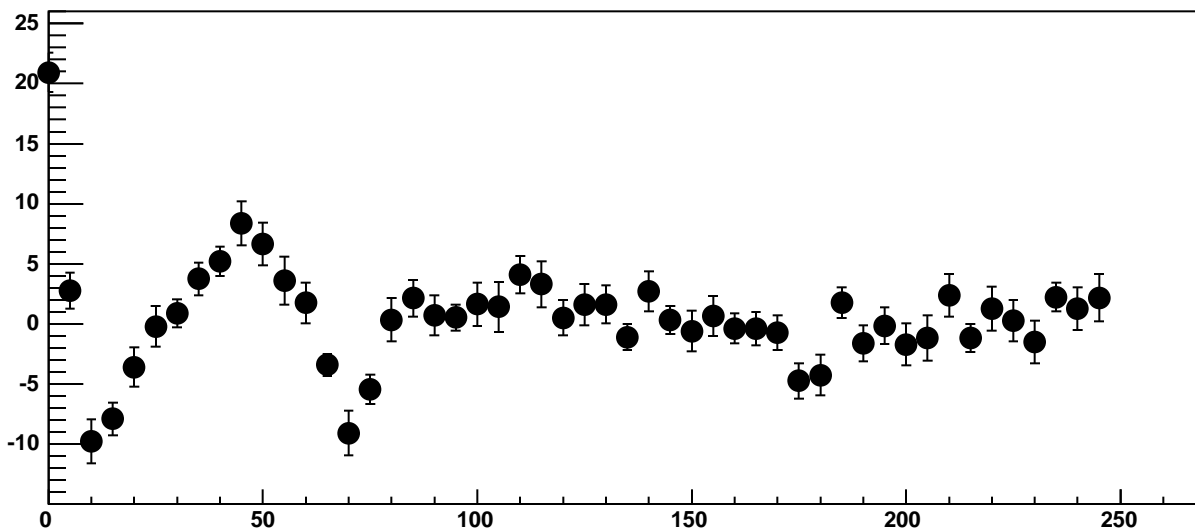


$\chi^2 / \text{ndf}$	210.1 / 41
p0	$284.8 \pm 0.4961$
p1	$70.21 \pm 0.4597$
p2	$-2.558e+04 \pm 4448$
p3	$1.276e+04 \pm 2232$
p4	$1.873 \pm 0.0234$

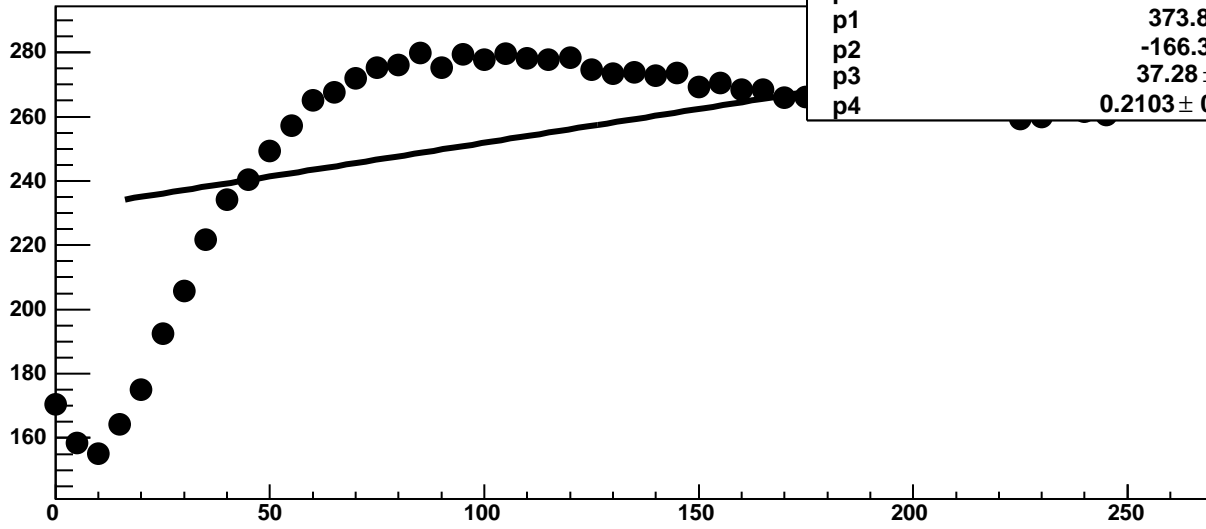
Chip 2, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold

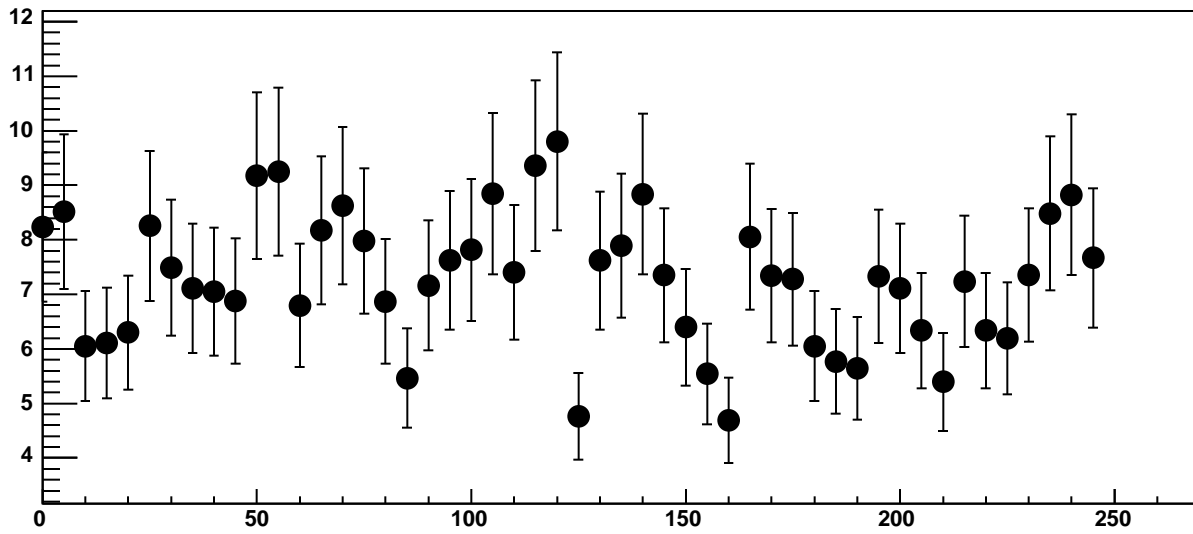


Chip 2, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

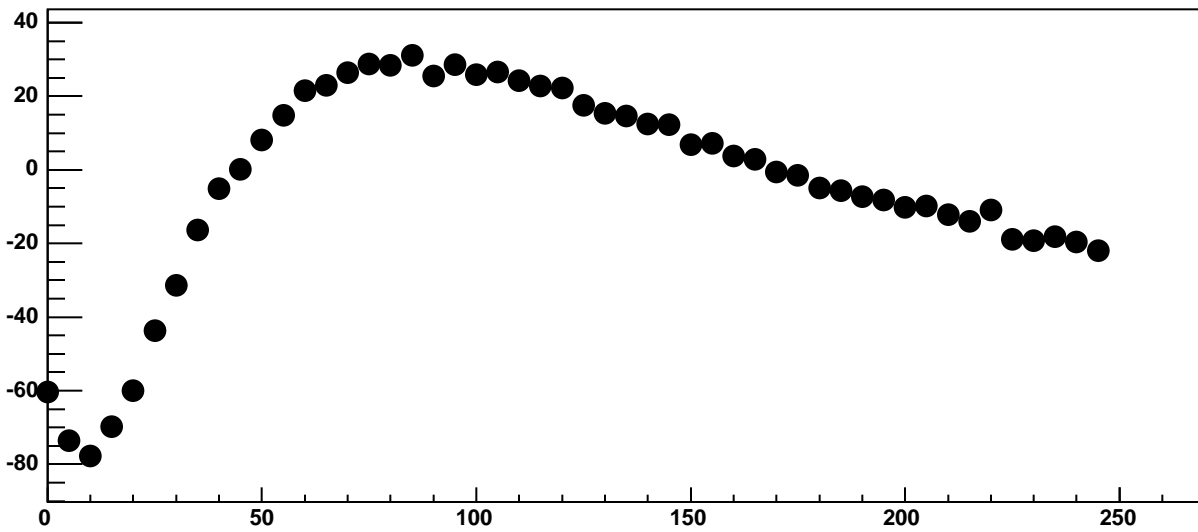


$\chi^2 / \text{ndf}$	9686 / 41
p0	$309.5 \pm 0.4703$
p1	$373.8 \pm 2.207$
p2	$-166.3 \pm 5.872$
p3	$37.28 \pm 0.3462$
p4	$0.2103 \pm 0.003581$

Chip 2, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold

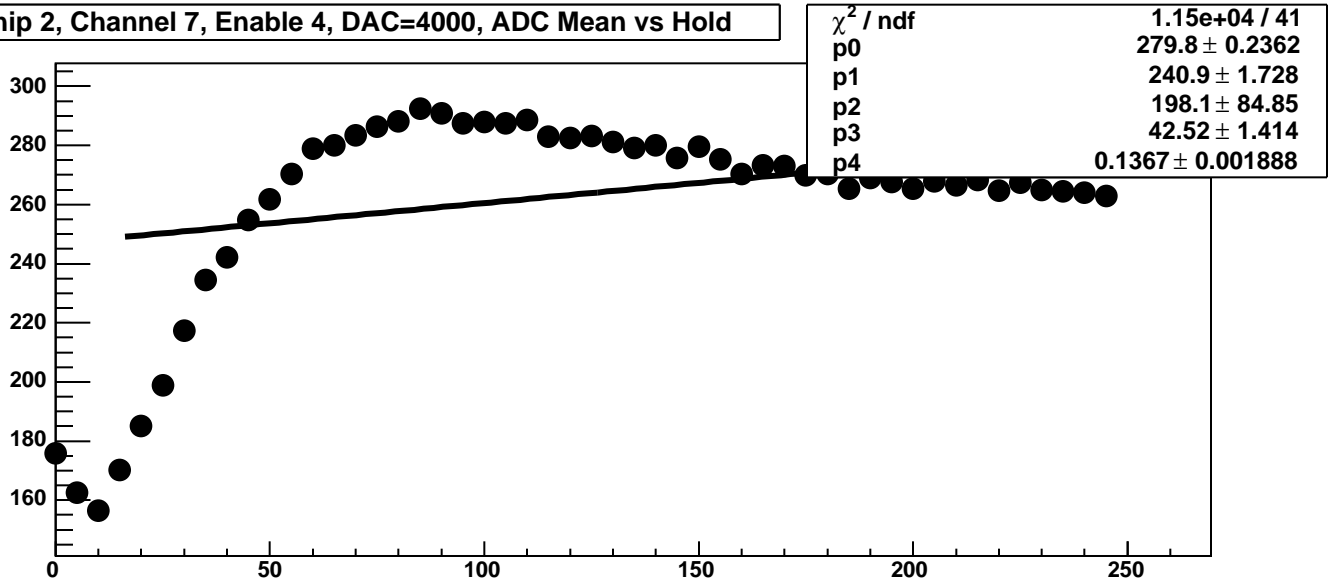


Chip 2, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

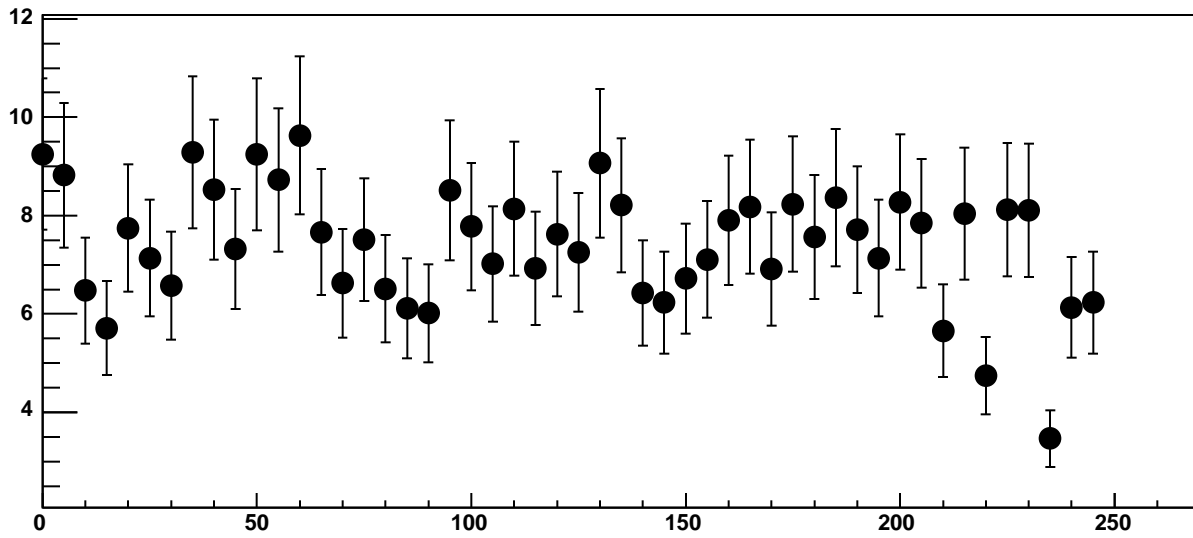




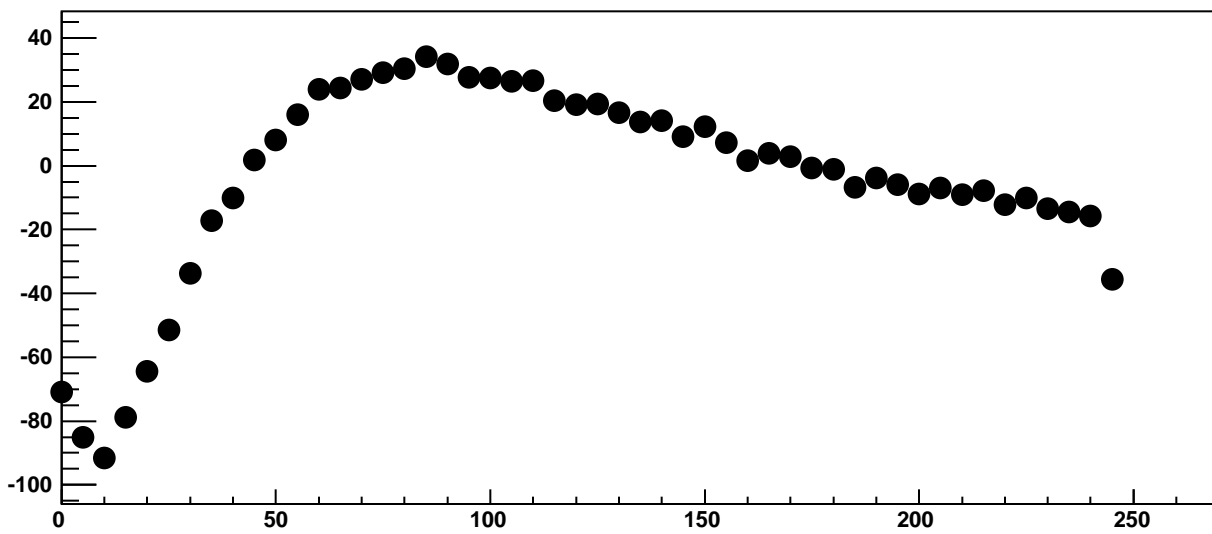
Chip 2, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold



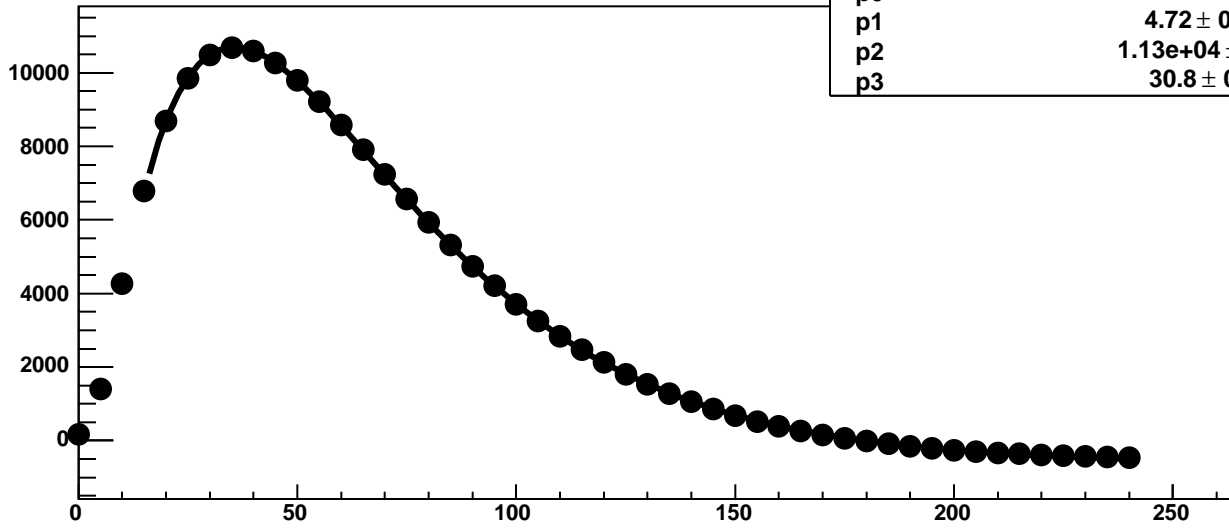
Chip 2, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

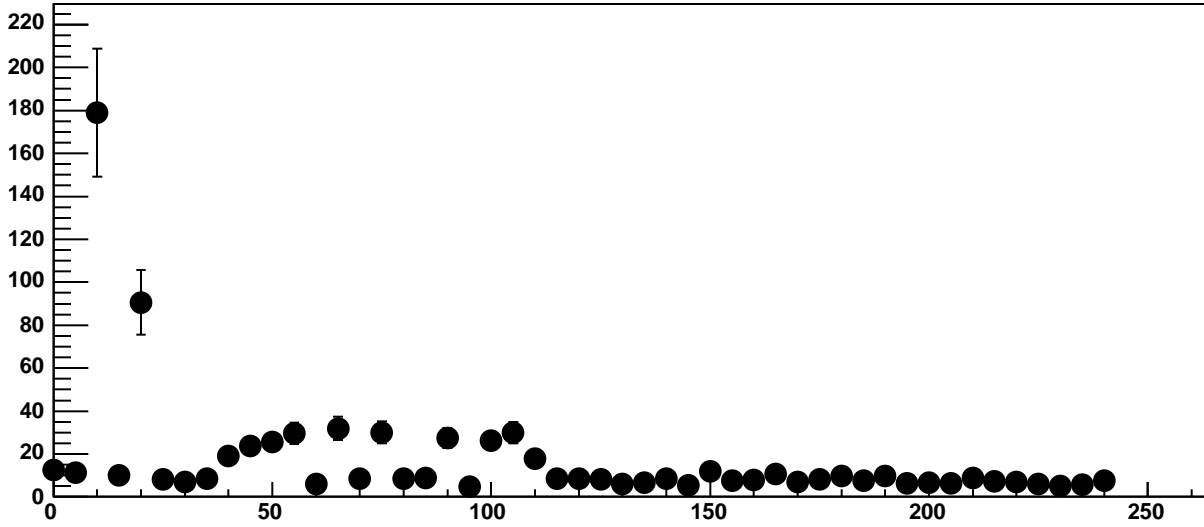


Chip 2, Channel 7, Enable 5!, DAC=4000, ADC Mean vs Hold

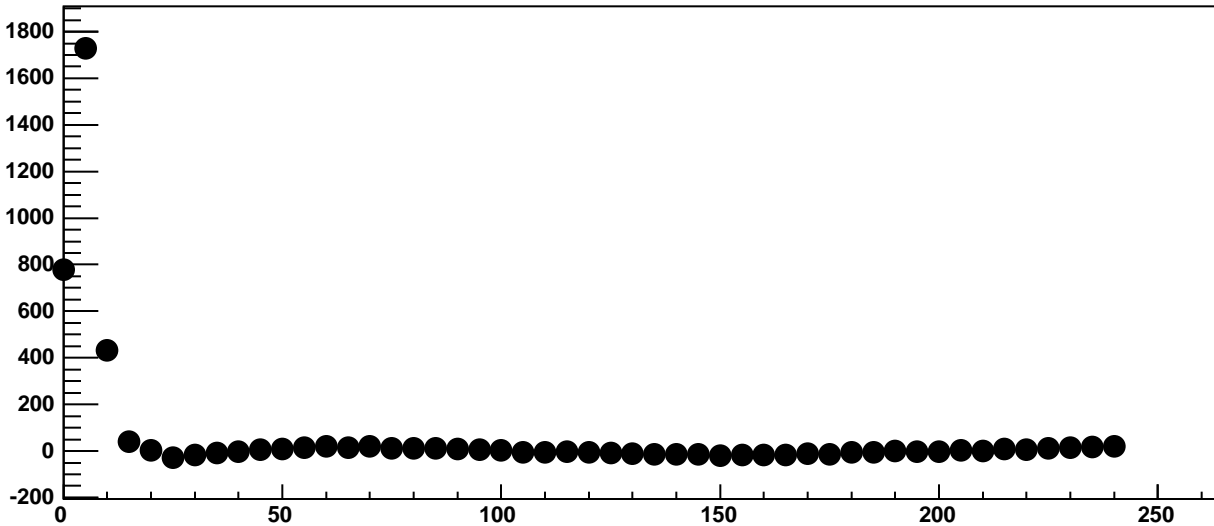


$\chi^2 / \text{ndf}$	2428 / 42
p0	-598.3 ± 0.5016
p1	4.72 ± 0.005797
p2	1.13e+04 ± 0.9382
p3	30.8 ± 0.004241

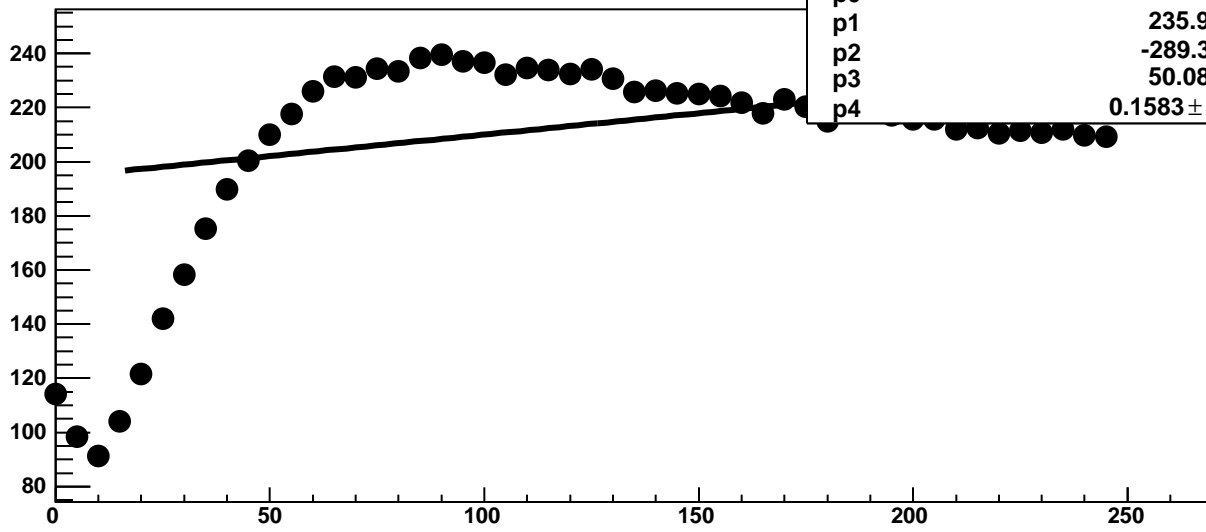
Chip 2, Channel 7, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 7, Enable 5!, DAC=4000, ADC Residuals vs Hold

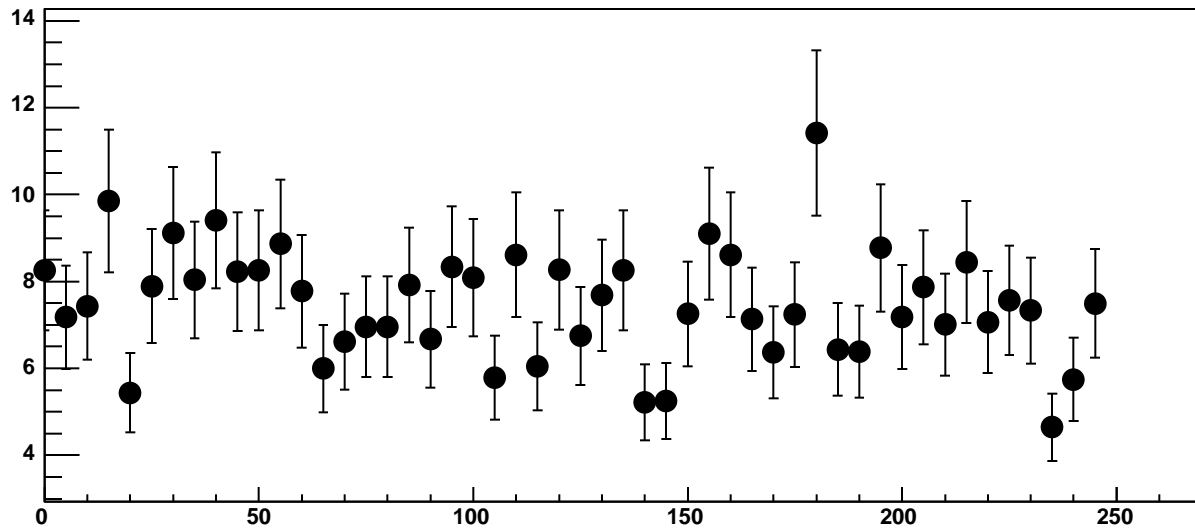


Chip 2, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

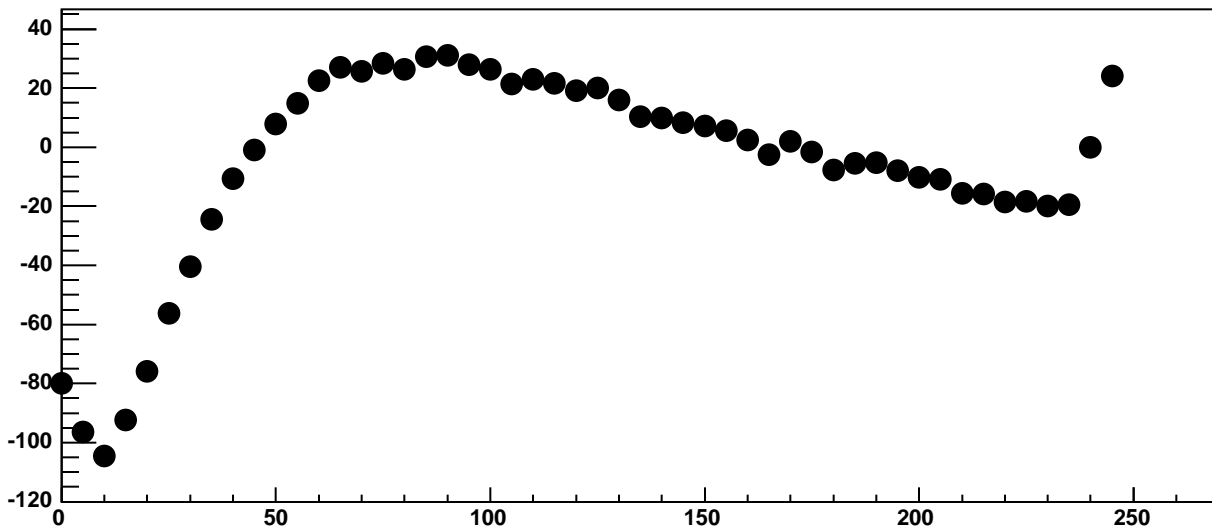


$\chi^2 / \text{ndf}$	1.167e+04 / 41
p0	231.5 ± 0.6102
p1	235.9 ± 2.505
p2	-289.3 ± 167.4
p3	50.08 ± 24.94
p4	0.1583 ± 0.00378

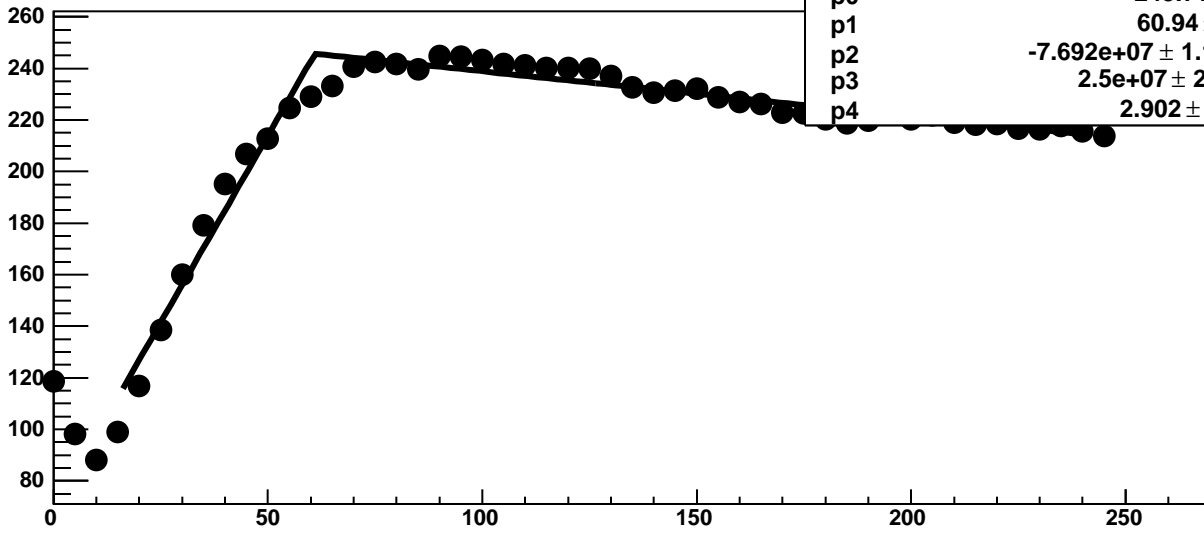
Chip 2, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

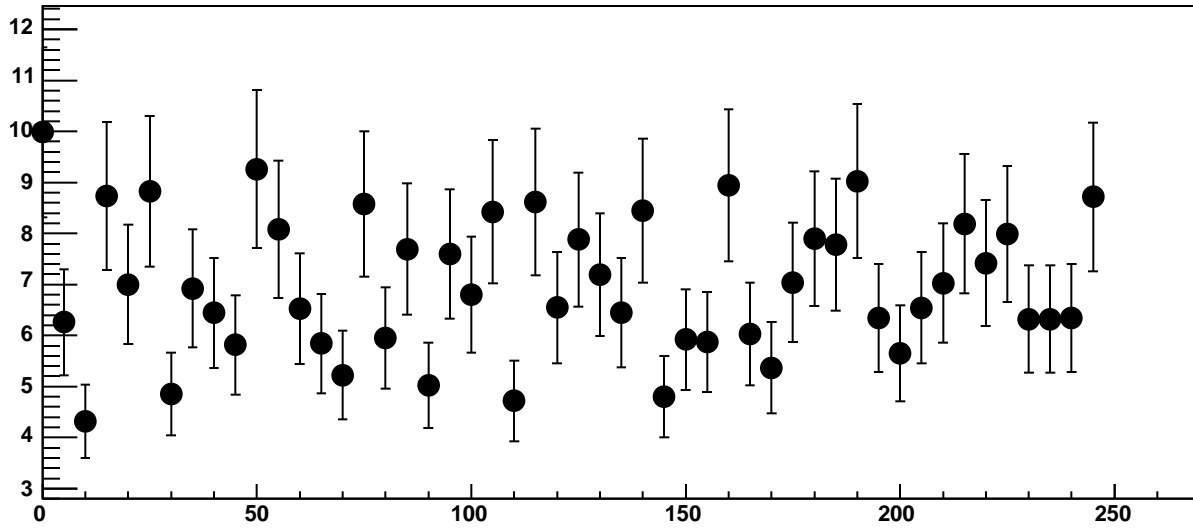


Chip 2, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

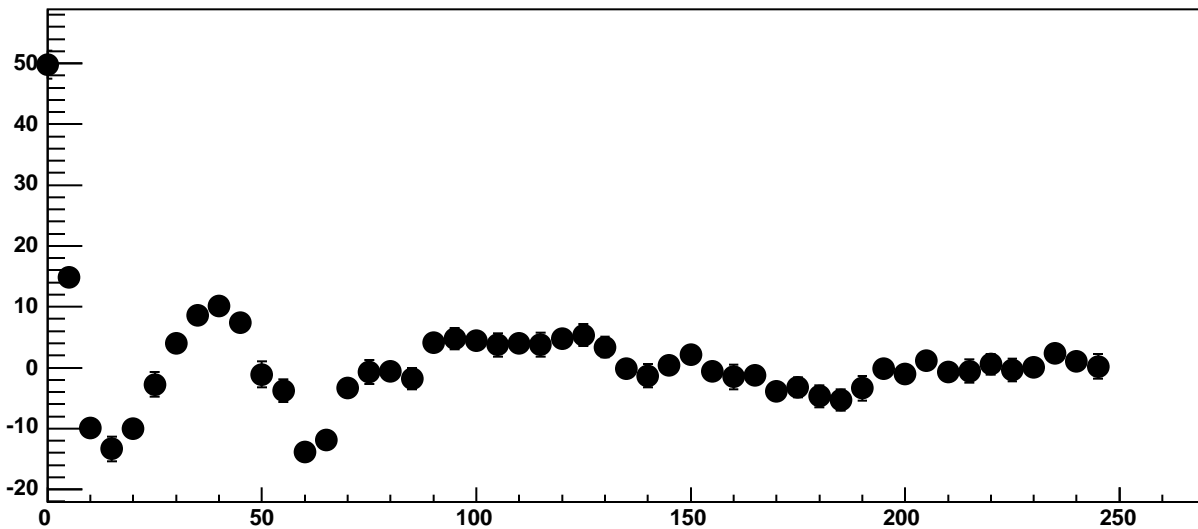


$\chi^2 / \text{ndf}$	496.7 / 41
p0	$245.7 \pm 0.4678$
p1	$60.94 \pm 0.3631$
p2	$-7.692\text{e}+07 \pm 1.134\text{e}+06$
p3	$2.5\text{e}+07 \pm 2.24\text{e}+05$
p4	$2.902 \pm 0.03744$

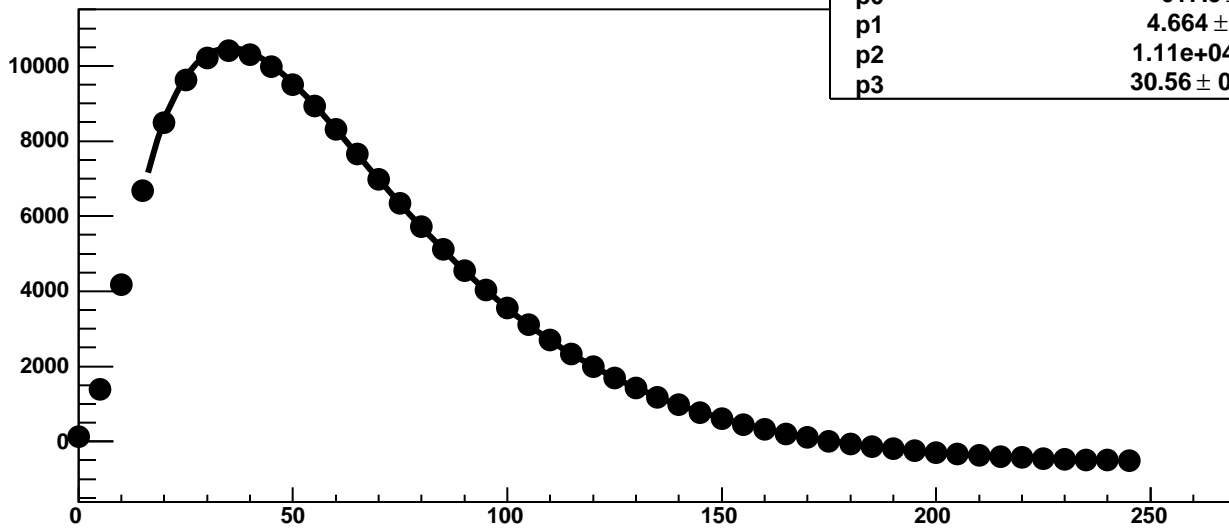
Chip 2, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

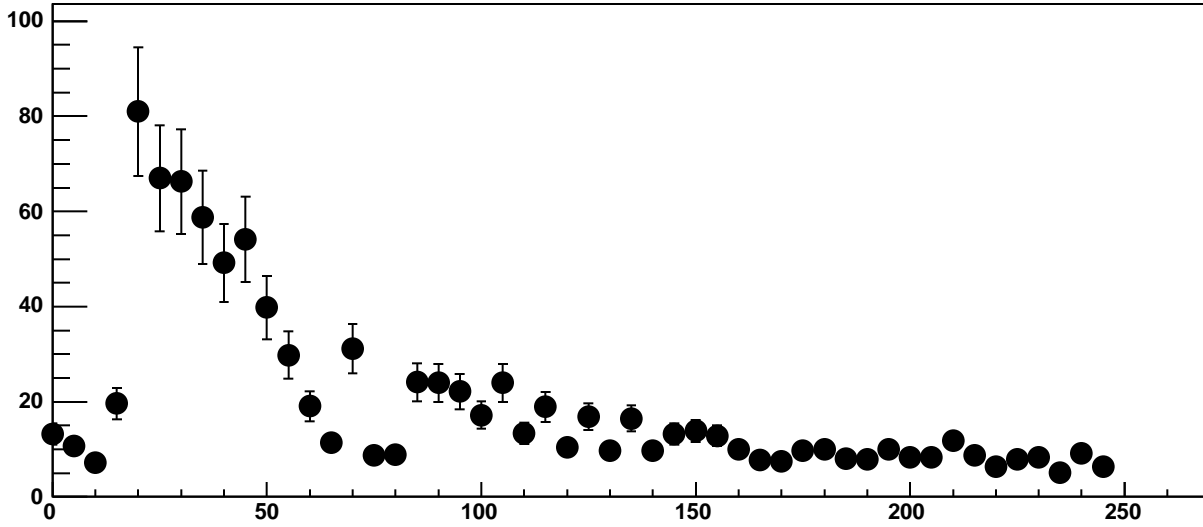


Chip 2, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold

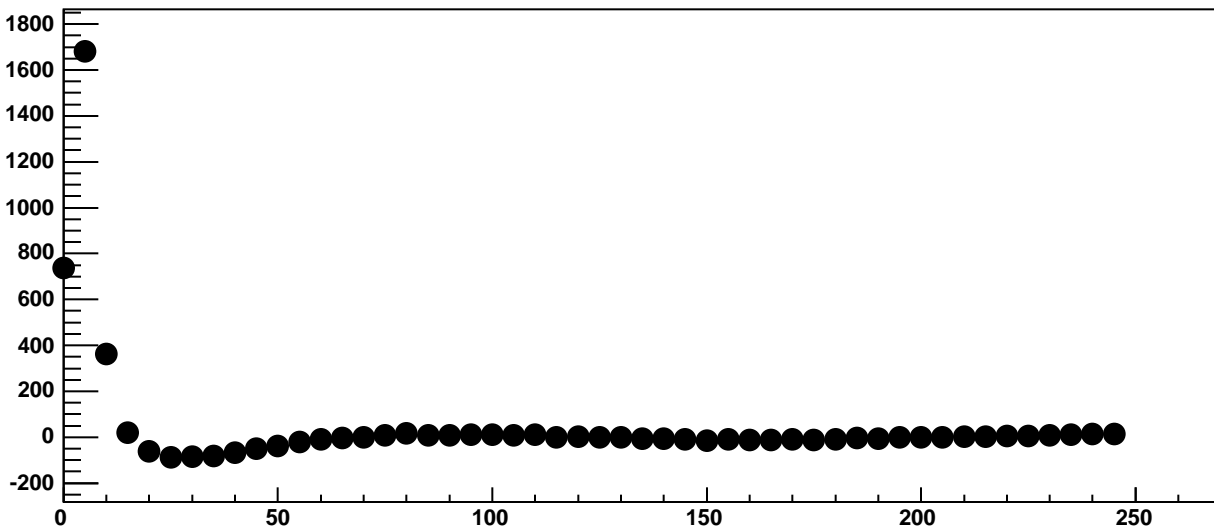


$\chi^2 / \text{ndf}$	721.3 / 42
p0	-617.9 ± 0.6738
p1	4.664 ± 0.01133
p2	1.11e+04 ± 2.658
p3	30.56 ± 0.008858

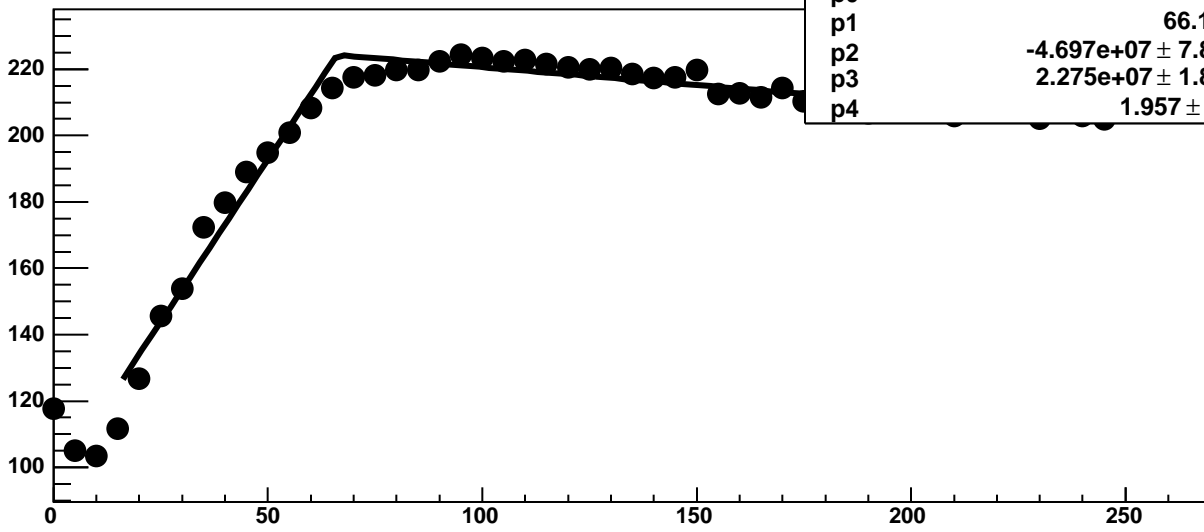
Chip 2, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold

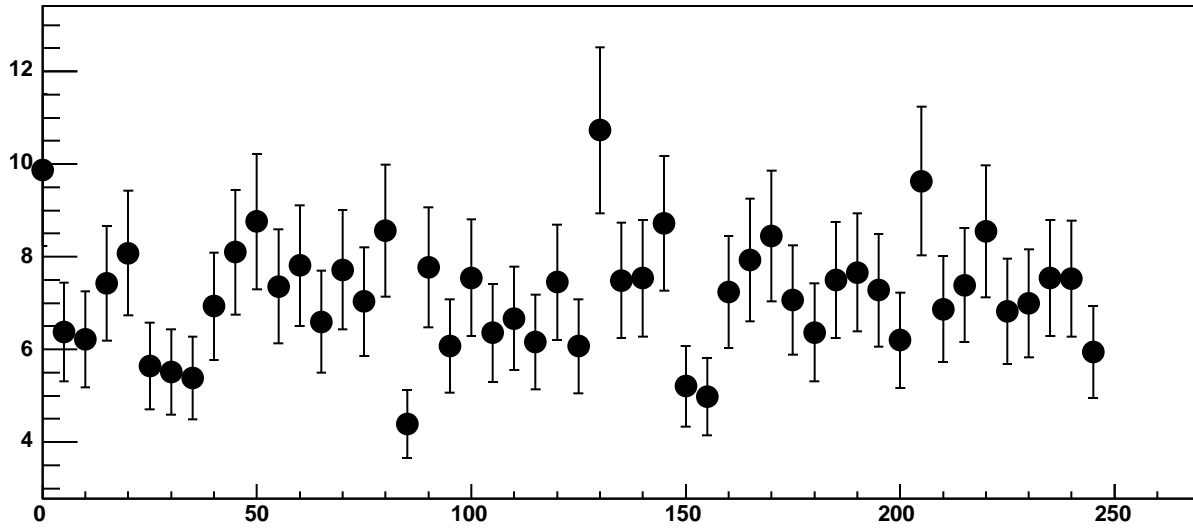


Chip 2, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

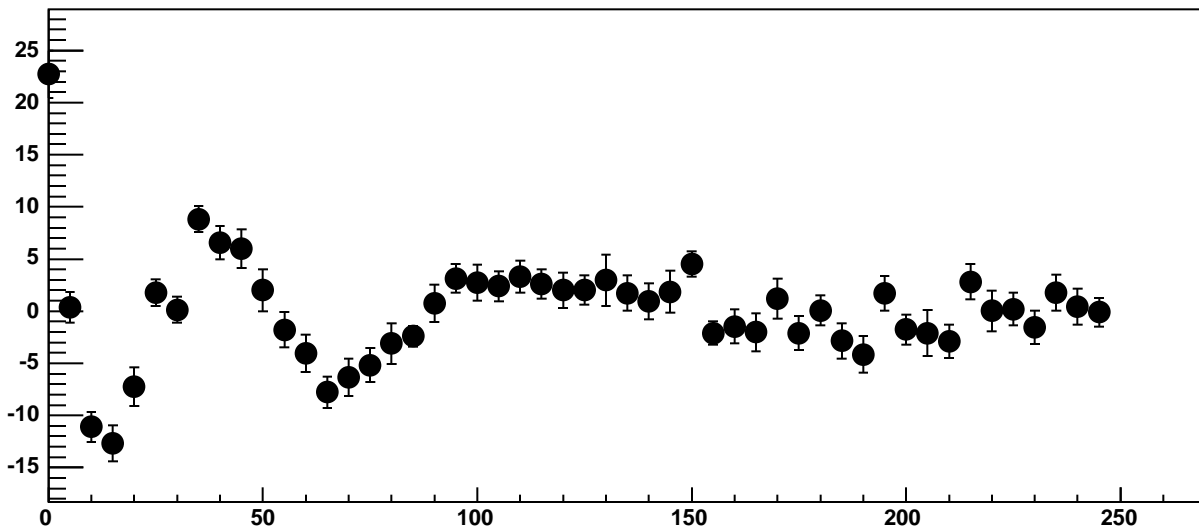


$\chi^2 / \text{ndf}$	283.4 / 41
p0	$224.3 \pm 0.5026$
p1	$66.1 \pm 0.536$
p2	$-4.697\text{e}+07 \pm 7.843\text{e}+05$
p3	$2.275\text{e}+07 \pm 1.899\text{e}+05$
p4	$1.957 \pm 0.03067$

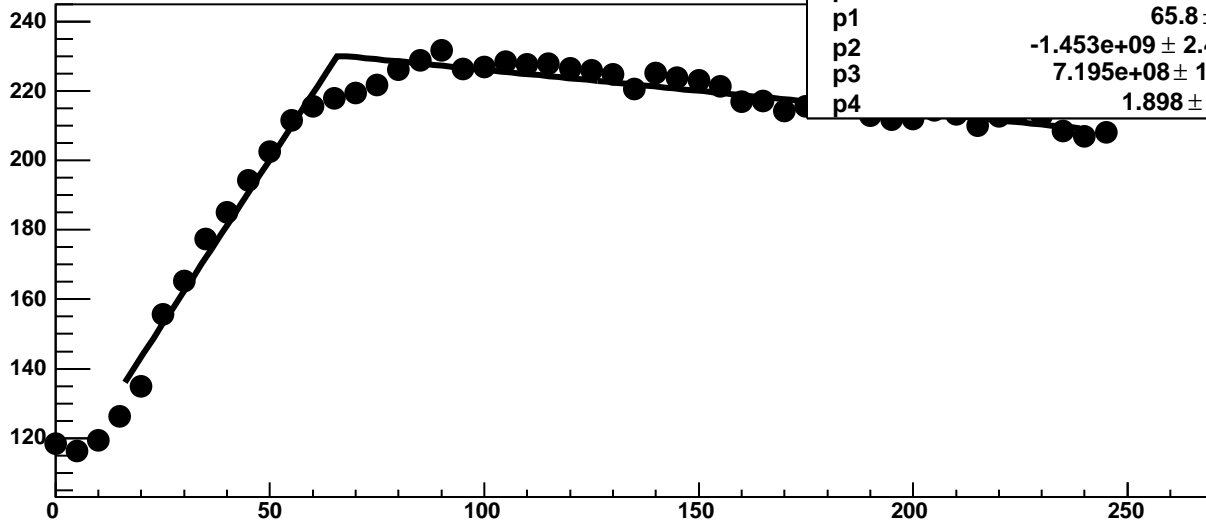
Chip 2, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

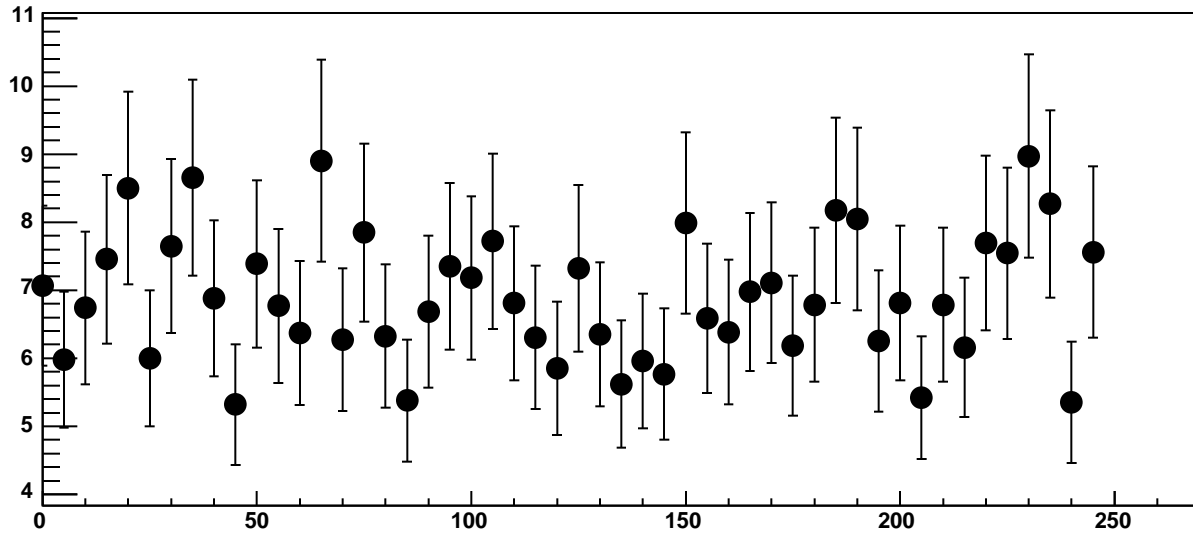


Chip 2, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

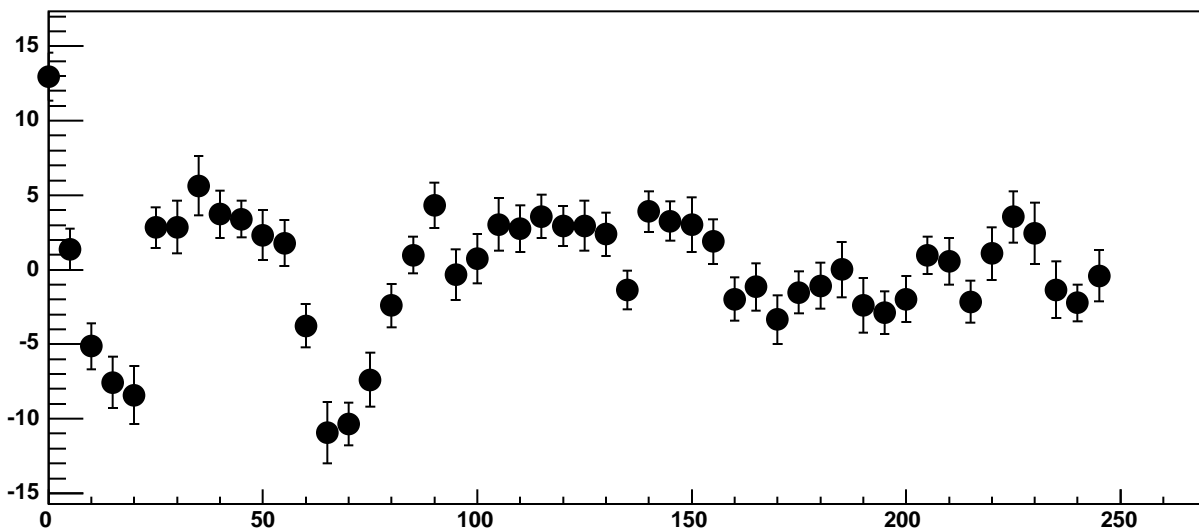


$\chi^2 / \text{ndf}$	255.7 / 41
p0	$230.3 \pm 0.5311$
p1	$65.8 \pm 0.6183$
p2	$-1.453\text{e}+09 \pm 2.445\text{e}+07$
p3	$7.195\text{e}+08 \pm 1.12\text{e}+05$
p4	$1.898 \pm 0.03448$

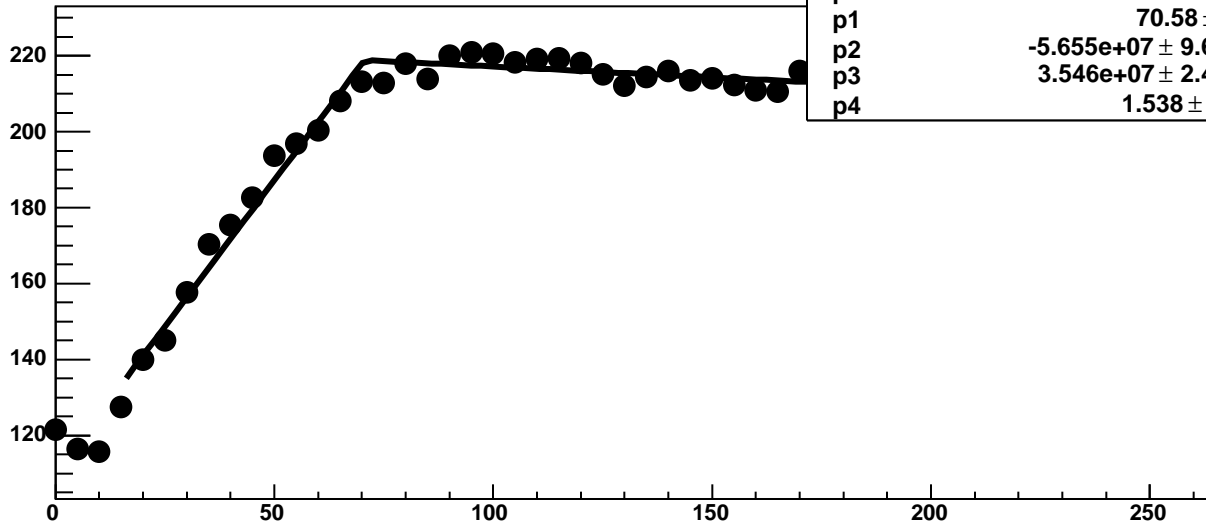
Chip 2, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

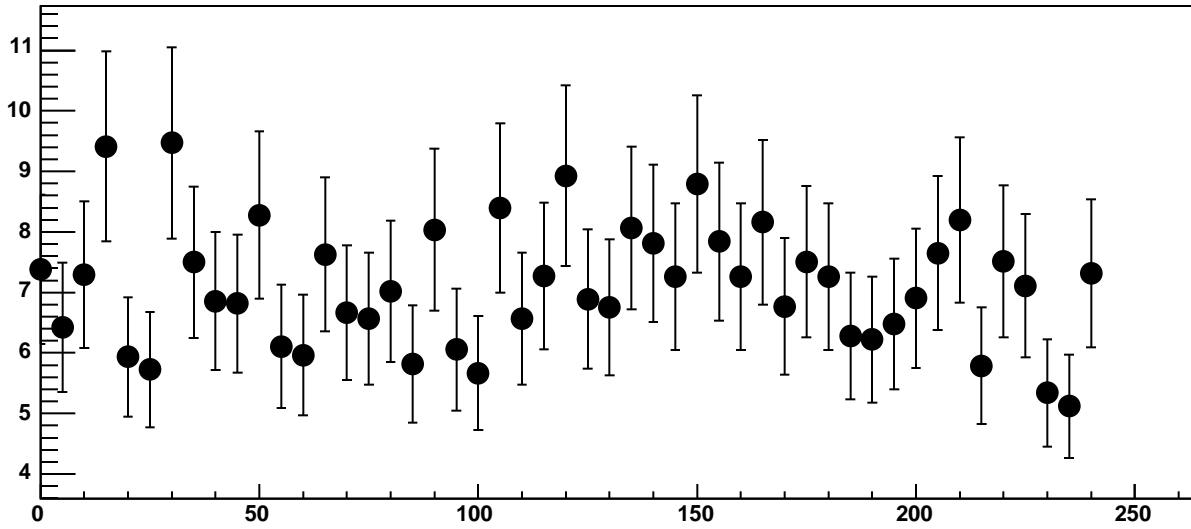


Chip 2, Channel 8, Enable 5, DAC=4000, ADC Mean vs Hold

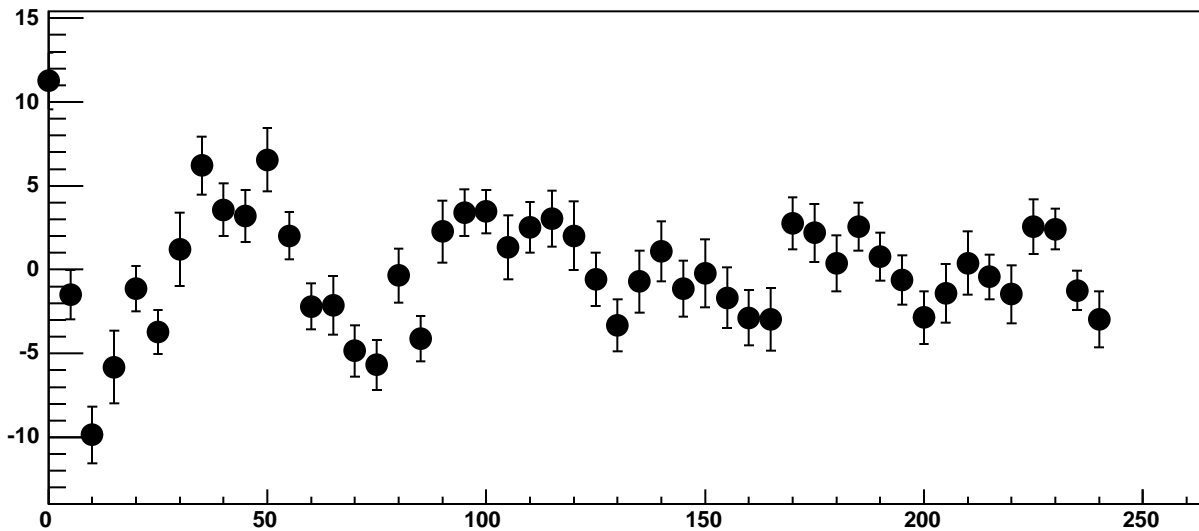


$\chi^2 / \text{ndf}$	148.6 / 41
p0	$218.8 \pm 0.5185$
p1	$70.58 \pm 0.6097$
p2	$-5.655e+07 \pm 9.679e+05$
p3	$3.546e+07 \pm 2.419e+05$
p4	$1.538 \pm 0.02556$

Chip 2, Channel 8, Enable 5, DAC=4000, ADC Noise vs Hold

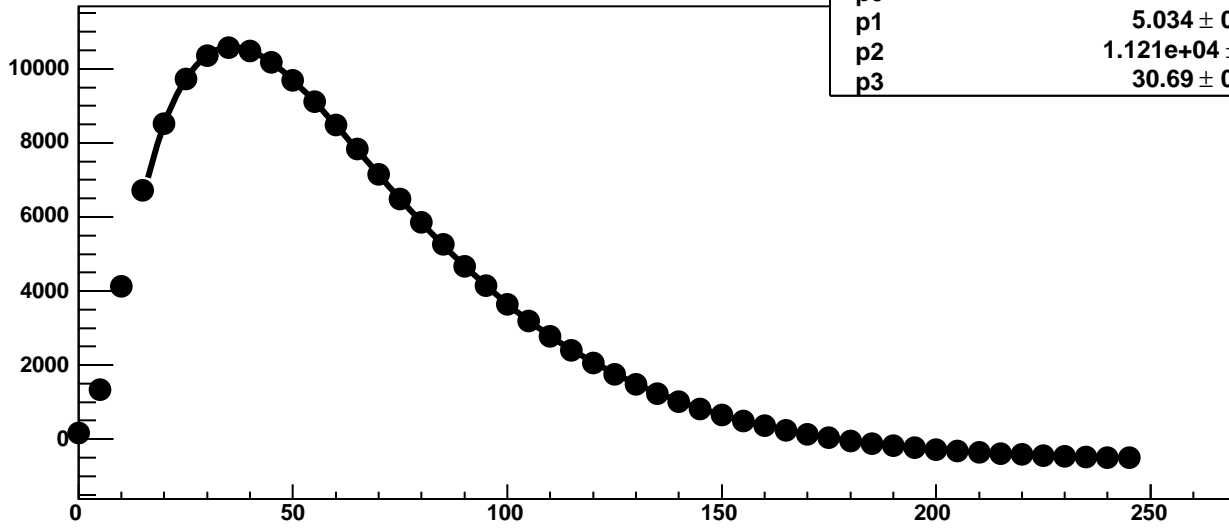


Chip 2, Channel 8, Enable 5, DAC=4000, ADC Residuals vs Hold



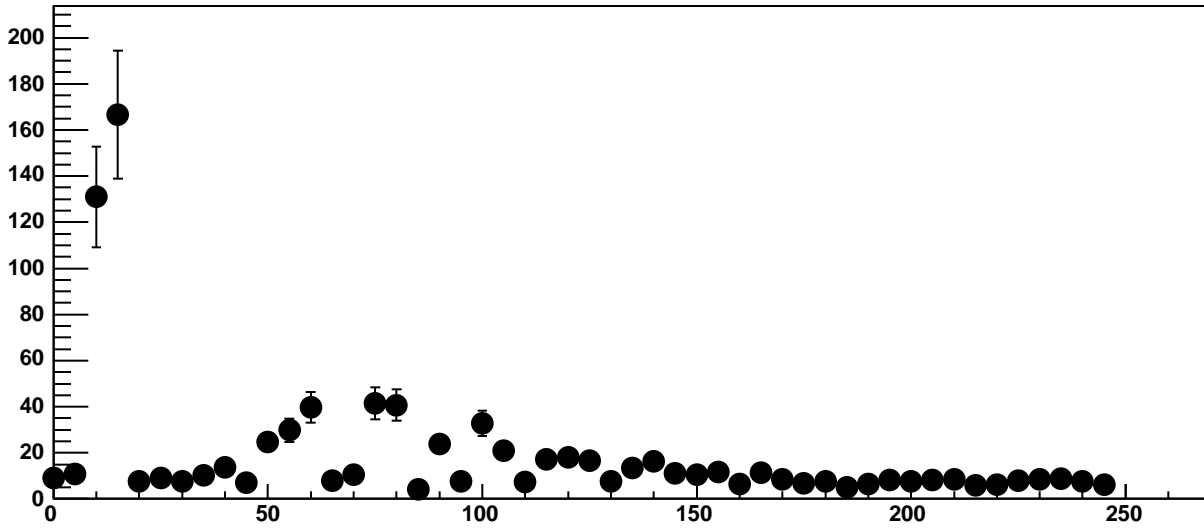


Chip 2, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold

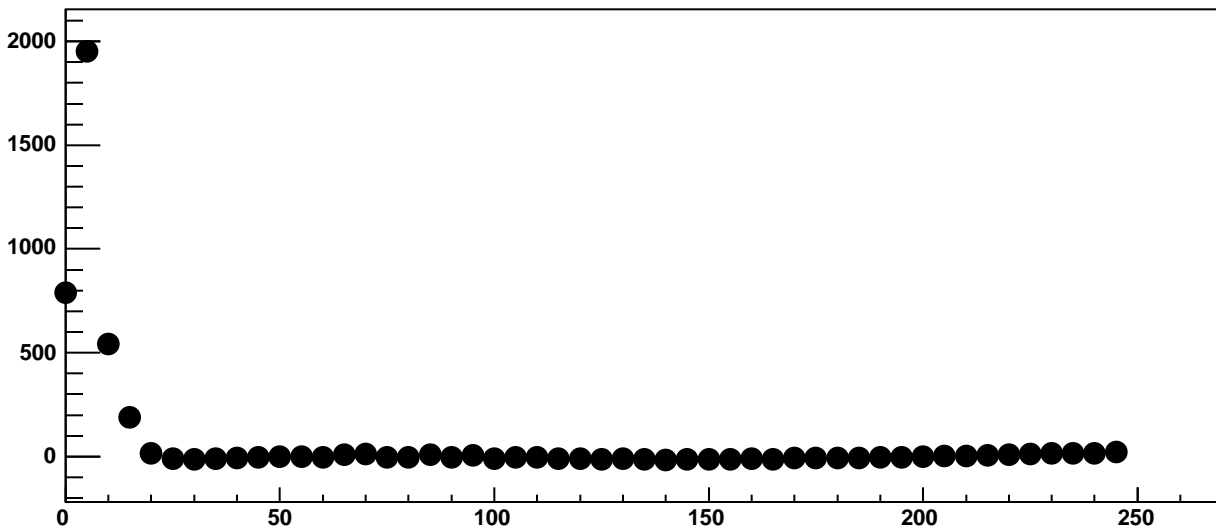


$\chi^2 / \text{ndf}$	1027 / 42
p0	-618.5 ± 0.5567
p1	5.034 ± 0.007642
p2	1.121e+04 ± 0.9338
p3	30.69 ± 0.005272

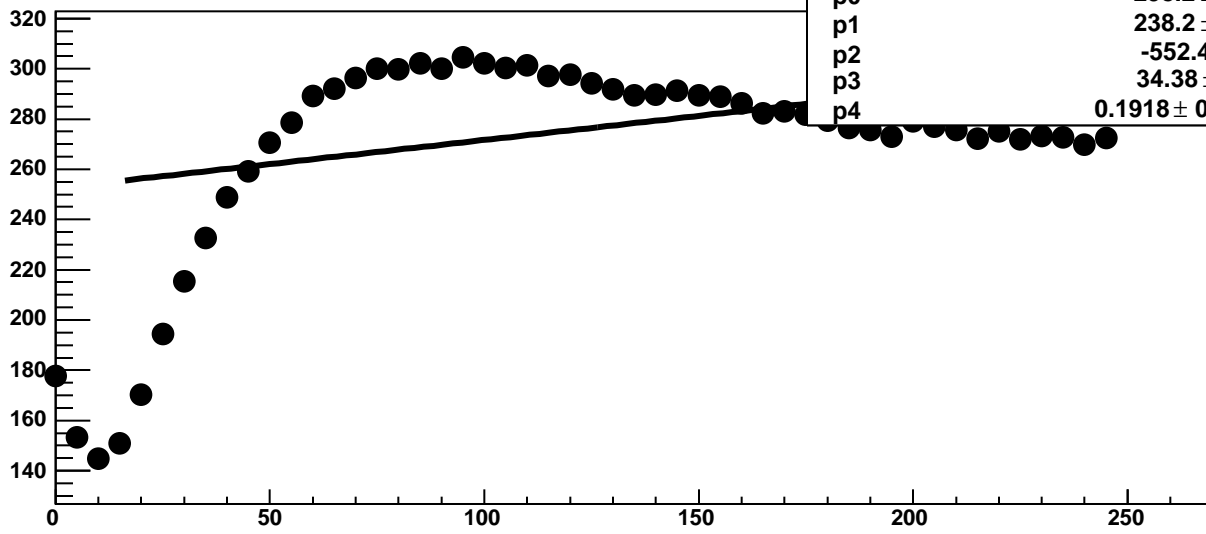
Chip 2, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold

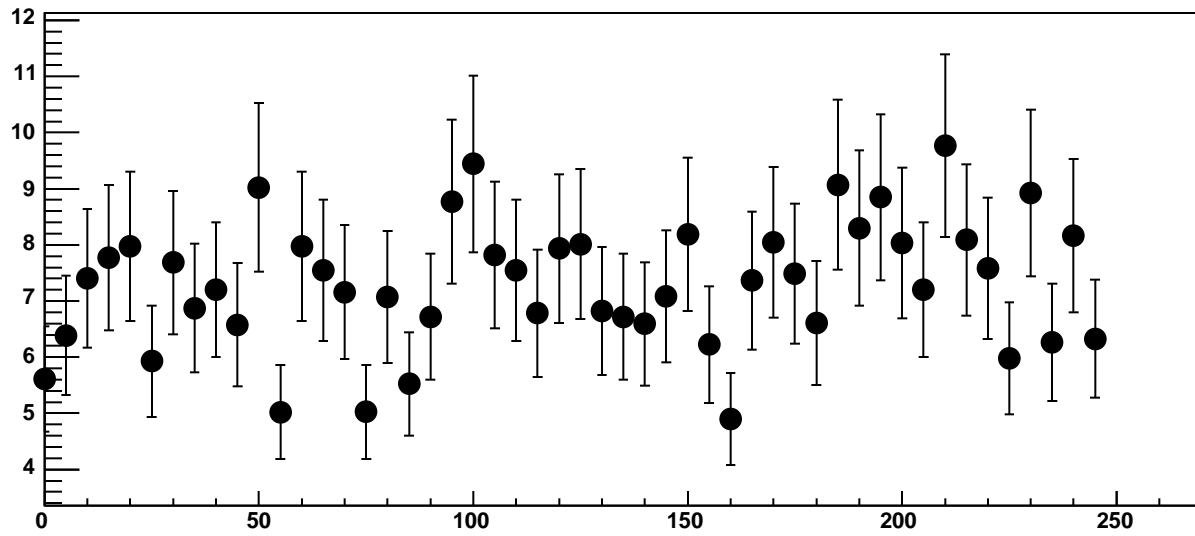


Chip 2, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

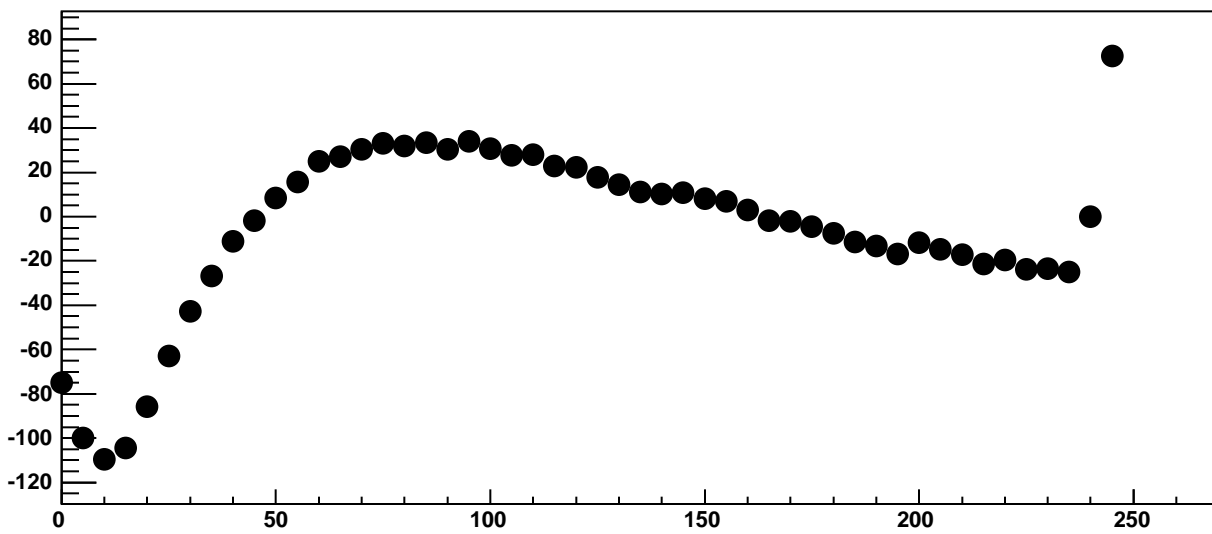


$\chi^2 / \text{ndf}$	1.506e+04 / 41
p0	298.2 ± 0.4946
p1	238.2 ± 0.1325
p2	-552.4 ± 11.54
p3	34.38 ± 0.5407
p4	0.1918 ± 0.003775

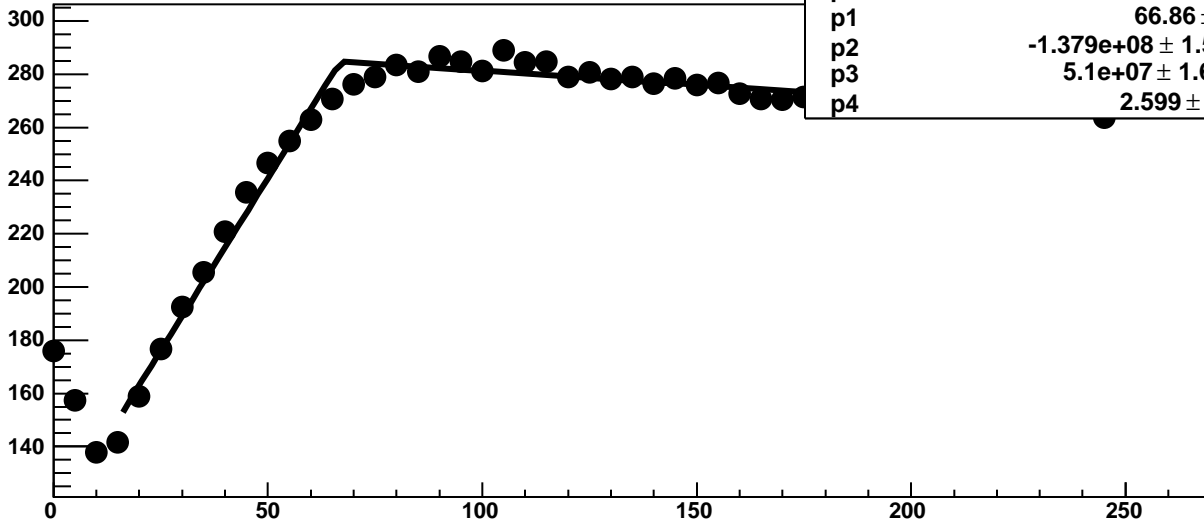
Chip 2, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

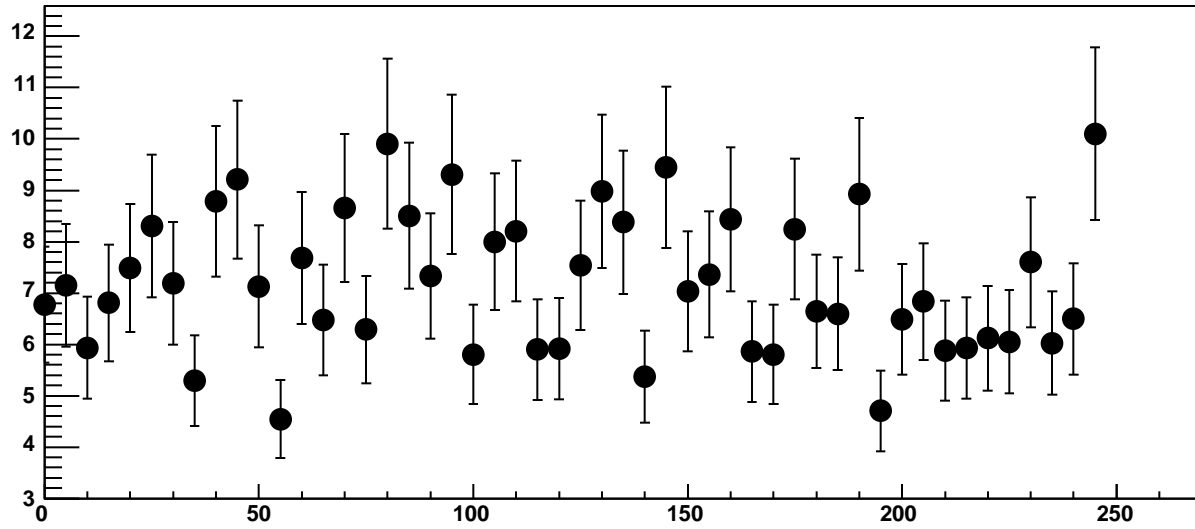


Chip 2, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

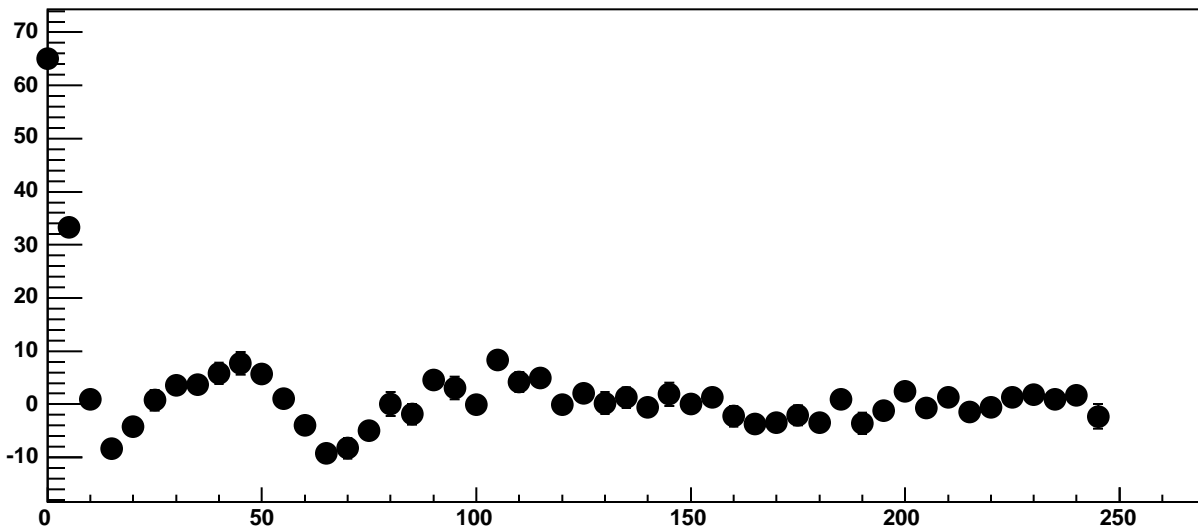


$\chi^2 / \text{ndf}$	244 / 41
p0	$284.8 \pm 0.5486$
p1	$66.86 \pm 0.3839$
p2	$-1.379\text{e}+08 \pm 1.573\text{e}+06$
p3	$5.1\text{e}+07 \pm 1.635\text{e}+05$
p4	$2.599 \pm 0.02936$

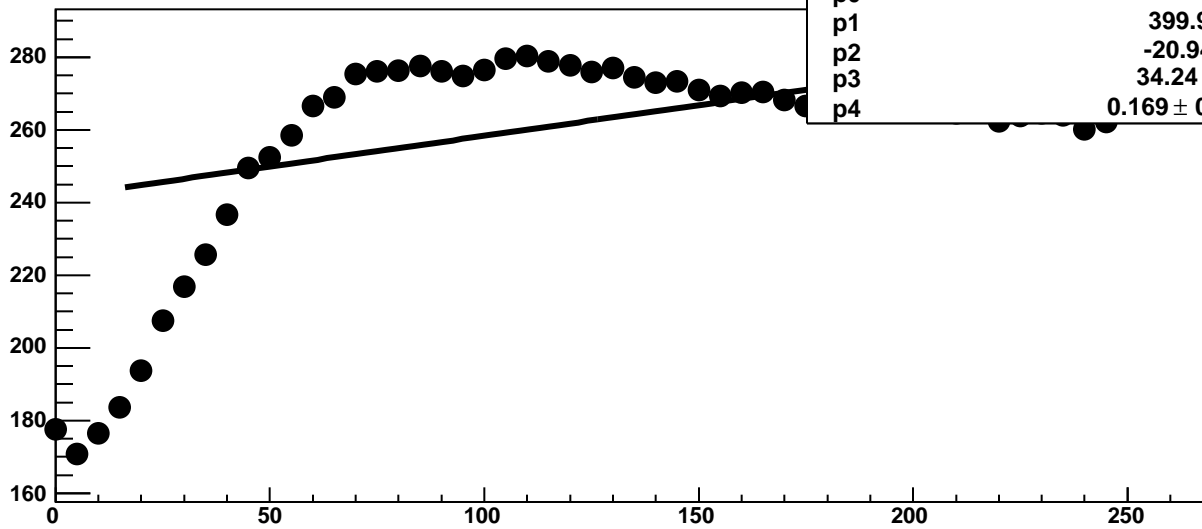
Chip 2, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold

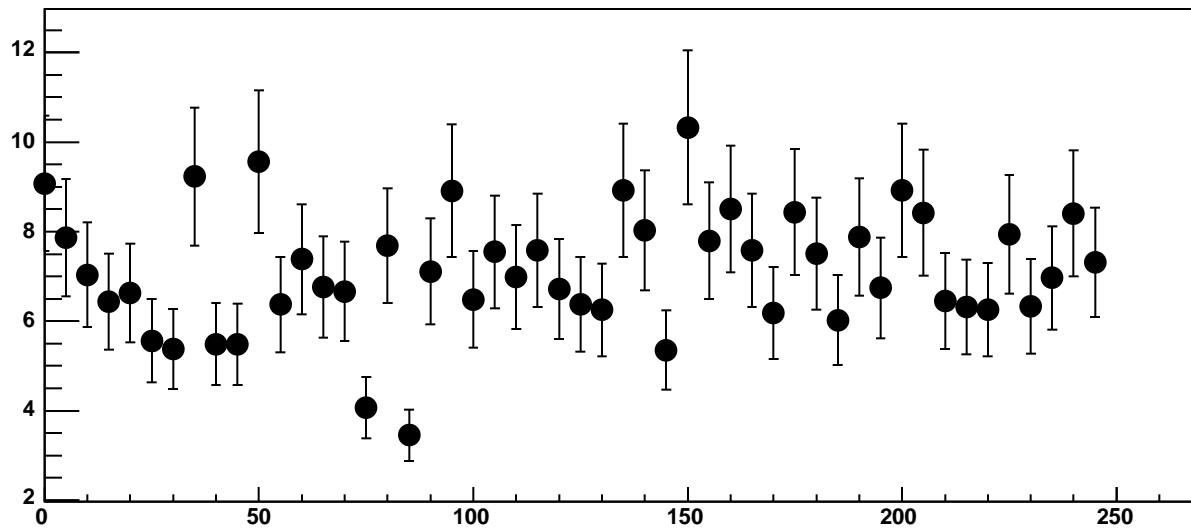


Chip 2, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold

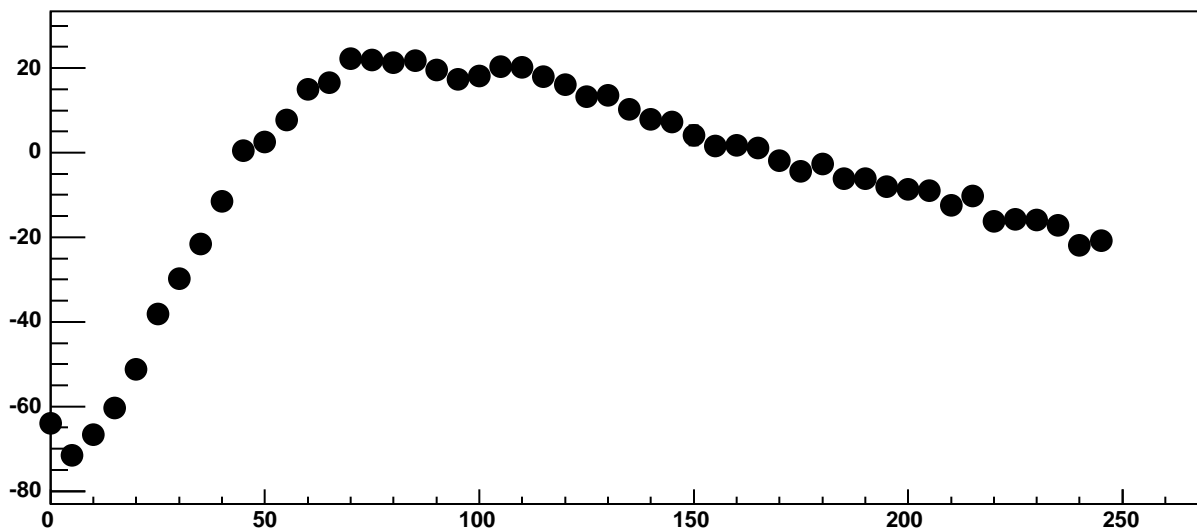


$\chi^2 / \text{ndf}$	8281 / 41
p0	$309.1 \pm 0.6103$
p1	$399.9 \pm 2.304$
p2	$-20.94 \pm 5.371$
p3	$34.24 \pm 0.3115$
p4	$0.169 \pm 0.003379$

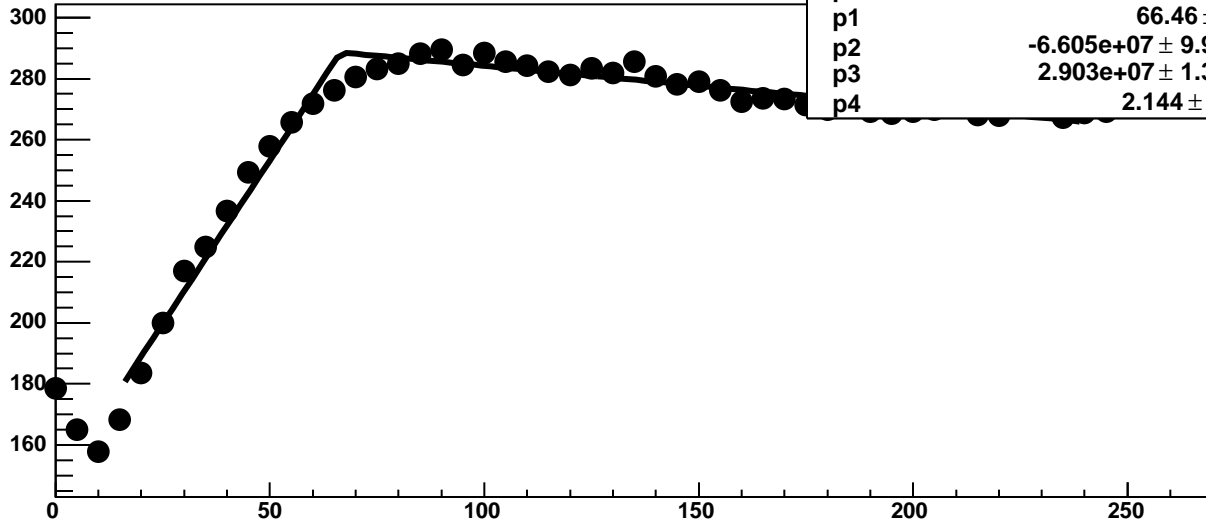
Chip 2, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold

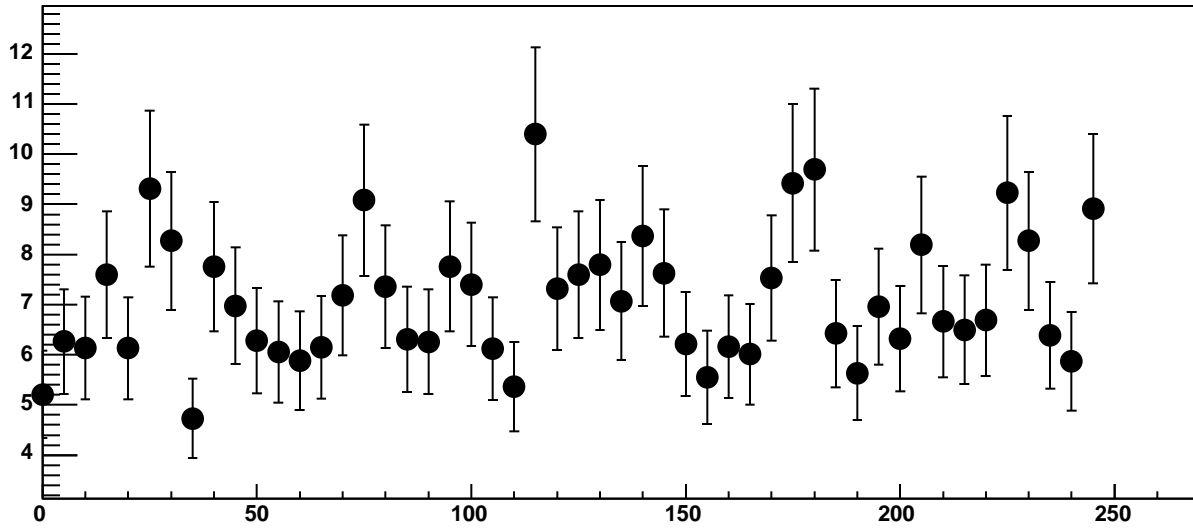


Chip 2, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

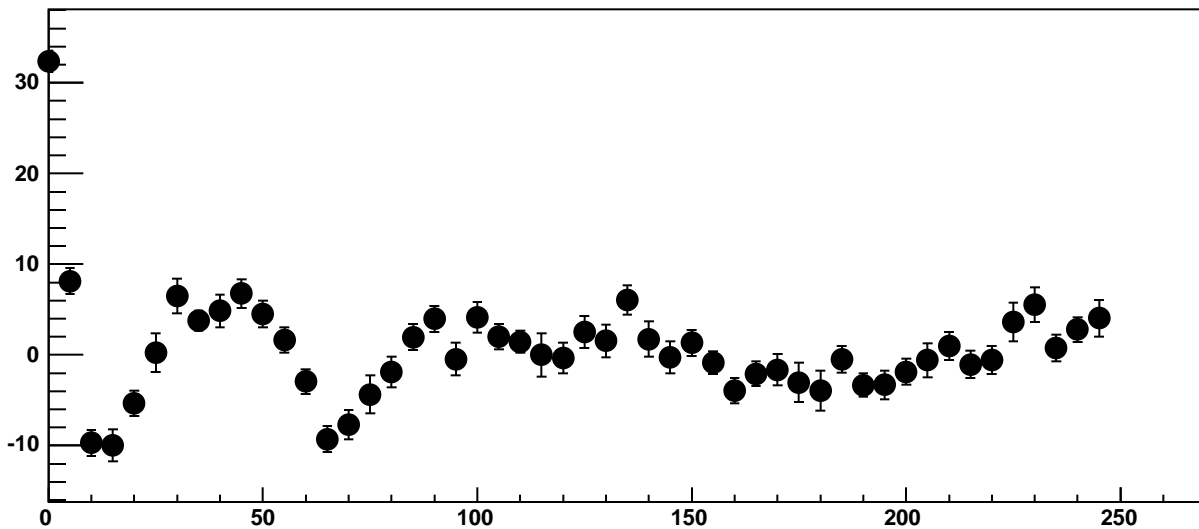


$\chi^2 / \text{ndf}$	266.9 / 41
p0	$288.6 \pm 0.5219$
p1	$66.46 \pm 0.4559$
p2	$-6.605e+07 \pm 9.938e+05$
p3	$2.903e+07 \pm 1.399e+05$
p4	$2.144 \pm 0.03145$

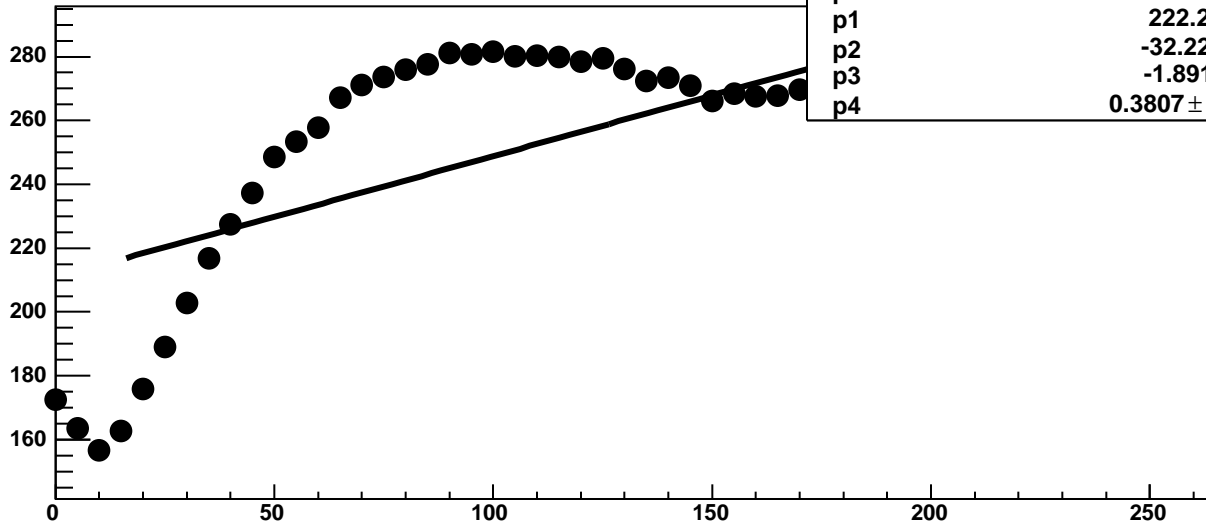
Chip 2, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

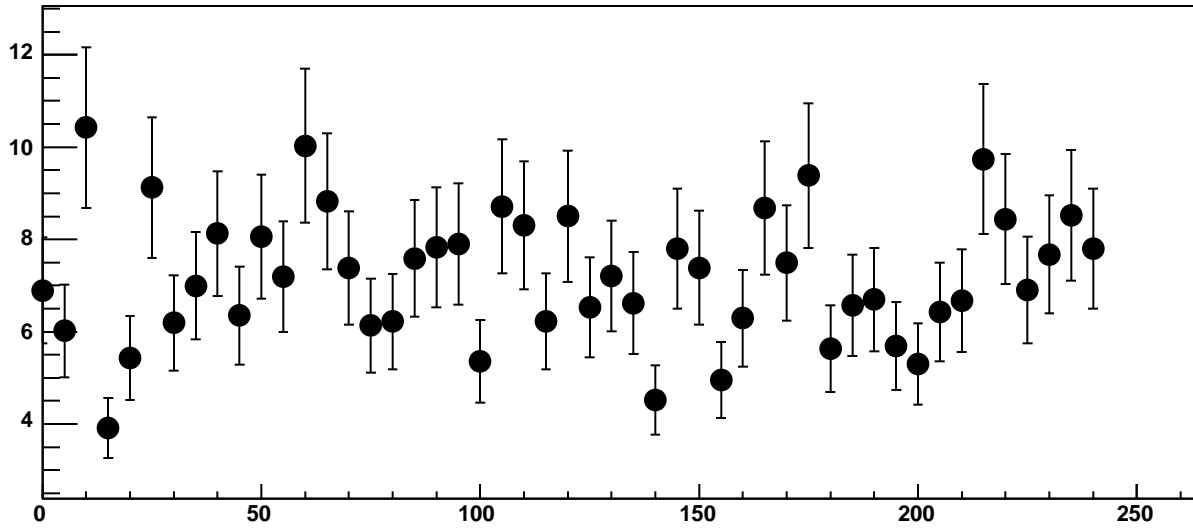


Chip 2, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold

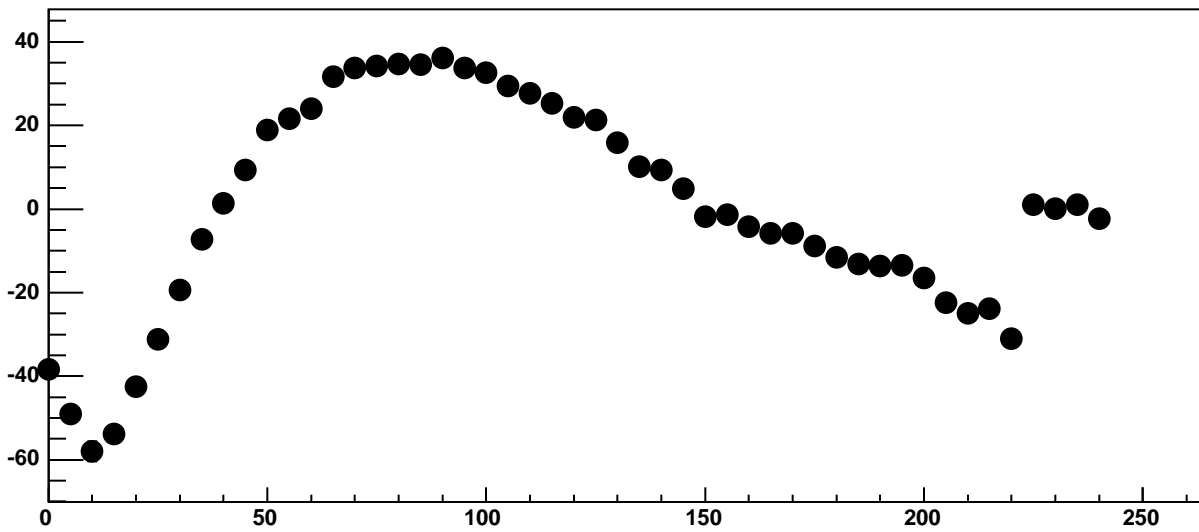


$\chi^2 / \text{ndf}$	1.202e+04 / 41
p0	295.4 ± 1.055
p1	222.2 ± 6.908
p2	-32.22 ± 3.228
p3	-1.891 ± 9.742
p4	0.3807 ± 0.00453

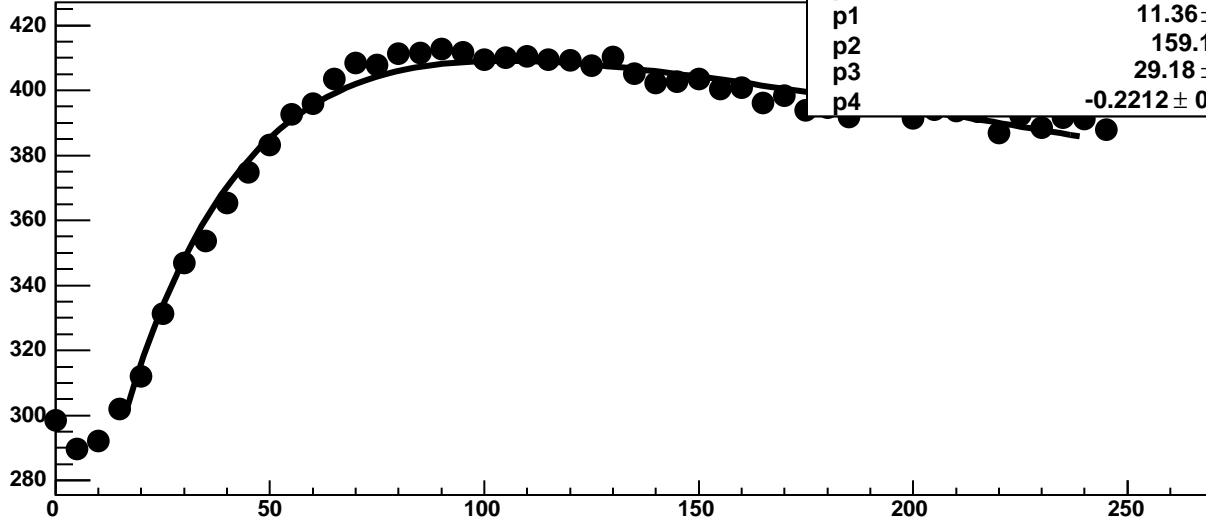
Chip 2, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold

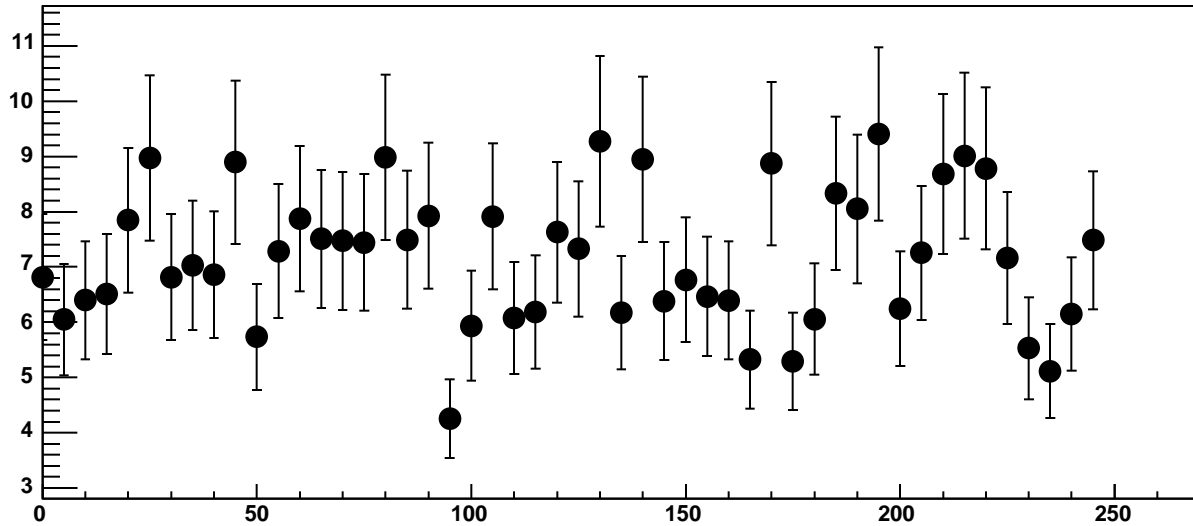


Chip 2, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold

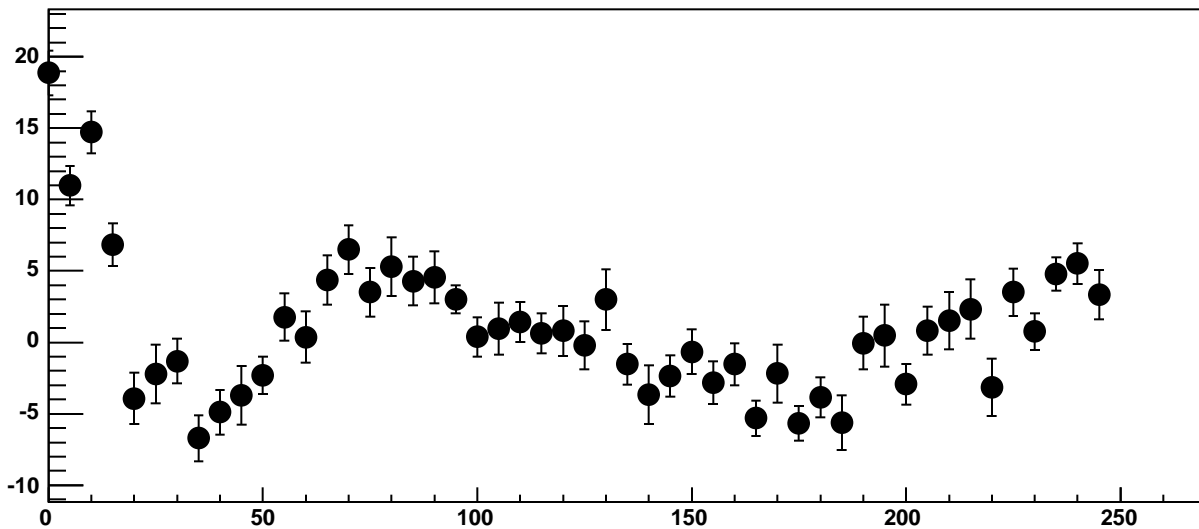


$\chi^2 / \text{ndf}$	235.3 / 41
p0	$277.2 \pm 2.942$
p1	$11.36 \pm 0.6104$
p2	$159.1 \pm 3.083$
p3	$29.18 \pm 0.6254$
p4	$-0.2212 \pm 0.007603$

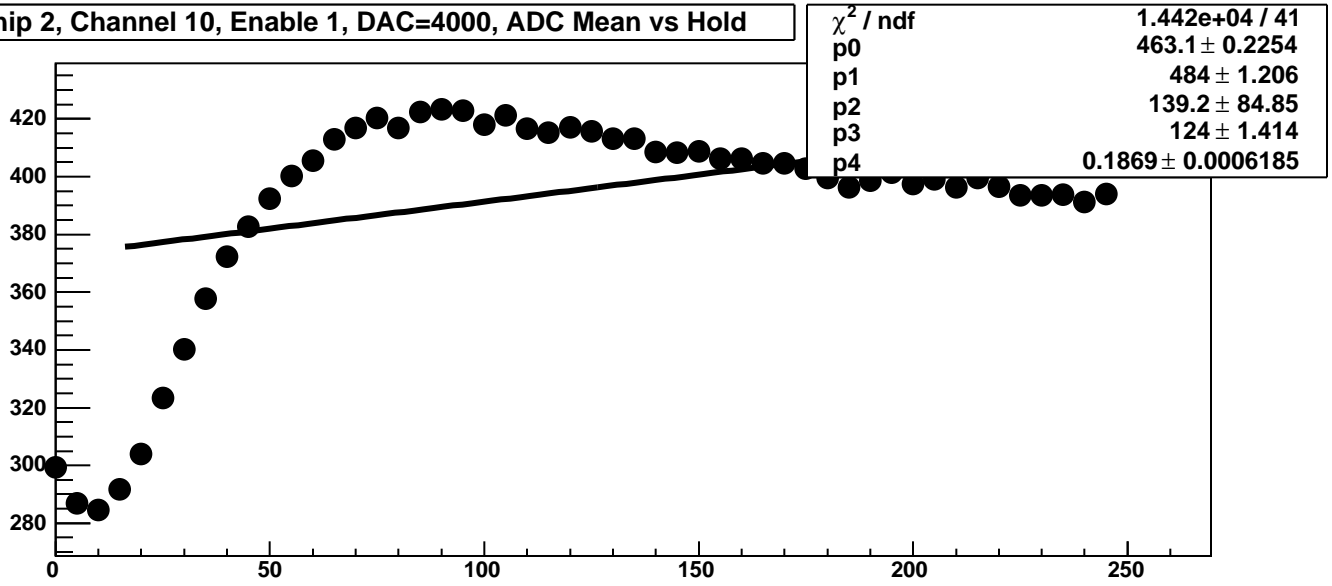
Chip 2, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold



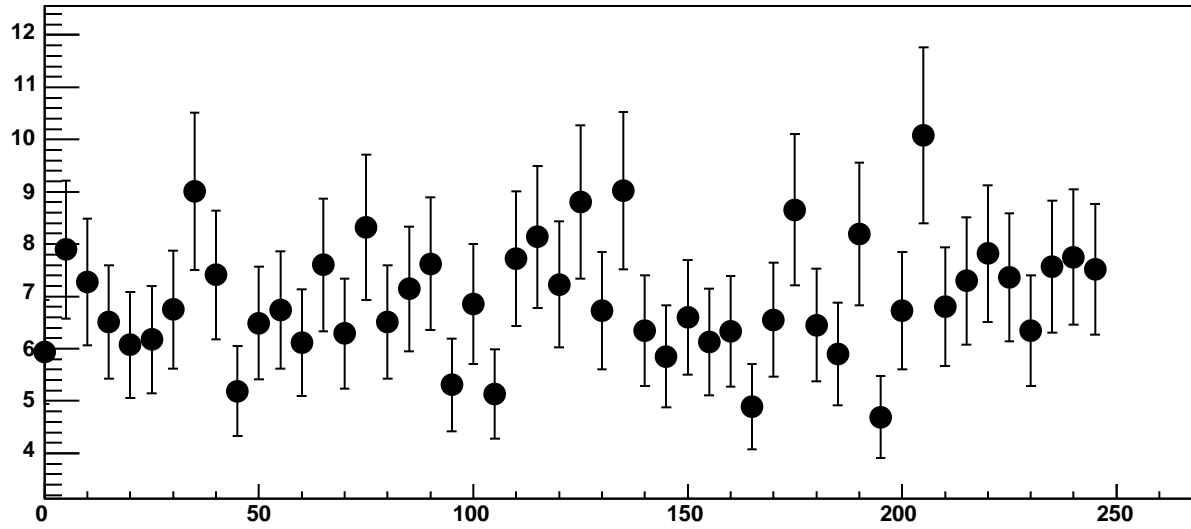
Chip 2, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold



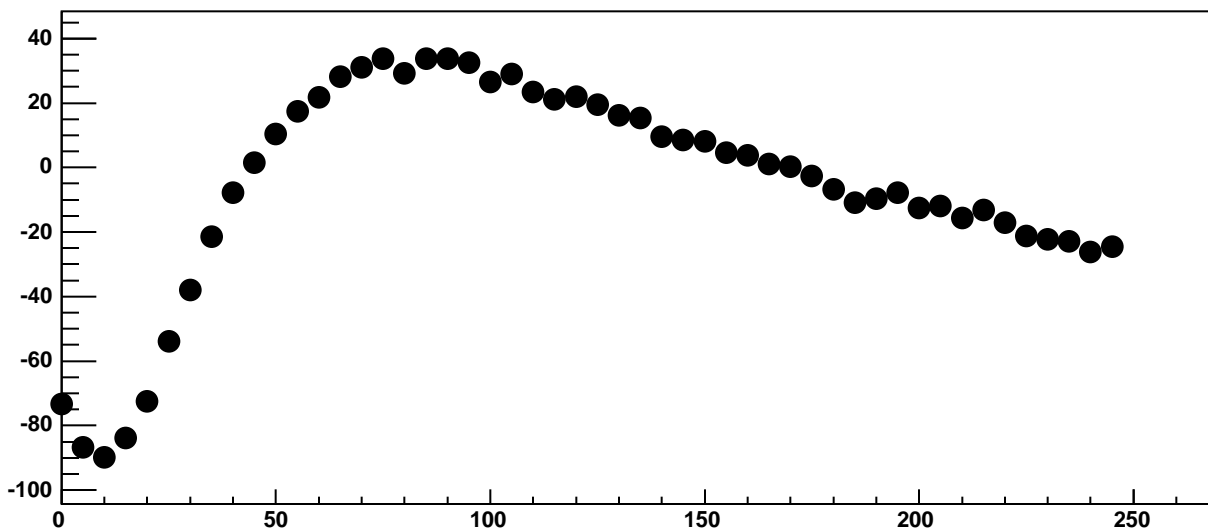
Chip 2, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 2, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold

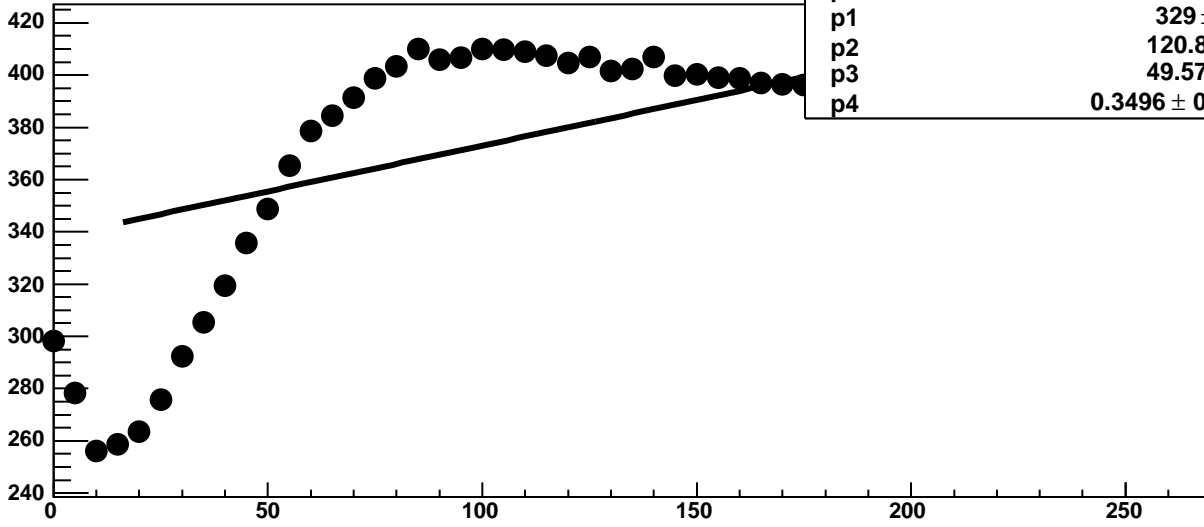


Chip 2, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold



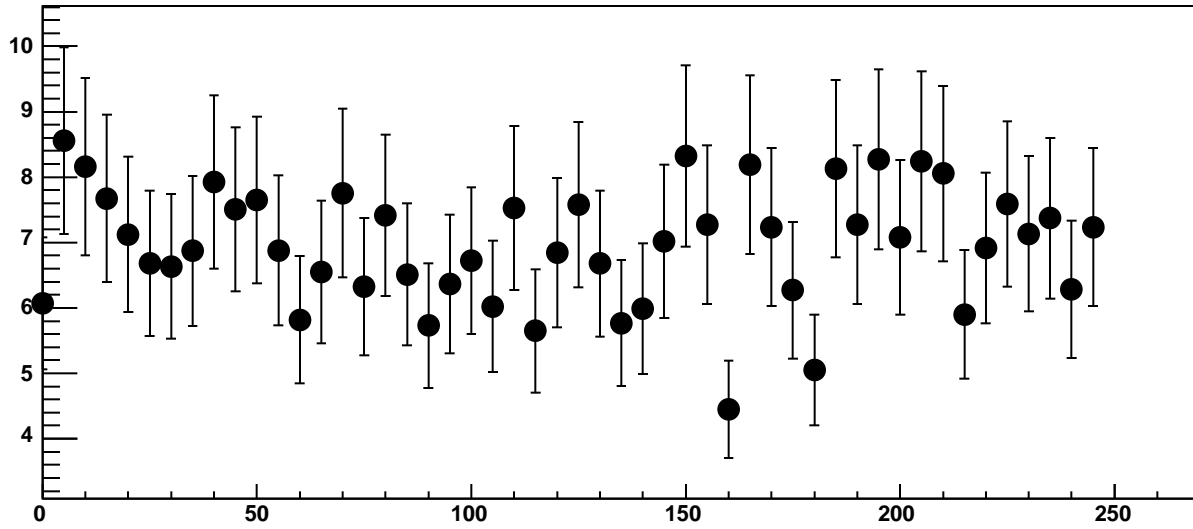


Chip 2, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

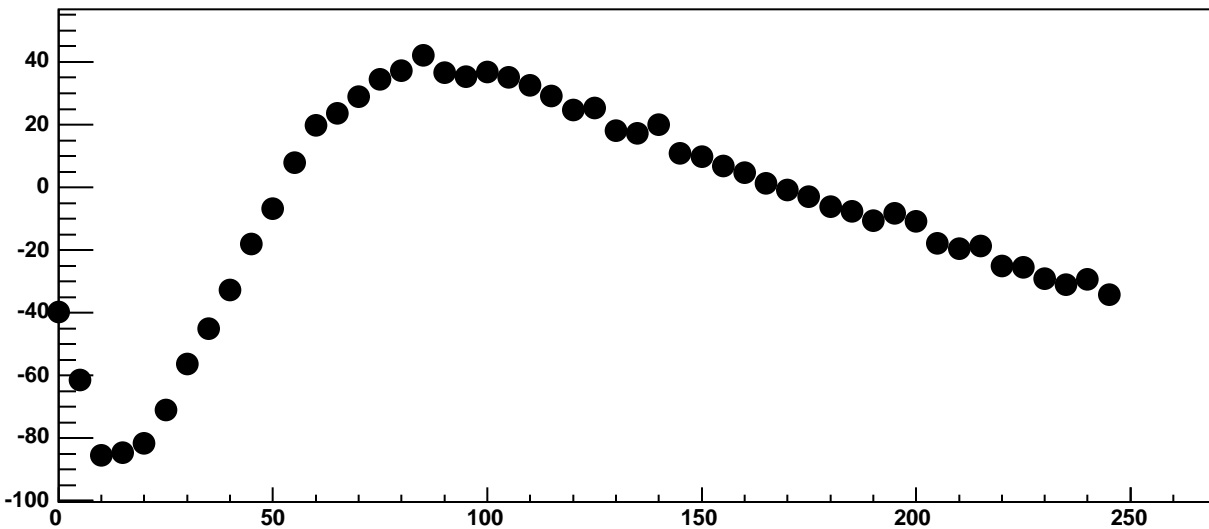


$\chi^2 / \text{ndf}$	1.879e+04 / 41
p0	453.1 ± 0.2276
p1	329 ± 0.6509
p2	120.8 ± 84.85
p3	49.57 ± 1.414
p4	0.3496 ± 0.001077

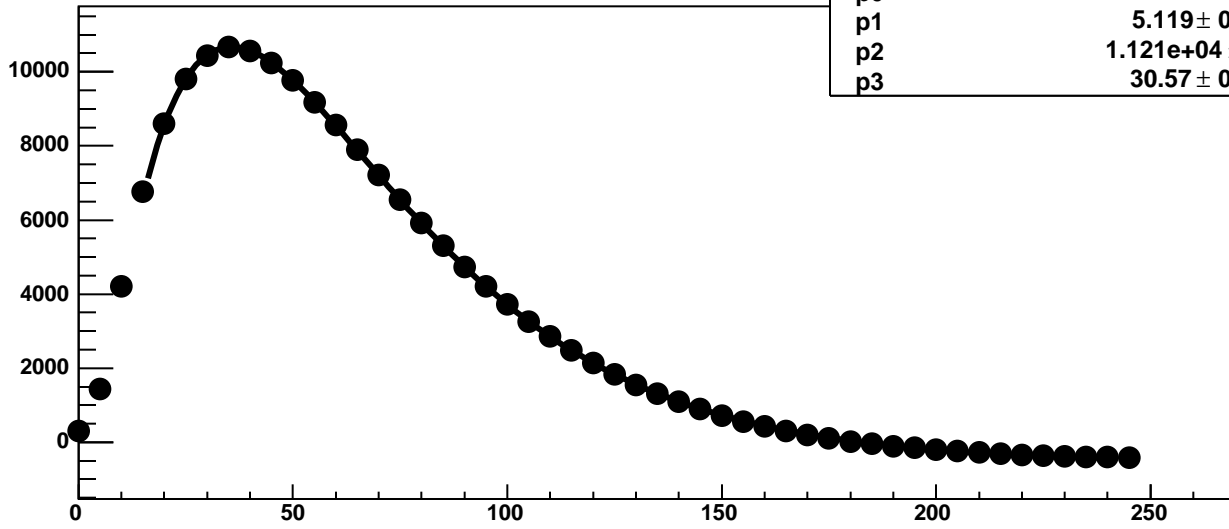
Chip 2, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

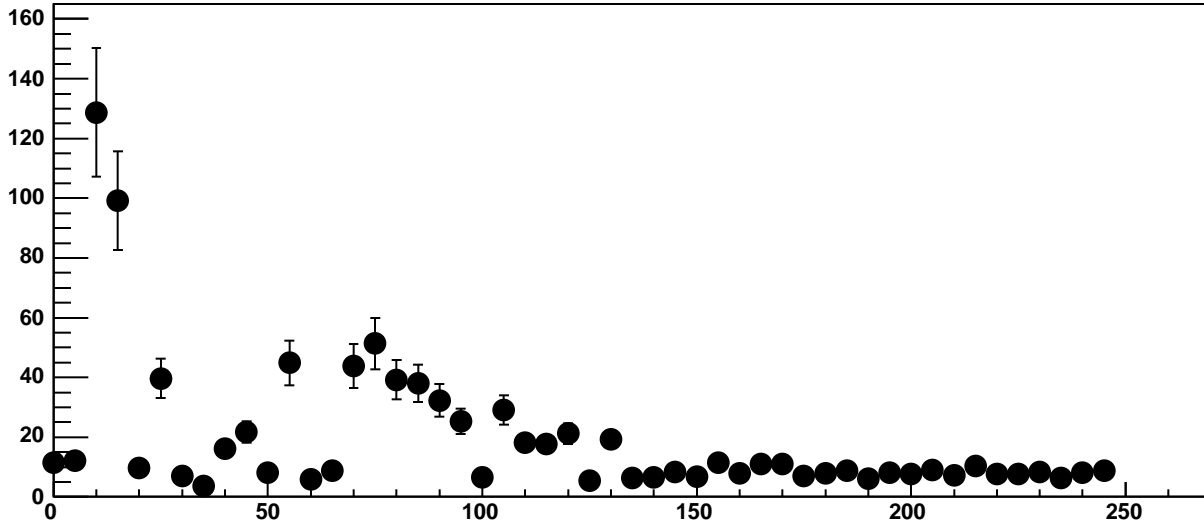


Chip 2, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold

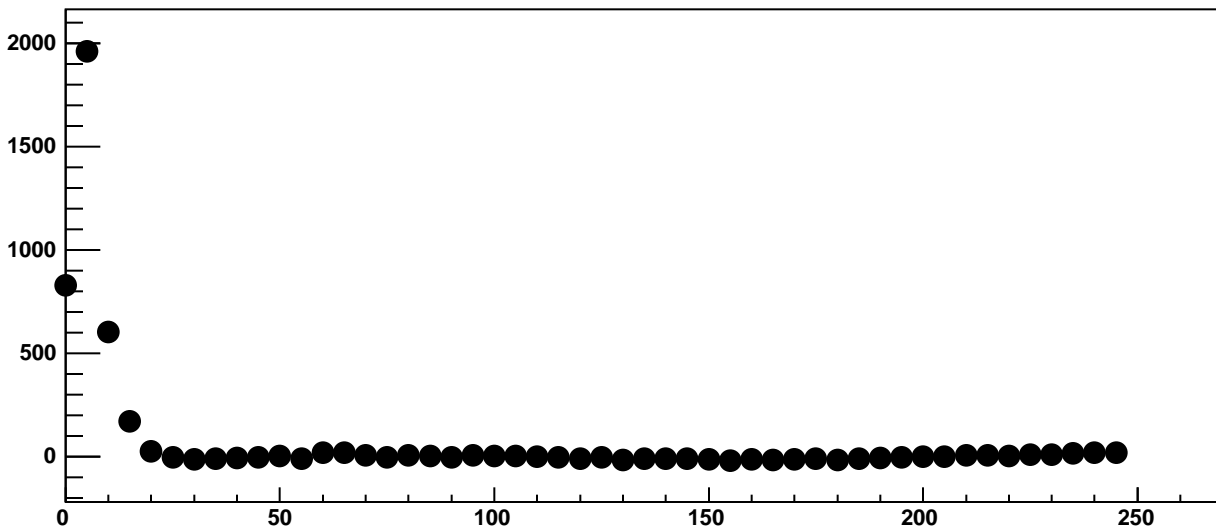


$\chi^2 / \text{ndf}$	1505 / 42
p0	-533.4 ± 0.5973
p1	5.119 ± 0.008199
p2	1.121e+04 ± 0.7891
p3	30.57 ± 0.005308

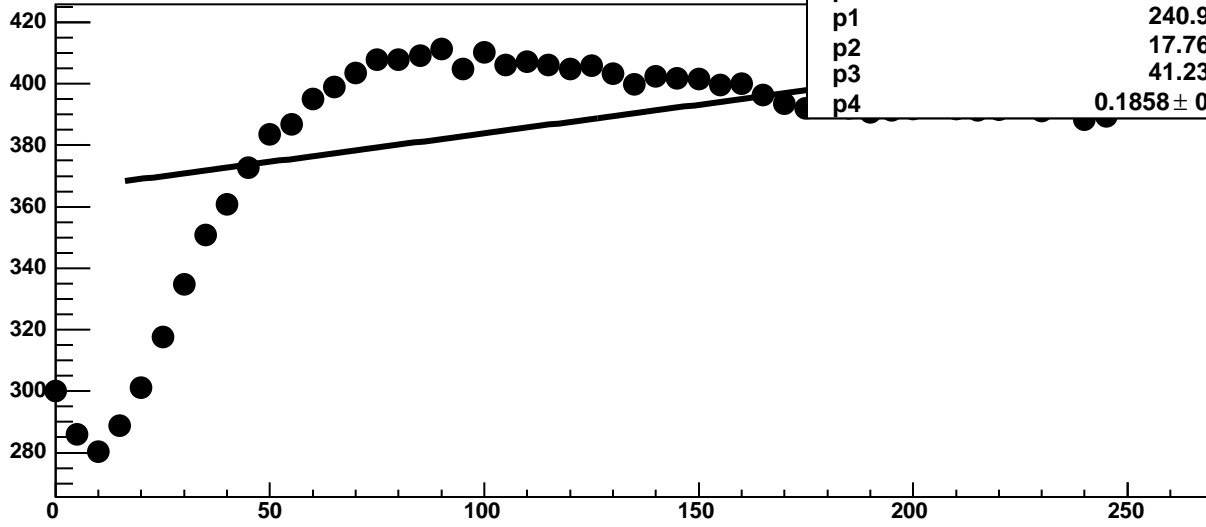
Chip 2, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold

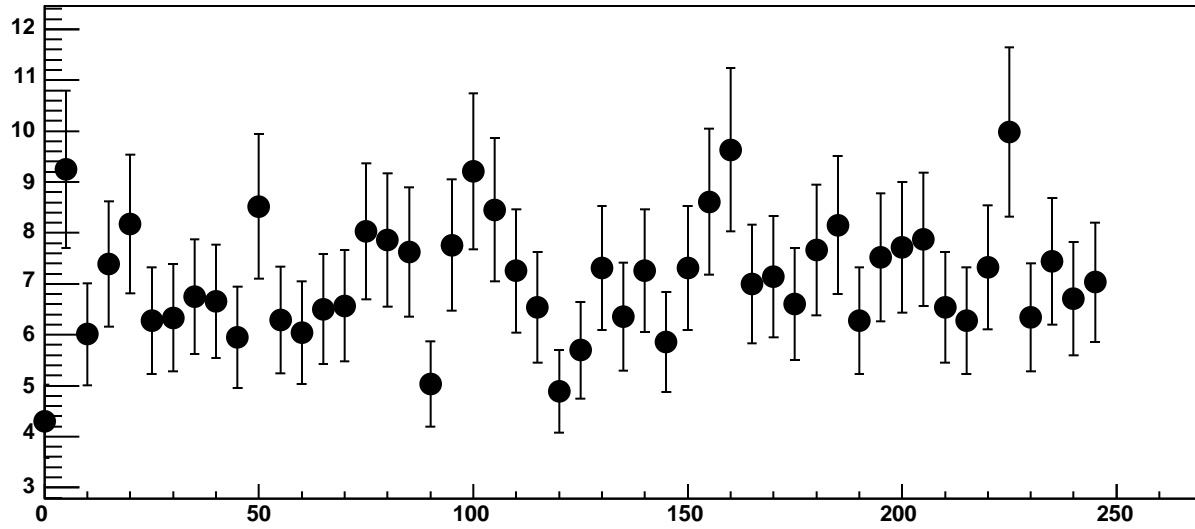


Chip 2, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

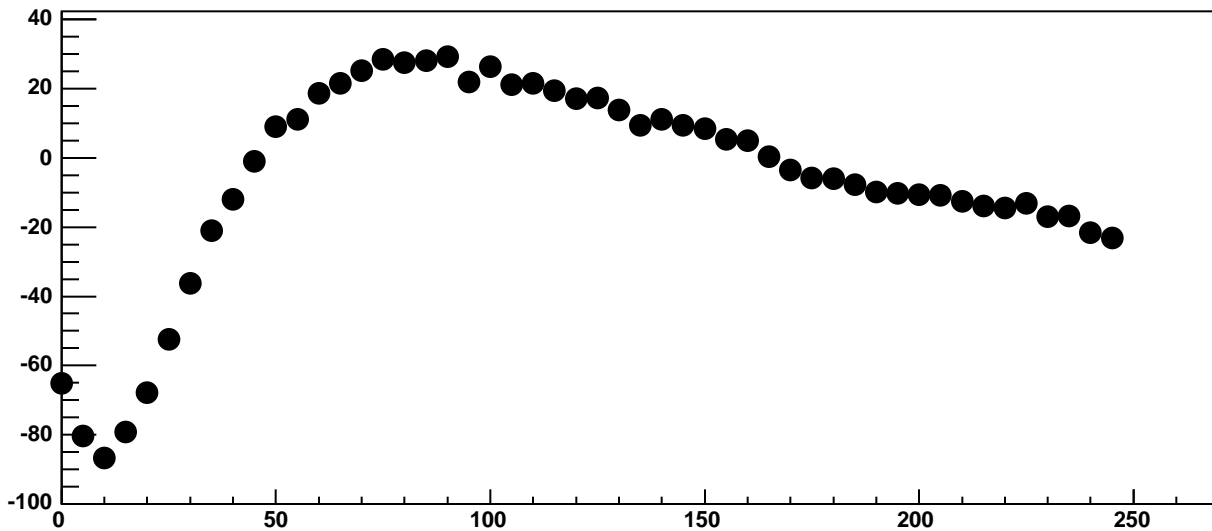


$\chi^2 / \text{ndf}$	1.013e+04 / 41
p0	410.1 ± 0.2337
p1	240.9 ± 1.258
p2	17.76 ± 84.85
p3	41.23 ± 1.414
p4	0.1858 ± 0.001749

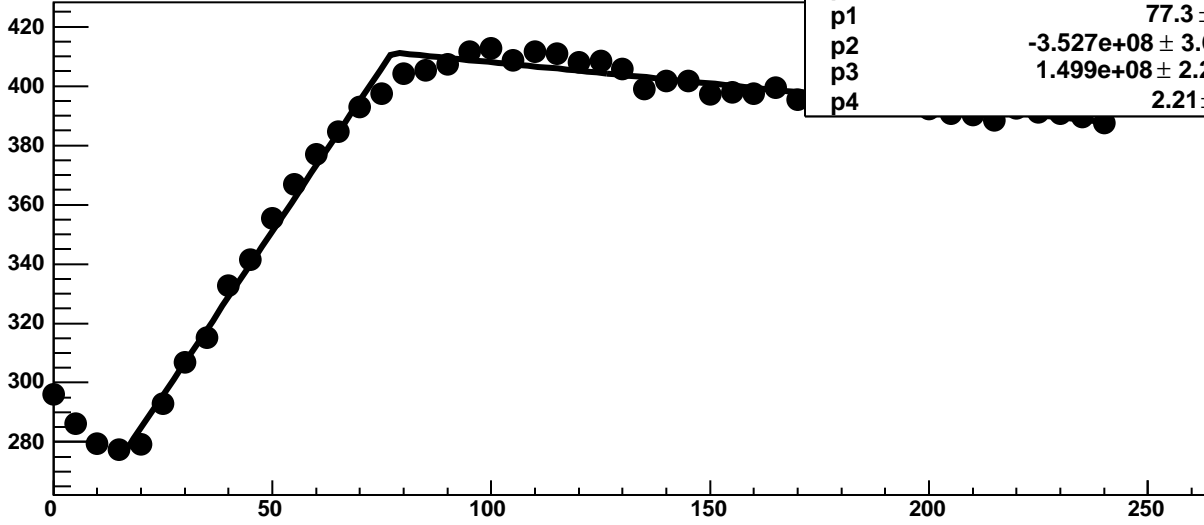
Chip 2, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



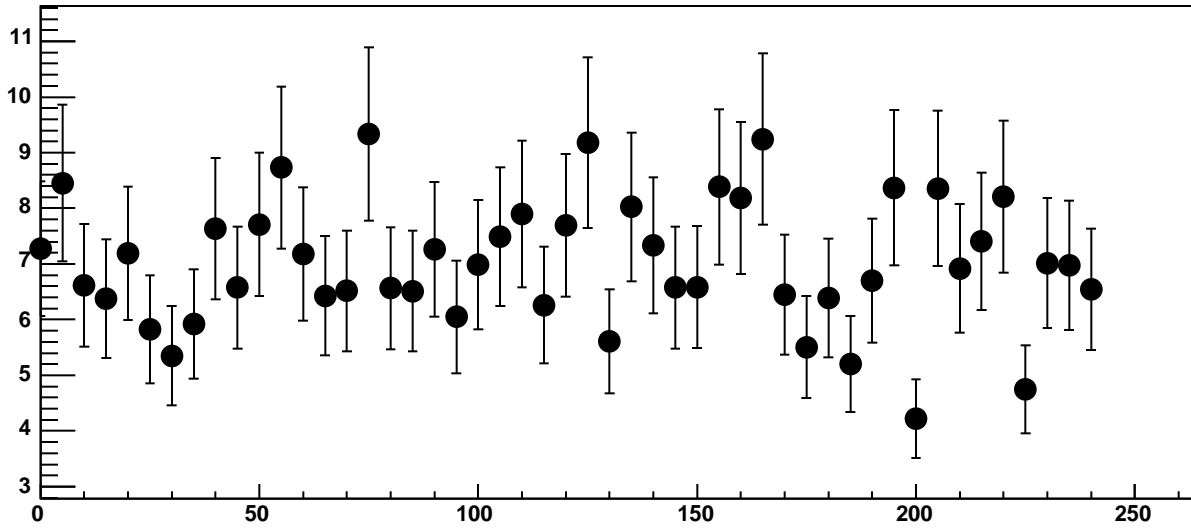
Chip 2, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold



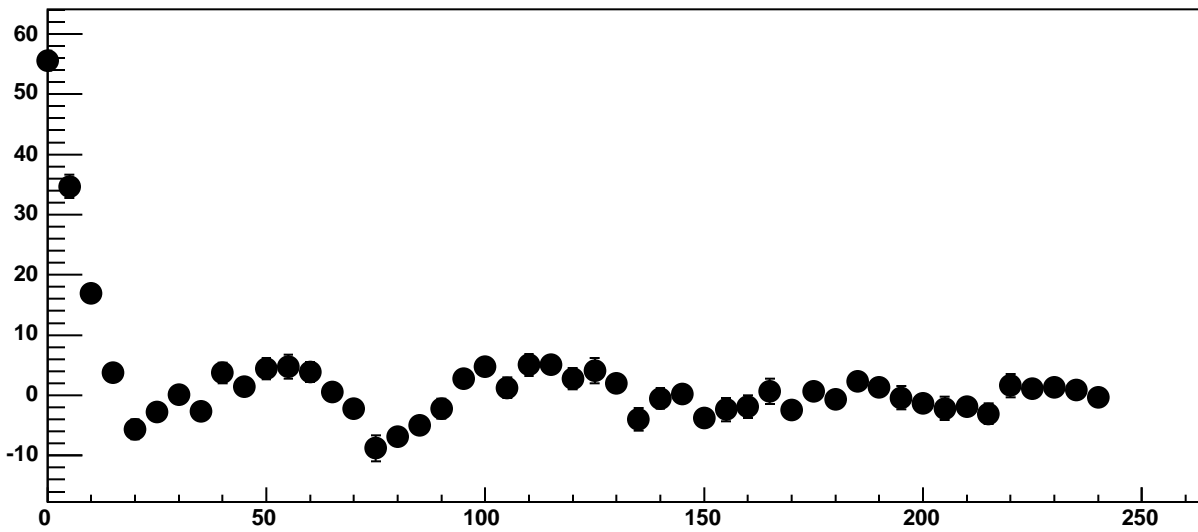
Chip 2, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold



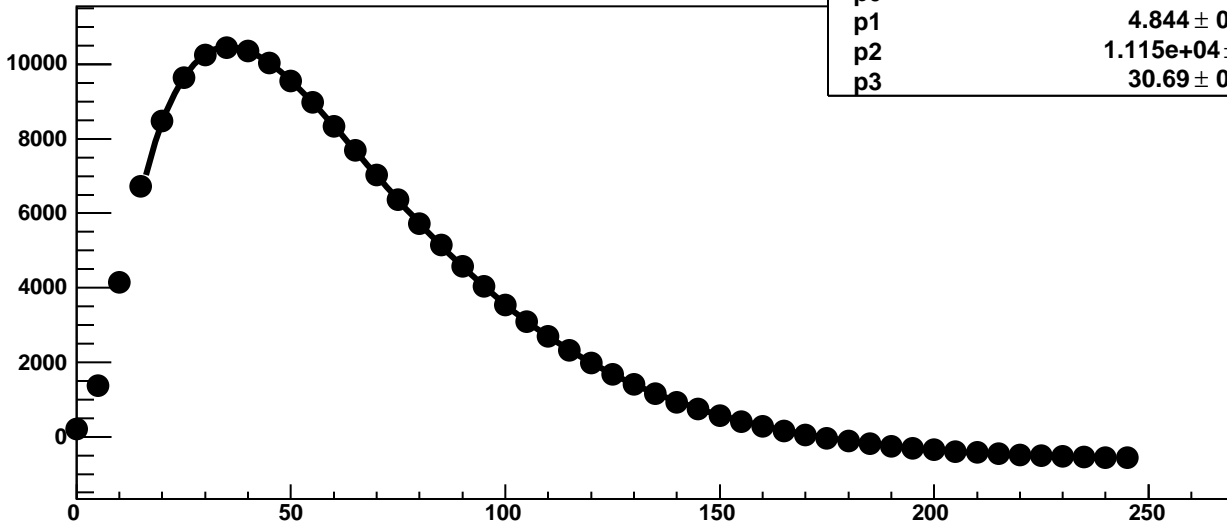
Chip 2, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

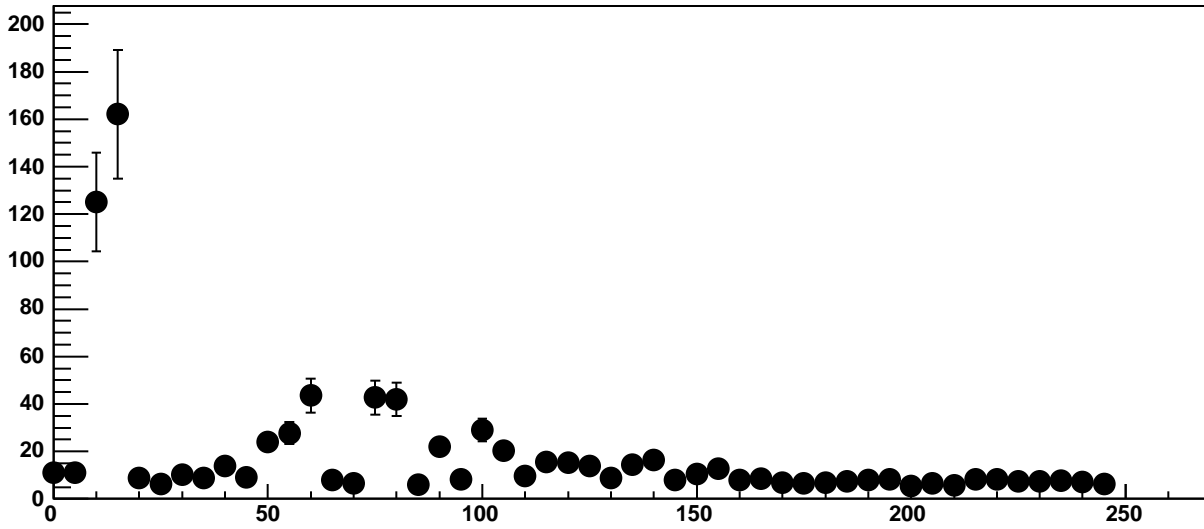


Chip 2, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold

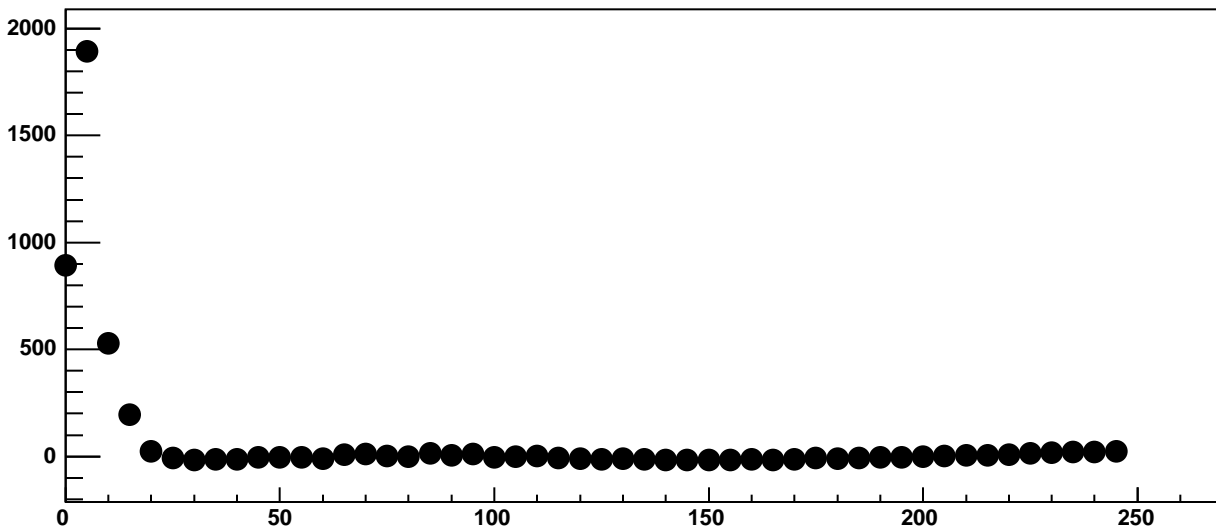


$\chi^2 / \text{ndf}$	1540 / 42
p0	-682.6 ± 0.5561
p1	4.844 ± 0.008408
p2	1.115e+04 ± 0.9417
p3	30.69 ± 0.005823

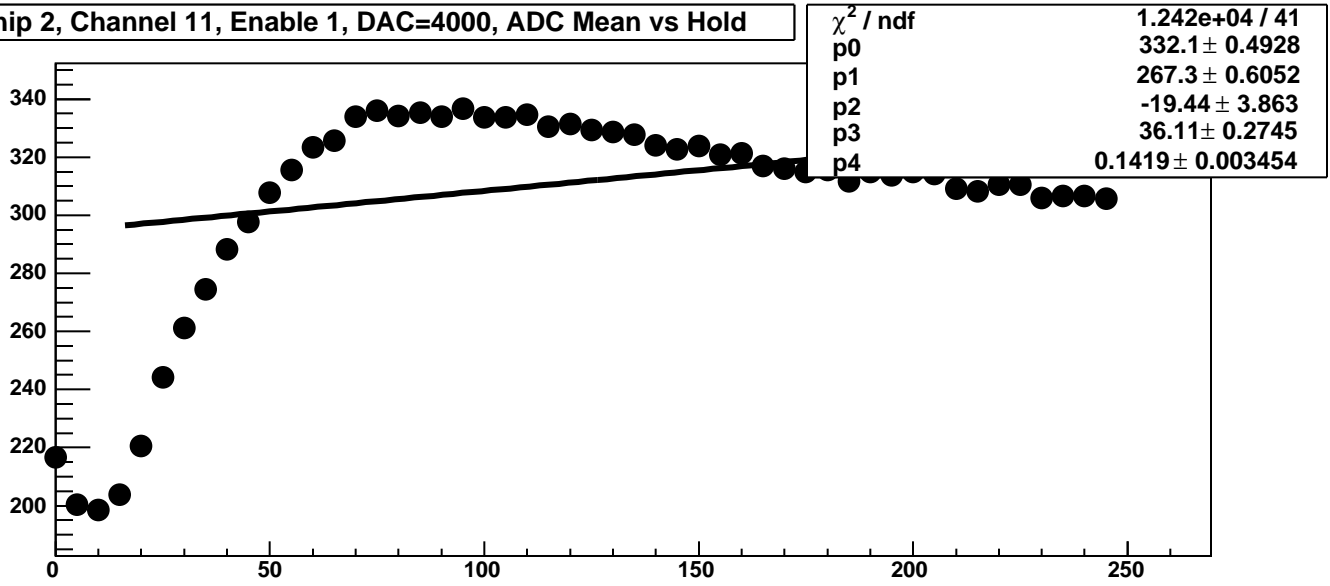
Chip 2, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold



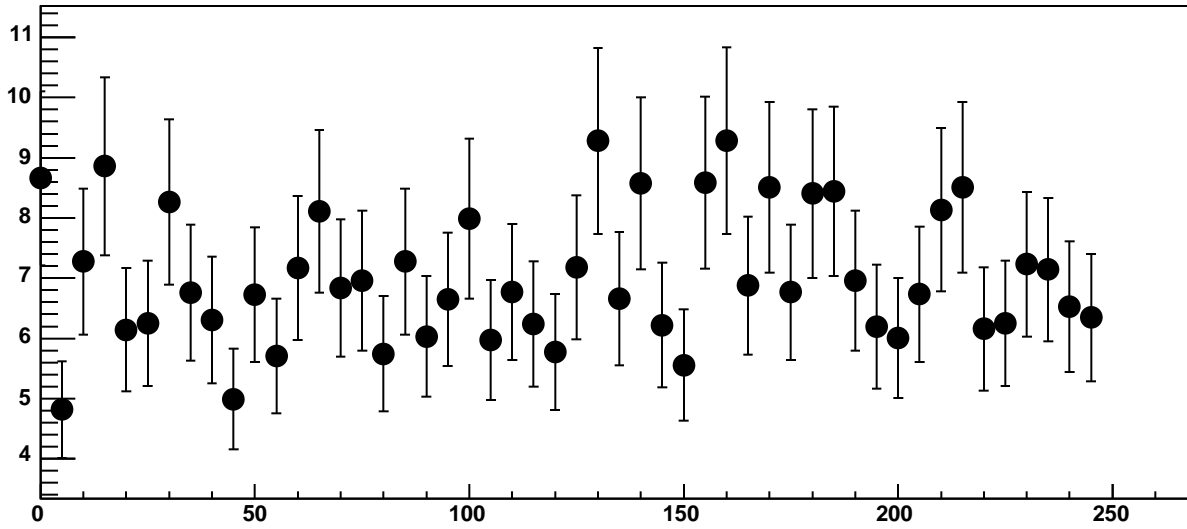
Chip 2, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold



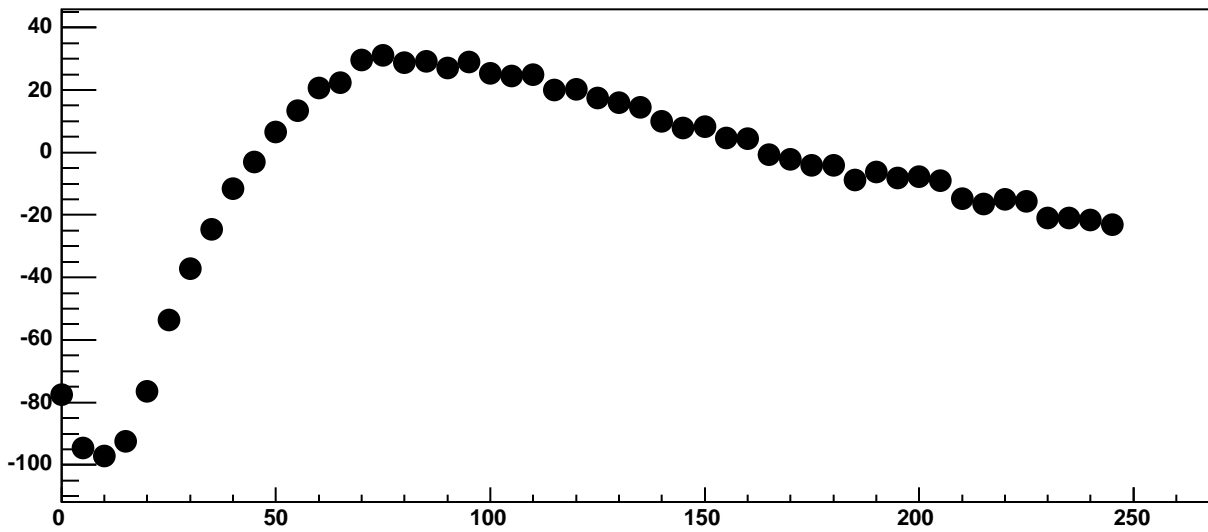
Chip 2, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold



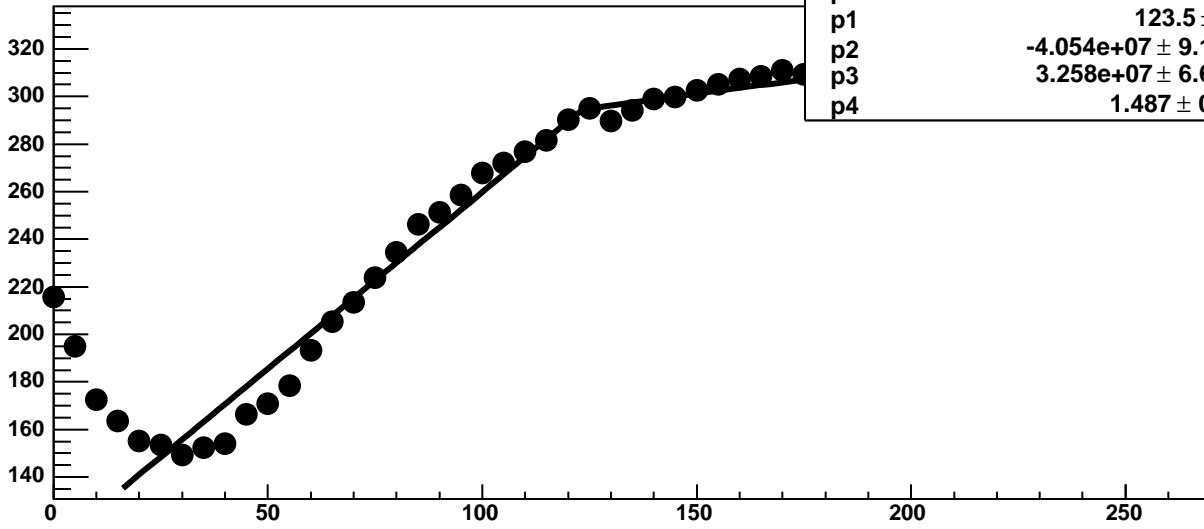
Chip 2, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold

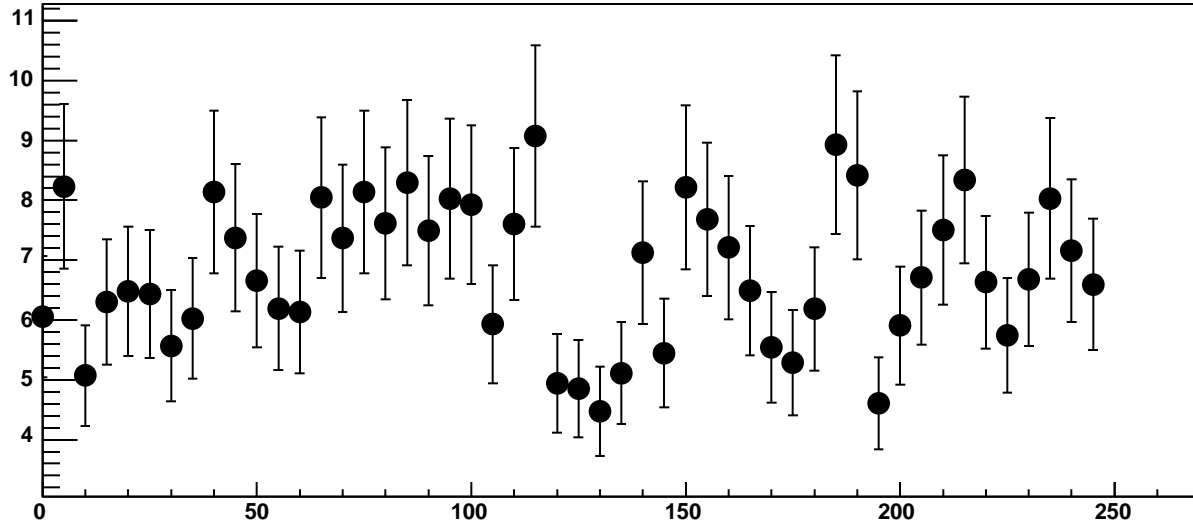


Chip 2, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

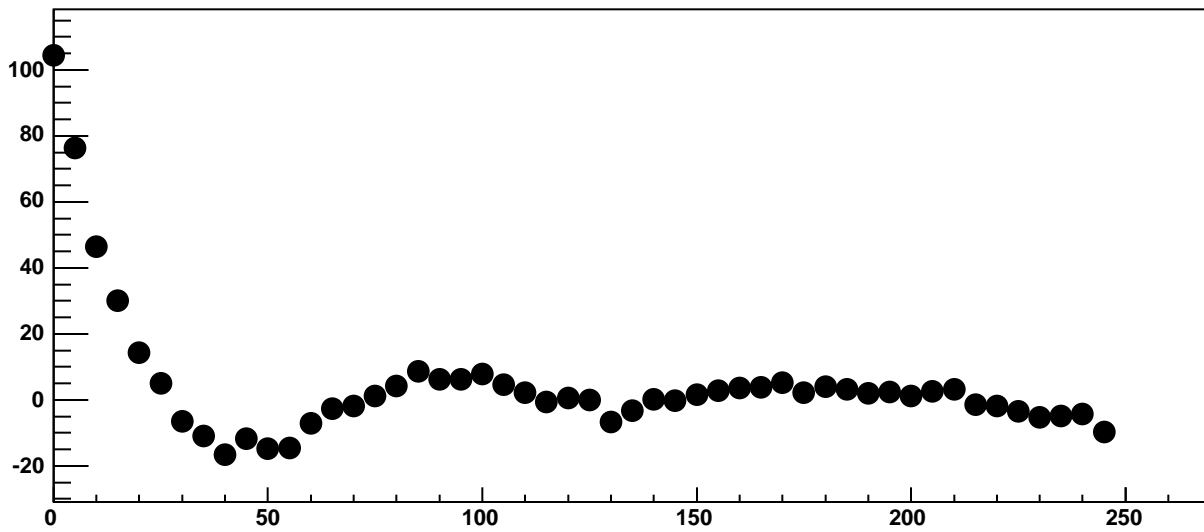


$\chi^2 / \text{ndf}$	1208 / 41
p0	$294.8 \pm 0.6408$
p1	$123.5 \pm 0.6832$
p2	$-4.054e+07 \pm 9.154e+05$
p3	$3.258e+07 \pm 6.661e+05$
p4	$1.487 \pm 0.00961$

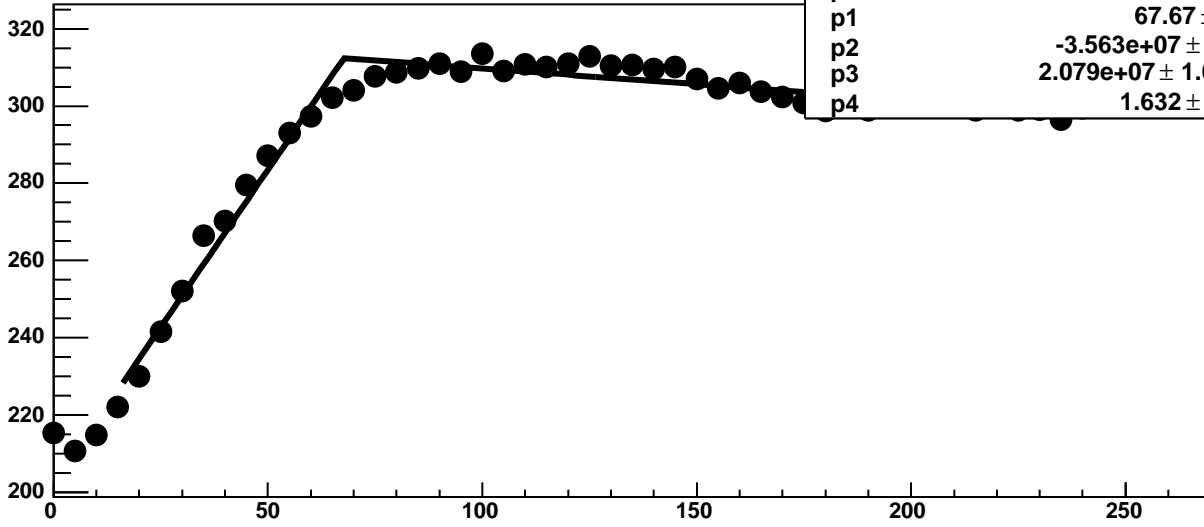
Chip 2, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

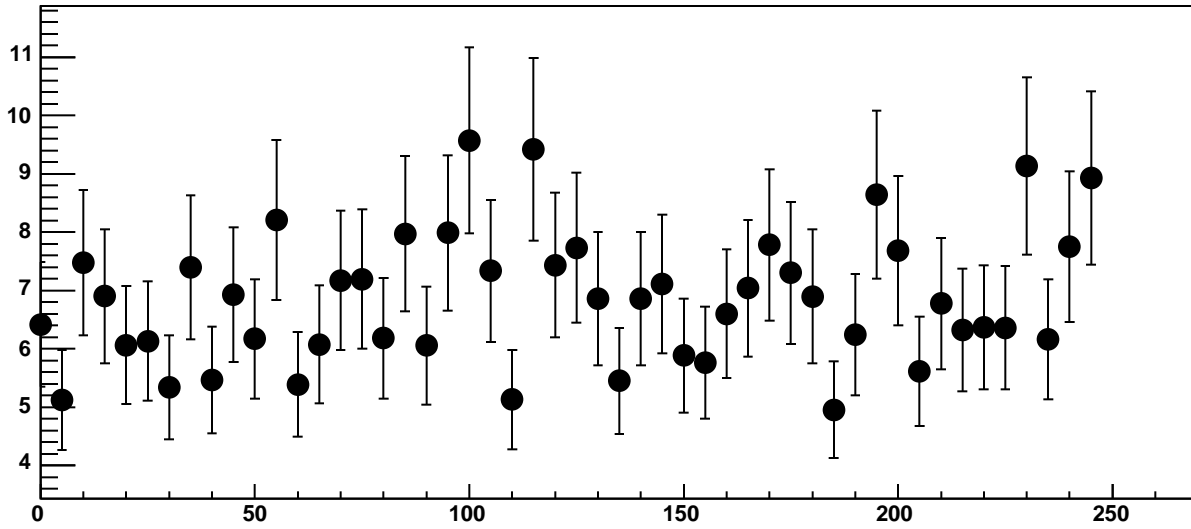


Chip 2, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold

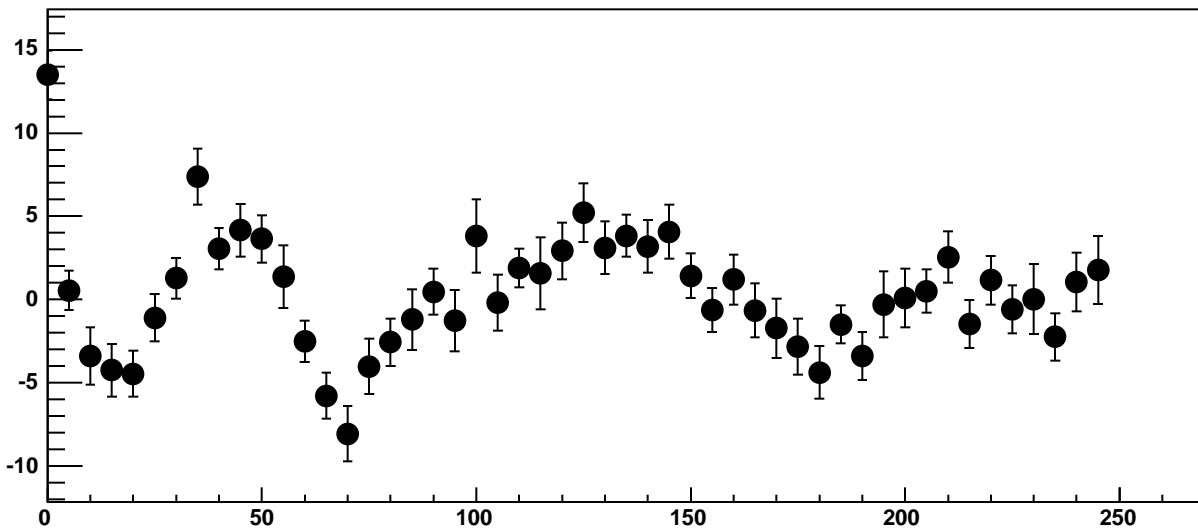


$\chi^2 / \text{ndf}$	183.8 / 41
p0	$312.4 \pm 0.5146$
p1	$67.67 \pm 0.5933$
p2	$-3.563\text{e}+07 \pm 1.9\text{e}+06$
p3	$2.079\text{e}+07 \pm 1.072\text{e}+06$
p4	$1.632 \pm 0.02717$

Chip 2, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold

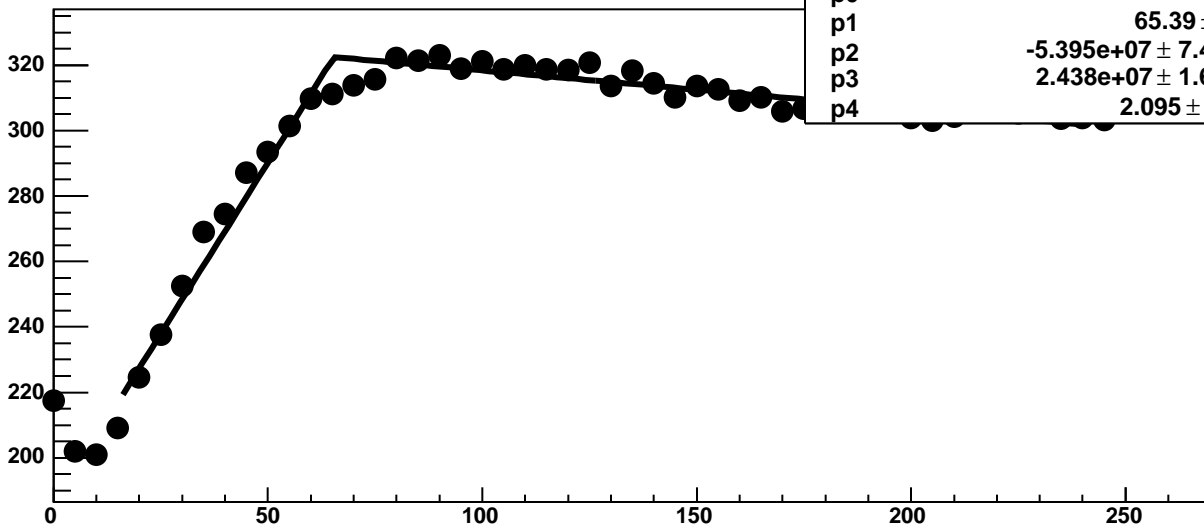


Chip 2, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold



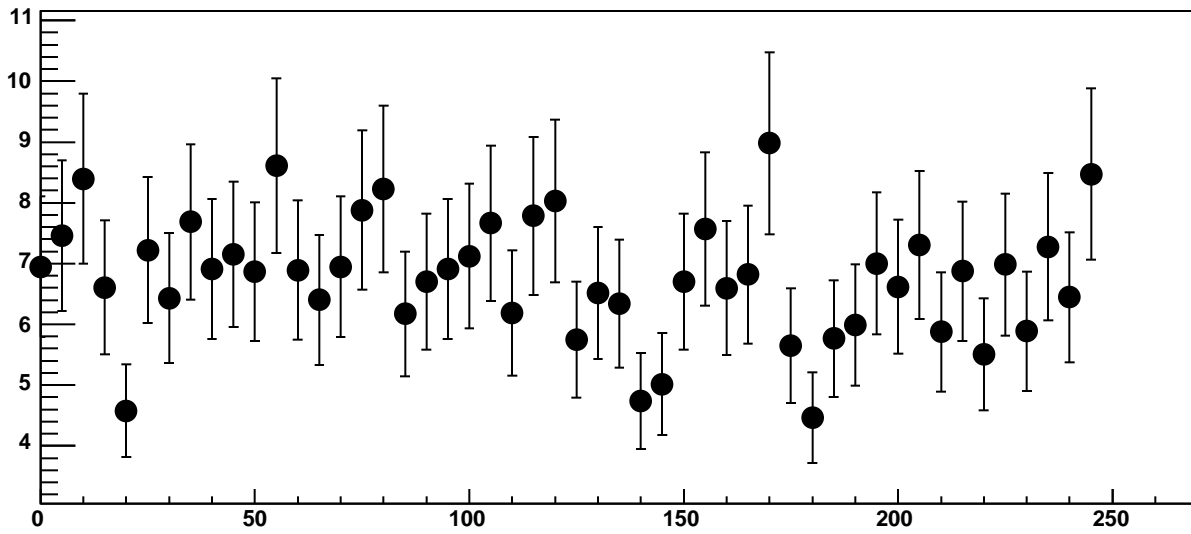


Chip 2, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

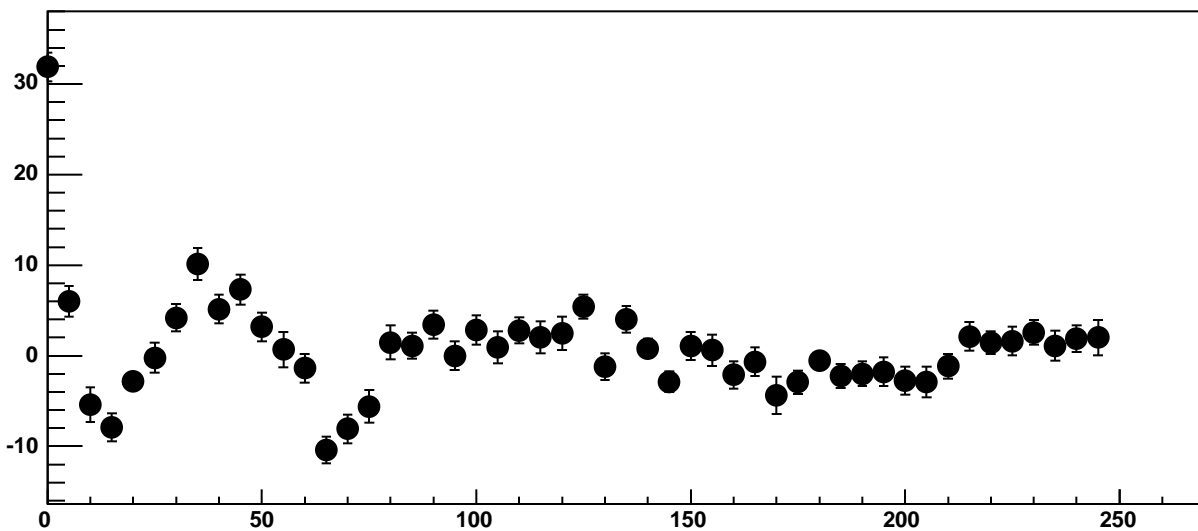


$\chi^2 / \text{ndf}$	279.8 / 41
p0	$322.5 \pm 0.4912$
p1	$65.39 \pm 0.4697$
p2	$-5.395e+07 \pm 7.488e+05$
p3	$2.438e+07 \pm 1.633e+05$
p4	$2.095 \pm 0.02767$

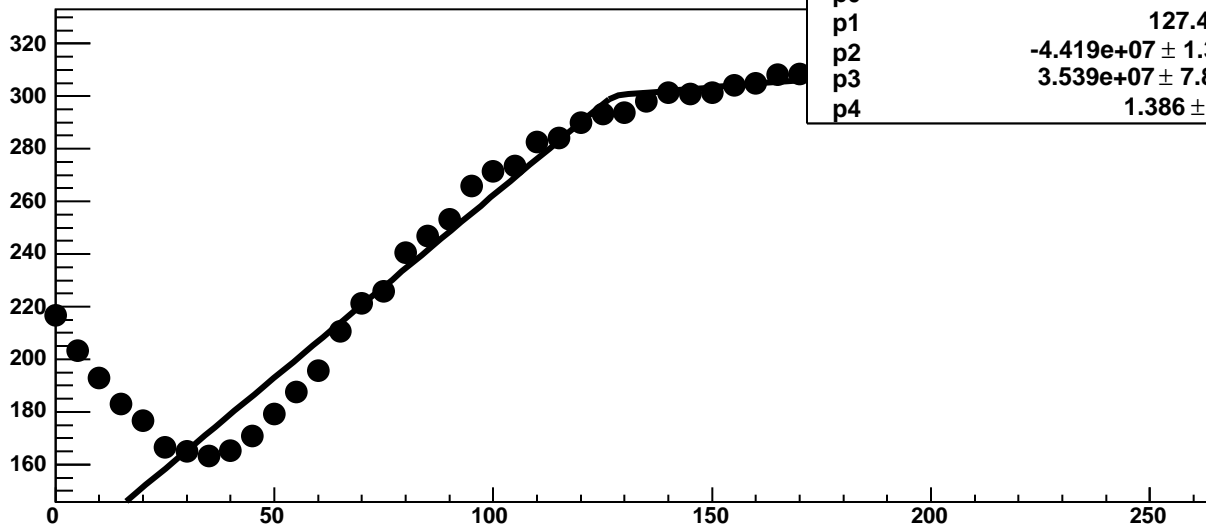
Chip 2, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

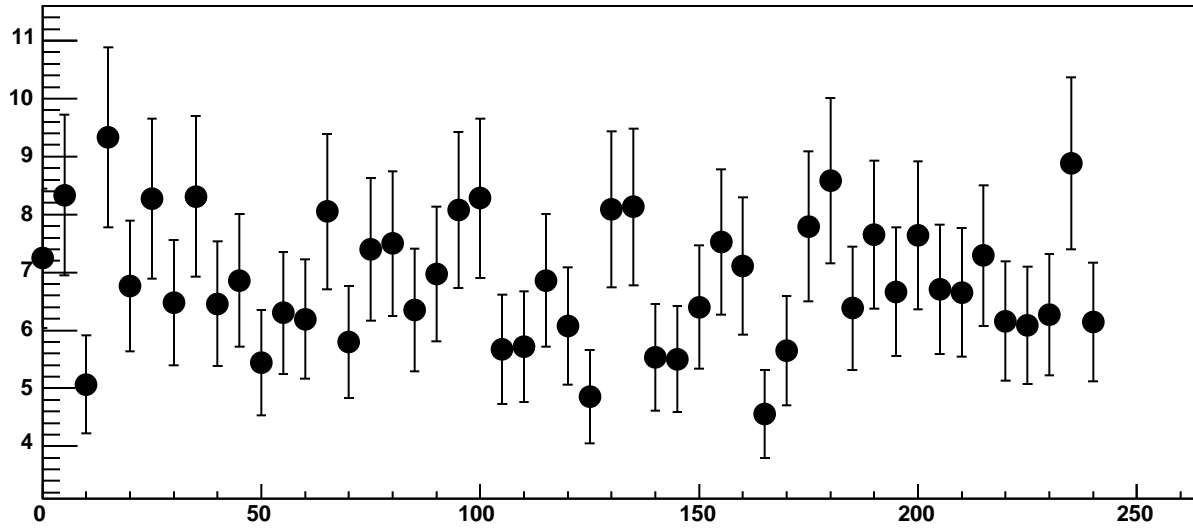


Chip 2, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold

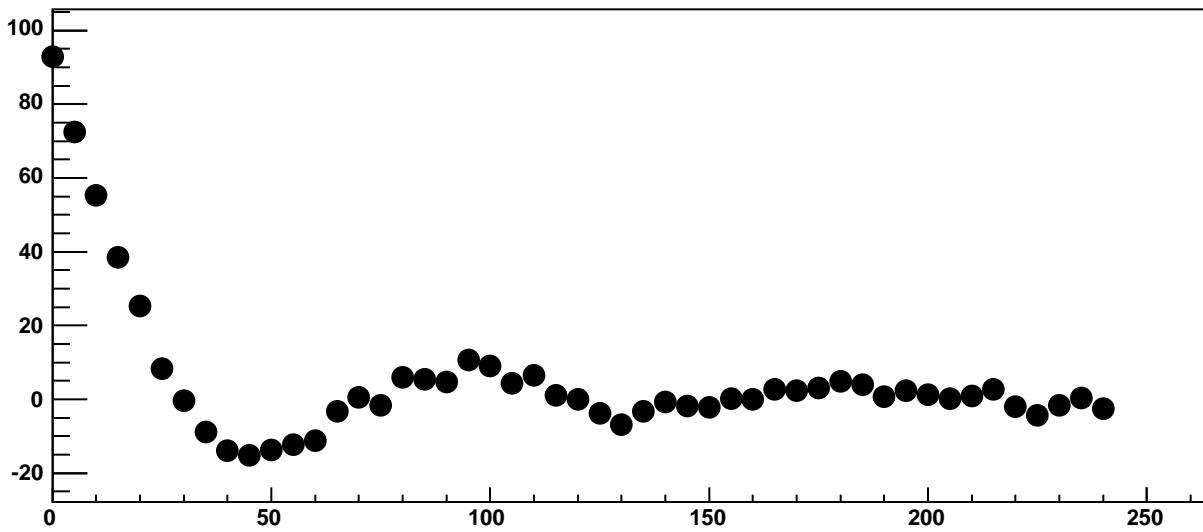


$\chi^2 / \text{ndf}$	1279 / 41
p0	$300.3 \pm 0.6995$
p1	$127.4 \pm 0.694$
p2	$-4.419\text{e}+07 \pm 1.331\text{e}+06$
p3	$3.539\text{e}+07 \pm 7.877\text{e}+05$
p4	$1.386 \pm 0.01051$

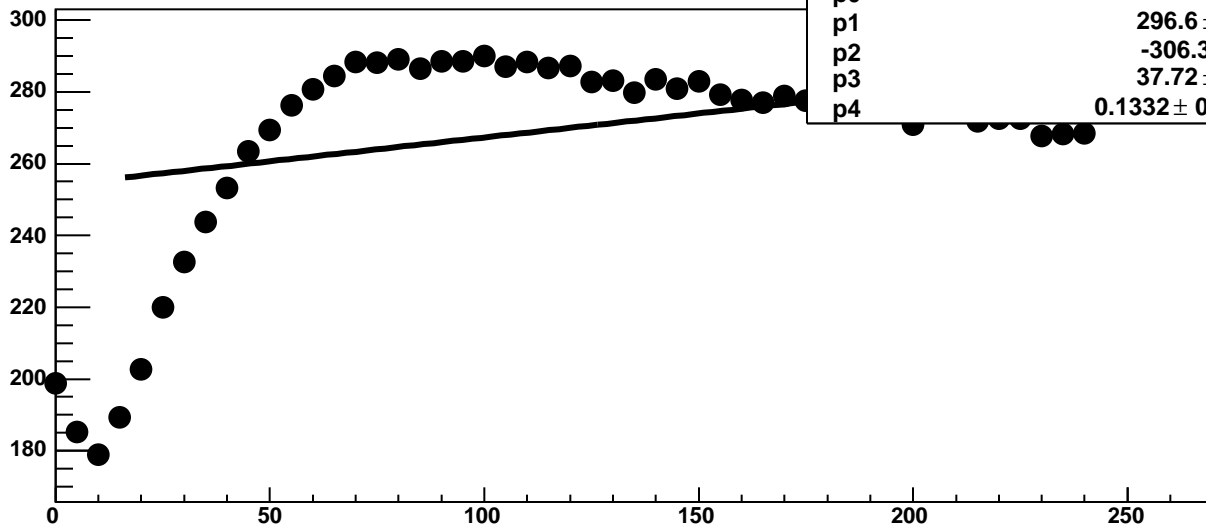
Chip 2, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

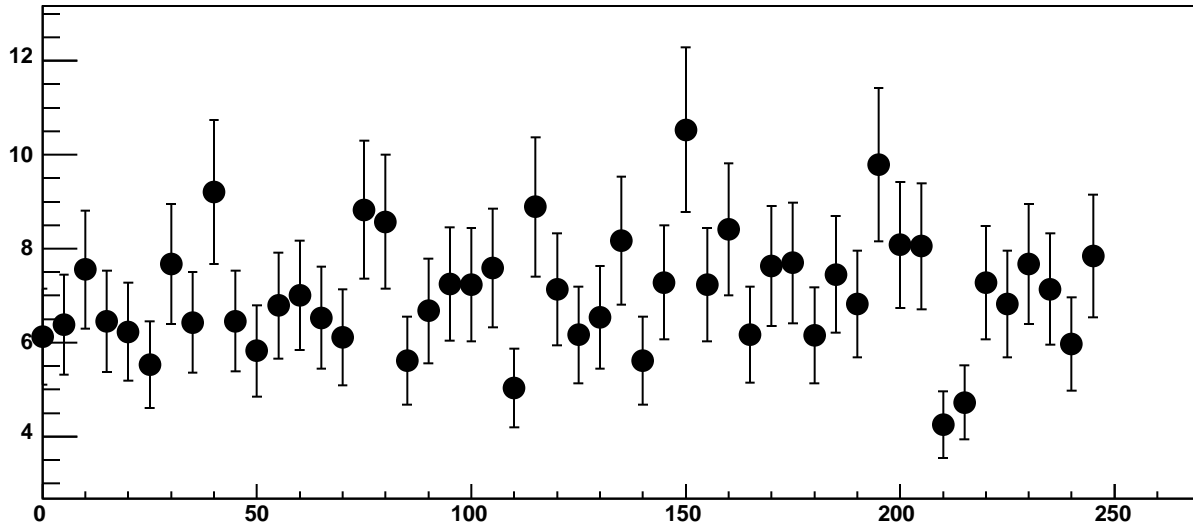


Chip 2, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

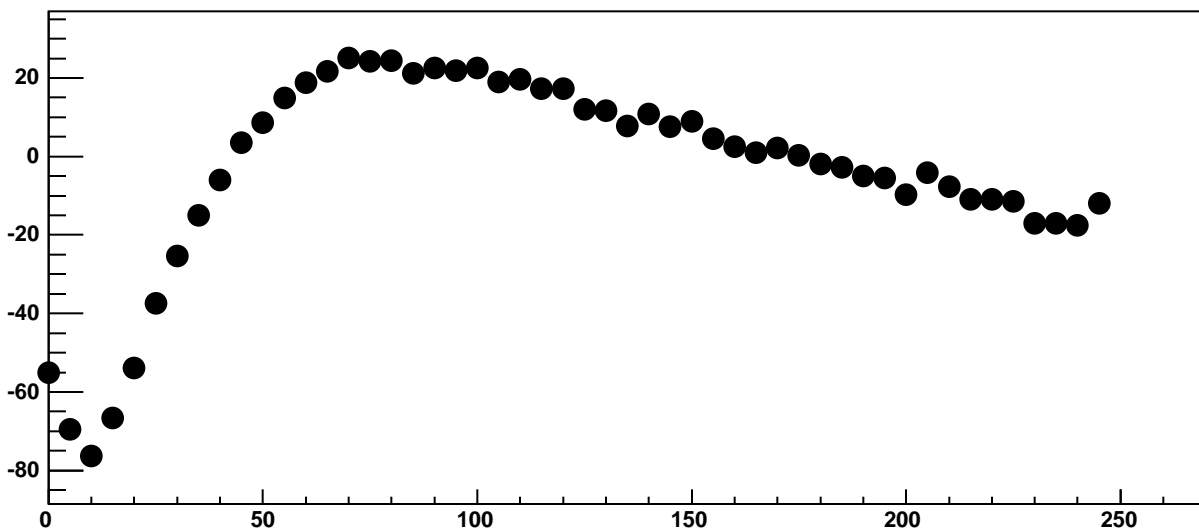


$\chi^2 / \text{ndf}$	8118 / 41
p0	$293.5 \pm 0.5113$
p1	$296.6 \pm 0.9069$
p2	$-306.3 \pm 7.315$
p3	$37.72 \pm 0.3689$
p4	$0.1332 \pm 0.003314$

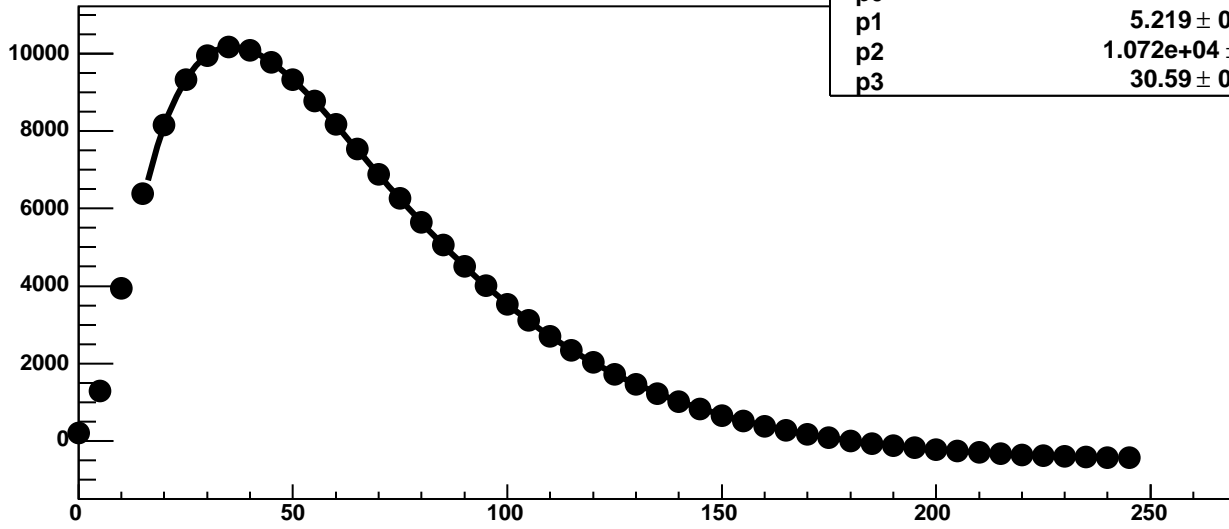
Chip 2, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

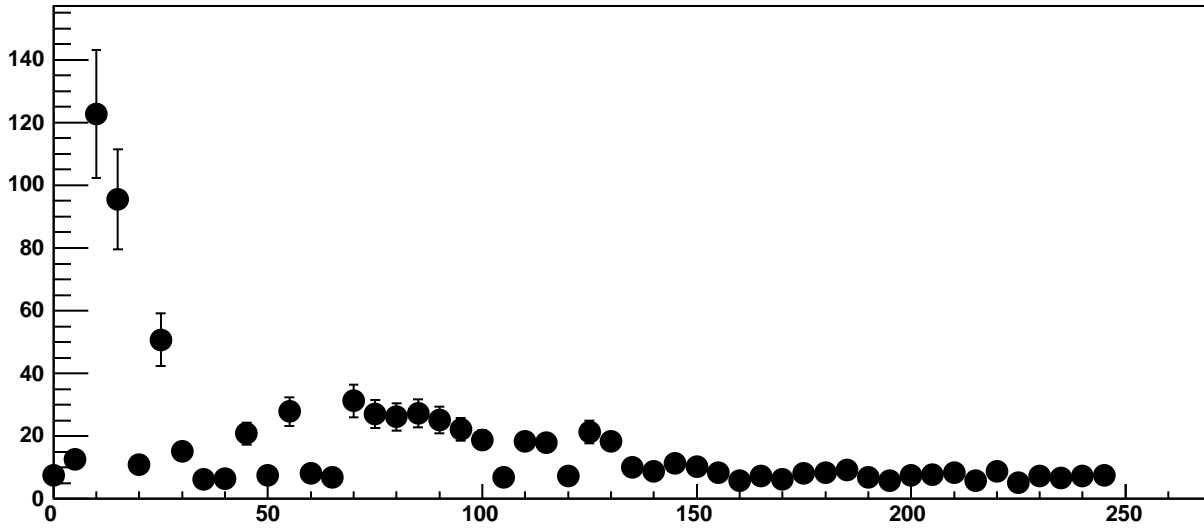


Chip 2, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold

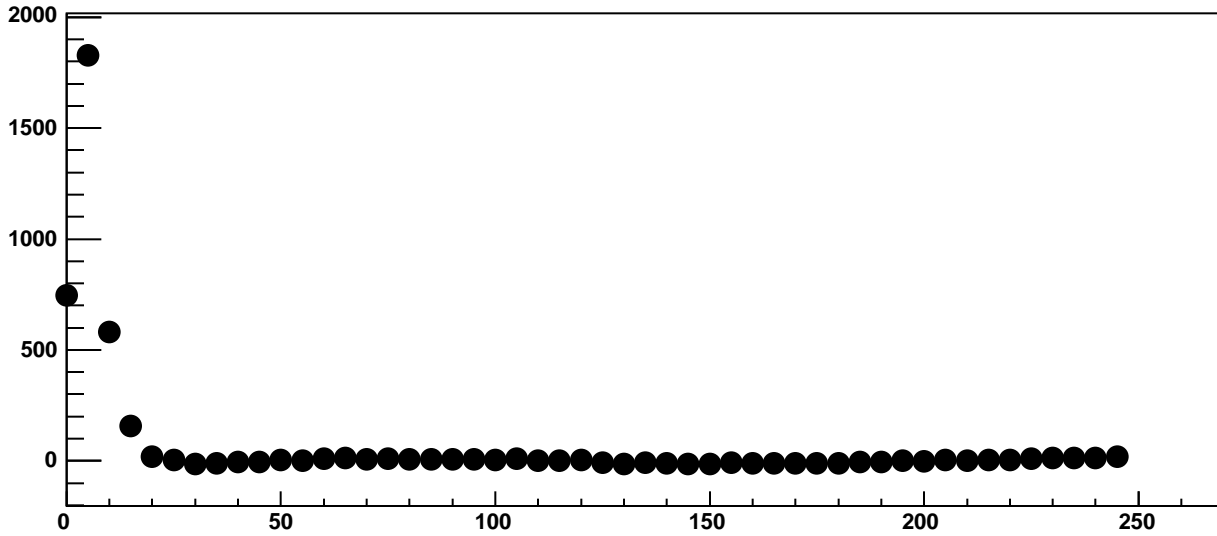


$\chi^2 / \text{ndf}$	1036 / 42
p0	-543.7 ± 0.5533
p1	5.219 ± 0.009236
p2	1.072e+04 ± 0.8803
p3	30.59 ± 0.005879

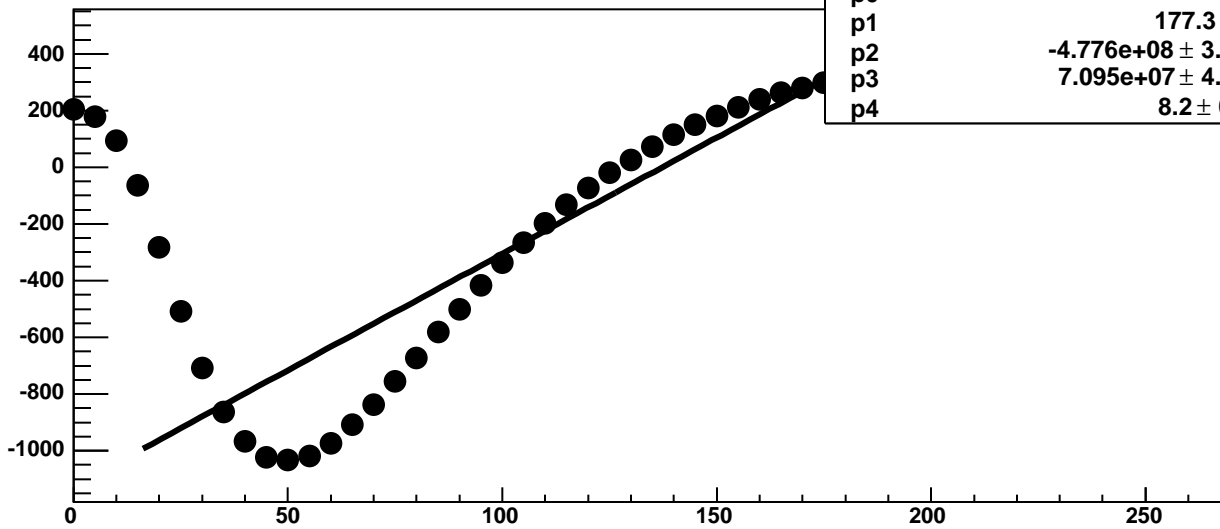
Chip 2, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold

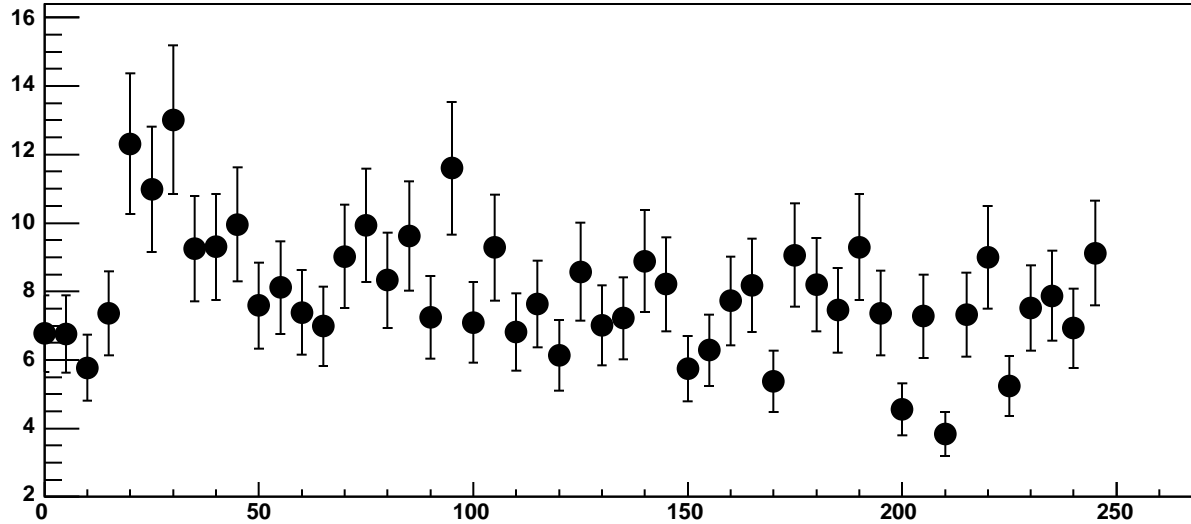


Chip 2, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

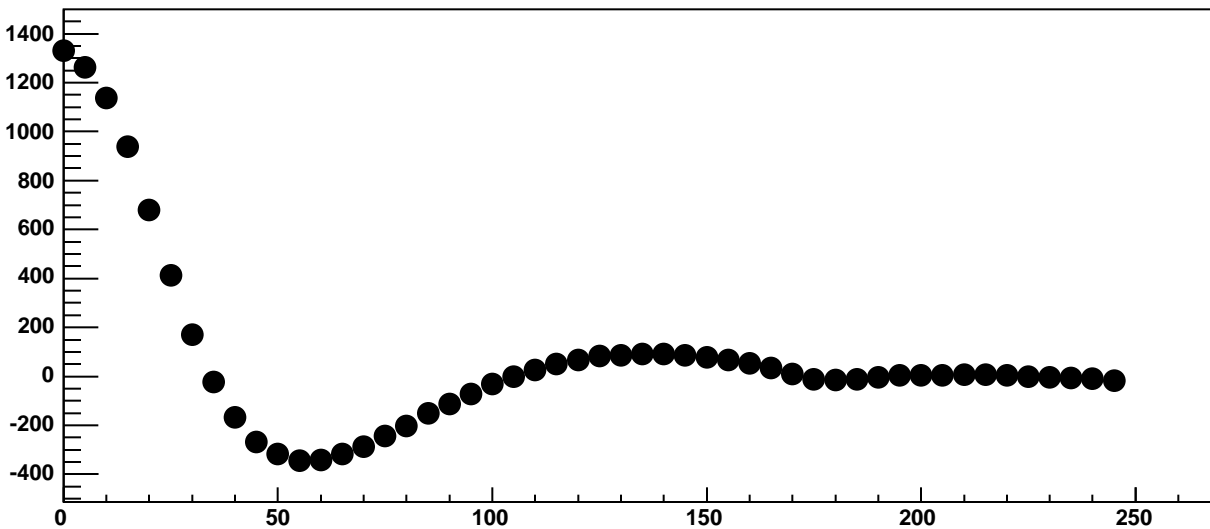


$\chi^2 / \text{ndf}$	6.385e+05 / 41
p0	328.5 ± 1.132
p1	177.3 ± 0.1623
p2	-4.776e+08 ± 3.213e+07
p3	7.095e+07 ± 4.763e+06
p4	8.2 ± 0.006781

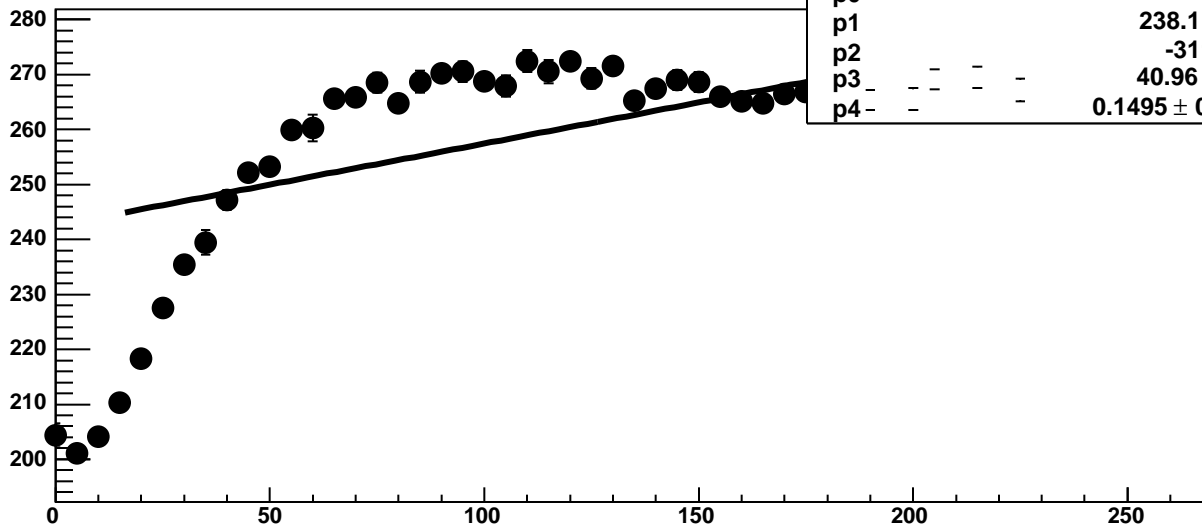
Chip 2, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

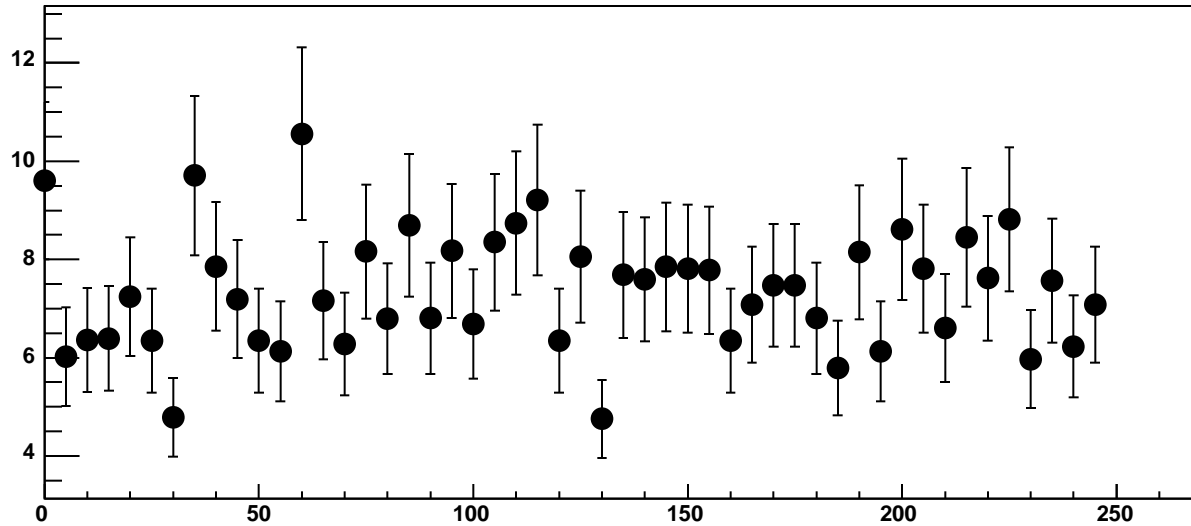


Chip 2, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

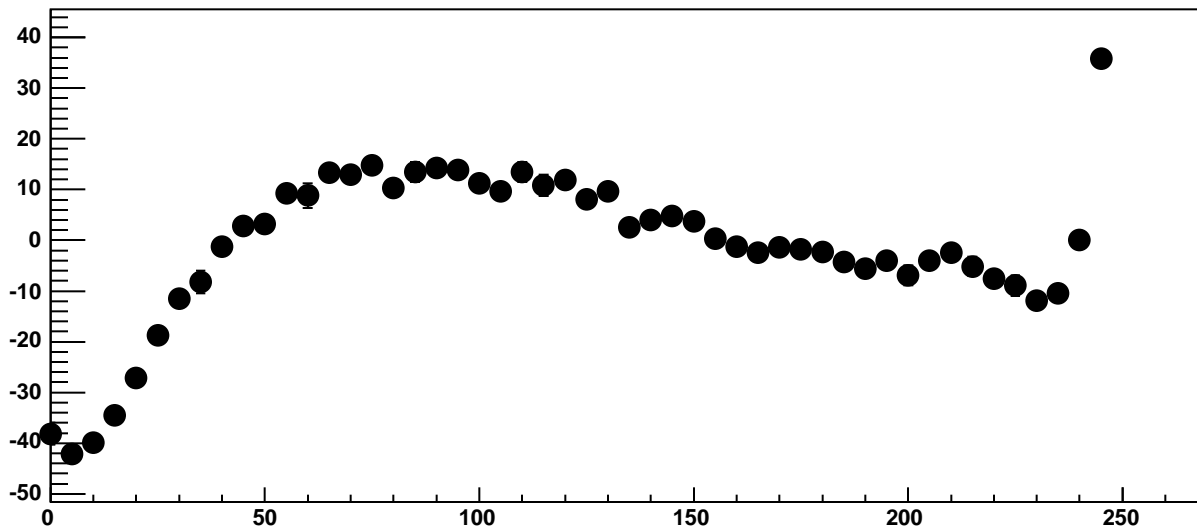


$\chi^2 / \text{ndf}$	2156 / 41
p0	$278.1 \pm 0.4806$
p1	$238.1 \pm 0.2027$
p2	$-311 \pm 7.854$
p3	$40.96 \pm 0.3949$
p4	$0.1495 \pm 0.003653$

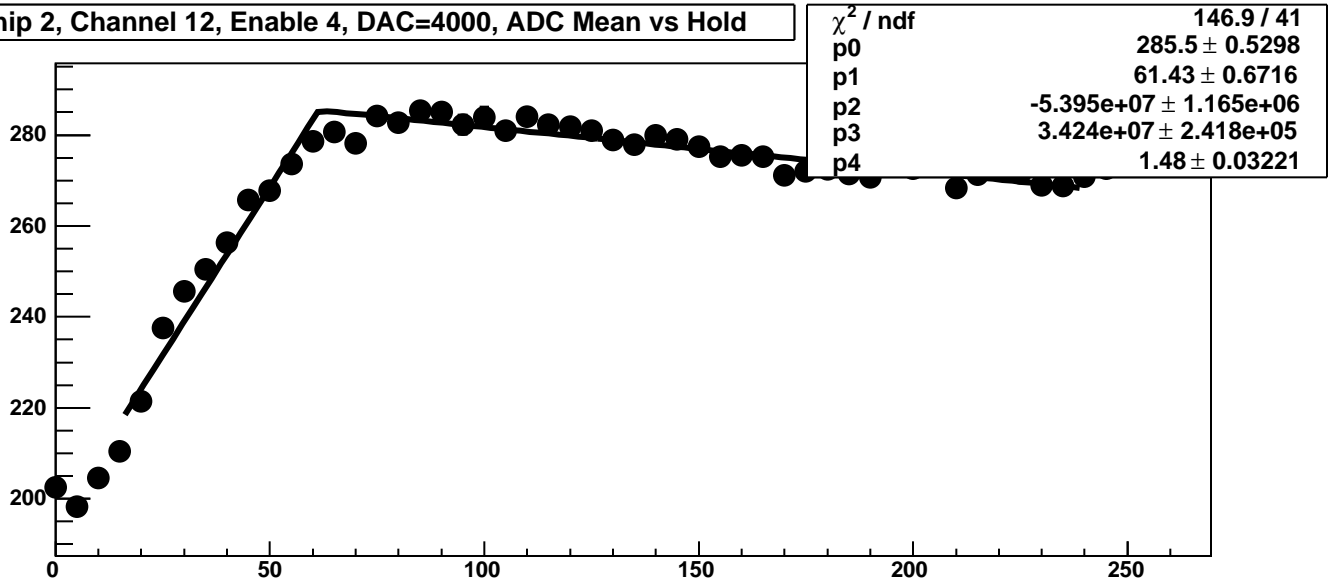
Chip 2, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



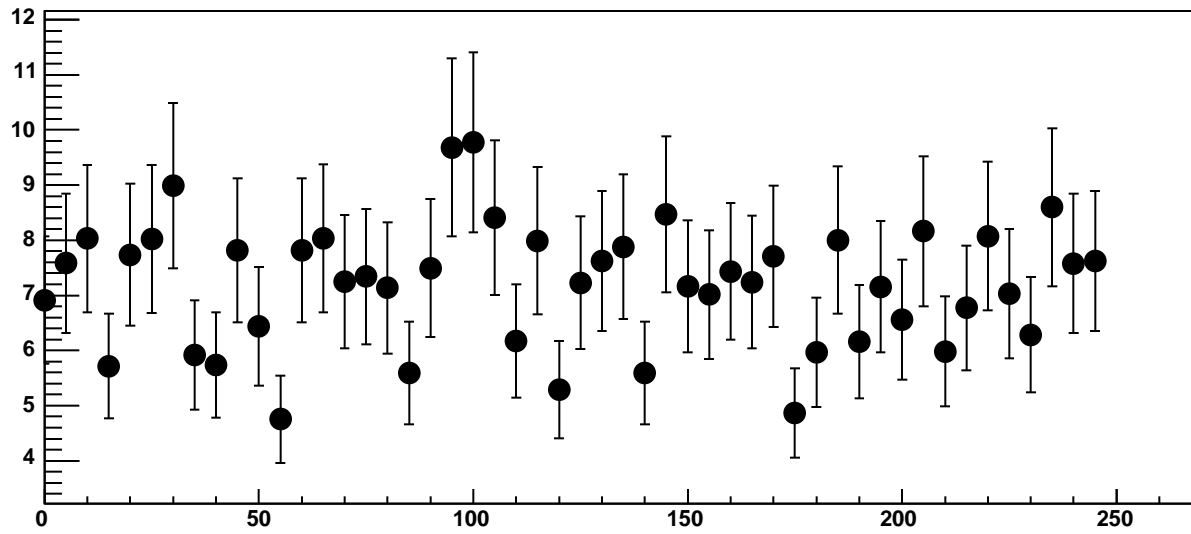
Chip 2, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold



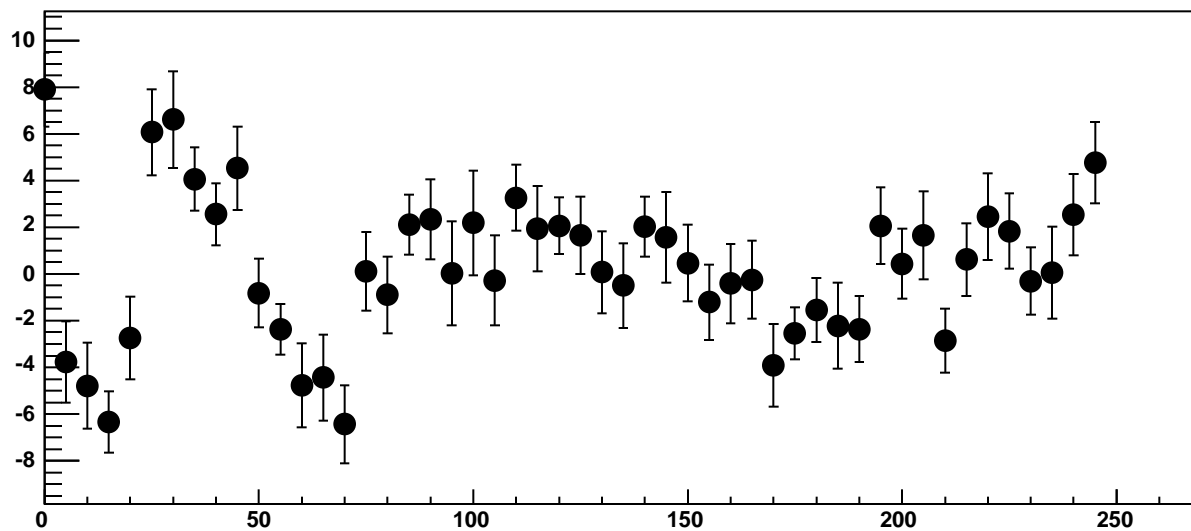
Chip 2, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold



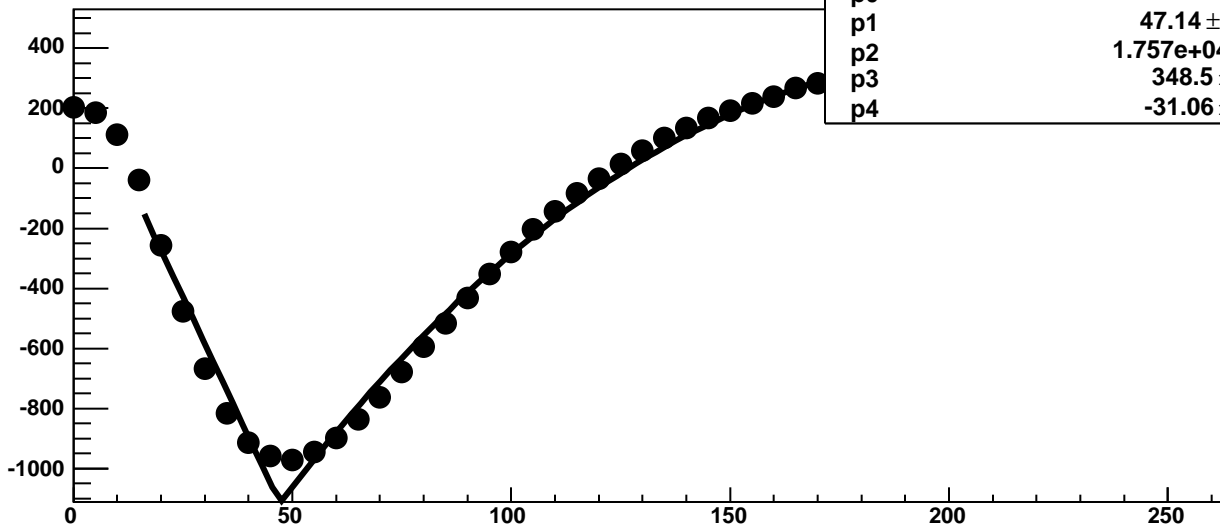
Chip 2, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold

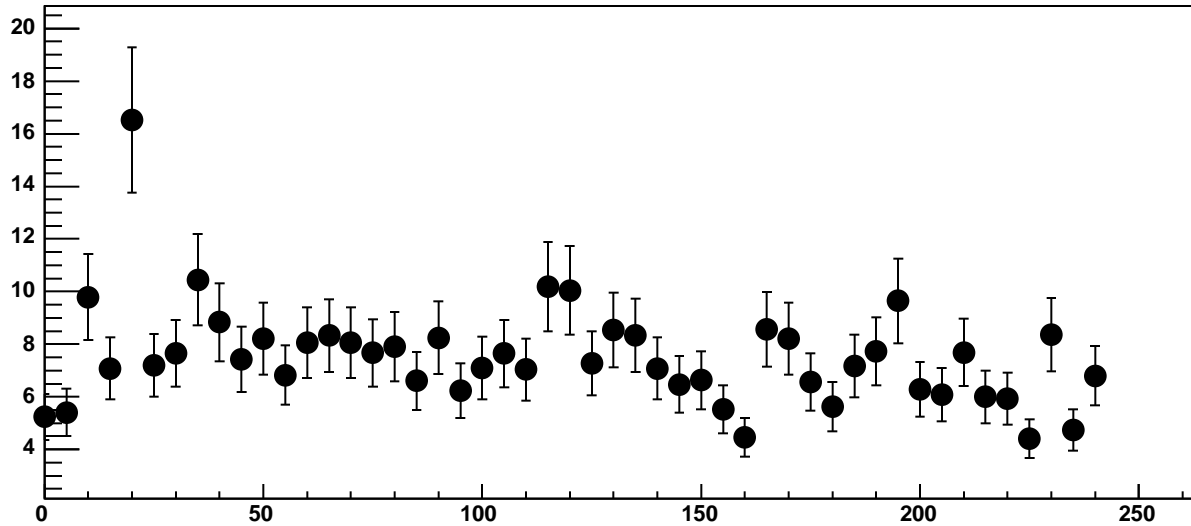


Chip 2, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

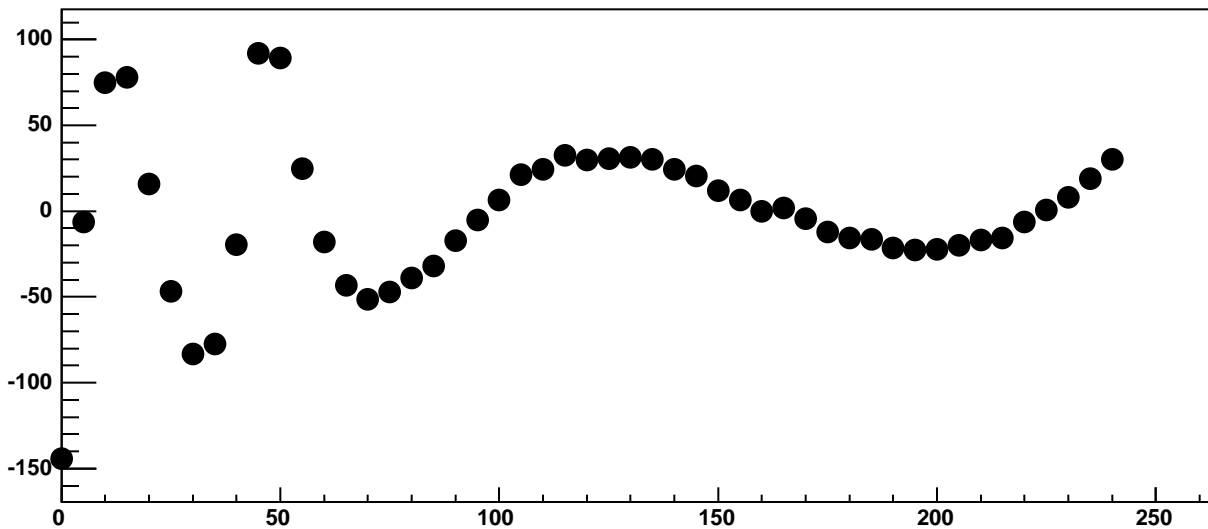


$\chi^2 / \text{ndf}$	1.903e+04 / 41
p0	-1116 ± 0.7478
p1	47.14 ± 0.03317
p2	1.757e+04 ± 60.41
p3	348.5 ± 0.8465
p4	-31.06 ± 0.0694

Chip 2, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold

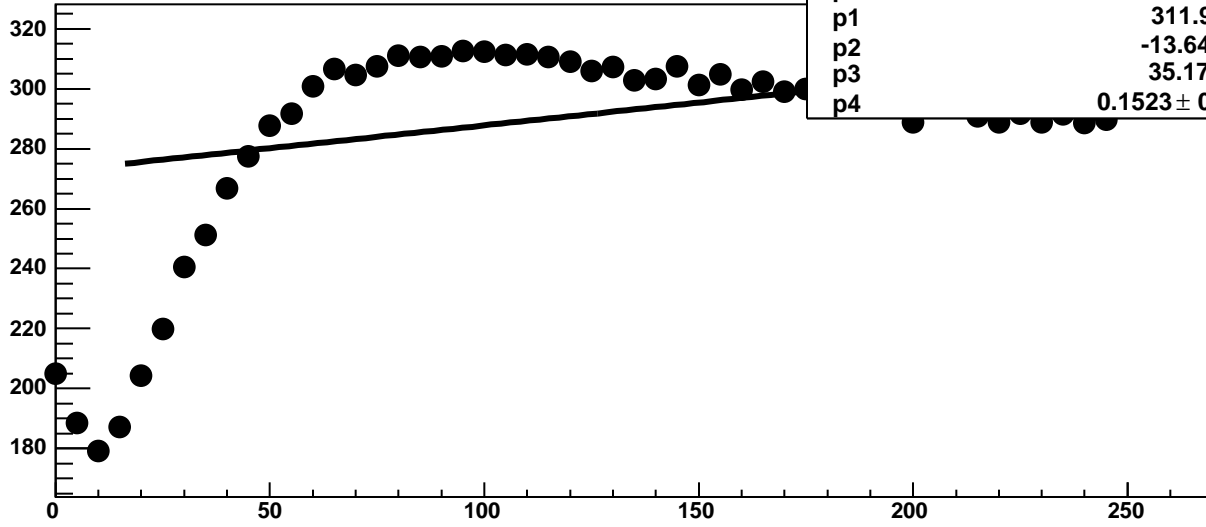


Chip 2, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold



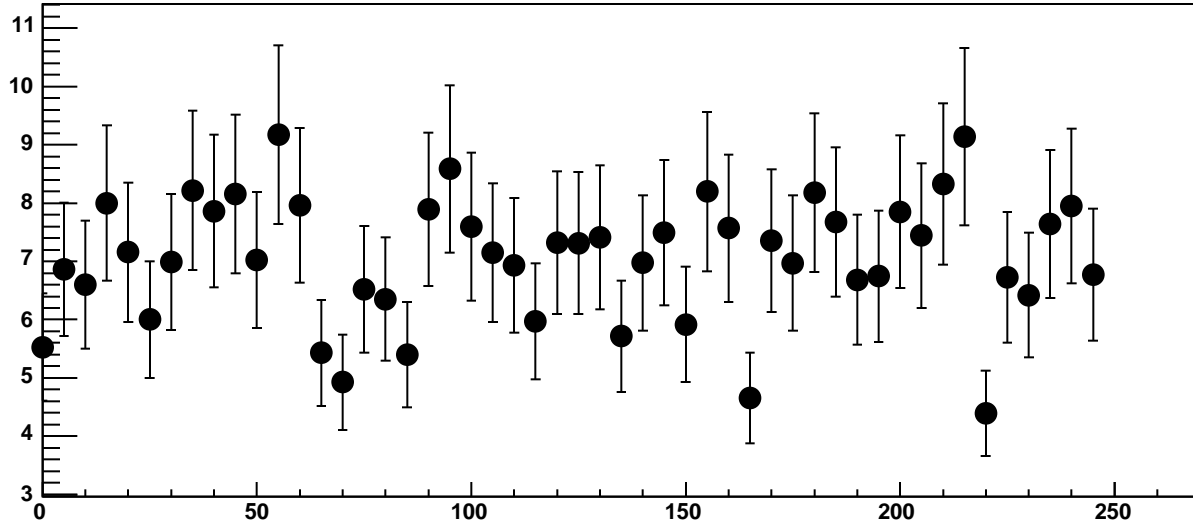


Chip 2, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold

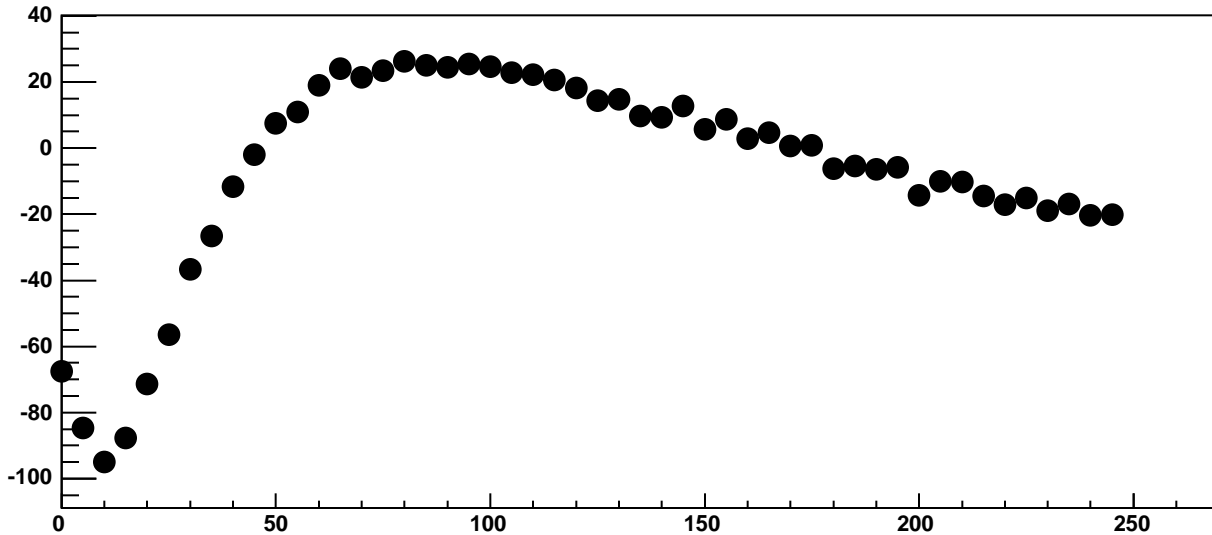


$\chi^2 / \text{ndf}$	1.112e+04 / 41
p0	320.1 ± 0.2307
p1	311.9 ± 1.515
p2	-13.64 ± 84.85
p3	35.17 ± 1.414
p4	0.1523 ± 0.001185

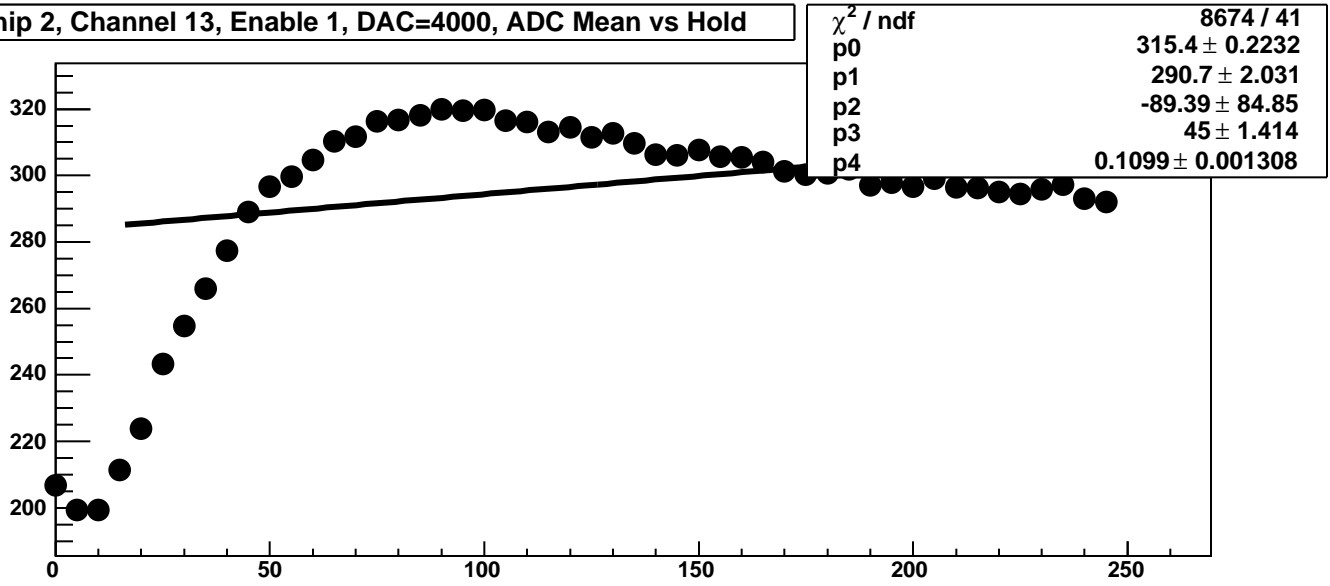
Chip 2, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



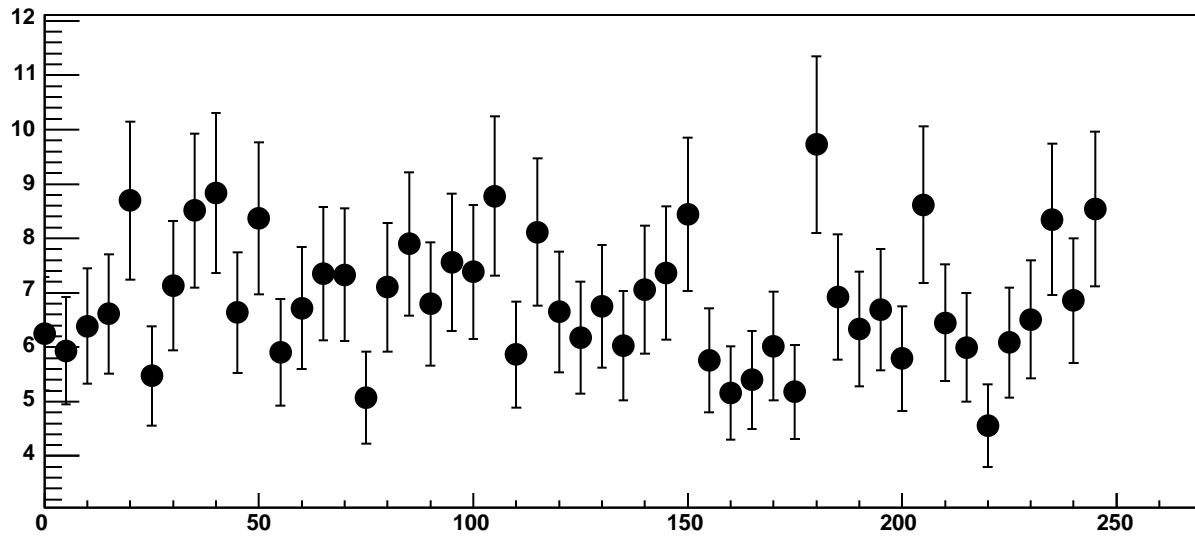
Chip 2, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold



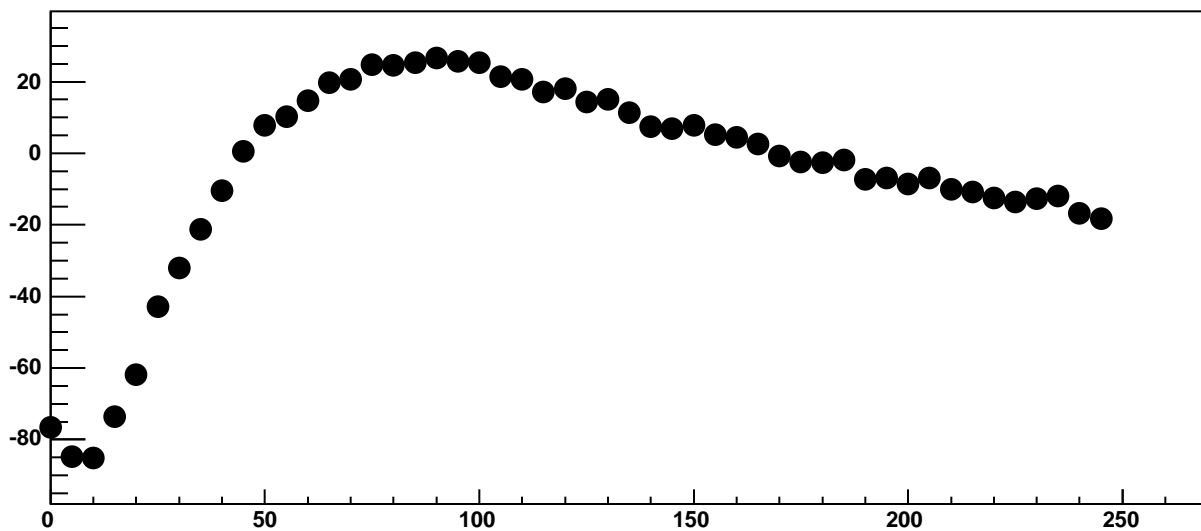
Chip 2, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold



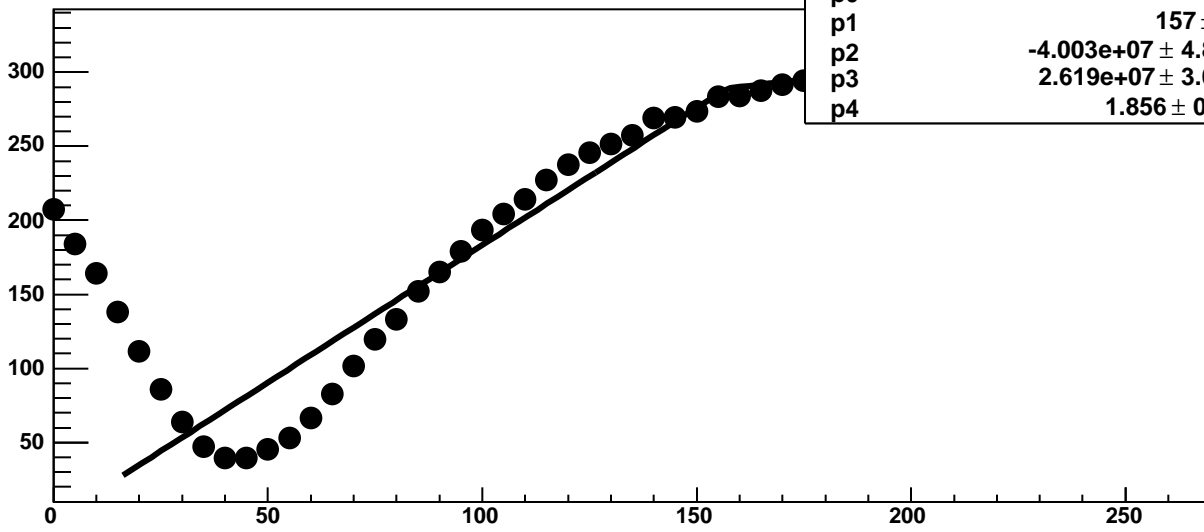
Chip 2, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold

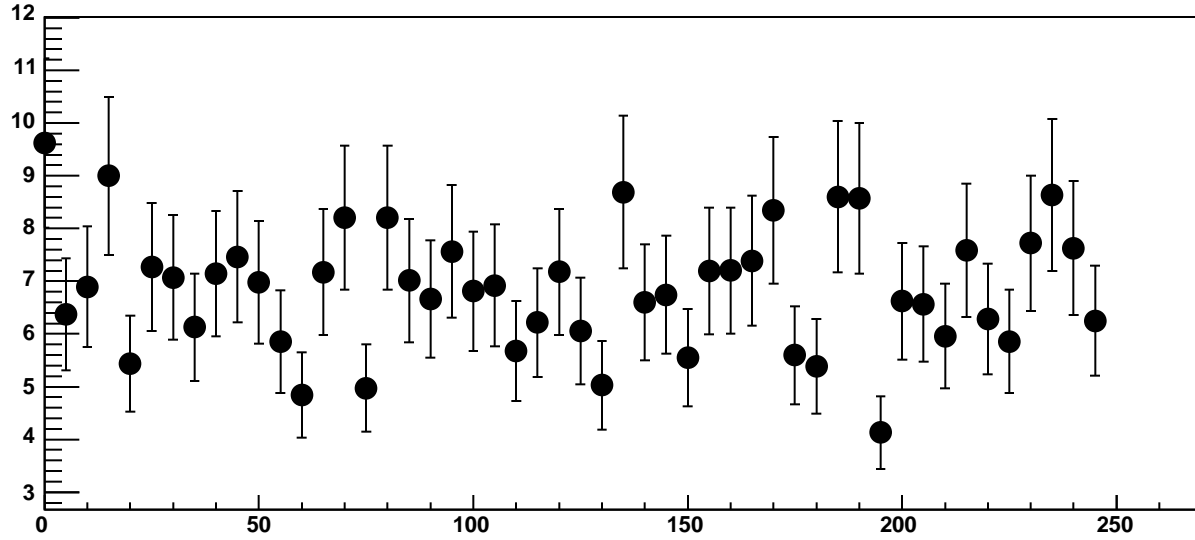


Chip 2, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

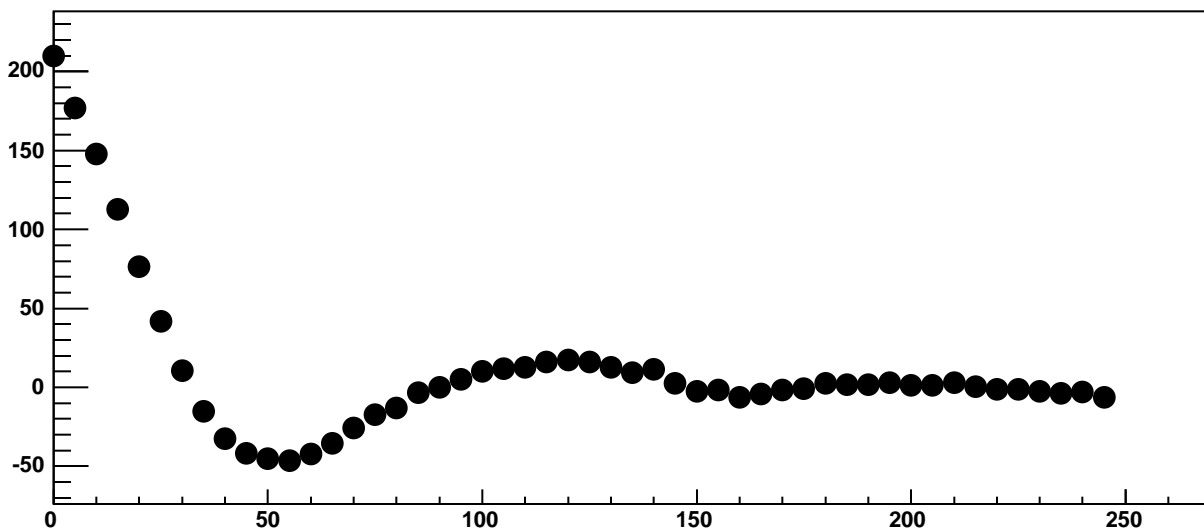


$\chi^2 / \text{ndf}$	1.375e+04 / 41
p0	289 ± 0.9424
p1	157 ± 0.6169
p2	-4.003e+07 ± 4.886e+06
p3	2.619e+07 ± 3.066e+06
p4	1.856 ± 0.006663

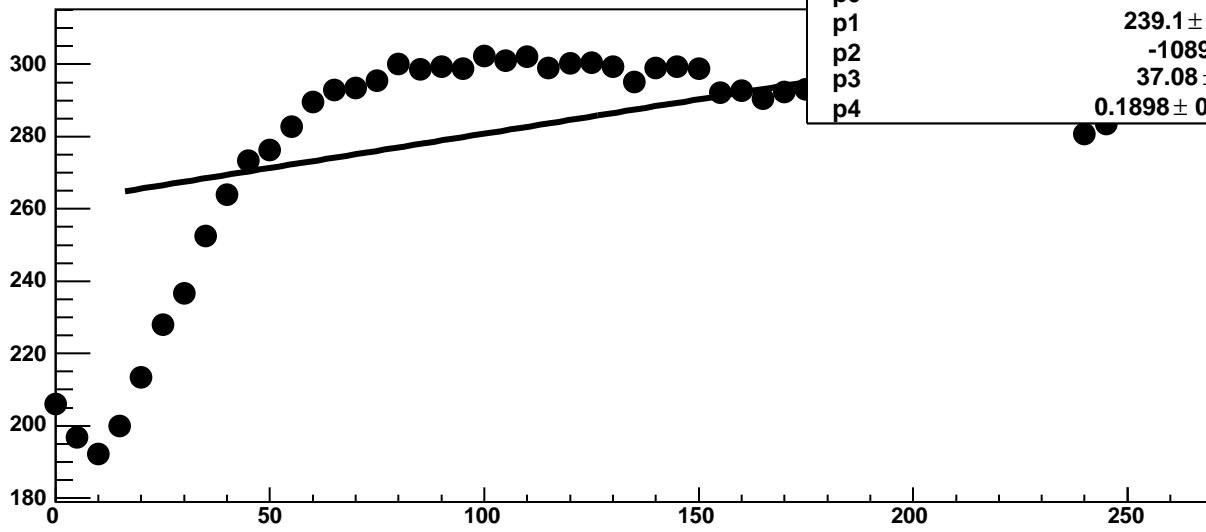
Chip 2, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

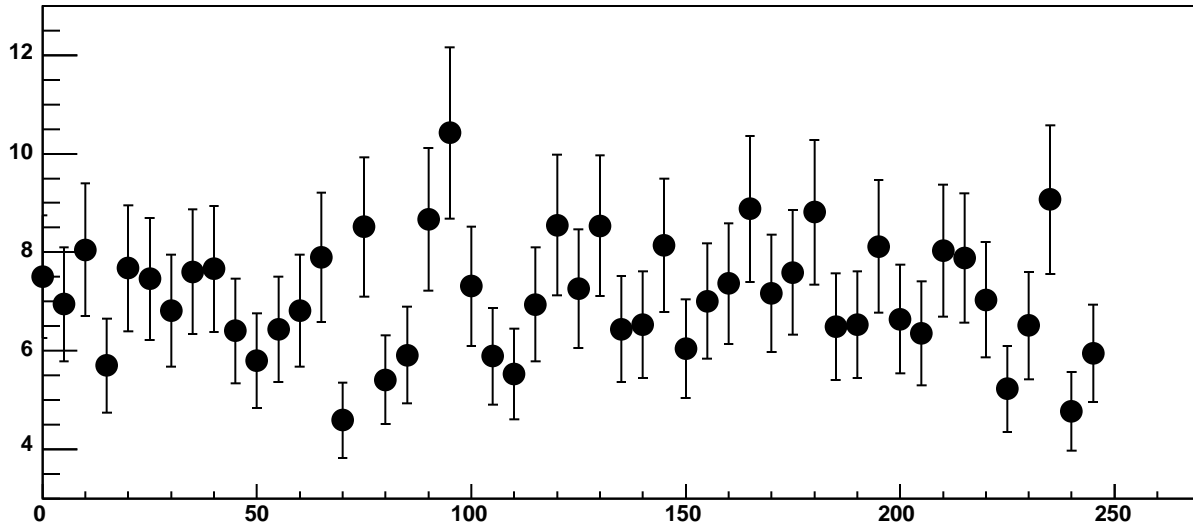


Chip 2, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

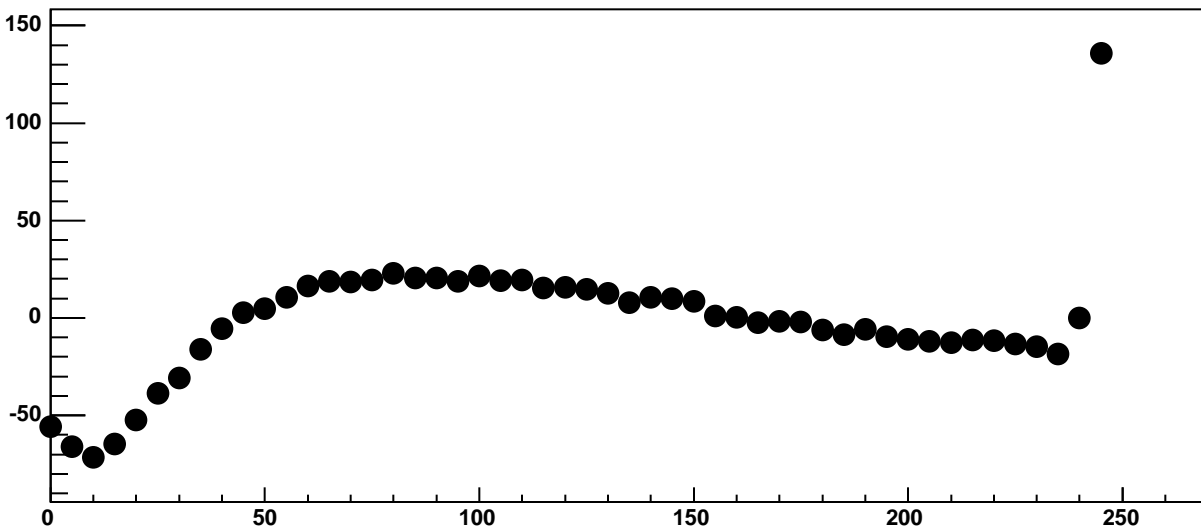


$\chi^2 / \text{ndf}$	7431 / 41
p0	$307.2 \pm 0.4785$
p1	$239.1 \pm 0.05182$
p2	$-1089 \pm 19.39$
p3	$37.08 \pm 0.6022$
p4	$0.1898 \pm 0.003589$

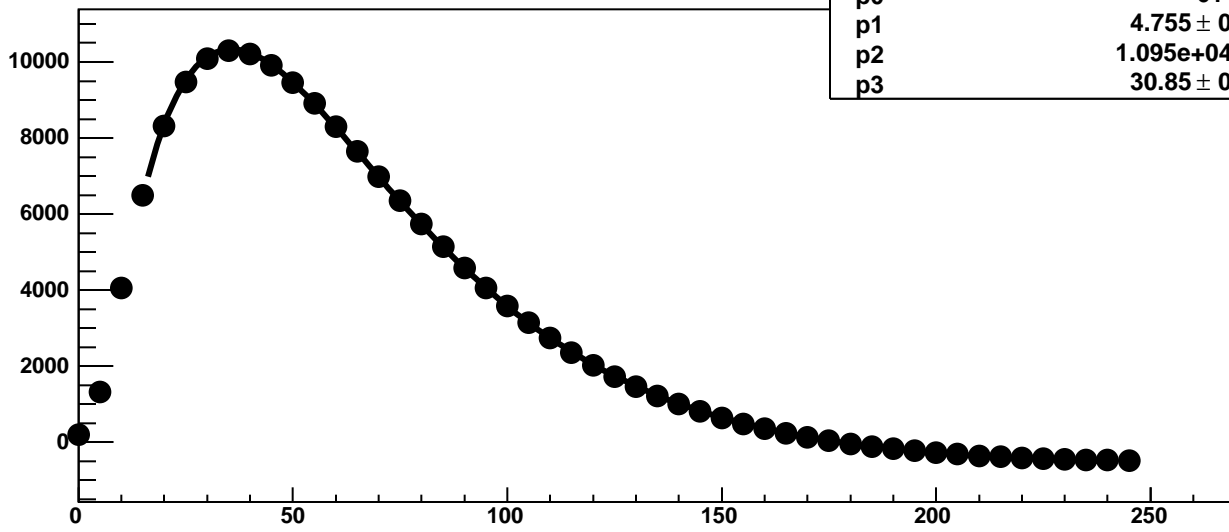
Chip 2, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

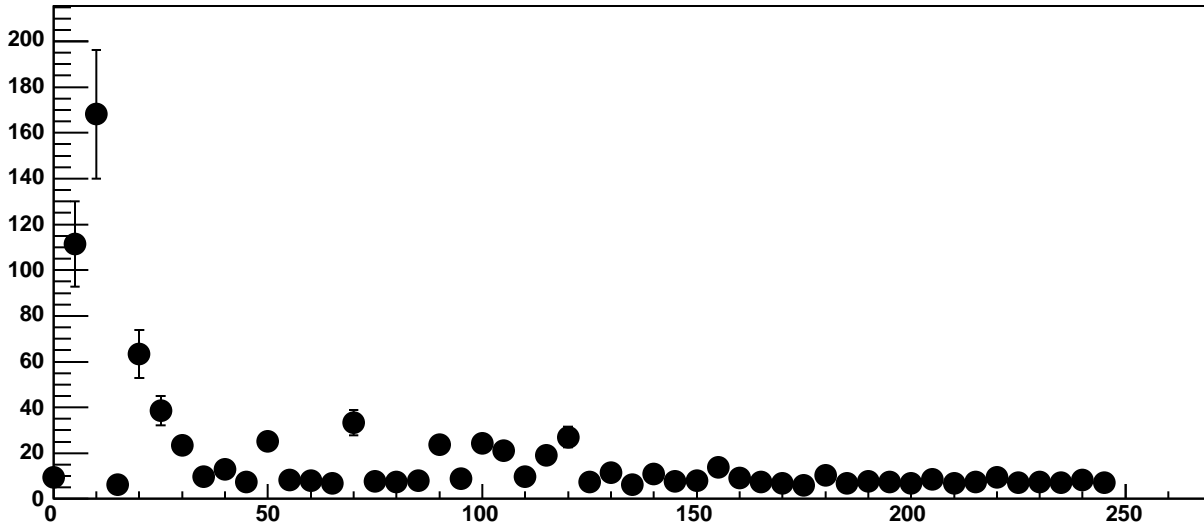


Chip 2, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold

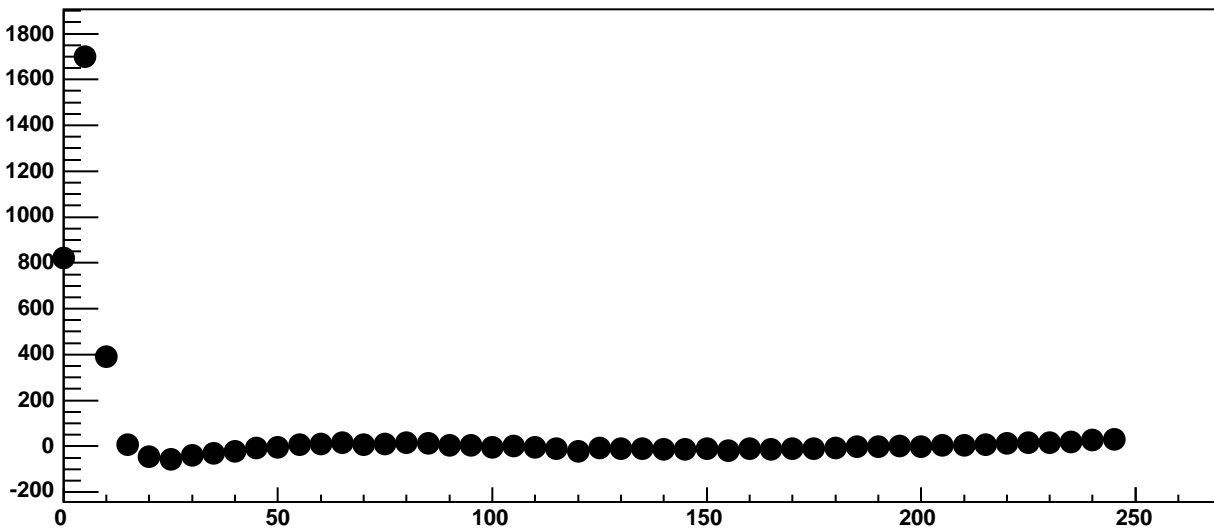


$\chi^2 / \text{ndf}$	1845 / 42
p0	-611 ± 0.564
p1	4.755 ± 0.004574
p2	1.095e+04 ± 0.994
p3	30.85 ± 0.004856

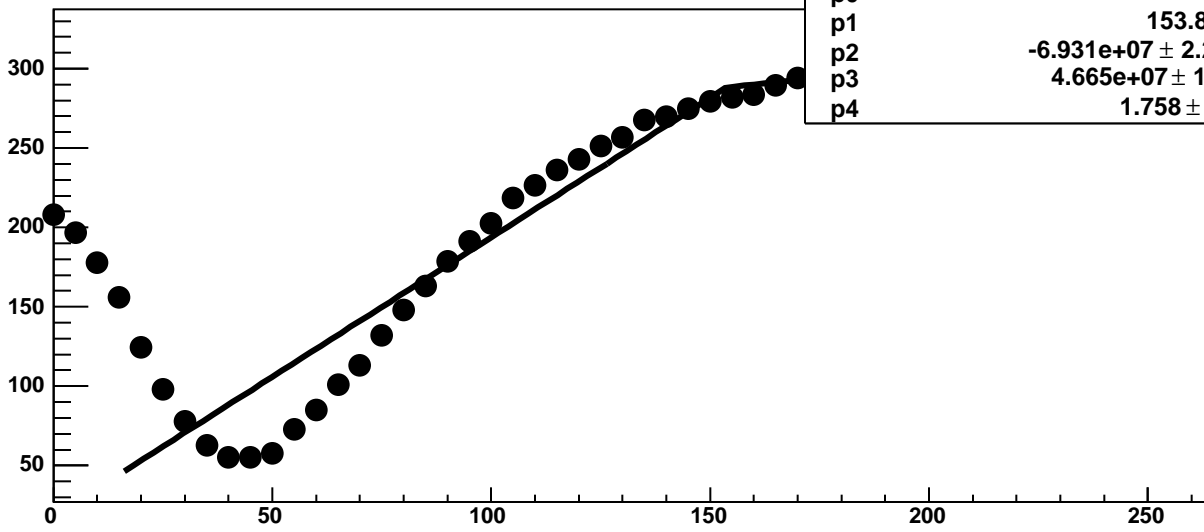
Chip 2, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold

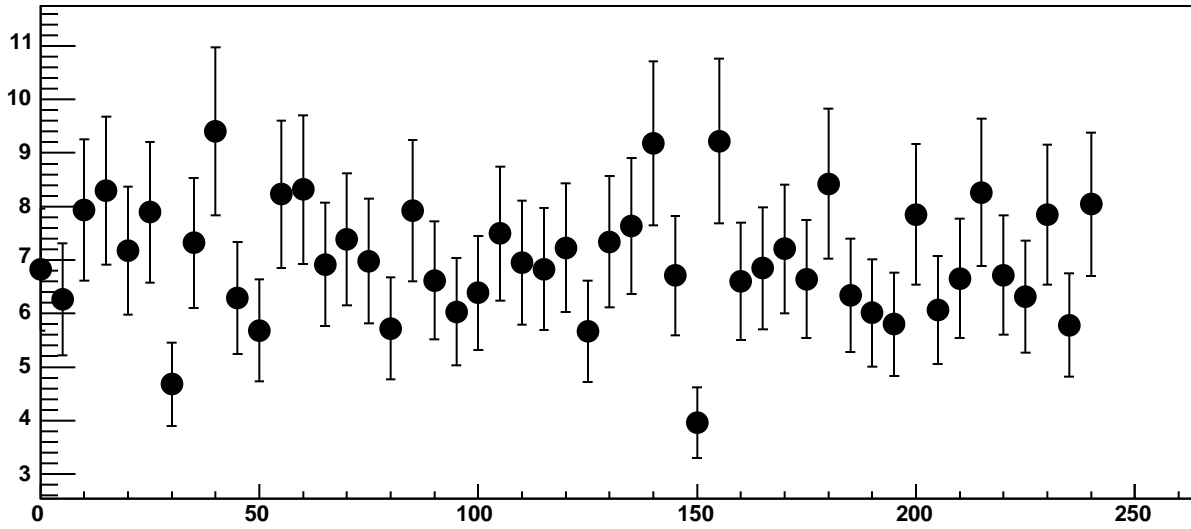


Chip 2, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

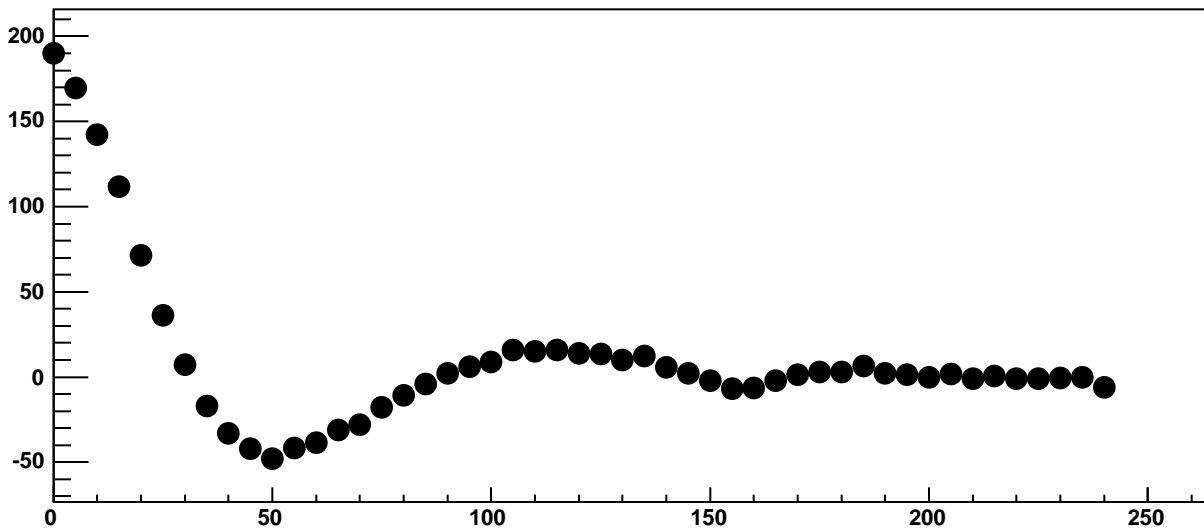


$\chi^2 / \text{ndf}$	1.074e+04 / 41
p0	288.3 ± 0.9047
p1	153.8 ± 0.629
p2	-6.931e+07 ± 2.209e+06
p3	4.665e+07 ± 1.36e+06
p4	1.758 ± 0.00686

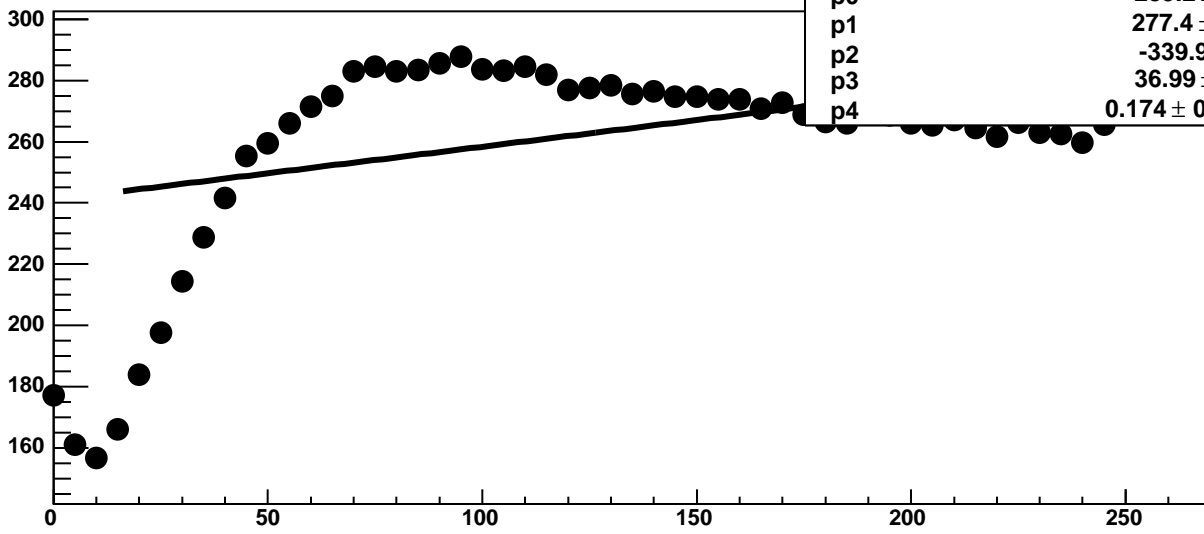
Chip 2, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold

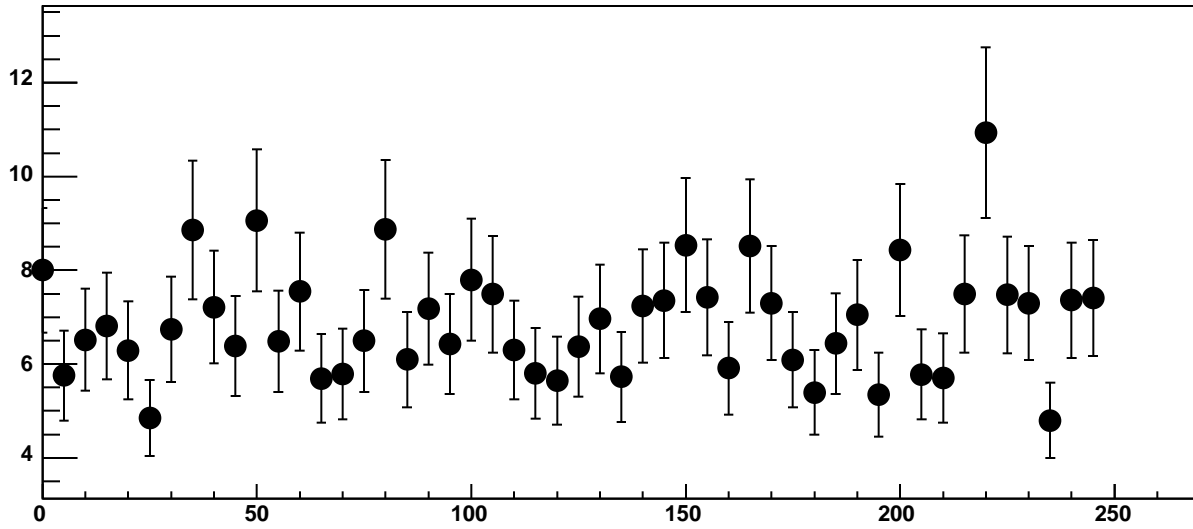


Chip 2, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

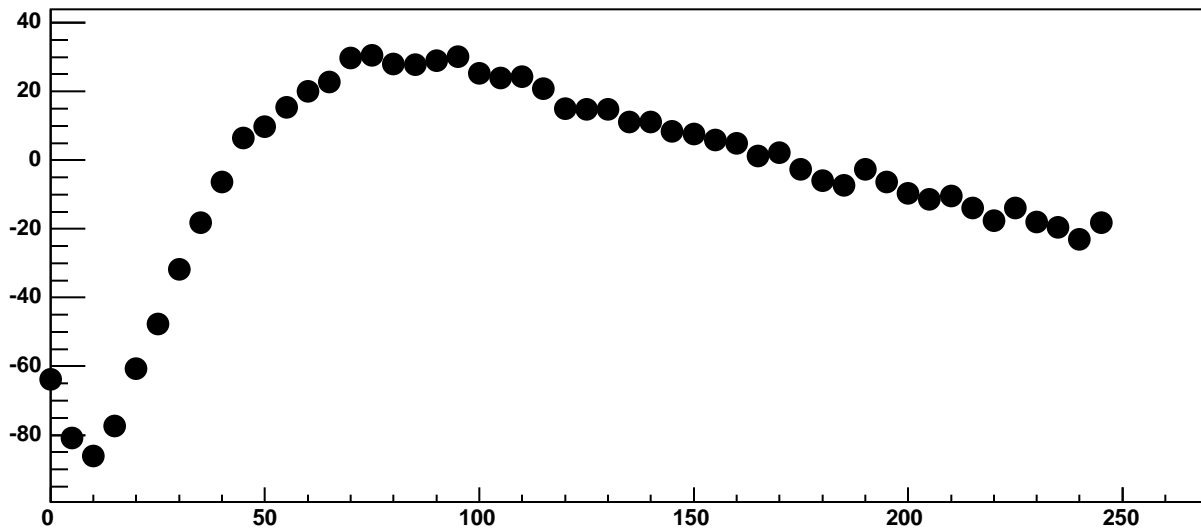


$\chi^2 / \text{ndf}$	1.185e+04 / 41
p0	289.2 ± 0.4907
p1	277.4 ± 0.4806
p2	-339.9 ± 10.49
p3	36.99 ± 0.4833
p4	0.174 ± 0.003337

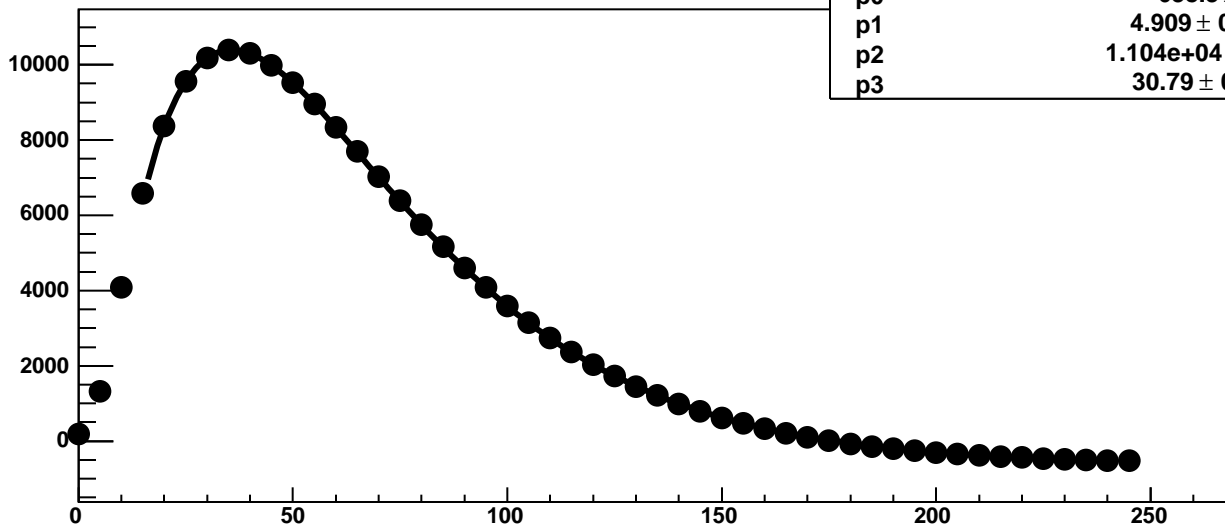
Chip 2, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

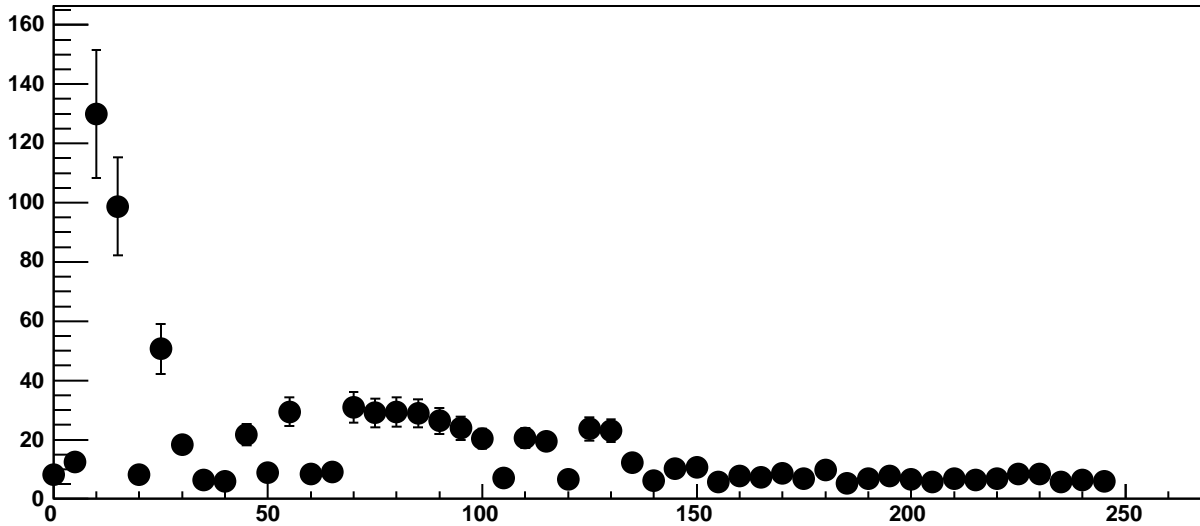


Chip 2, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold

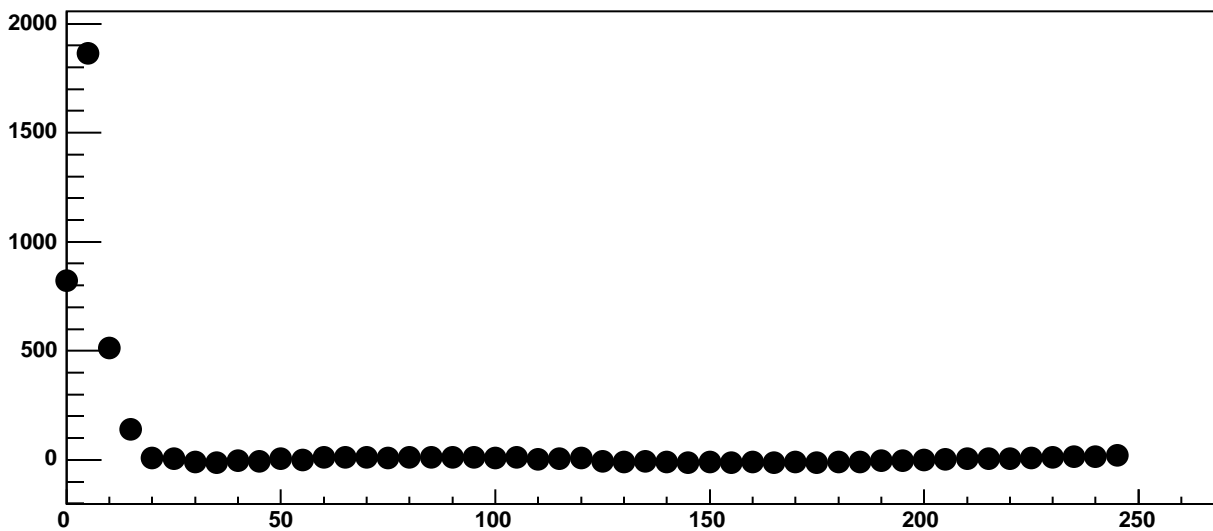


$\chi^2 / \text{ndf}$	1078 / 42
p0	$-638.3 \pm 0.5424$
p1	$4.909 \pm 0.007795$
p2	$1.104\text{e}+04 \pm 0.8751$
p3	$30.79 \pm 0.005482$

Chip 2, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold

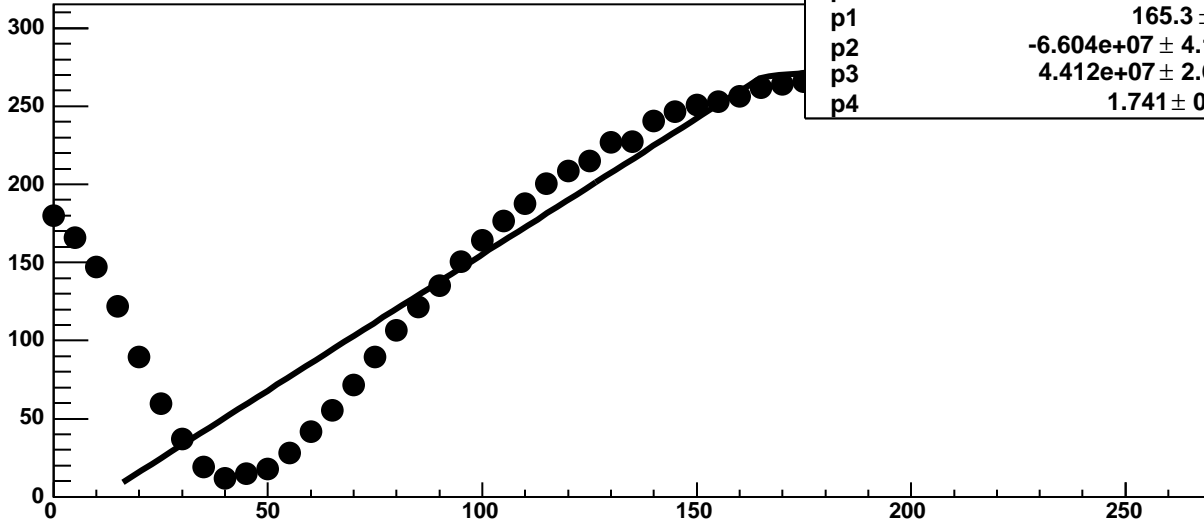


Chip 2, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold



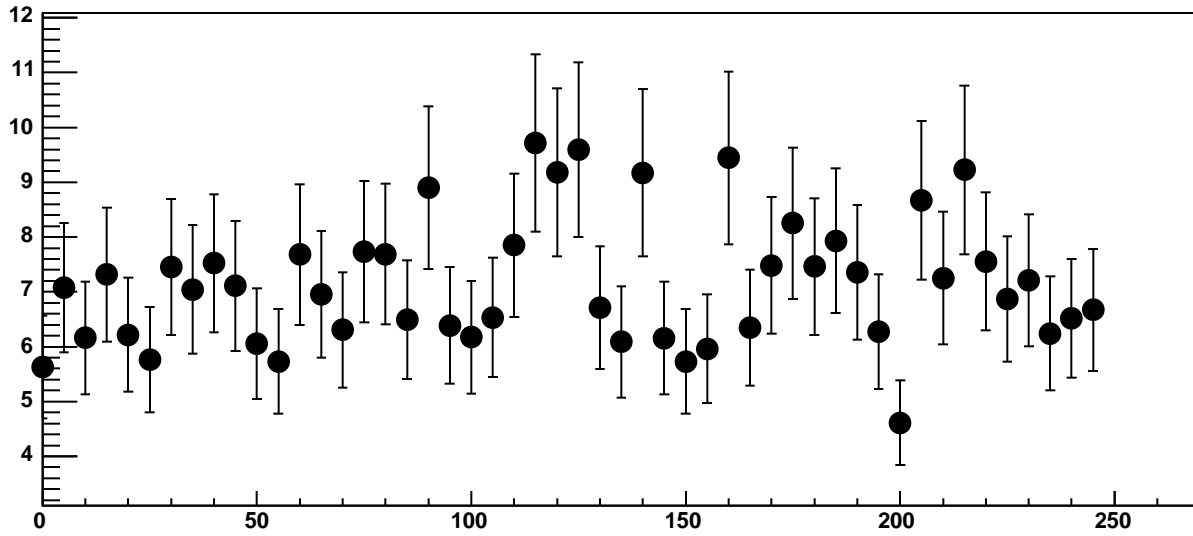


Chip 2, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

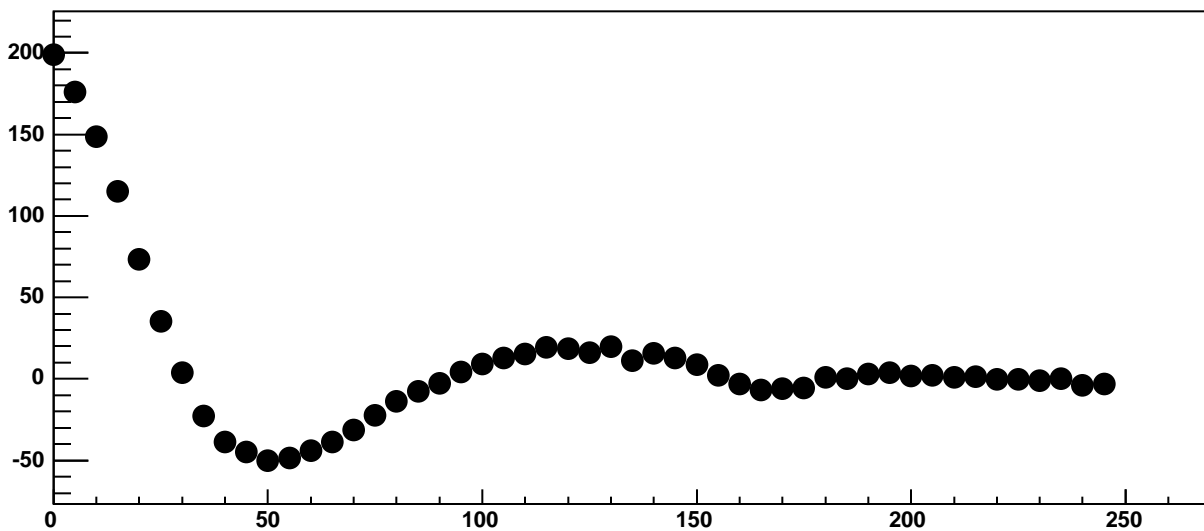


$\chi^2 / \text{ndf}$	1.498e+04 / 41
p0	268.9 ± 1.05
p1	165.3 ± 0.7065
p2	-6.604e+07 ± 4.183e+06
p3	4.412e+07 ± 2.674e+06
p4	1.741 ± 0.006225

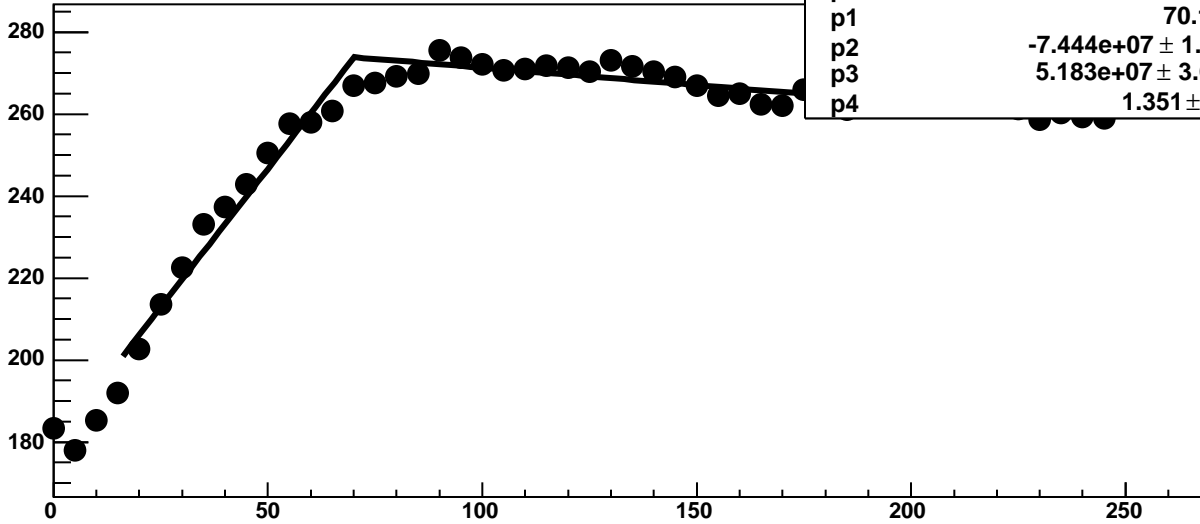
Chip 2, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

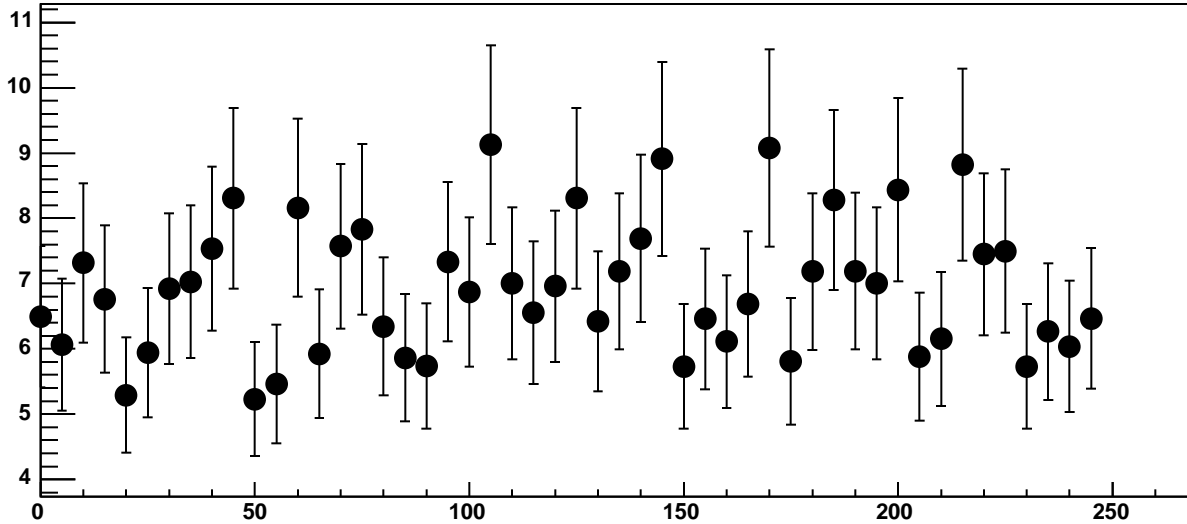


Chip 2, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

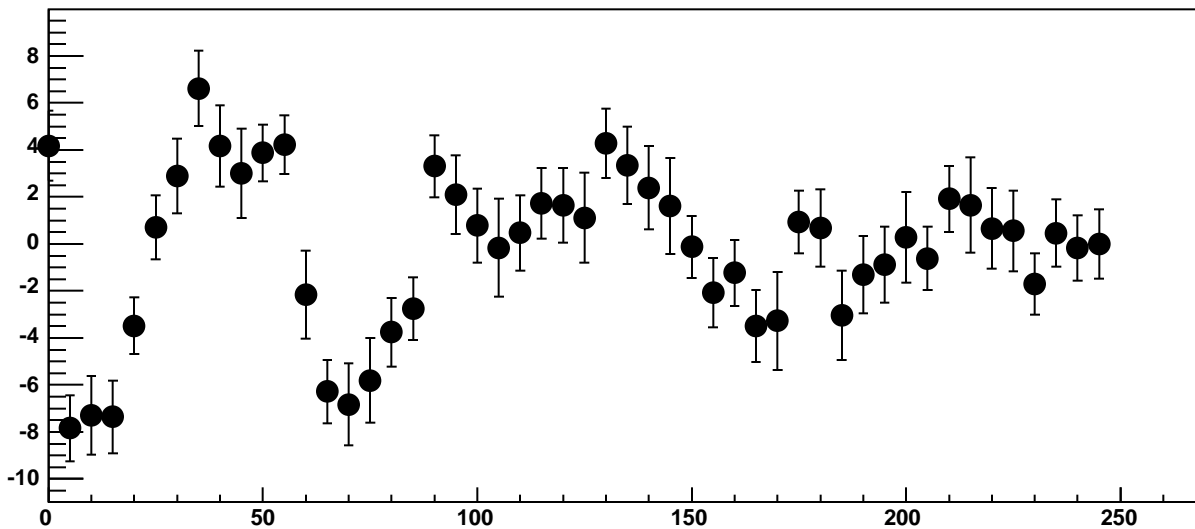


$\chi^2 / \text{ndf}$	186.1 / 41
p0	$273.9 \pm 0.5131$
p1	$70.1 \pm 0.682$
p2	$-7.444\text{e}+07 \pm 1.396\text{e}+06$
p3	$5.183\text{e}+07 \pm 3.605\text{e}+05$
p4	$1.351 \pm 0.02453$

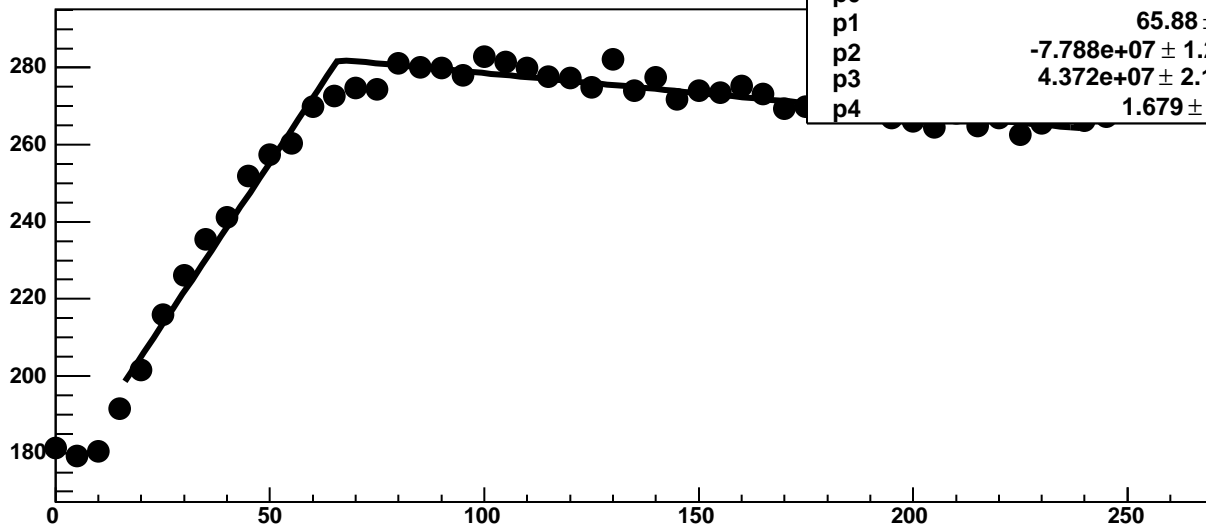
Chip 2, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

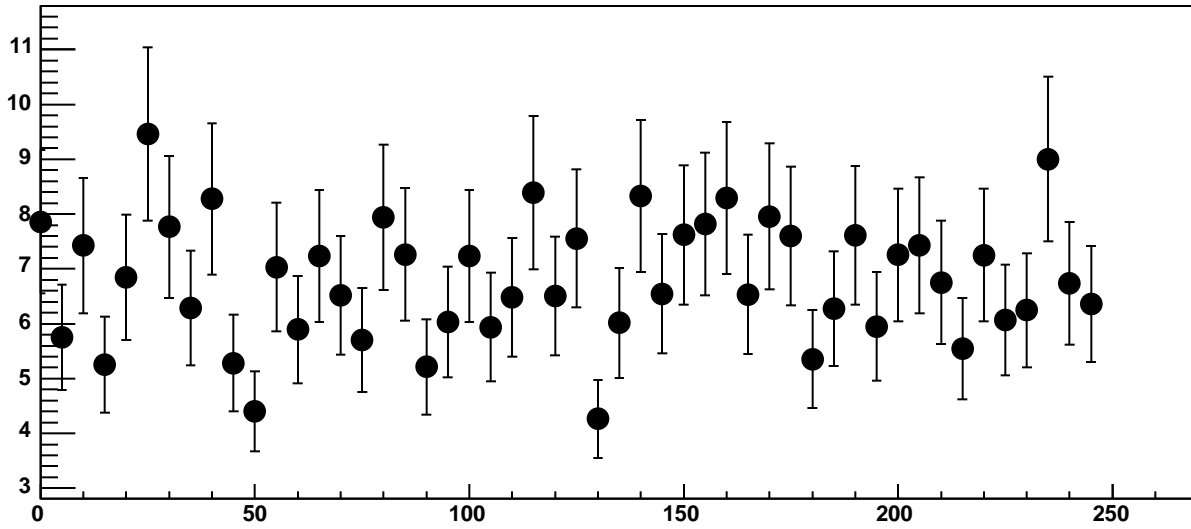


Chip 2, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold

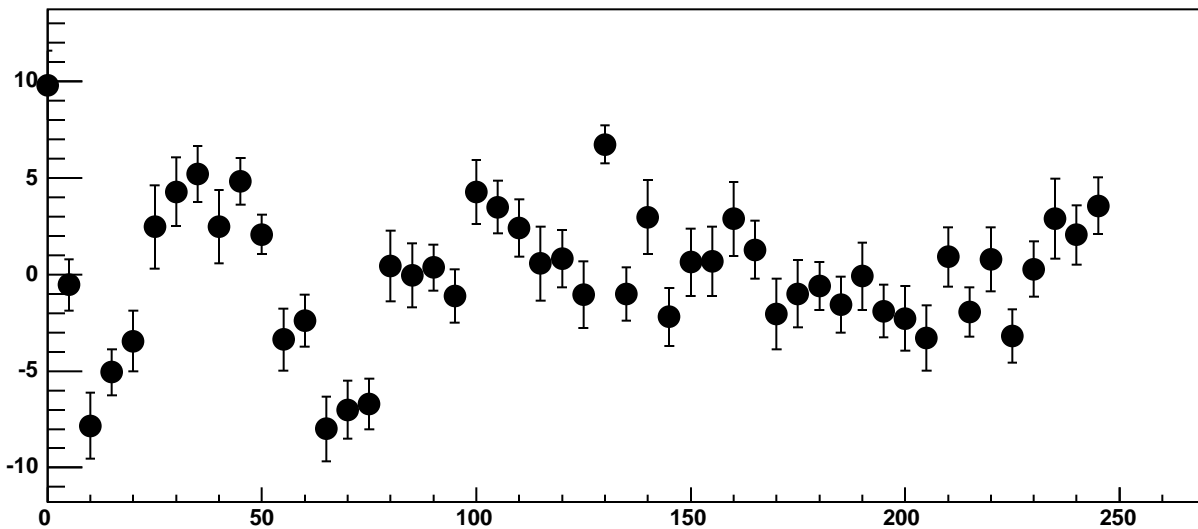


$\chi^2 / \text{ndf}$	239.1 / 41
p0	$282.1 \pm 0.4781$
p1	$65.88 \pm 0.5399$
p2	$-7.788\text{e}+07 \pm 1.258\text{e}+06$
p3	$4.372\text{e}+07 \pm 2.135\text{e}+05$
p4	$1.679 \pm 0.02762$

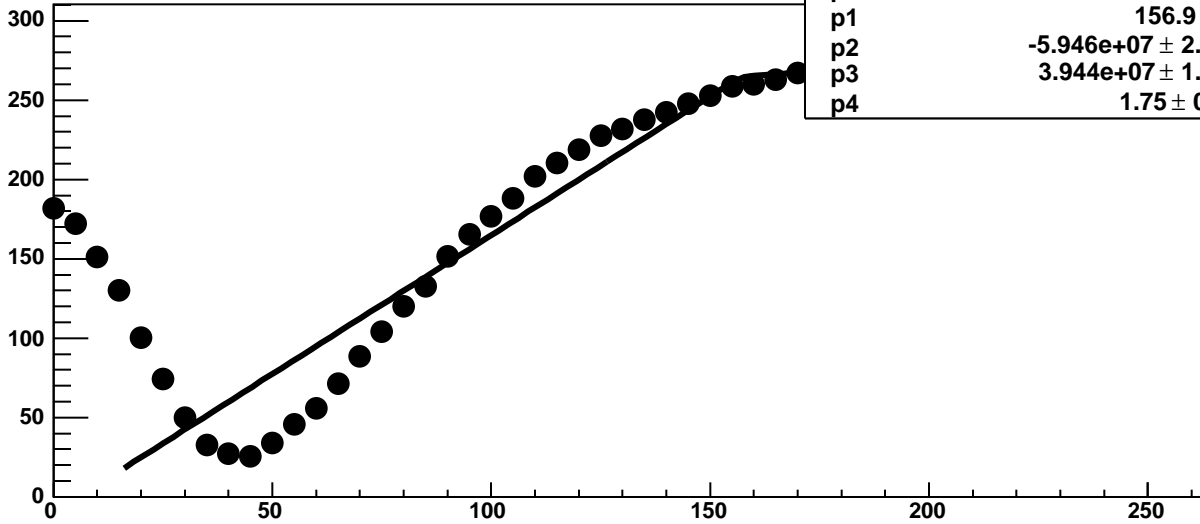
Chip 2, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold

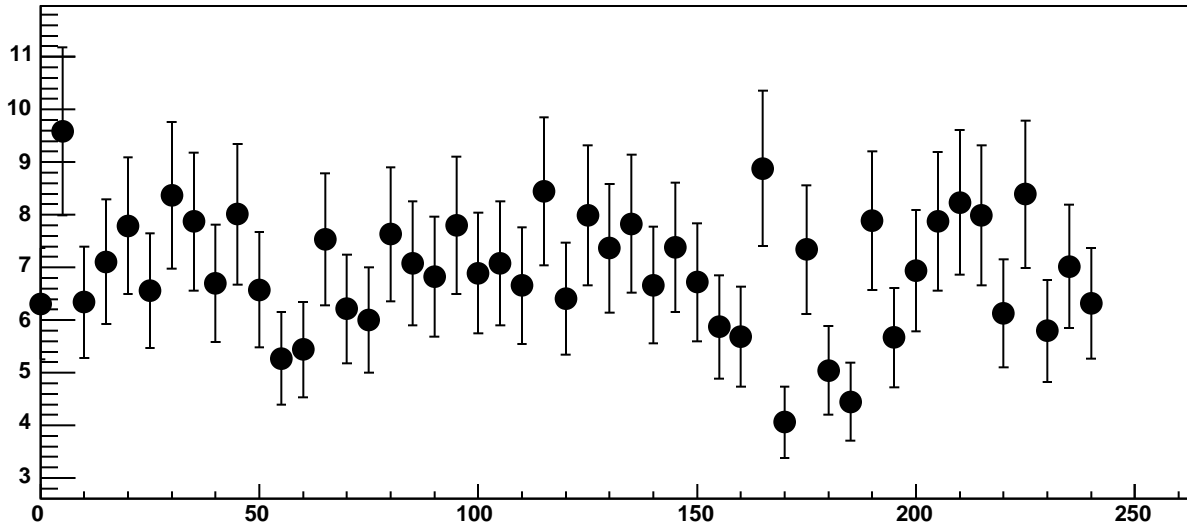


Chip 2, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

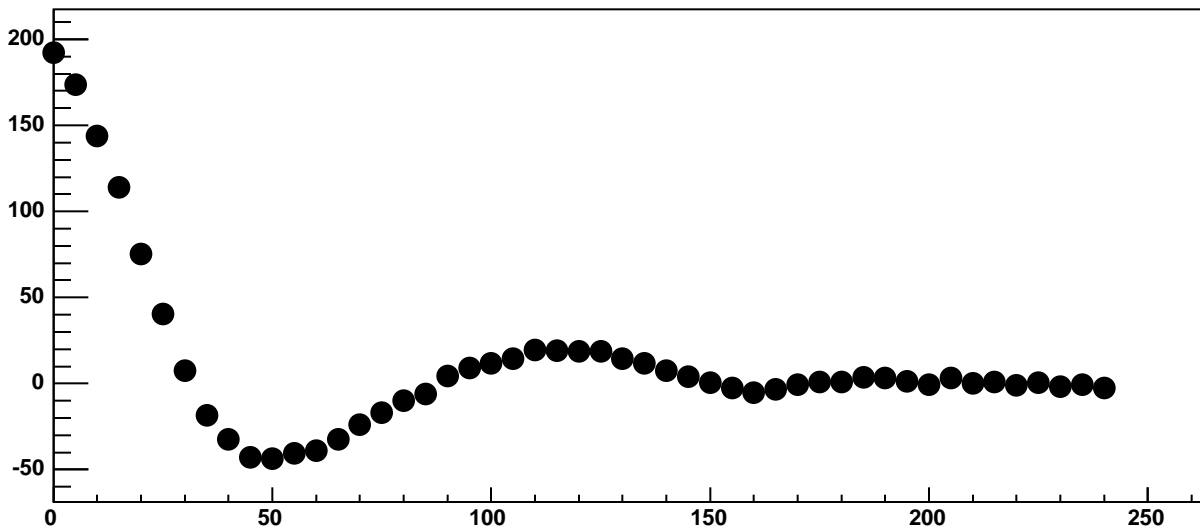


$\chi^2 / \text{ndf}$	1.316e+04 / 41
p0	264.5 ± 0.7547
p1	156.9 ± 0.5807
p2	-5.946e+07 ± 2.529e+06
p3	3.944e+07 ± 1.661e+06
p4	1.75 ± 0.007104

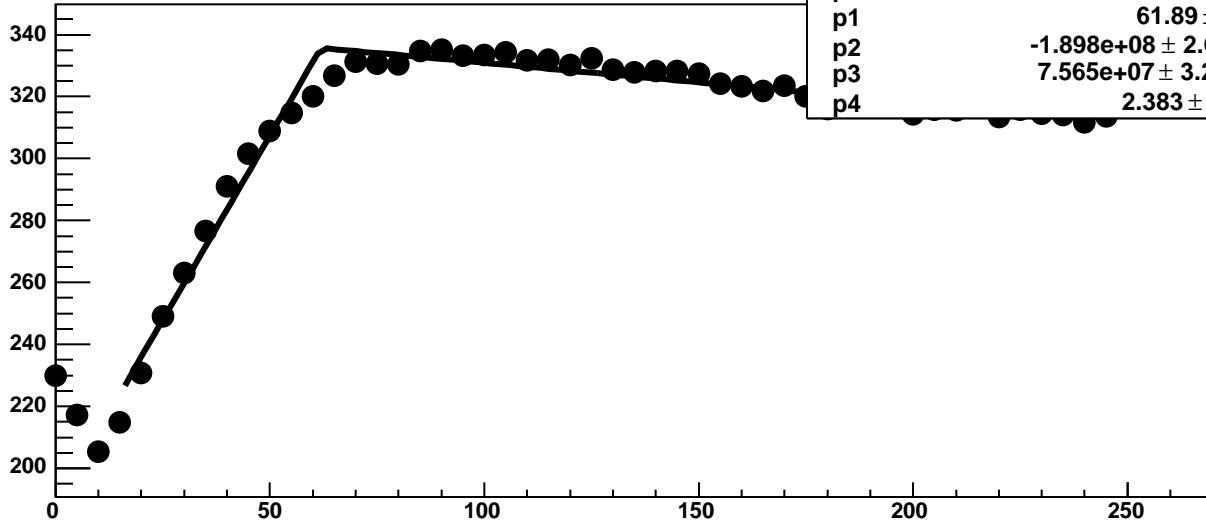
Chip 2, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

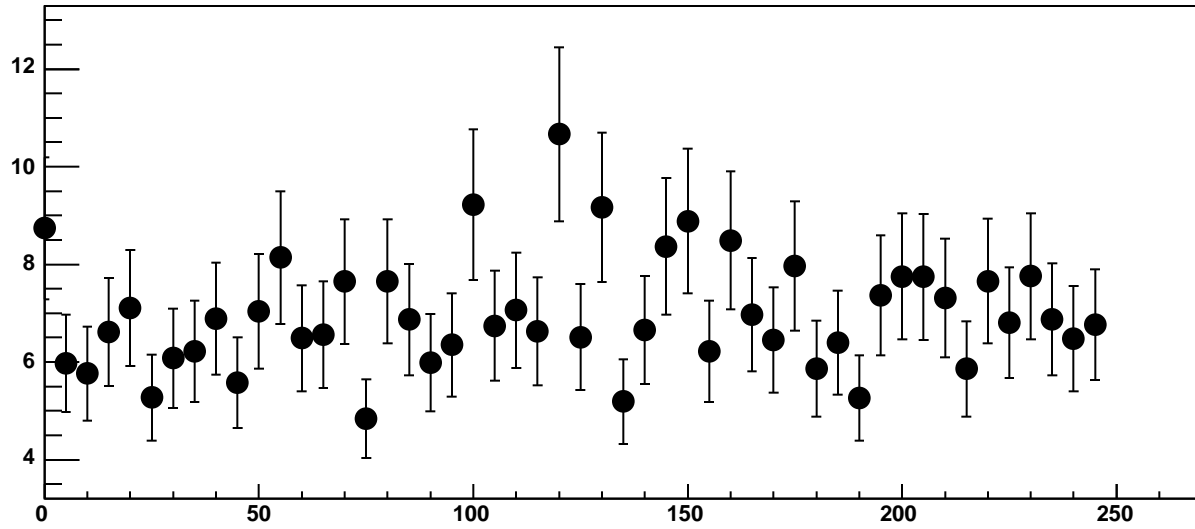


Chip 2, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

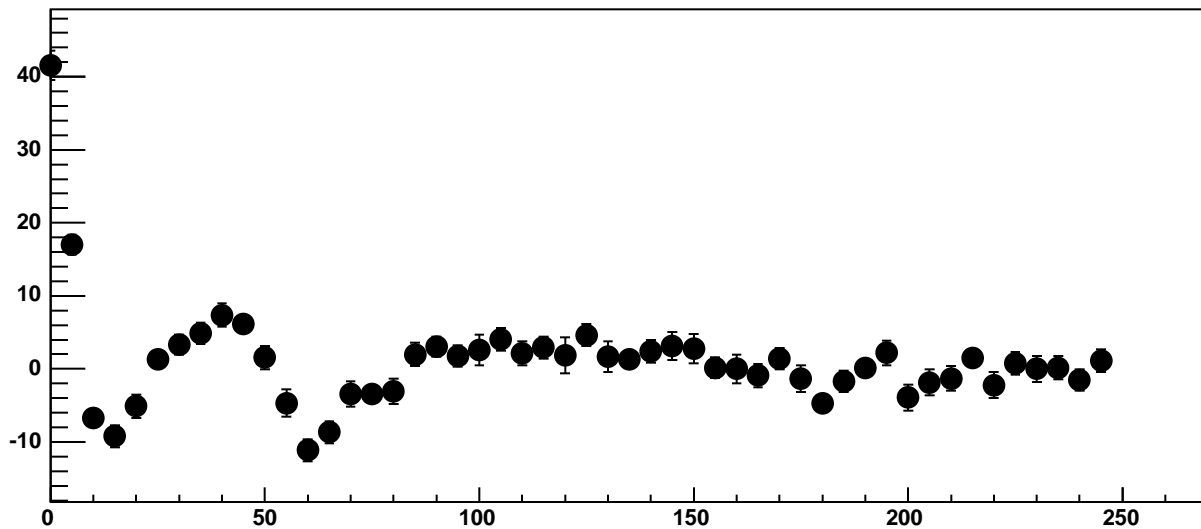


$\chi^2 / \text{ndf}$	291.3 / 41
p0	$335.7 \pm 0.5638$
p1	$61.89 \pm 0.4643$
p2	$-1.898\text{e}+08 \pm 2.621\text{e}+06$
p3	$7.565\text{e}+07 \pm 3.281\text{e}+05$
p4	$2.383 \pm 0.03449$

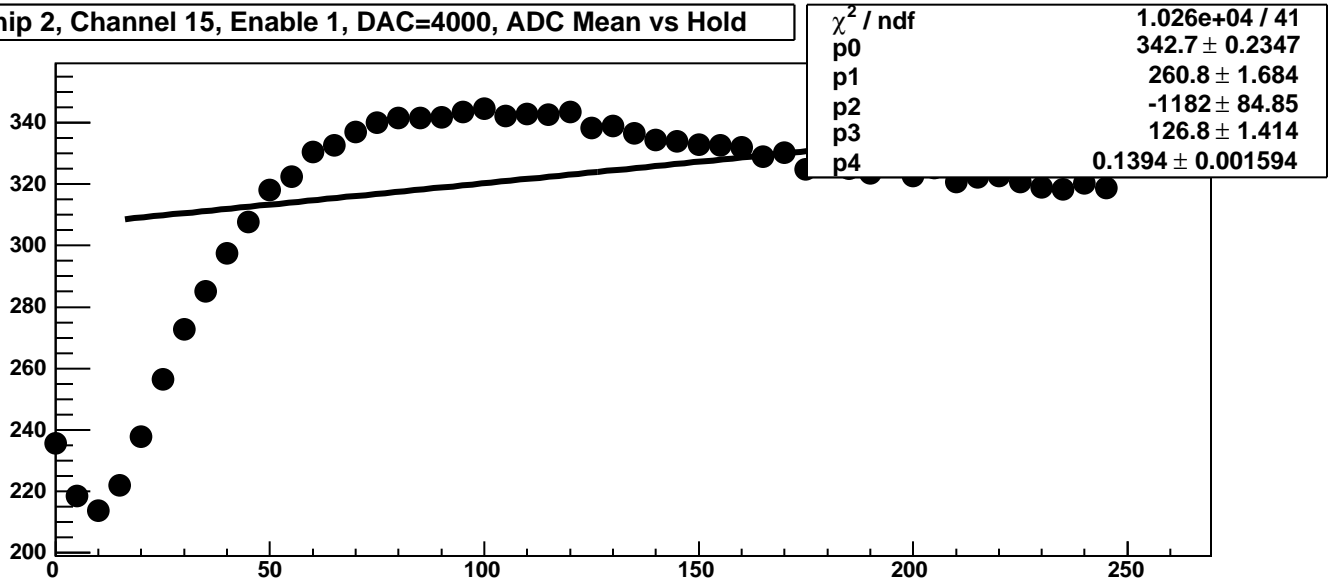
Chip 2, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



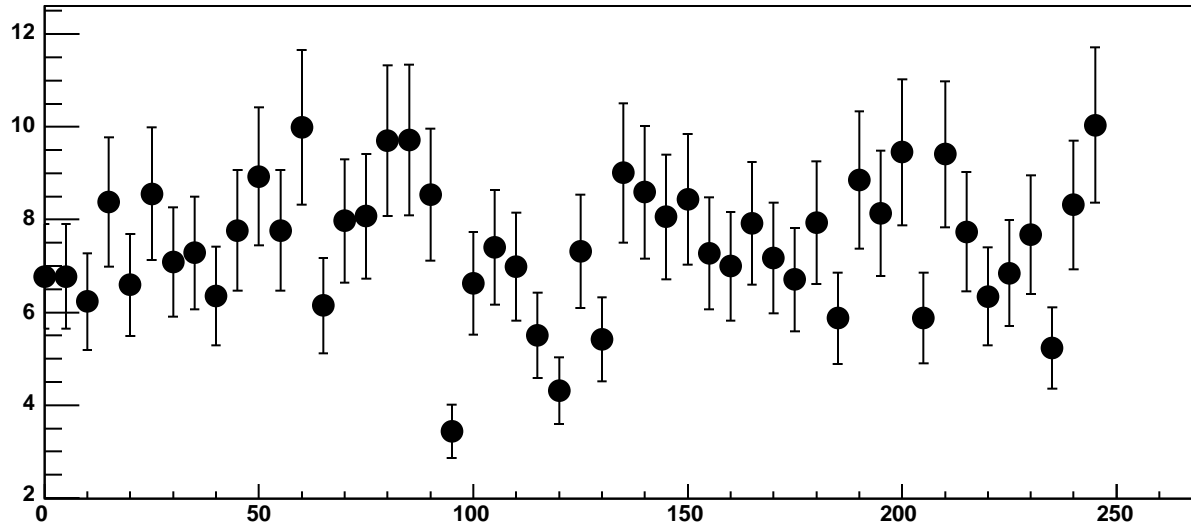
Chip 2, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold



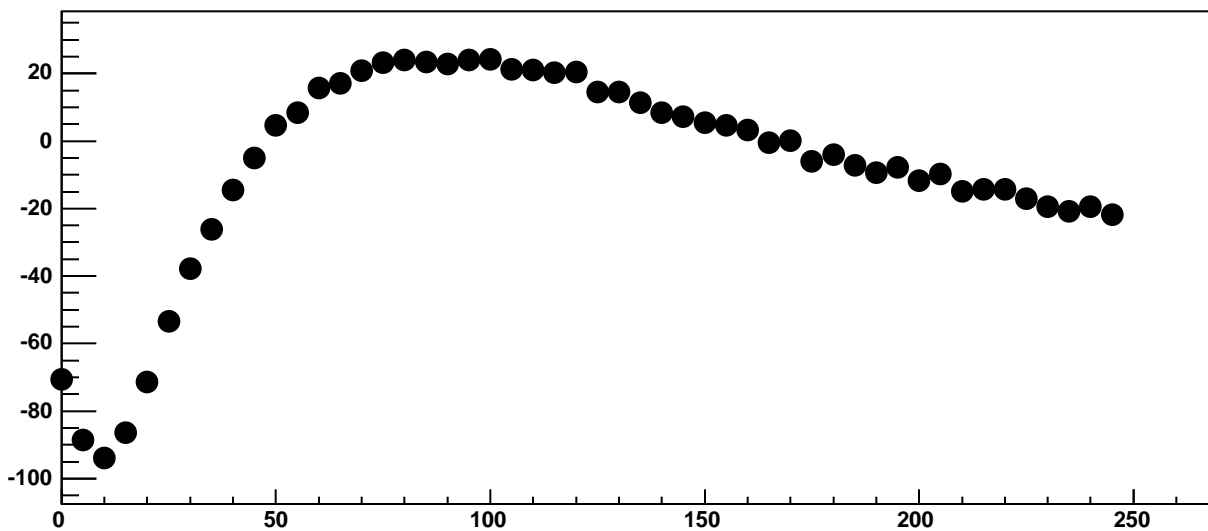
Chip 2, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold



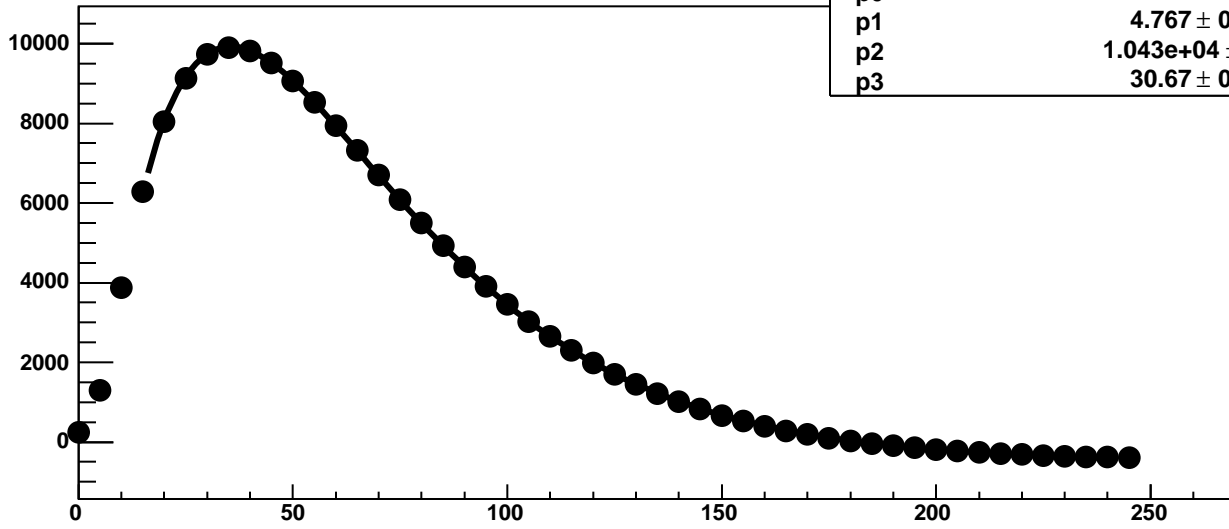
Chip 2, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

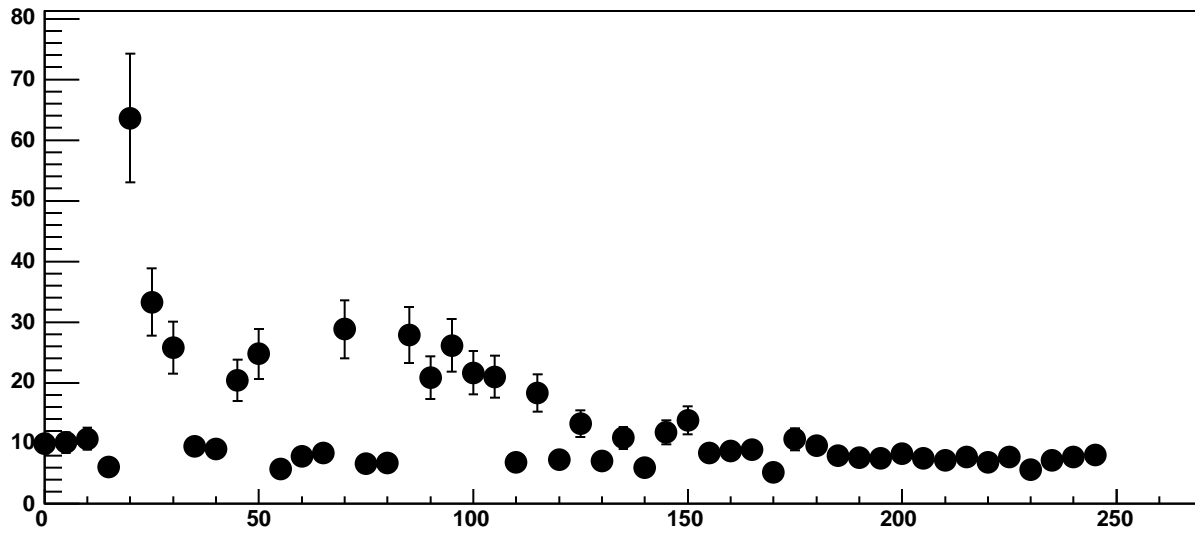


Chip 2, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold

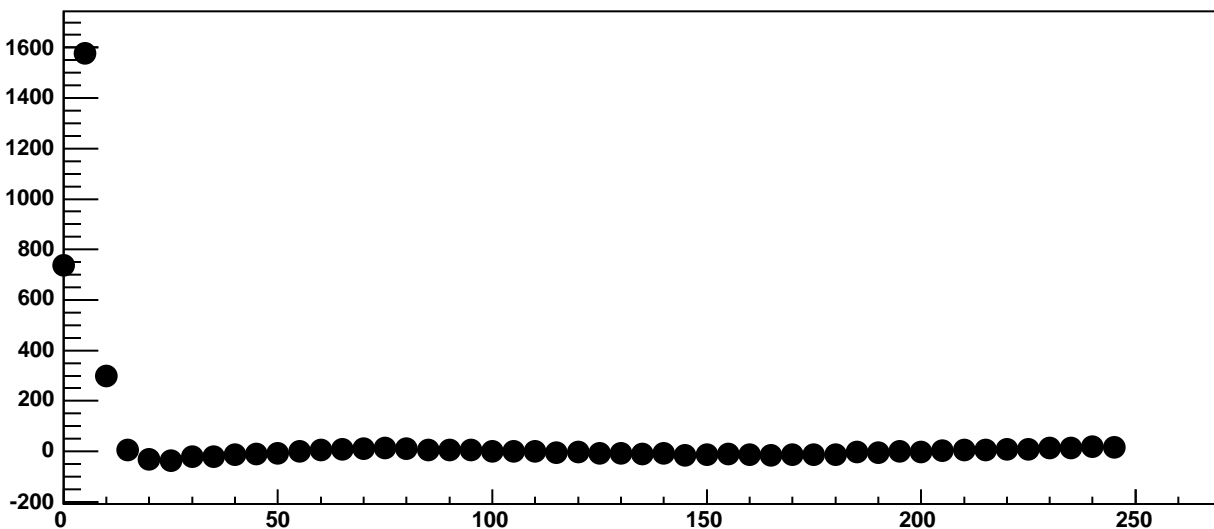


$\chi^2 / \text{ndf}$	1204 / 42
p0	-498.1 ± 0.5506
p1	4.767 ± 0.004675
p2	1.043e+04 ± 0.9967
p3	30.67 ± 0.005048

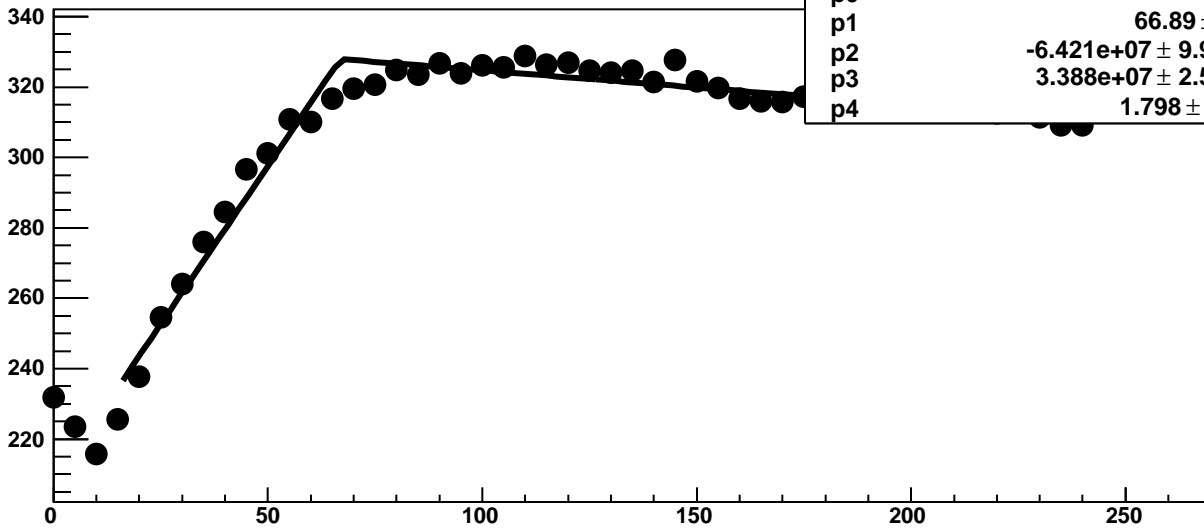
Chip 2, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold

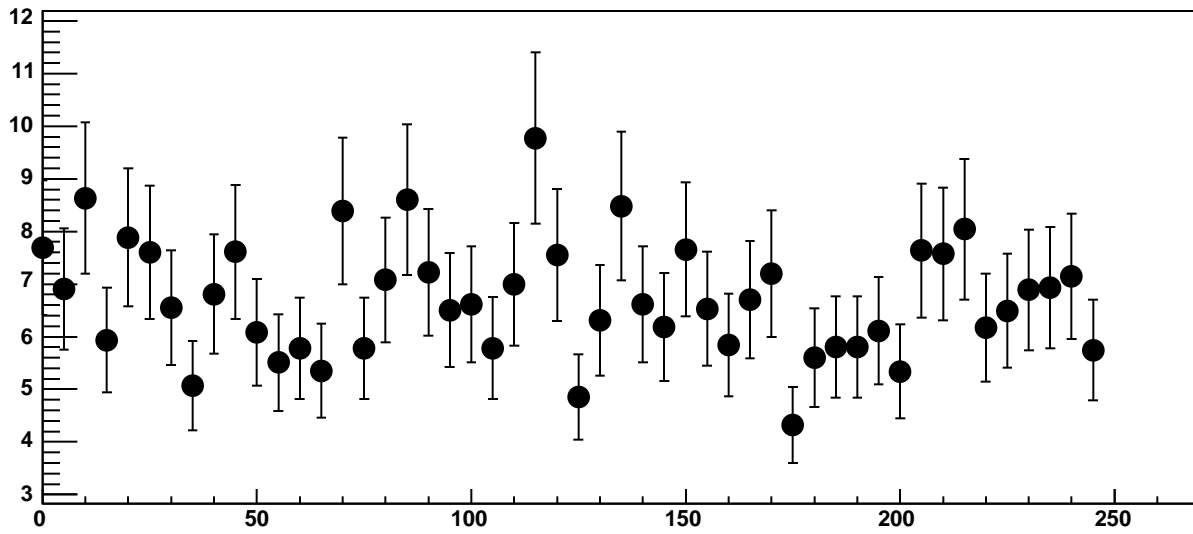


Chip 2, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

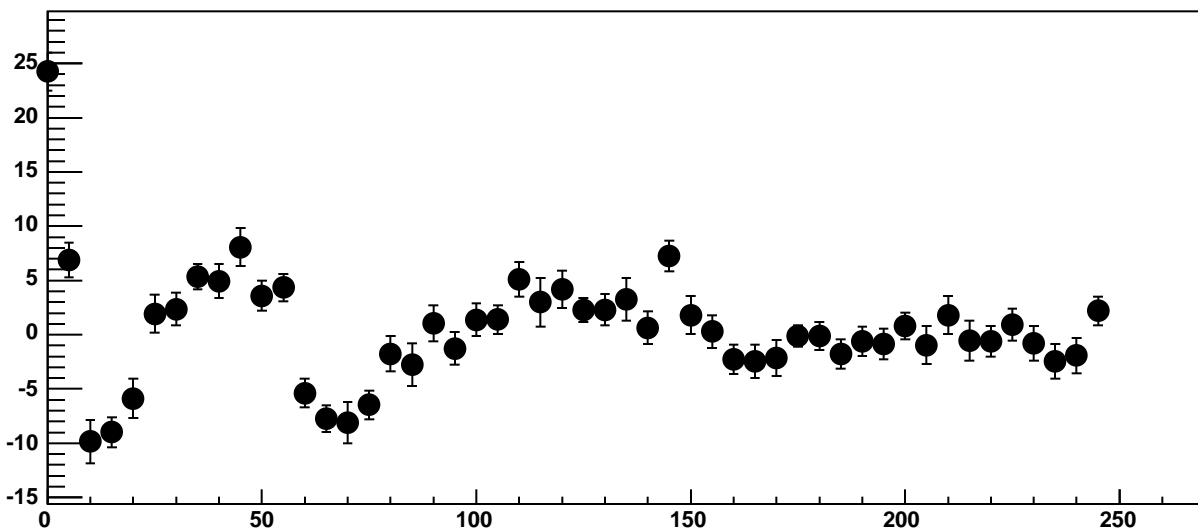


$\chi^2 / \text{ndf}$	303.6 / 41
p0	$327.9 \pm 0.5119$
p1	$66.89 \pm 0.5033$
p2	$-6.421\text{e}+07 \pm 9.972\text{e}+05$
p3	$3.388\text{e}+07 \pm 2.577\text{e}+05$
p4	$1.798 \pm 0.02666$

Chip 2, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold

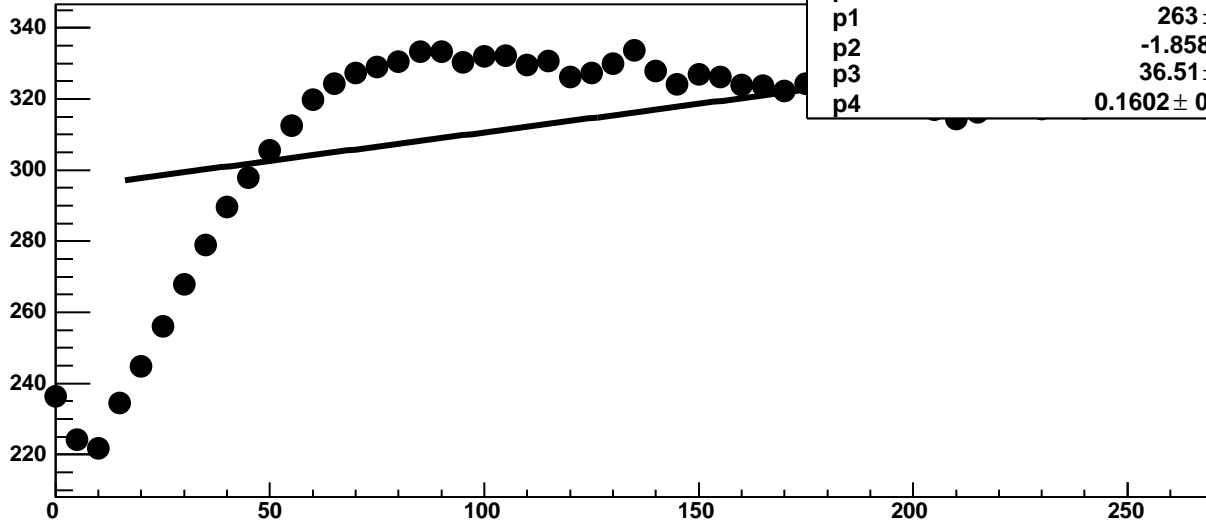


Chip 2, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold



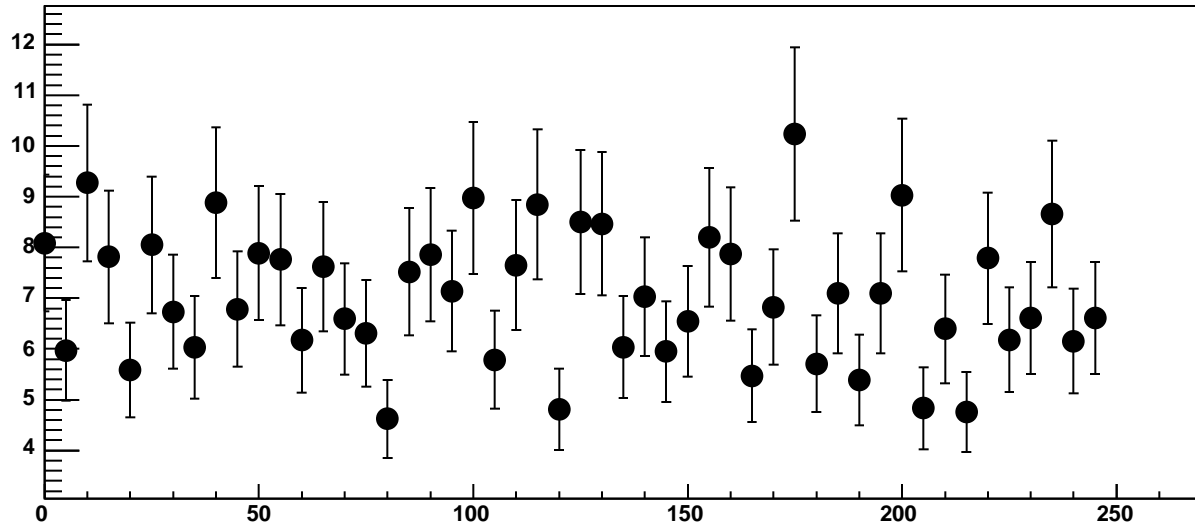


Chip 2, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

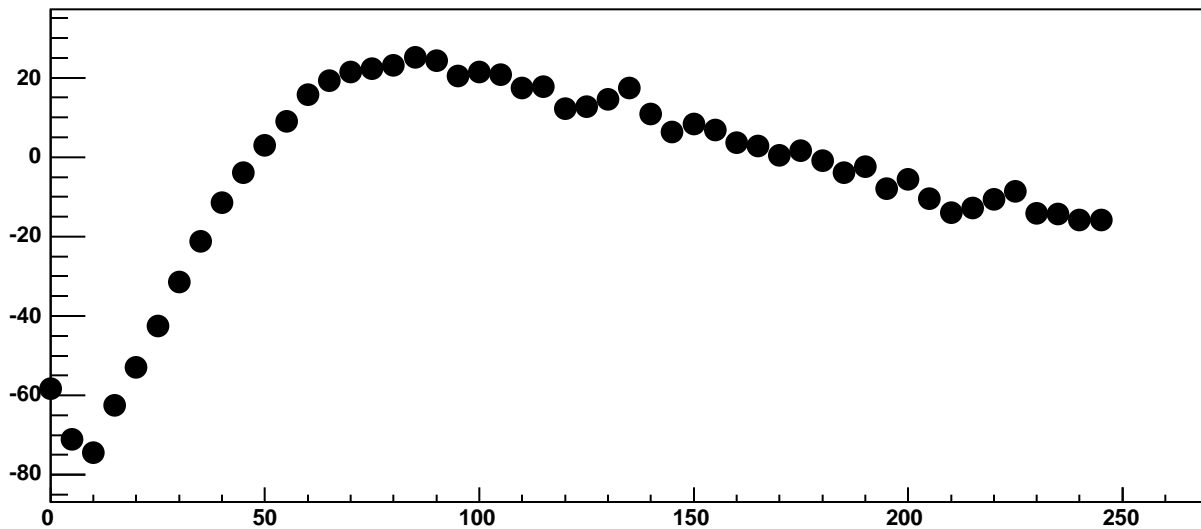


$\chi^2 / \text{ndf}$	7604 / 41
p0	$336.7 \pm 0.4342$
p1	$263 \pm 0.6438$
p2	$-1.858 \pm 3.373$
p3	$36.51 \pm 0.2613$
p4	$0.1602 \pm 0.003373$

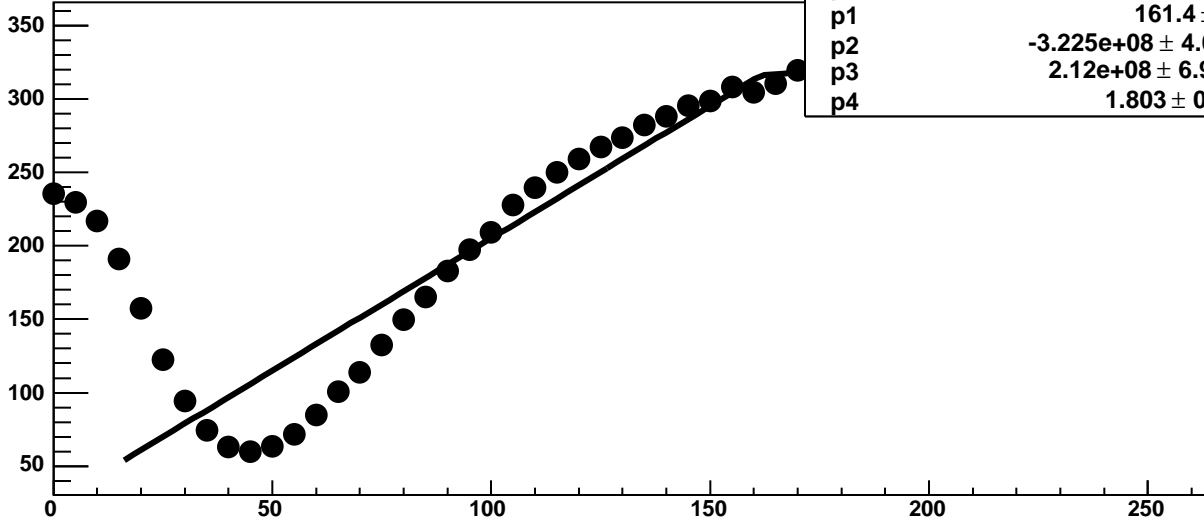
Chip 2, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



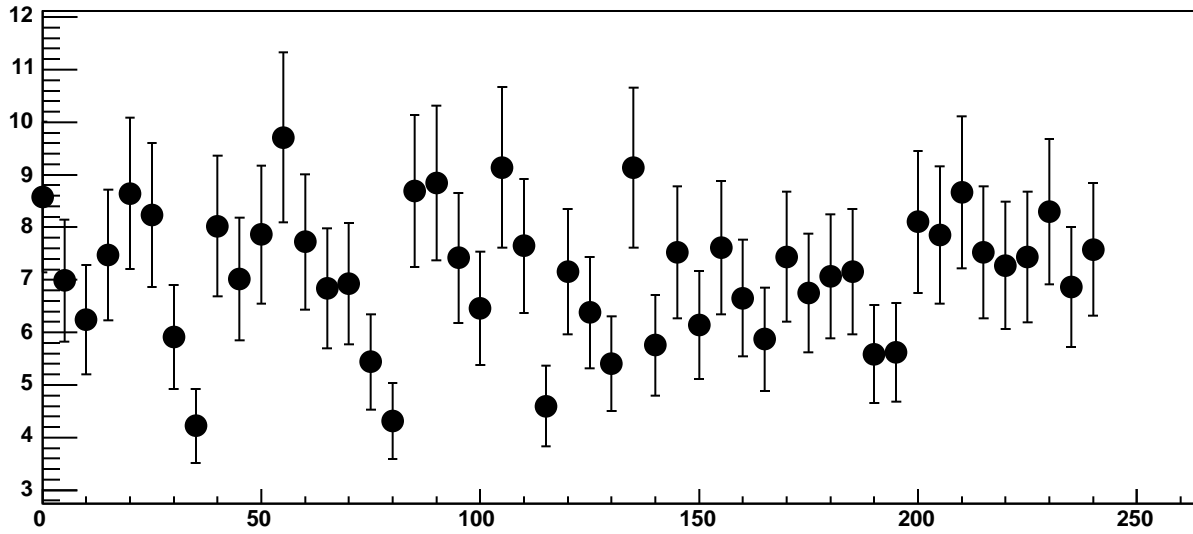
Chip 2, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold



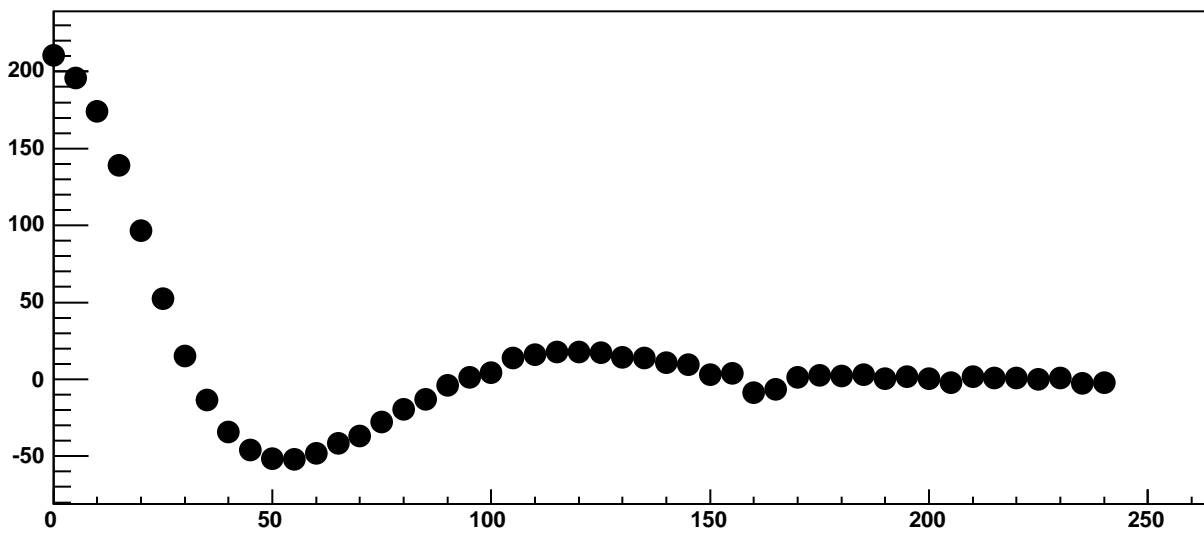
Chip 2, Channel 15, Enable 5, DAC=4000, ADC Mean vs Hold



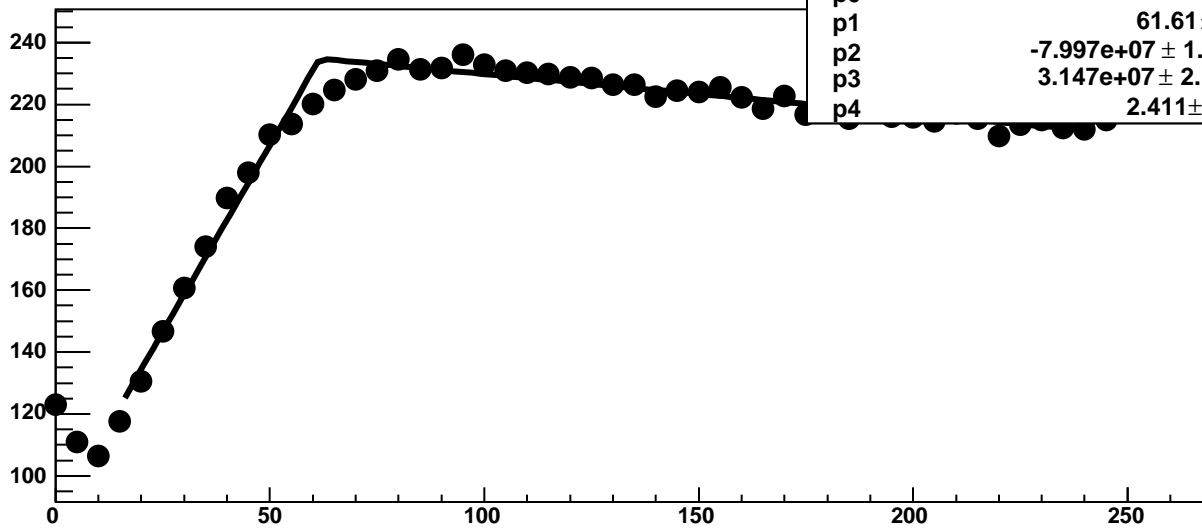
Chip 2, Channel 15, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 15, Enable 5, DAC=4000, ADC Residuals vs Hold

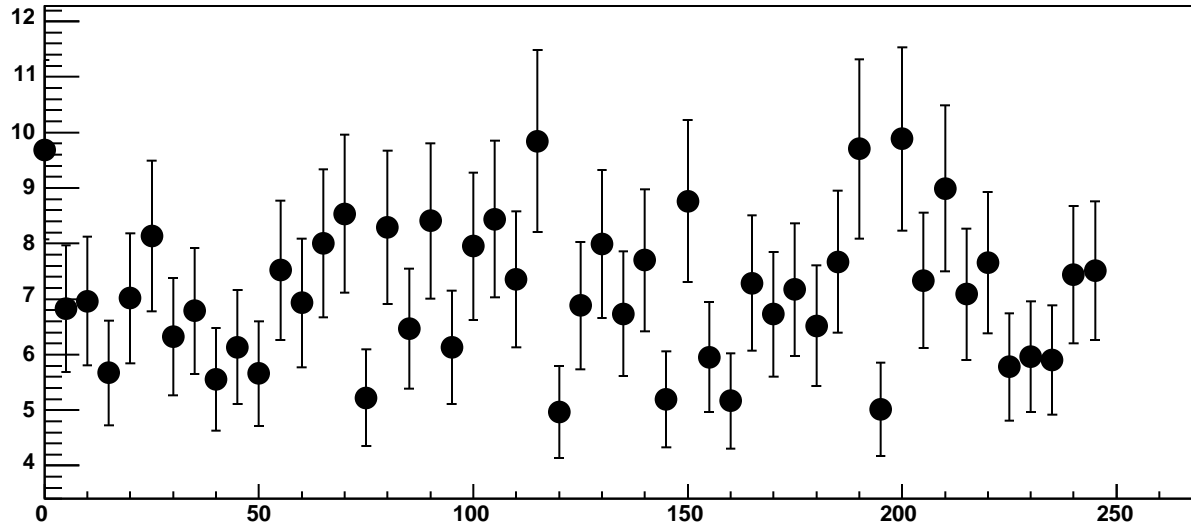


Chip 2, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

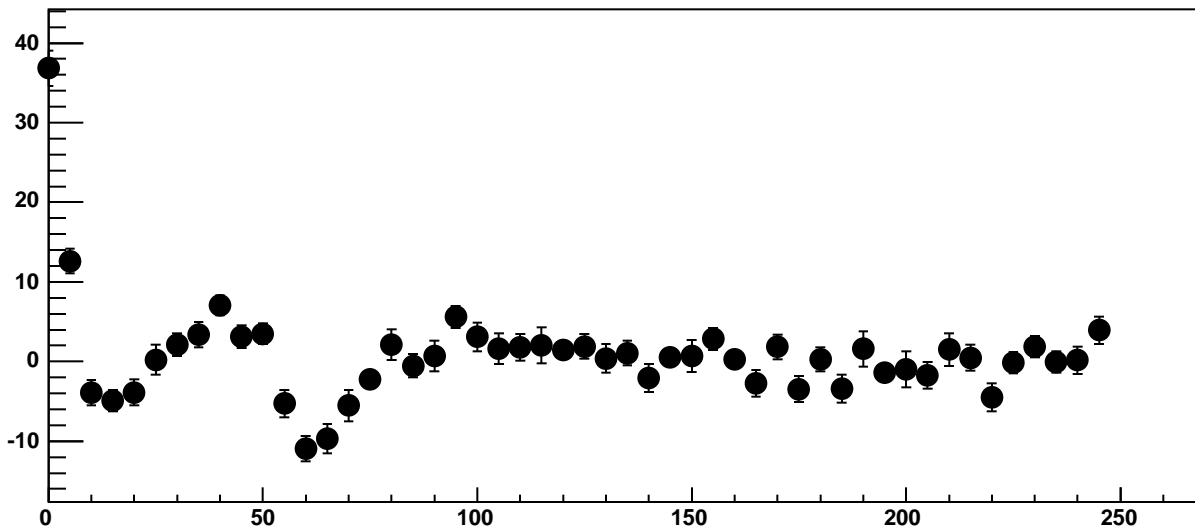


$\chi^2 / \text{ndf}$	222.5 / 41
p0	$234.8 \pm 0.5132$
p1	$61.61 \pm 0.4214$
p2	$-7.997\text{e}+07 \pm 1.159\text{e}+06$
p3	$3.147\text{e}+07 \pm 2.148\text{e}+05$
p4	$2.411 \pm 0.03297$

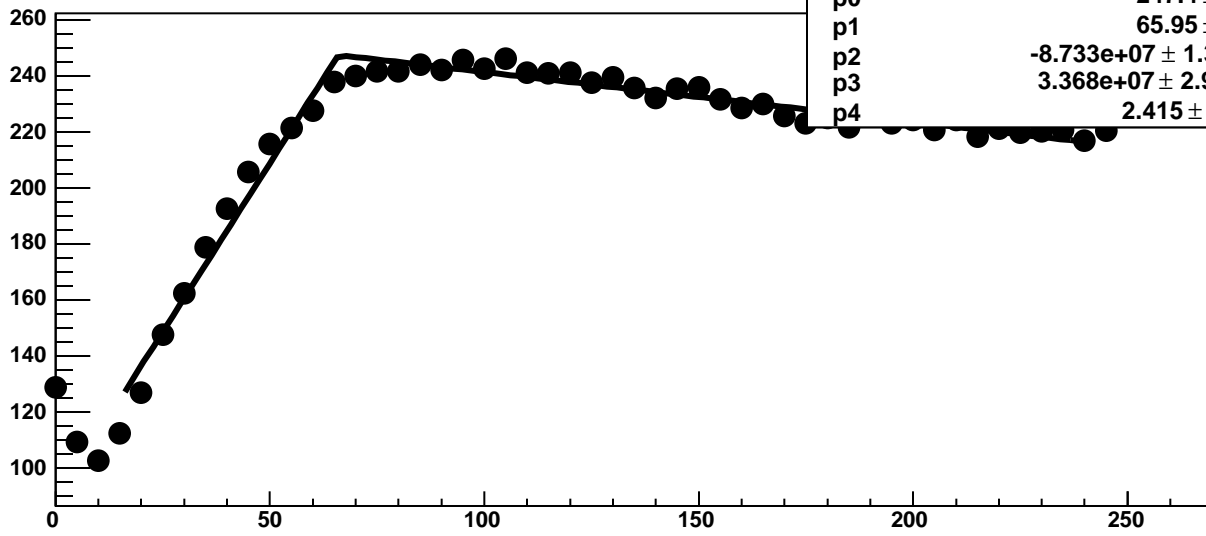
Chip 2, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

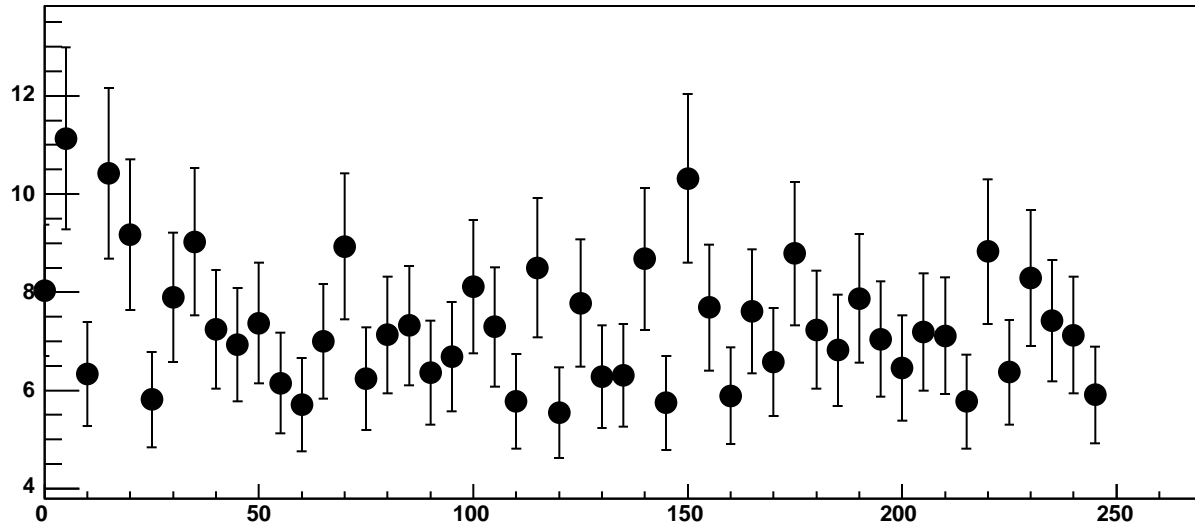


Chip 2, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold

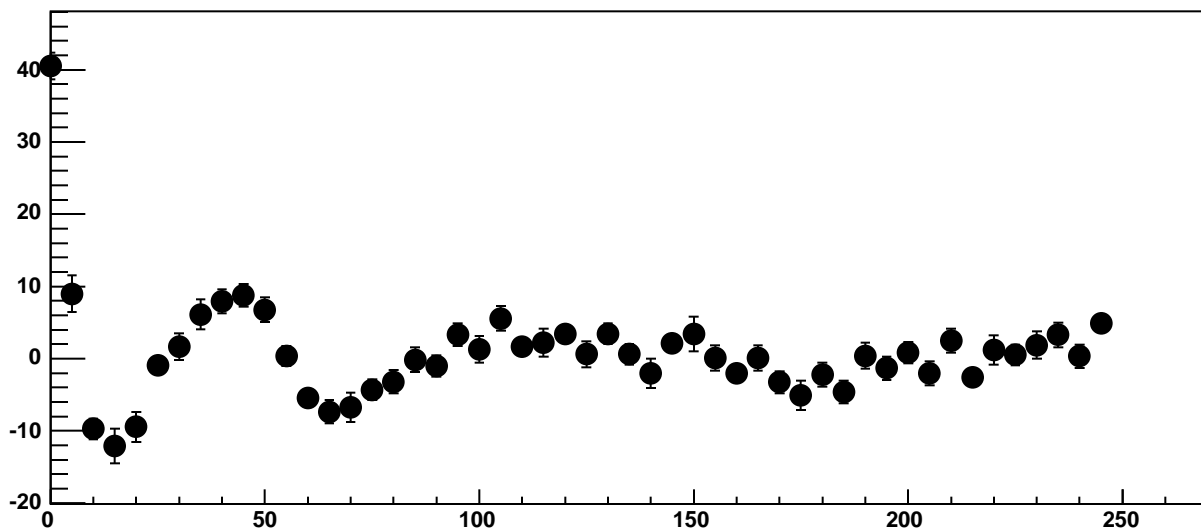


$\chi^2 / \text{ndf}$	263.1 / 41
p0	$247.4 \pm 0.5163$
p1	$65.95 \pm 0.4028$
p2	$-8.733\text{e}+07 \pm 1.322\text{e}+06$
p3	$3.368\text{e}+07 \pm 2.924\text{e}+05$
p4	$2.415 \pm 0.03234$

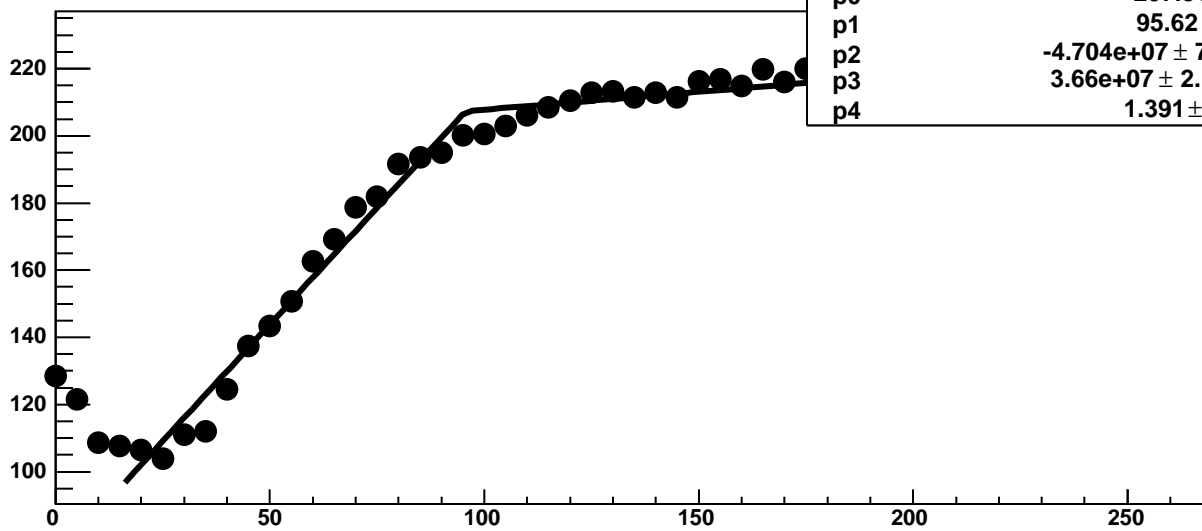
Chip 2, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold

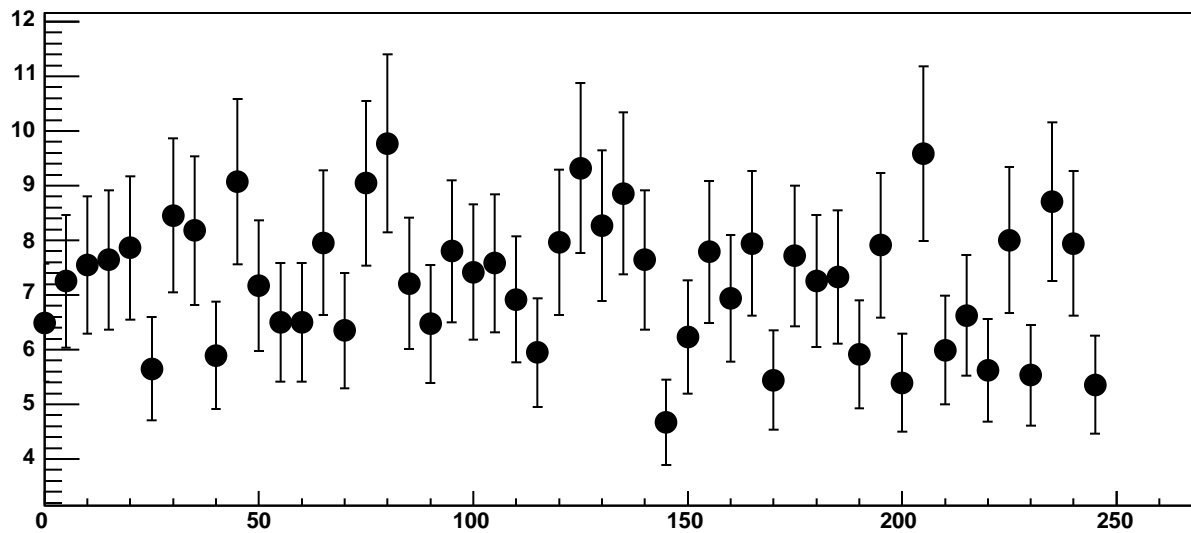


Chip 2, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold

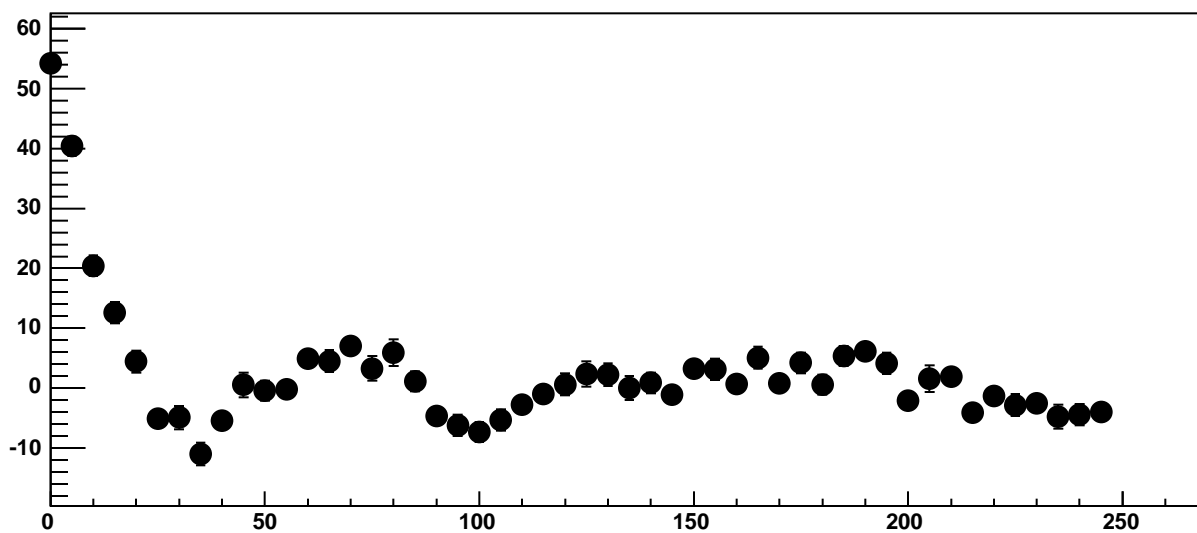


$\chi^2 / \text{ndf}$	328 / 41
p0	$207.3 \pm 0.6736$
p1	$95.62 \pm 0.7863$
p2	$-4.704\text{e}+07 \pm 7.06\text{e}+05$
p3	$3.66\text{e}+07 \pm 2.124\text{e}+05$
p4	$1.391 \pm 0.01663$

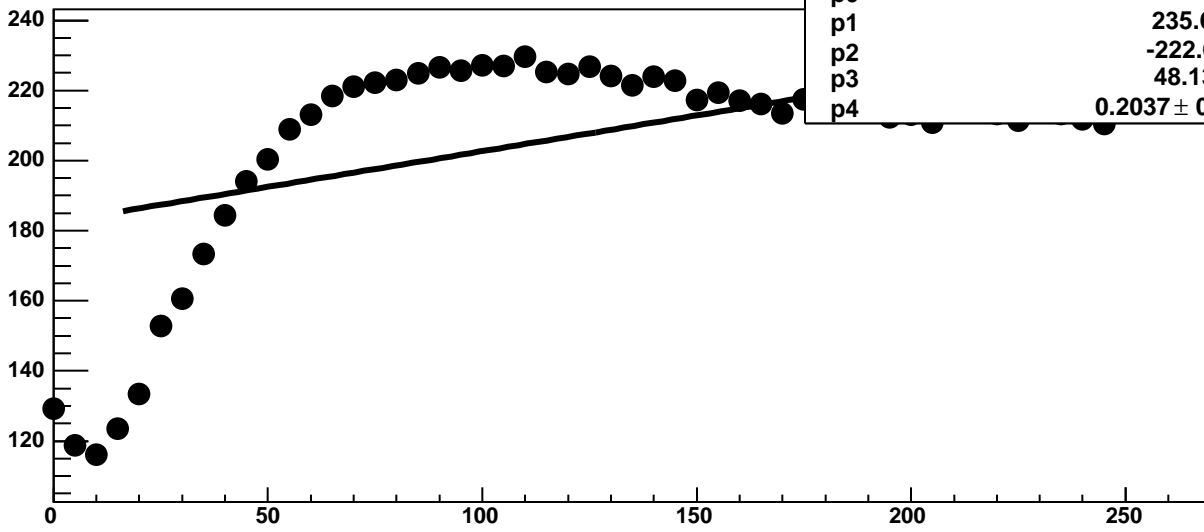
Chip 2, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold

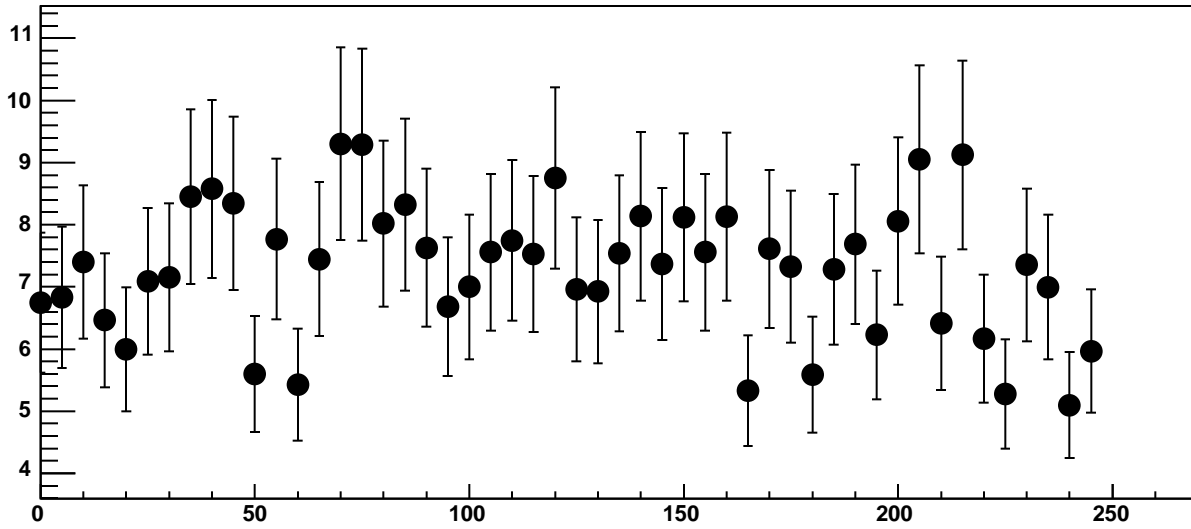


Chip 2, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

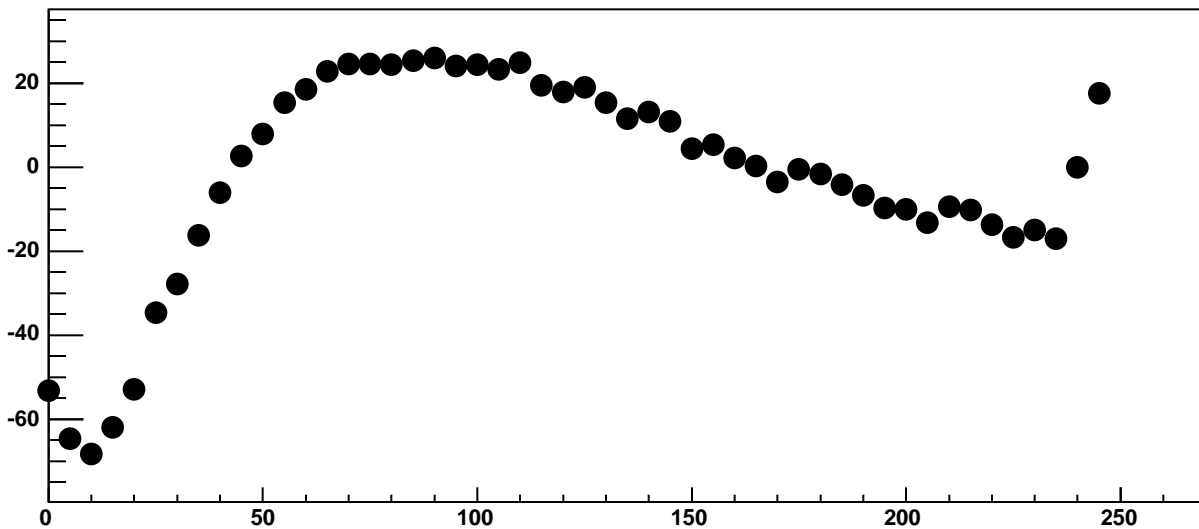


$\chi^2 / \text{ndf}$	7531 / 41
p0	$230.3 \pm 0.7795$
p1	$235.6 \pm 3.111$
p2	$-222.6 \pm 146$
p3	$48.13 \pm 27.3$
p4	$0.2037 \pm 0.003642$

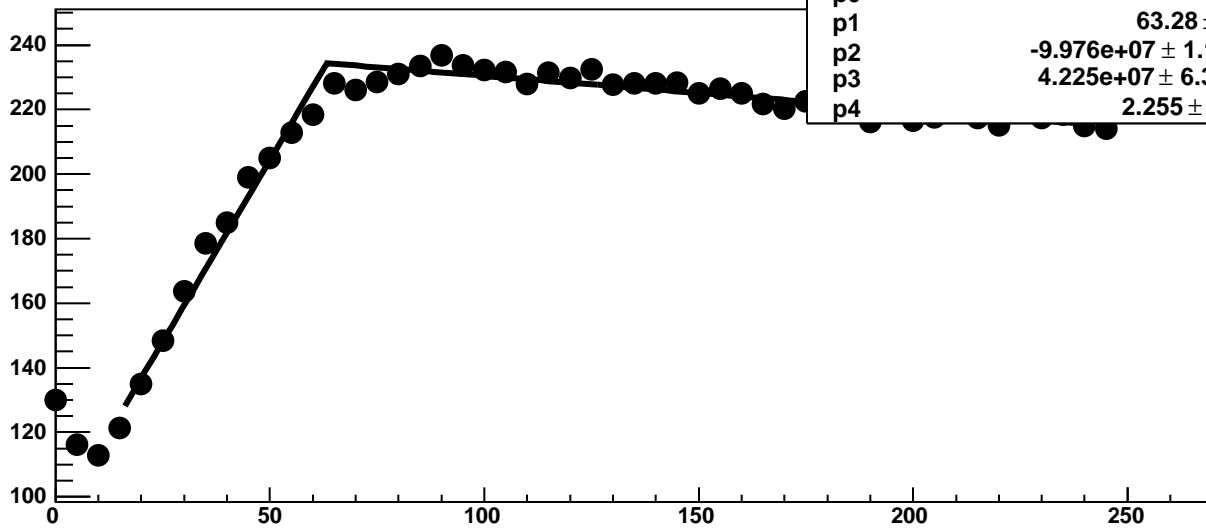
Chip 2, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

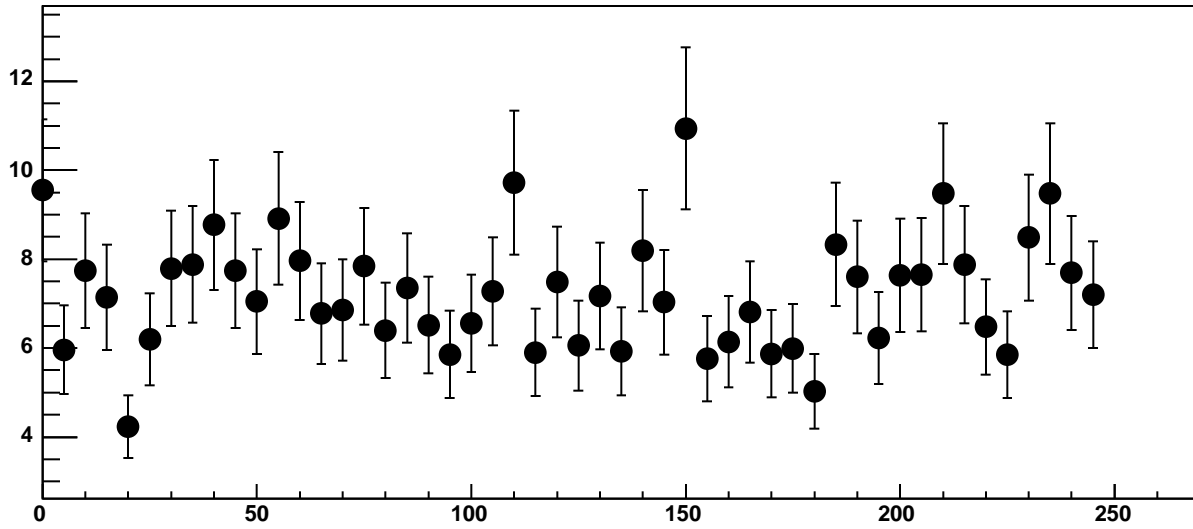


Chip 2, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

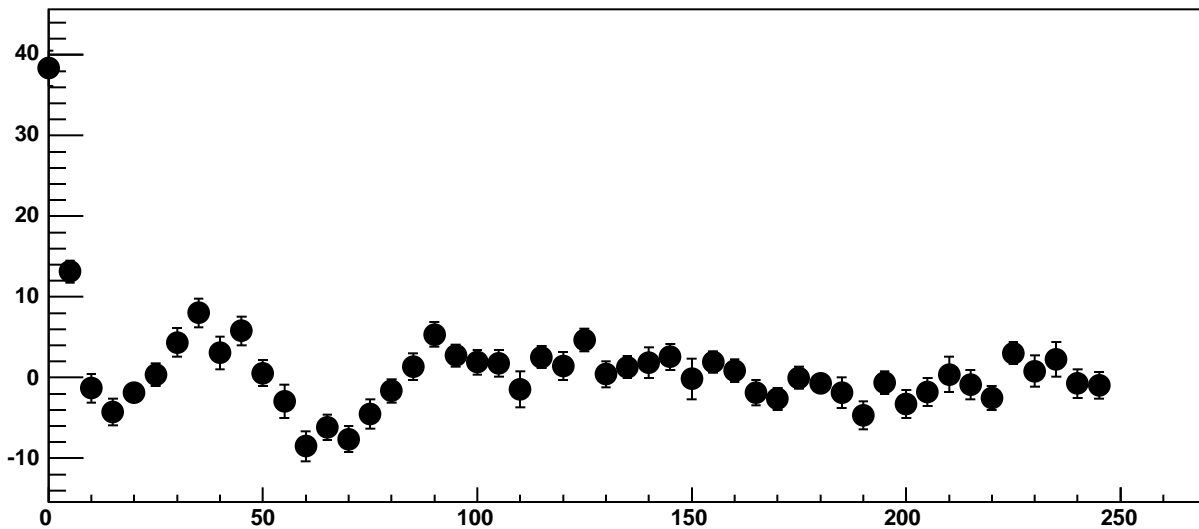


$\chi^2 / \text{ndf}$	190.8 / 41
p0	$234.4 \pm 0.4876$
p1	$63.28 \pm 0.4088$
p2	$-9.976e+07 \pm 1.132e+06$
p3	$4.225e+07 \pm 6.347e+05$
p4	$2.255 \pm 0.02149$

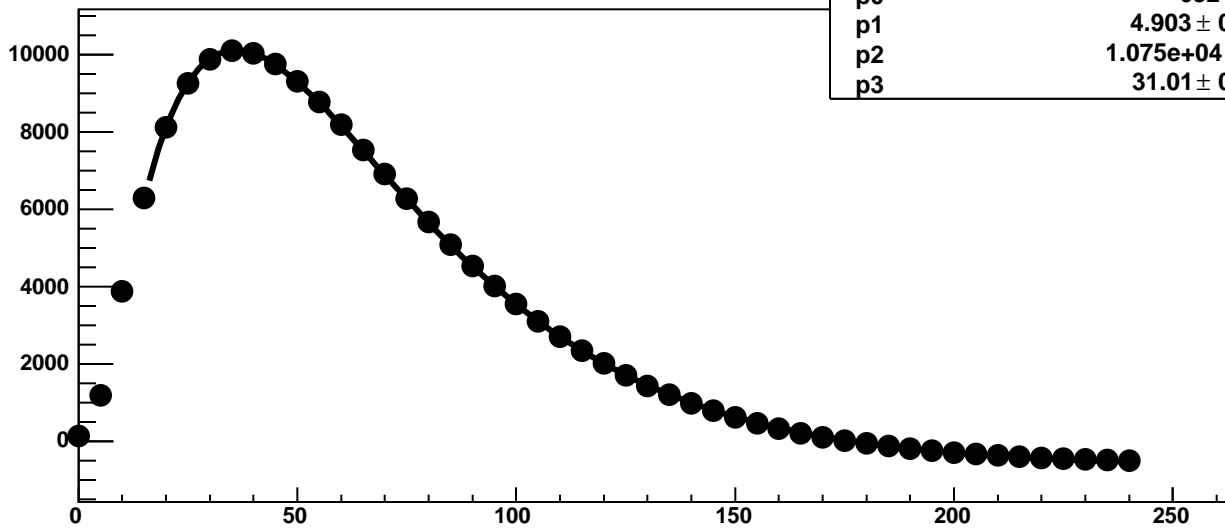
Chip 2, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold



Chip 2, Channel 16, Enable 5!, DAC=4000, ADC Mean vs Hold



$\chi^2 / \text{ndf}$

2626 / 42

p0

-632 ± 0.5107

p1

4.903 ± 0.006546

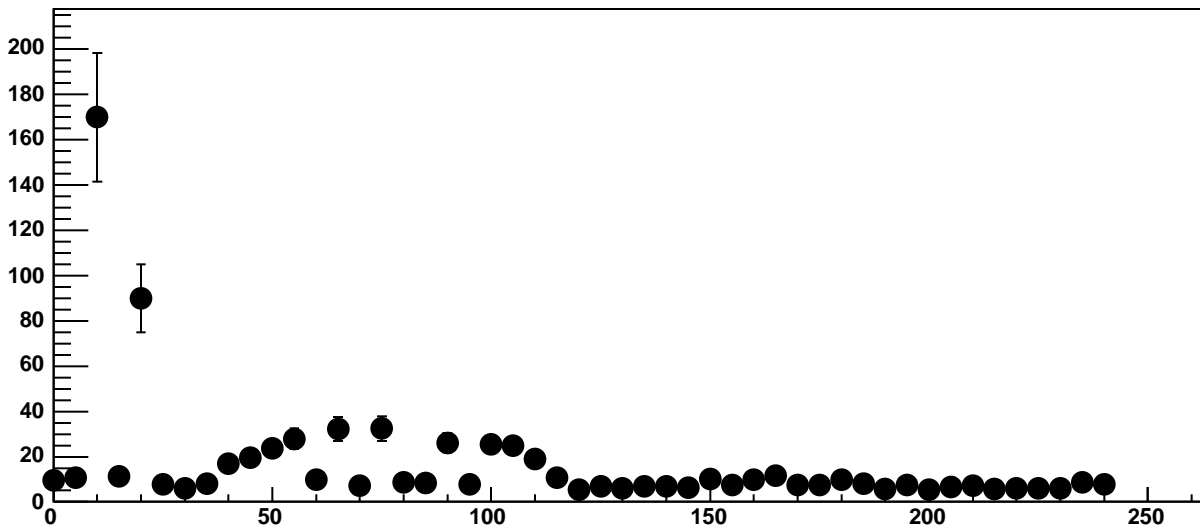
p2

1.075e+04 ± 0.9221

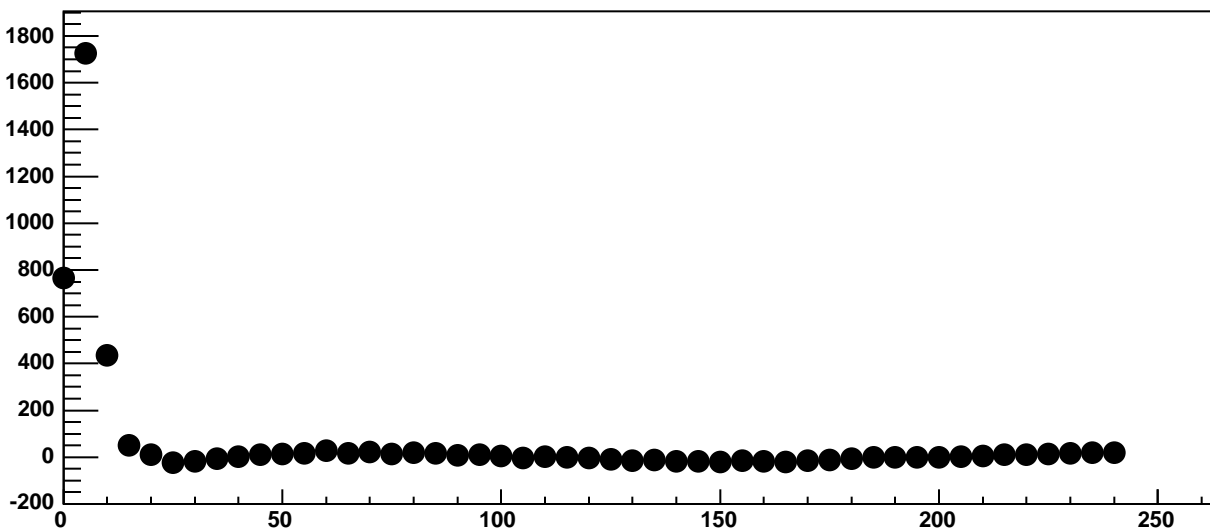
p3

31.01 ± 0.004583

Chip 2, Channel 16, Enable 5!, DAC=4000, ADC Noise vs Hold

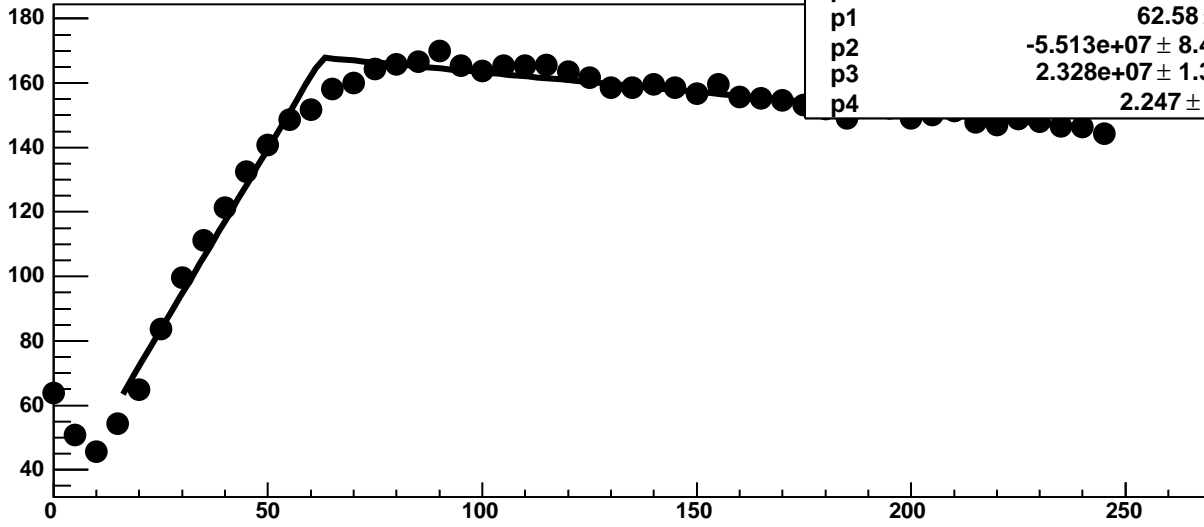


Chip 2, Channel 16, Enable 5!, DAC=4000, ADC Residuals vs Hold



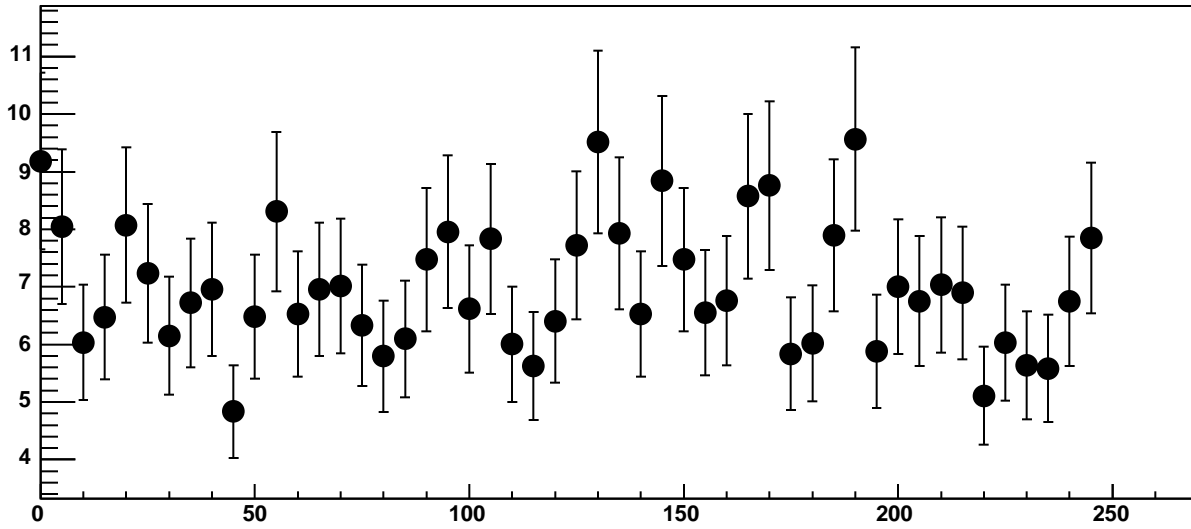


Chip 2, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

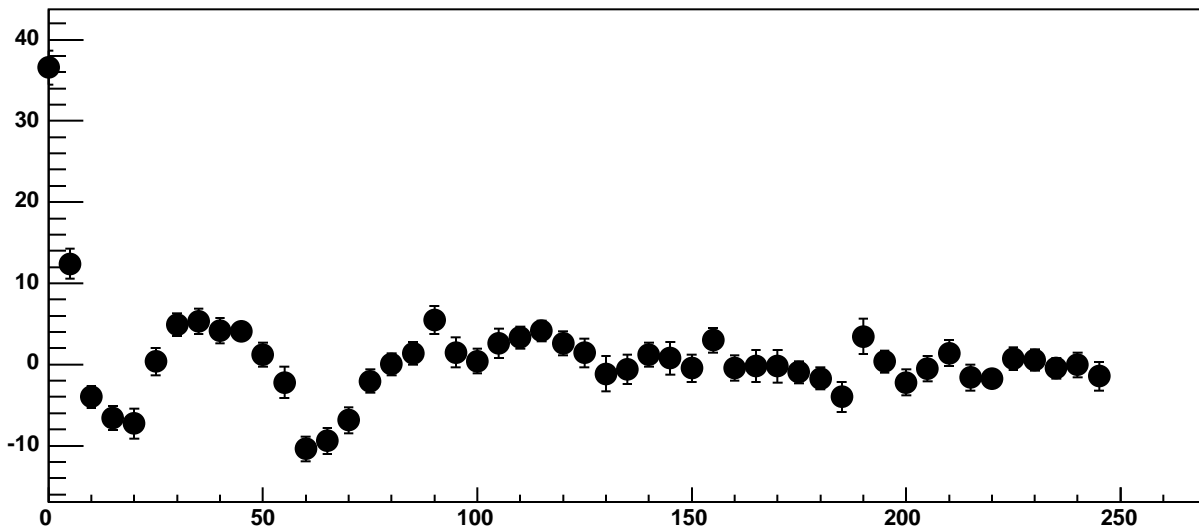


$\chi^2 / \text{ndf}$	240 / 41
p0	$167.9 \pm 0.488$
p1	$62.58 \pm 0.4651$
p2	$-5.513e+07 \pm 8.448e+05$
p3	$2.328e+07 \pm 1.356e+05$
p4	$2.247 \pm 0.03444$

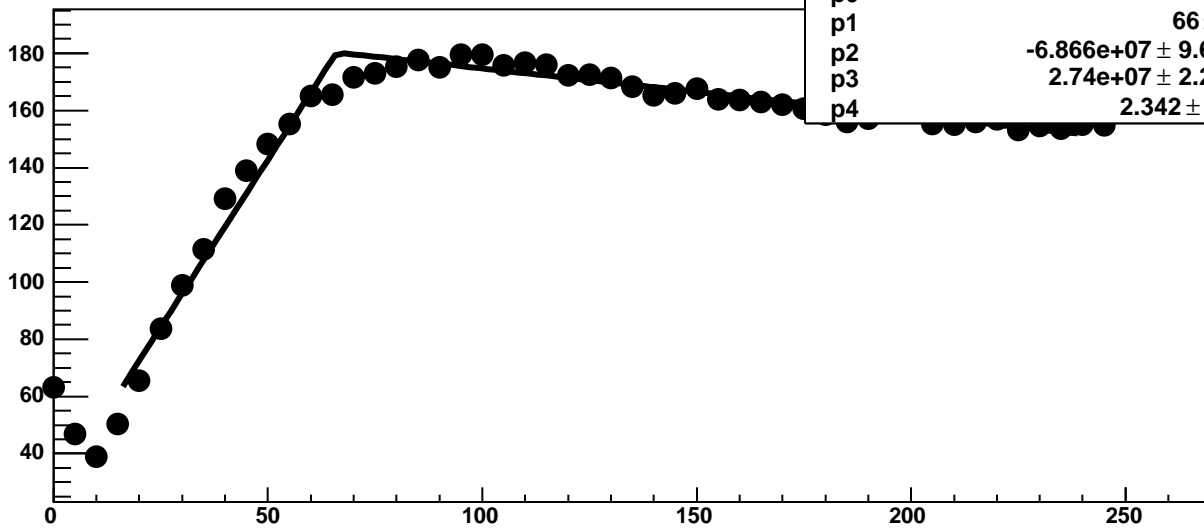
Chip 2, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

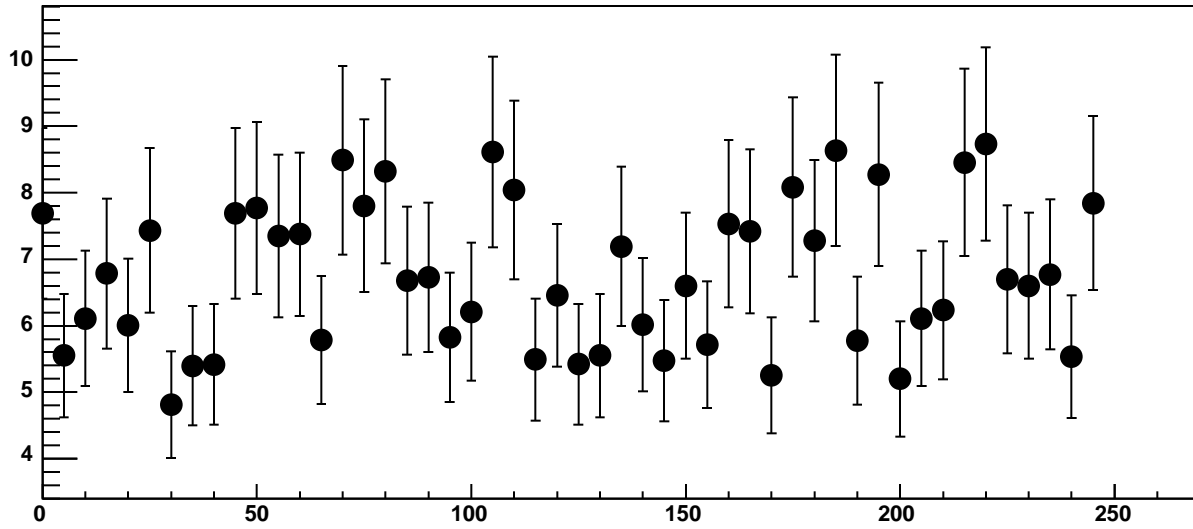


Chip 2, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

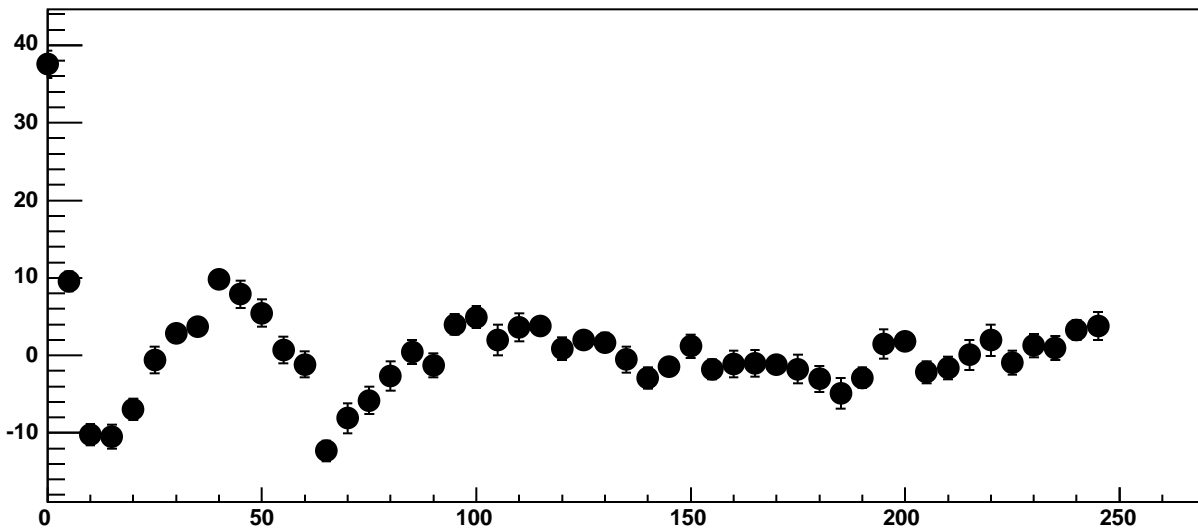


$\chi^2 / \text{ndf}$	375.3 / 41
p0	$180.3 \pm 0.5047$
p1	$66 \pm 0.4121$
p2	$-6.866e+07 \pm 9.613e+05$
p3	$2.74e+07 \pm 2.212e+05$
p4	$2.342 \pm 0.02827$

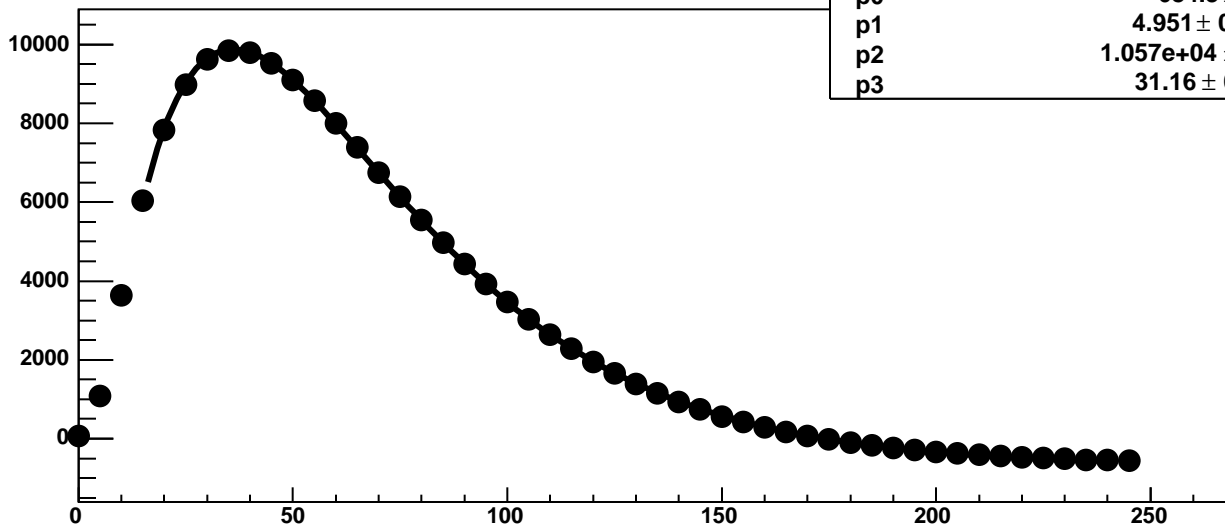
Chip 2, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

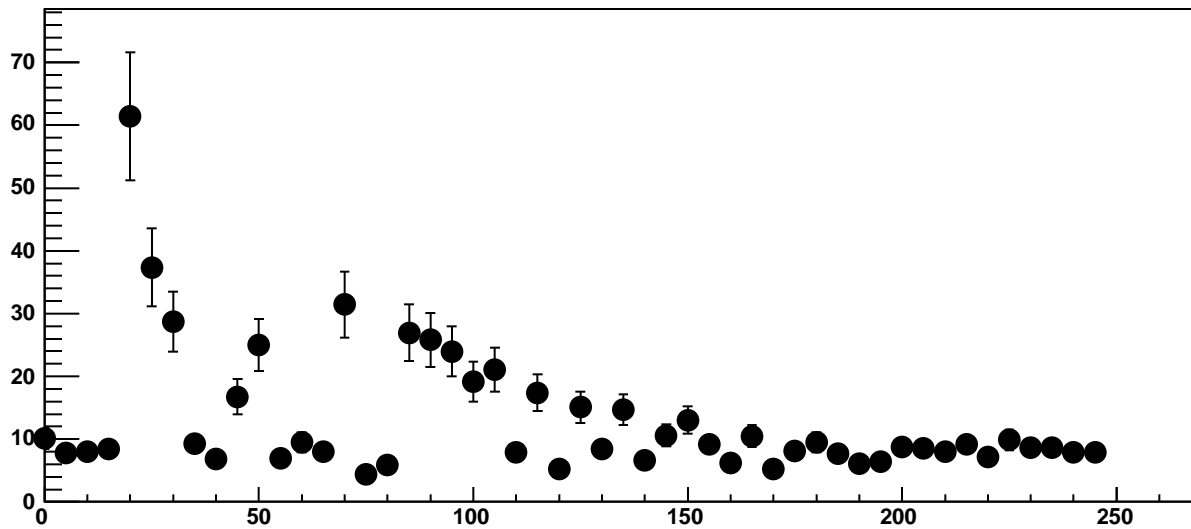


Chip 2, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold

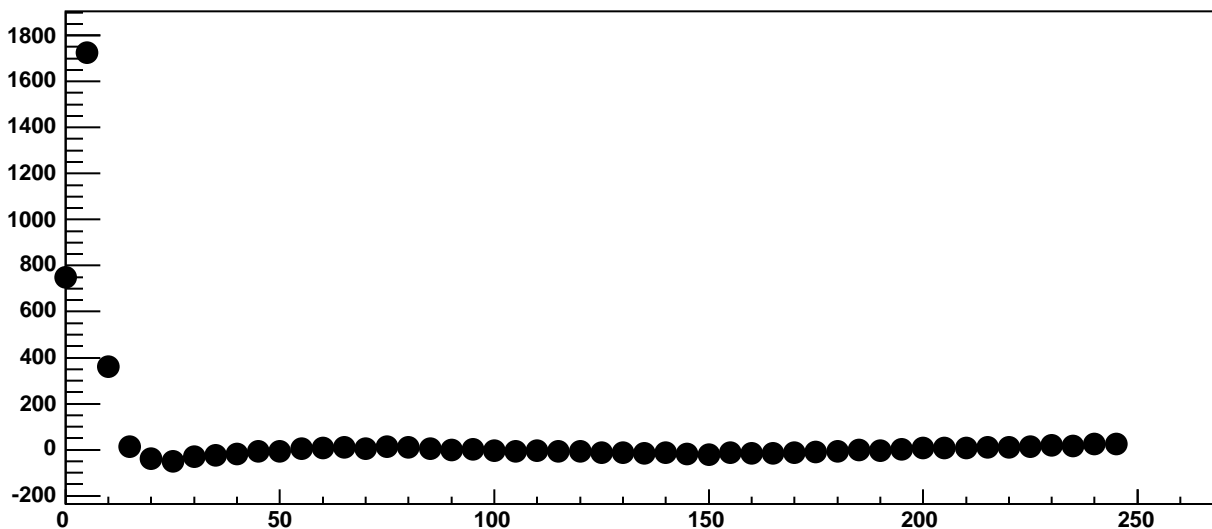


$\chi^2 / \text{ndf}$	1784 / 42
p0	-684.3 $\pm$ 0.5815
p1	4.951 $\pm$ 0.005407
p2	1.057e+04 $\pm$ 0.9576
p3	31.16 $\pm$ 0.004841

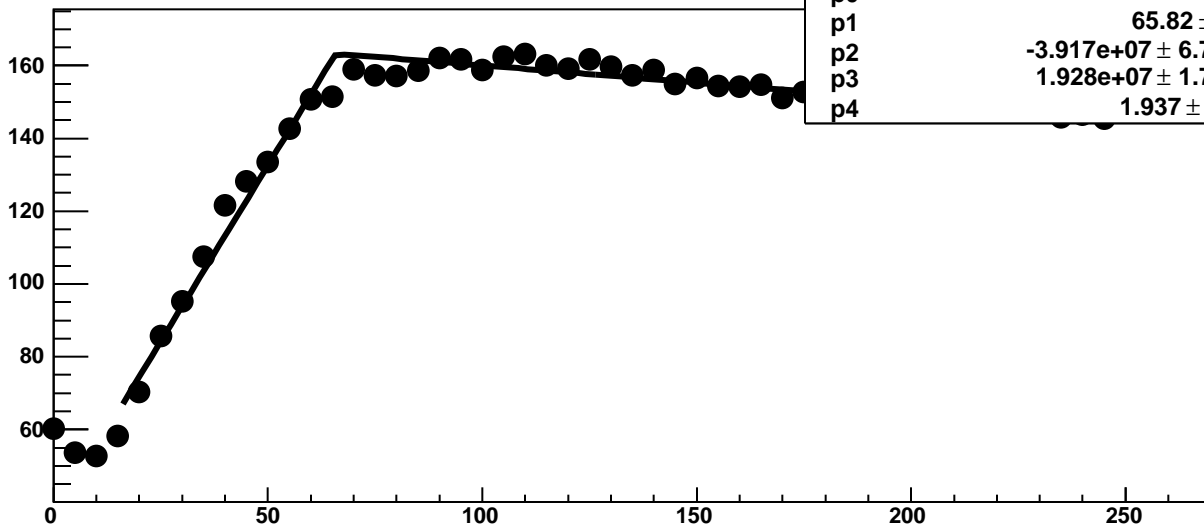
Chip 2, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold

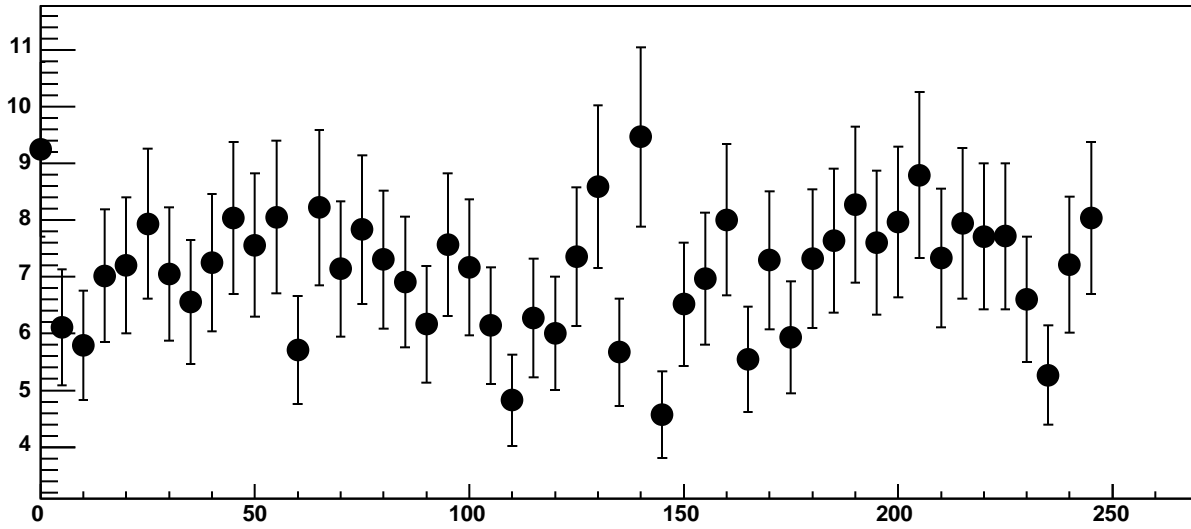


Chip 2, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold

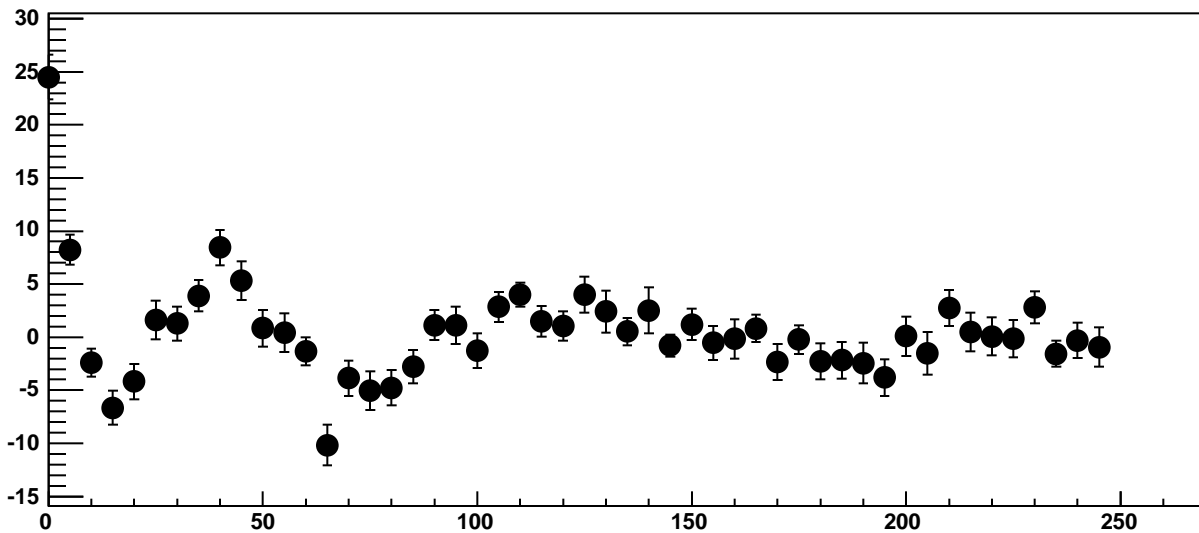


$\chi^2 / \text{ndf}$	171.8 / 41
p0	$163.3 \pm 0.5014$
p1	$65.82 \pm 0.5324$
p2	$-3.917\text{e}+07 \pm 6.782\text{e}+05$
p3	$1.928\text{e}+07 \pm 1.768\text{e}+05$
p4	$1.937 \pm 0.03129$

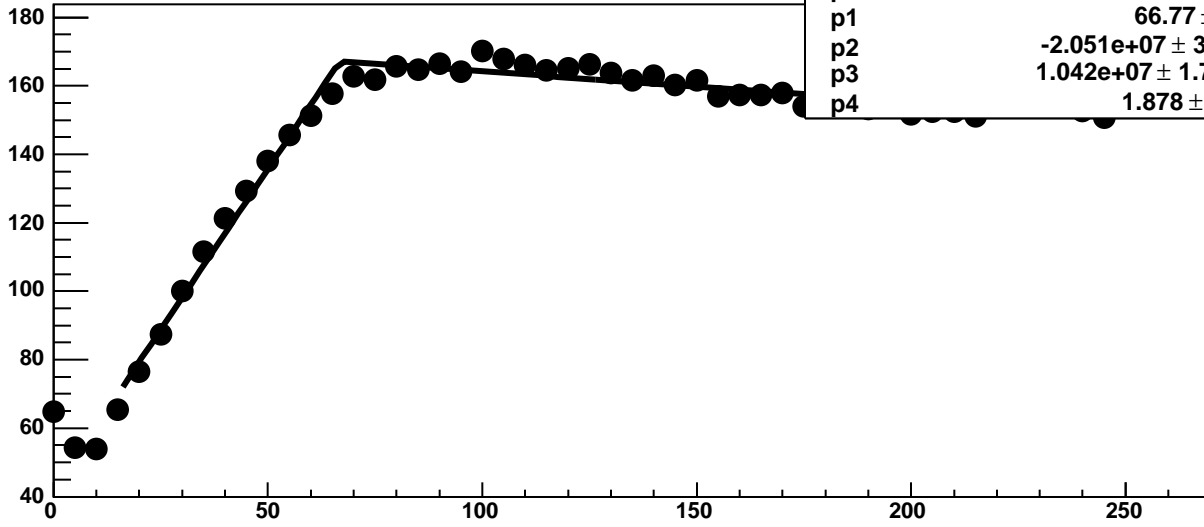
Chip 2, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold

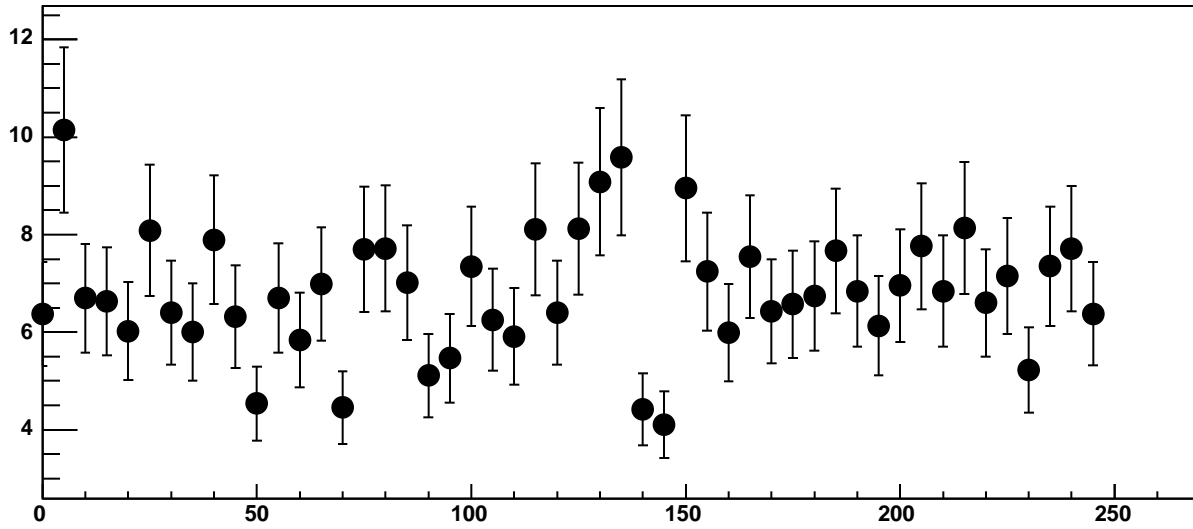


Chip 2, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold

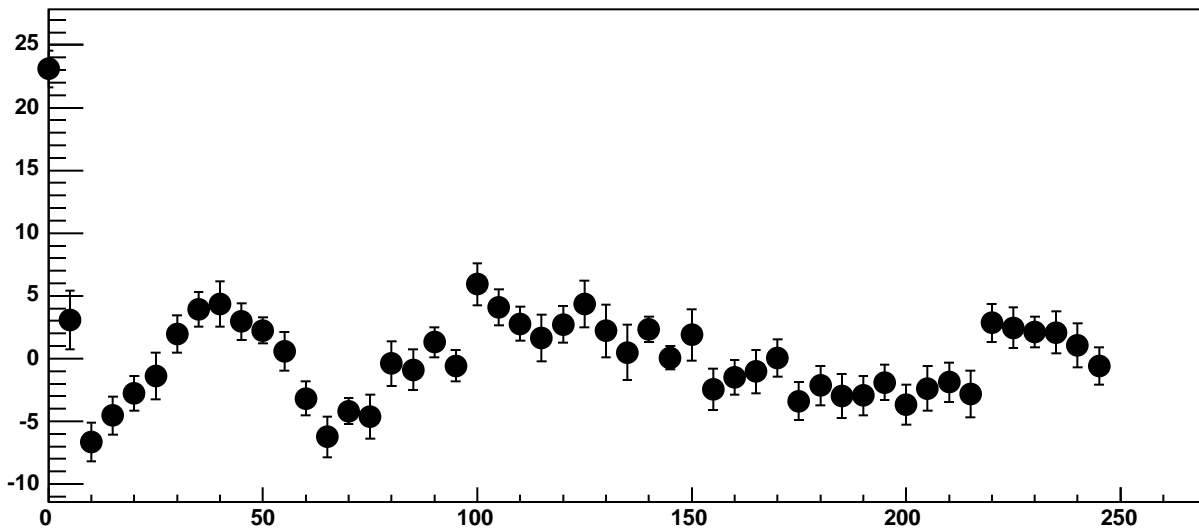


$\chi^2 / \text{ndf}$	166.3 / 41
p0	$167.2 \pm 0.455$
p1	$66.77 \pm 0.4217$
p2	$-2.051\text{e}+07 \pm 3.18\text{e}+05$
p3	$1.042\text{e}+07 \pm 1.768\text{e}+05$
p4	$1.878 \pm 0.02051$

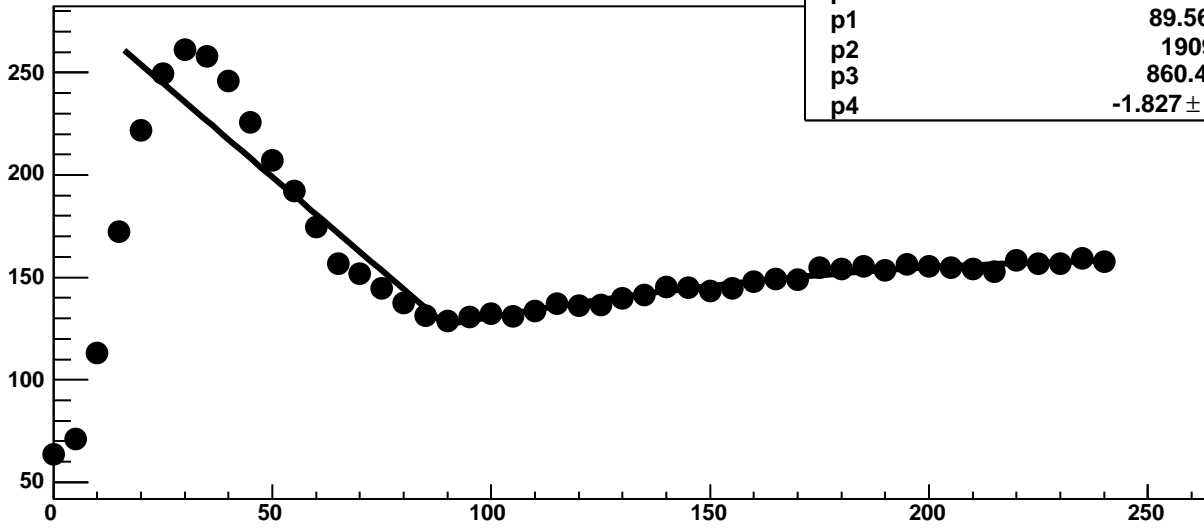
Chip 2, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

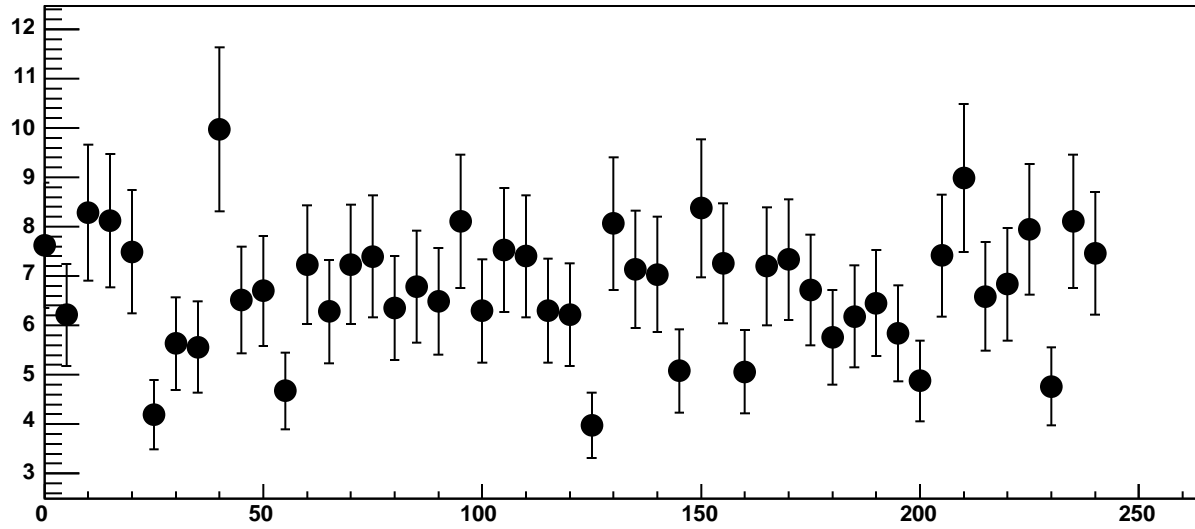


Chip 2, Channel 17, Enable 5, DAC=4000, ADC Mean vs Hold

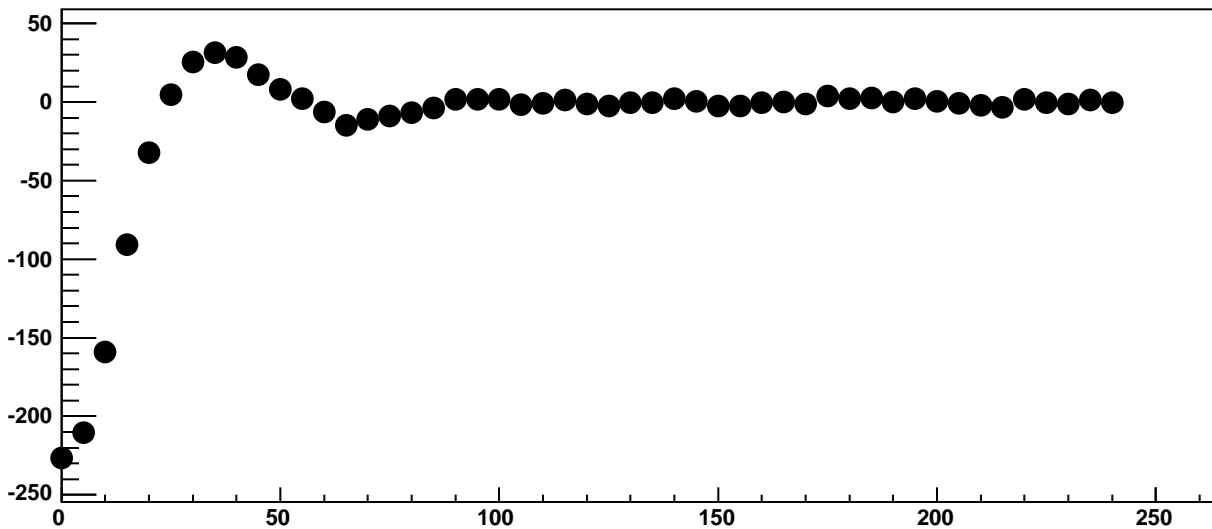


$\chi^2 / \text{ndf}$	4334 / 41
p0	$126.8 \pm 0.6034$
p1	$89.56 \pm 0.481$
p2	$1909 \pm 177$
p3	$860.4 \pm 86.22$
p4	$-1.827 \pm 0.01772$

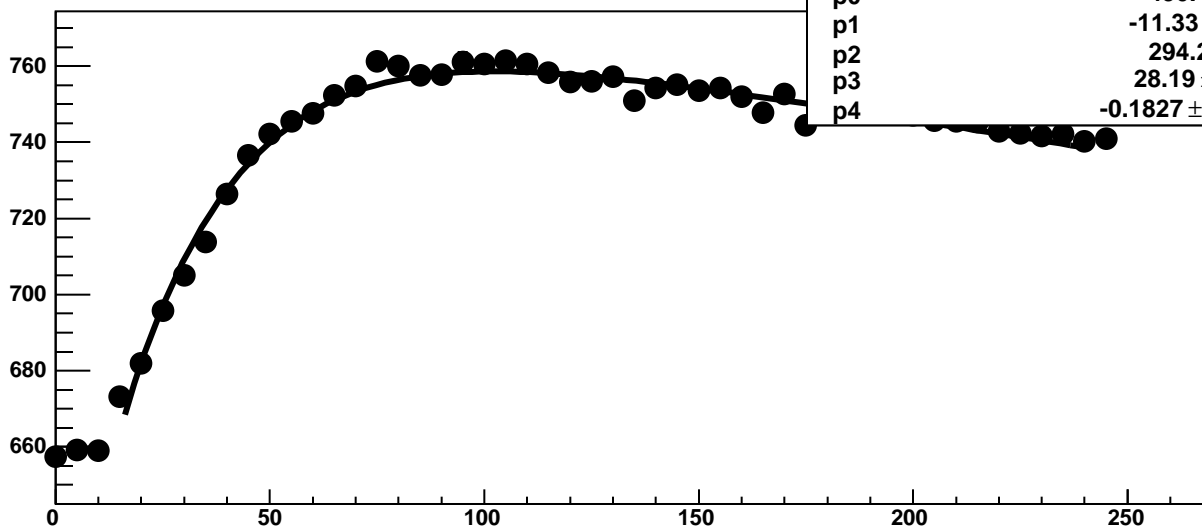
Chip 2, Channel 17, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 17, Enable 5, DAC=4000, ADC Residuals vs Hold

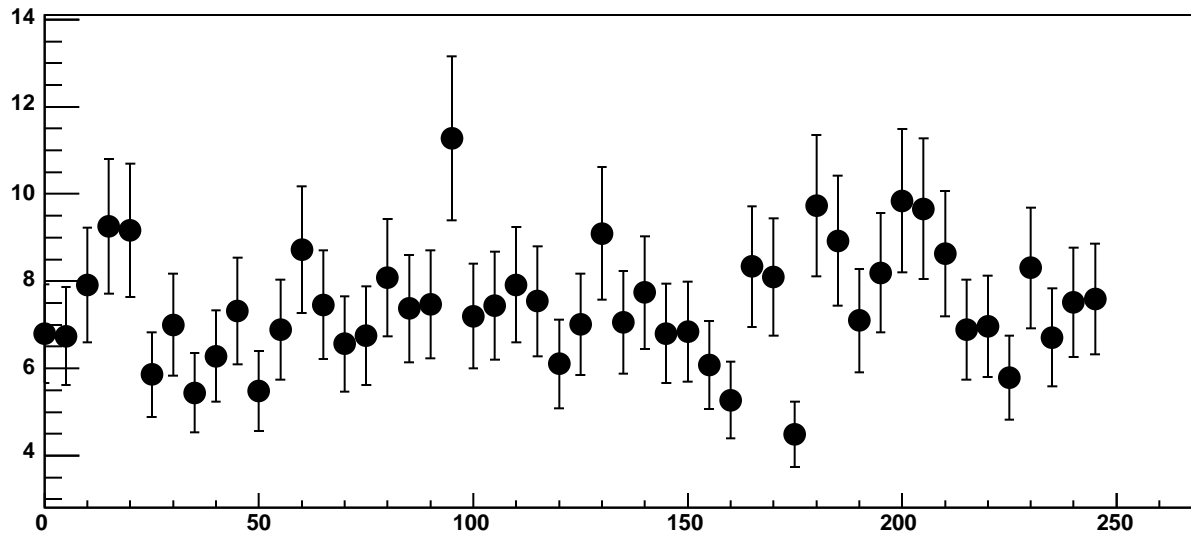


Chip 3, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold

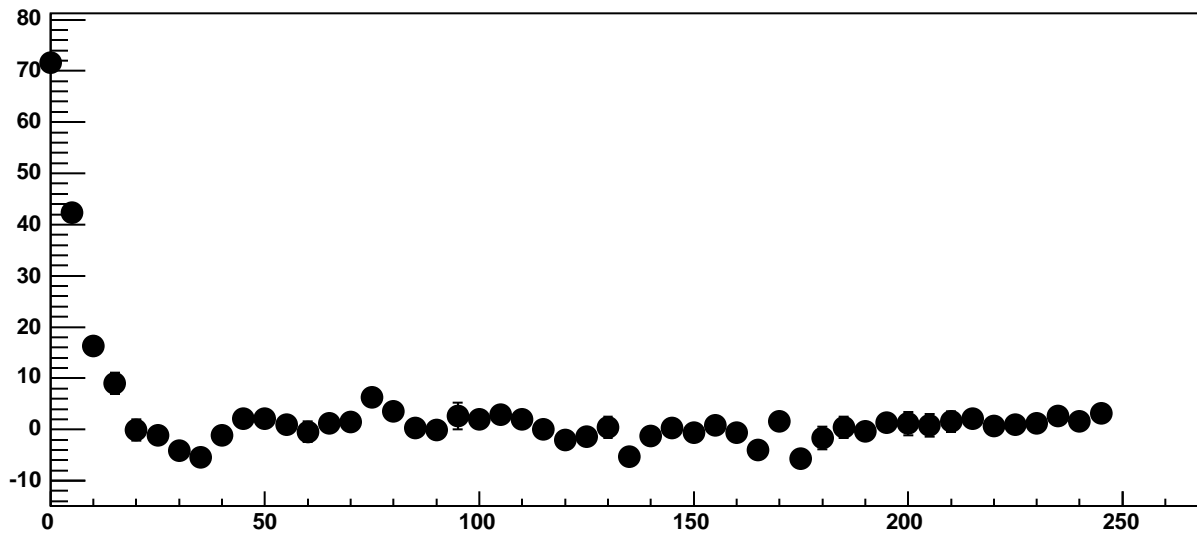


$\chi^2 / \text{ndf}$	137.7 / 41
p0	$490.4 \pm 3.036$
p1	$-11.33 \pm 0.8403$
p2	$294.2 \pm 3.093$
p3	$28.19 \pm 0.7406$
p4	$-0.1827 \pm 0.00784$

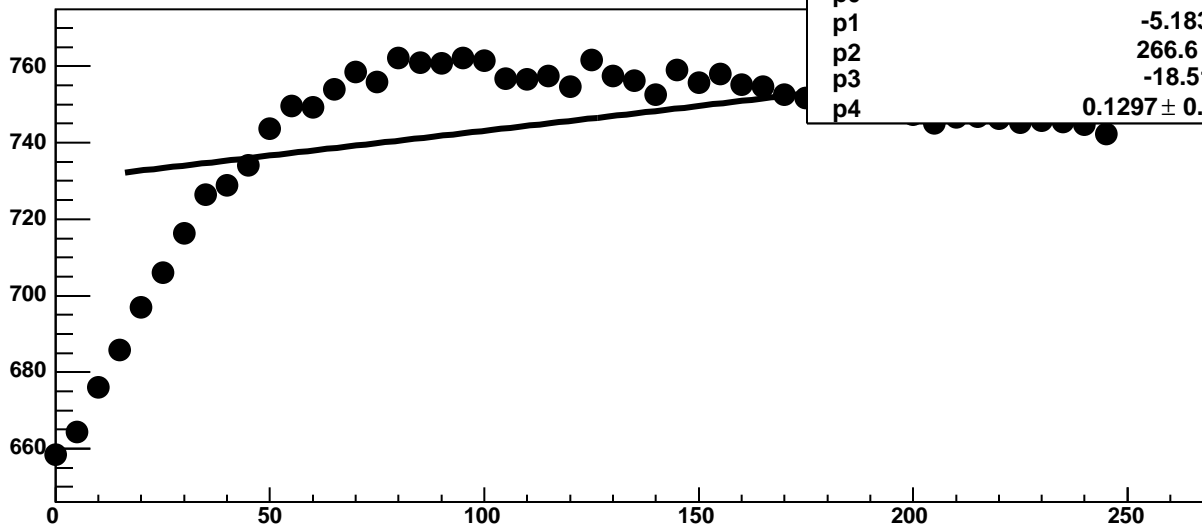
Chip 3, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold



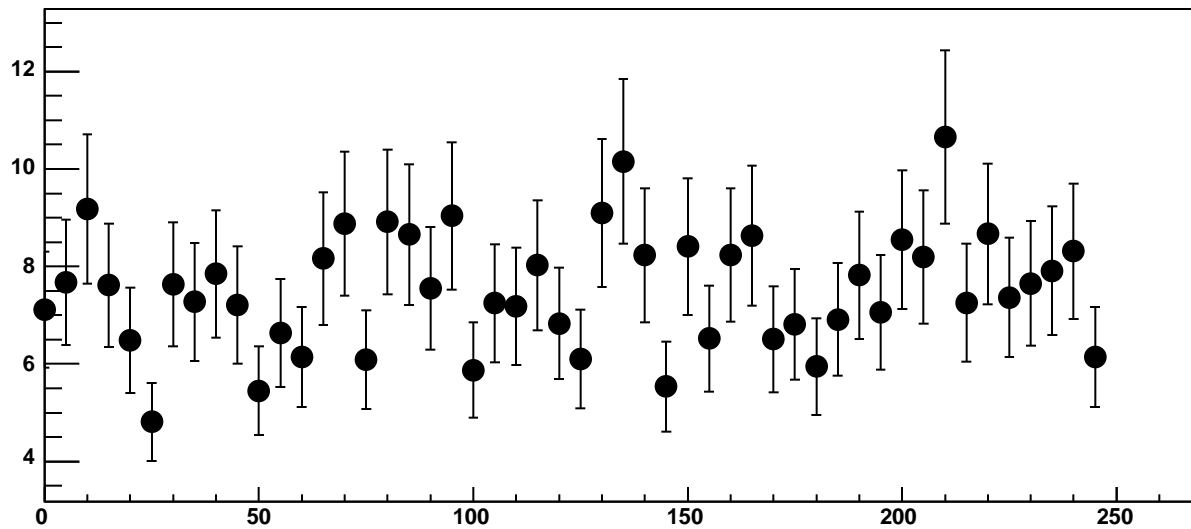
Chip 3, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold



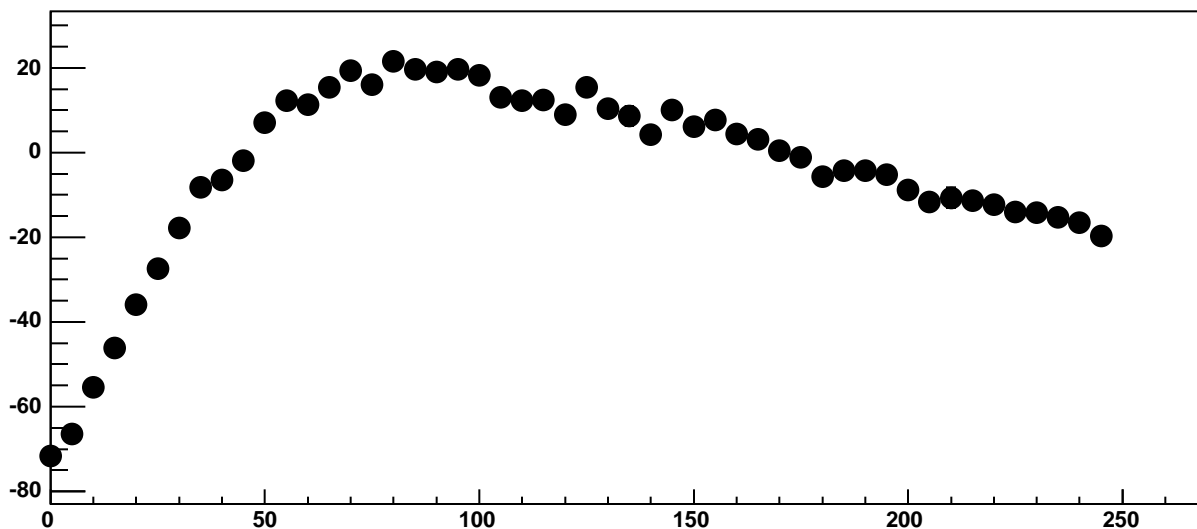
Chip 3, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 3, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold

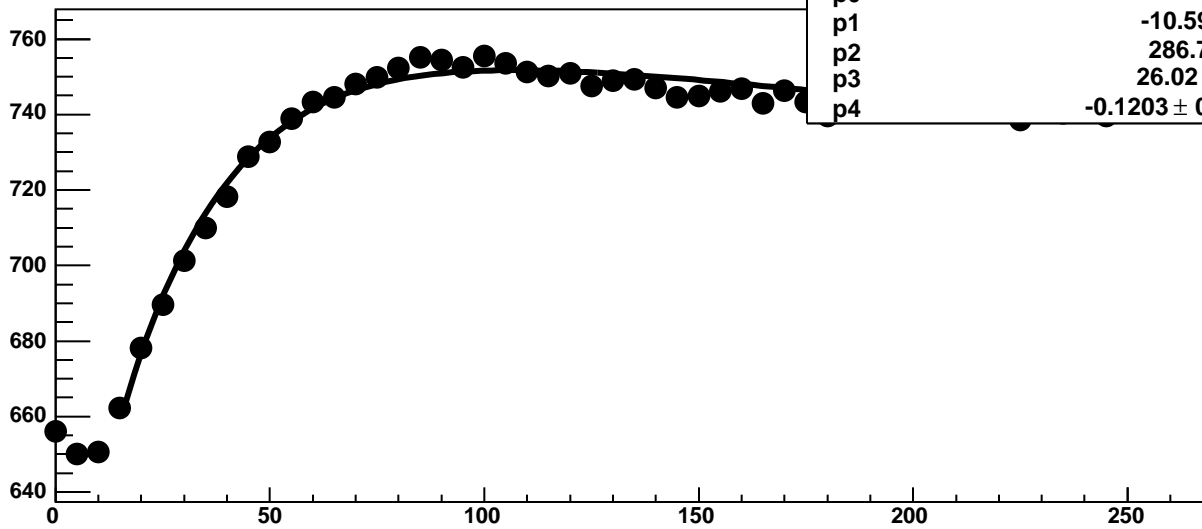


Chip 3, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold



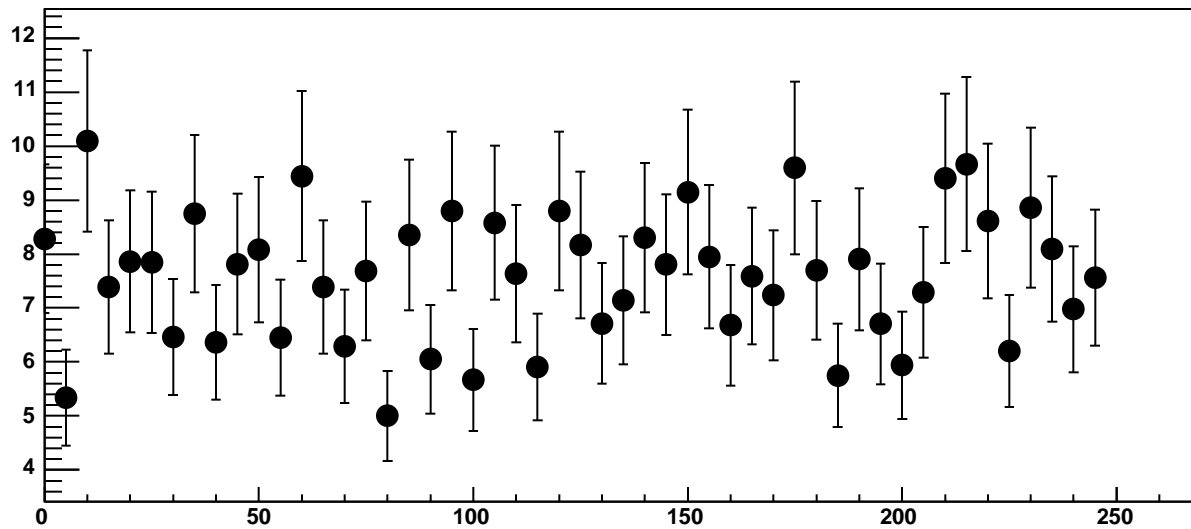


Chip 3, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

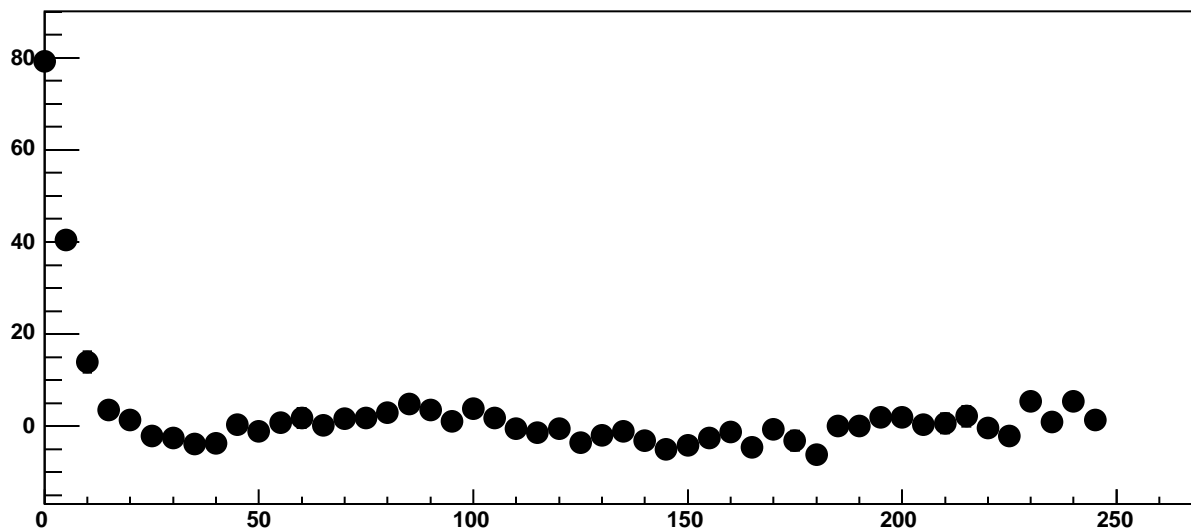


$\chi^2 / \text{ndf}$	123.9 / 41
p0	$482.3 \pm 3.1$
p1	$-10.59 \pm 0.822$
p2	$286.7 \pm 3.136$
p3	$26.02 \pm 0.7289$
p4	$-0.1203 \pm 0.007479$

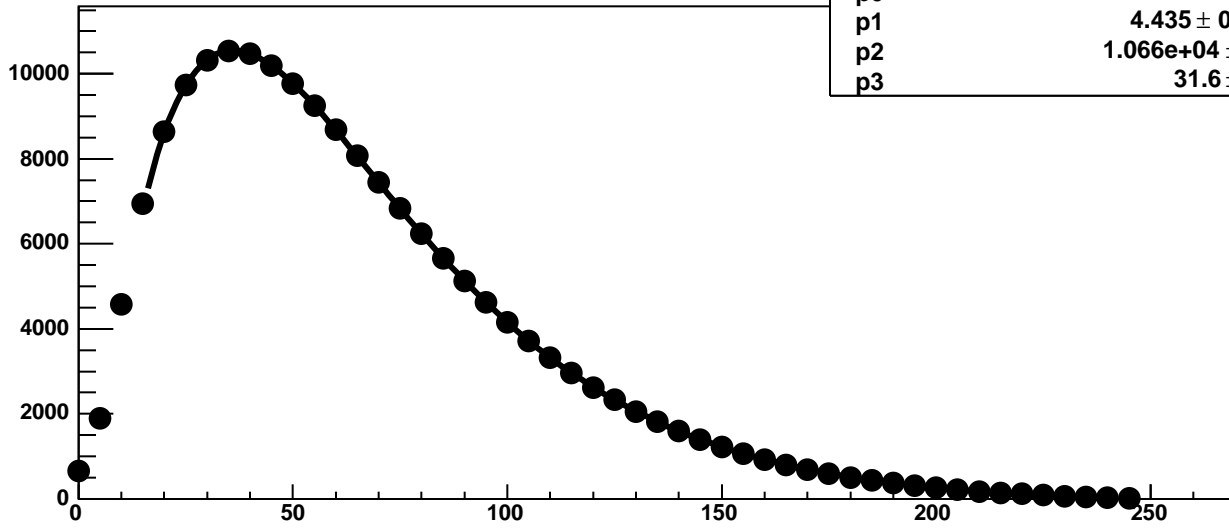
Chip 3, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

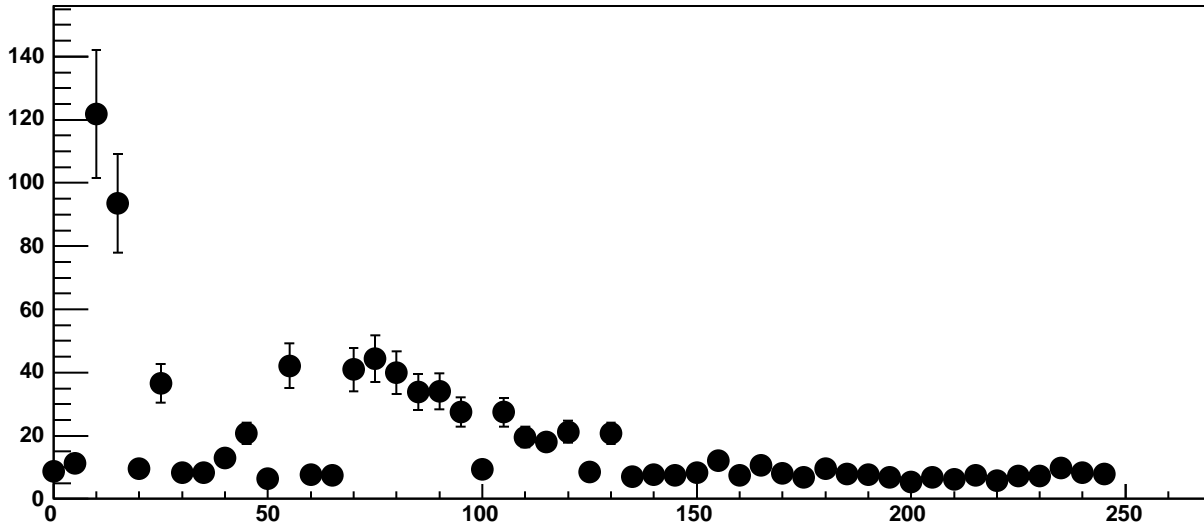


Chip 3, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold

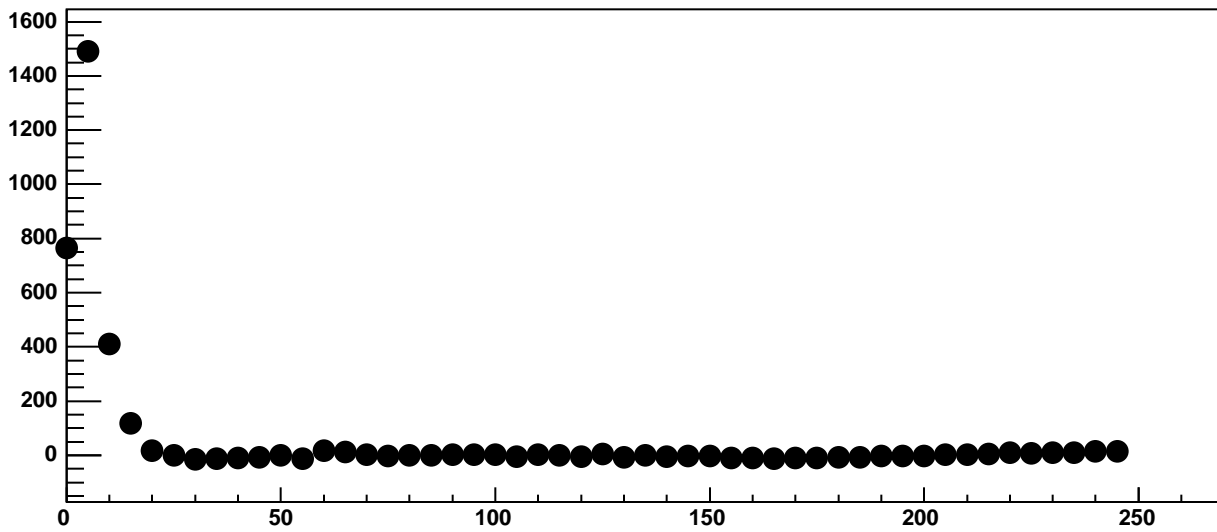


$\chi^2 / \text{ndf}$	736 / 42
p0	-108.8 ± 0.6196
p1	4.435 ± 0.009403
p2	1.066e+04 ± 0.9267
p3	31.6 ± 0.0067

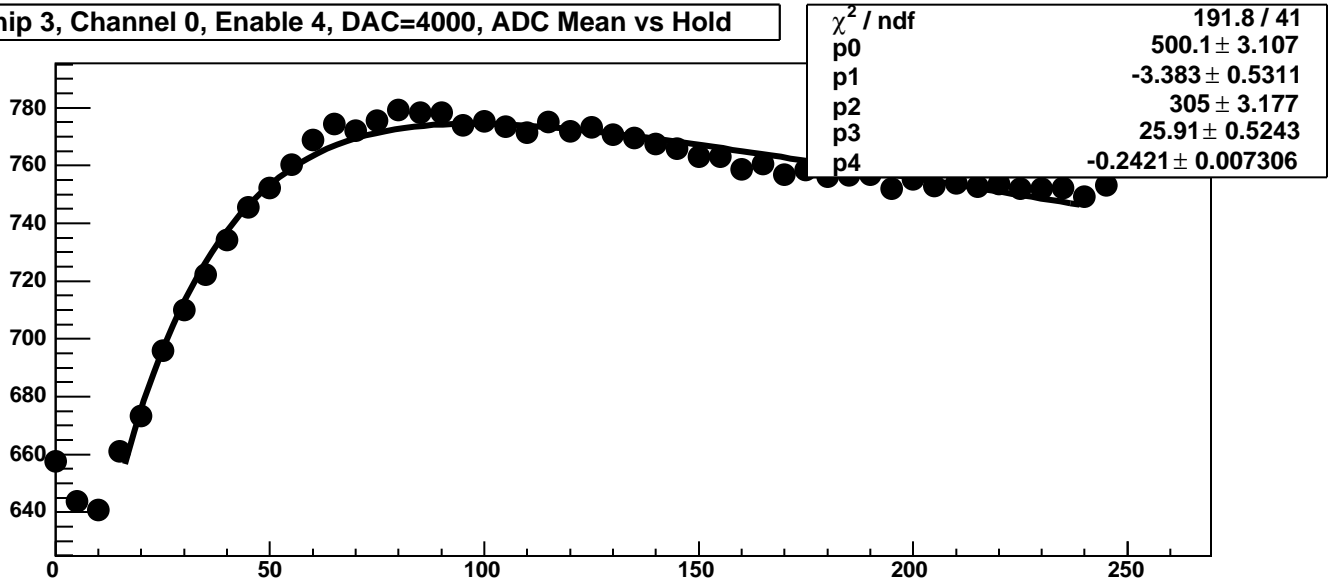
Chip 3, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold



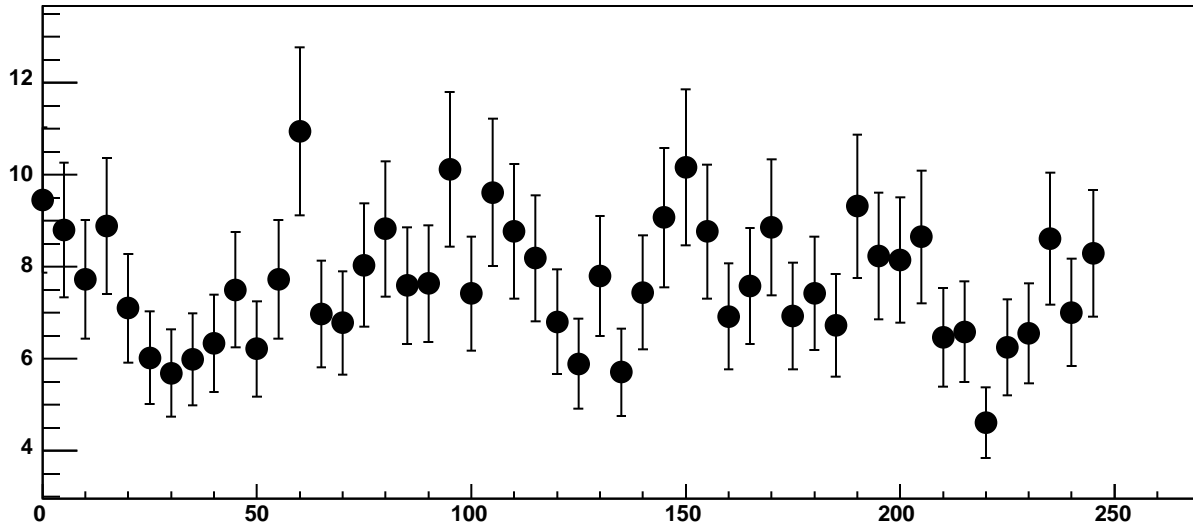
Chip 3, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold



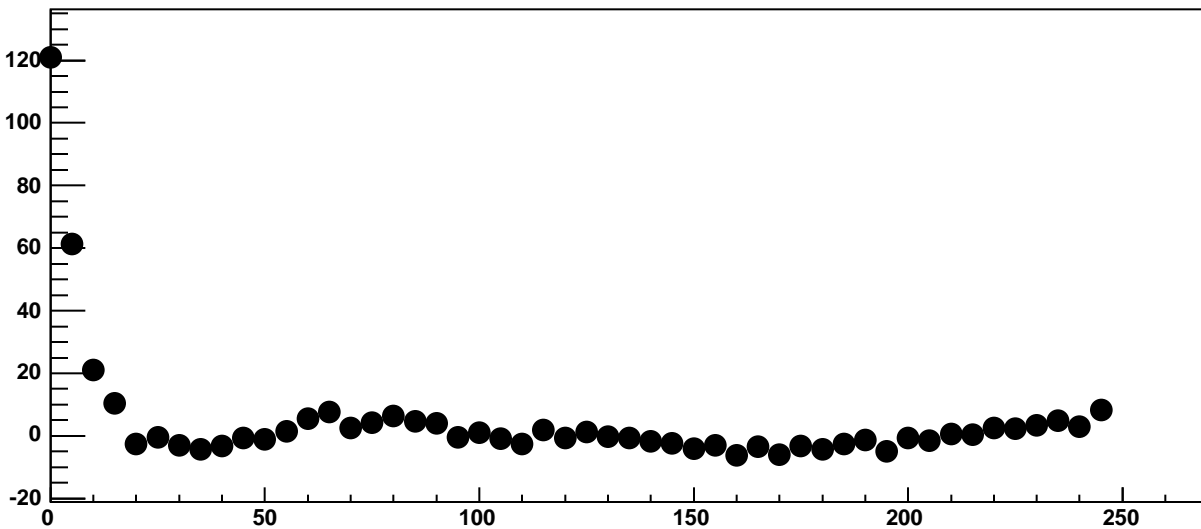
Chip 3, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold



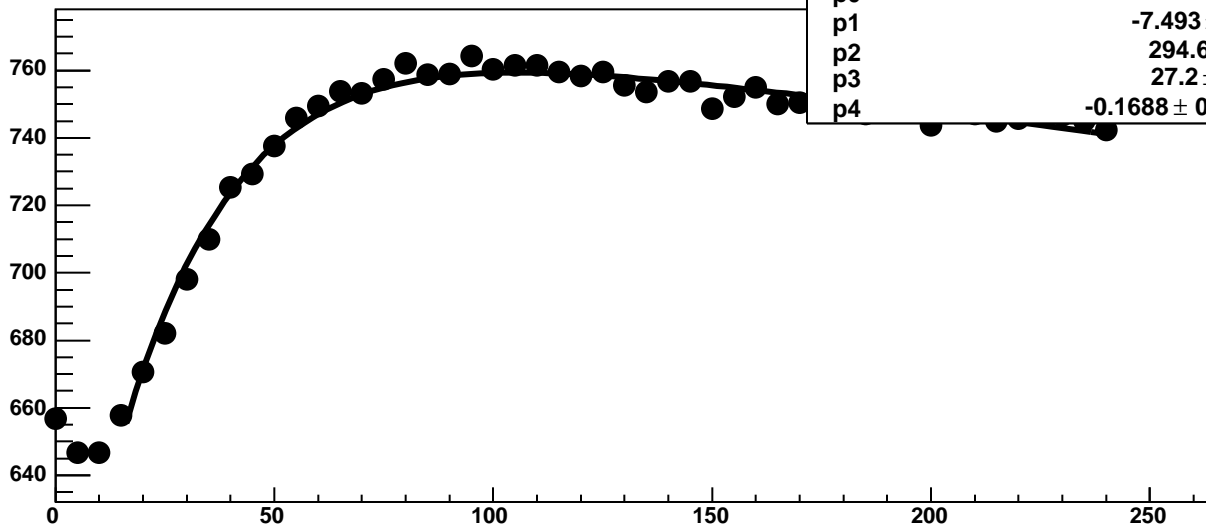
Chip 3, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

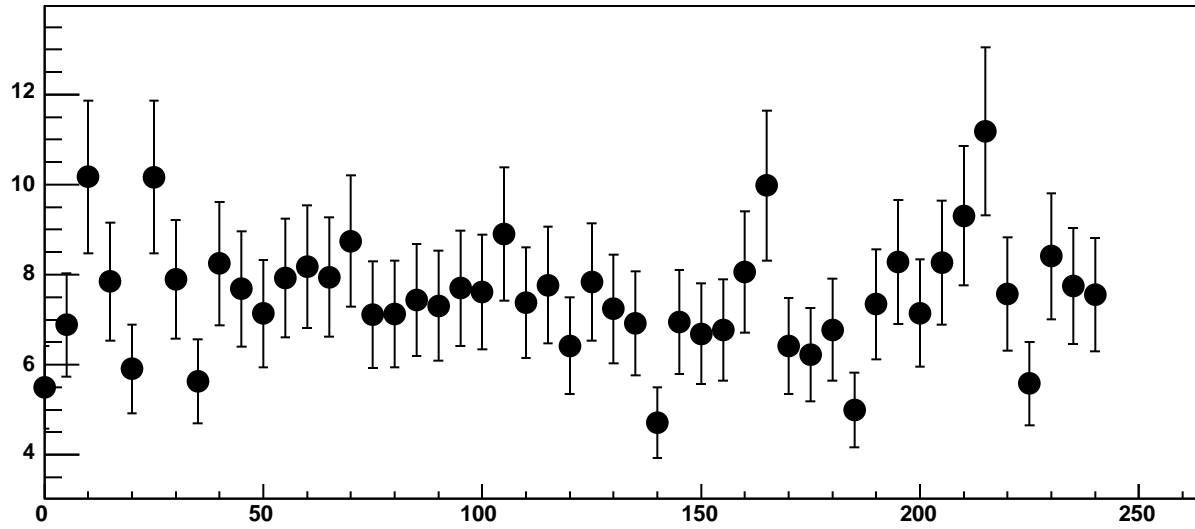


Chip 3, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold

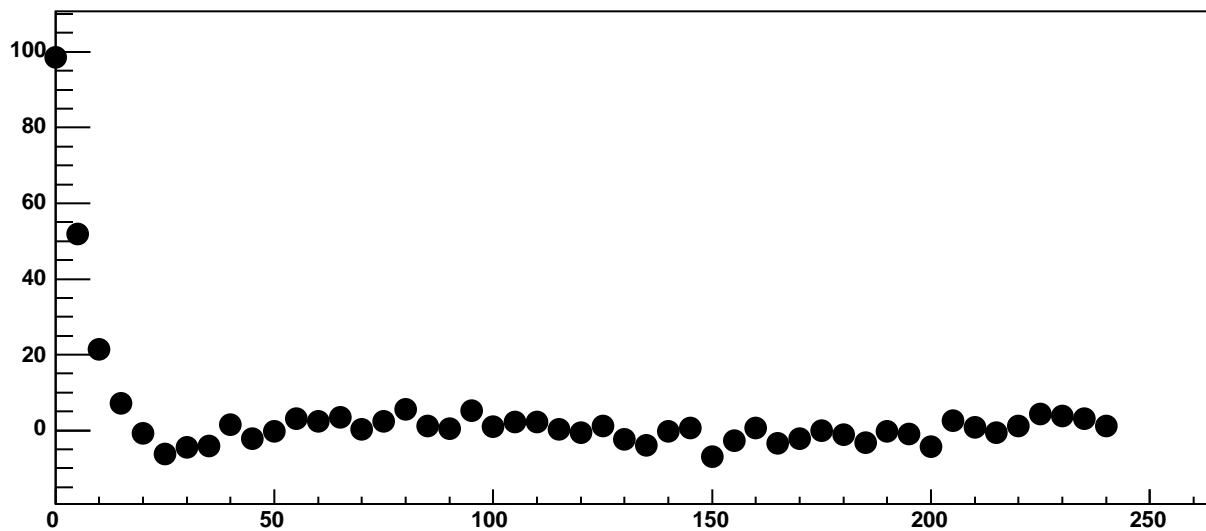


$\chi^2 / \text{ndf}$	151 / 41
p0	$488.5 \pm 3.093$
p1	$-7.493 \pm 0.6541$
p2	$294.6 \pm 3.146$
p3	$27.2 \pm 0.6452$
p4	$-0.1688 \pm 0.007826$

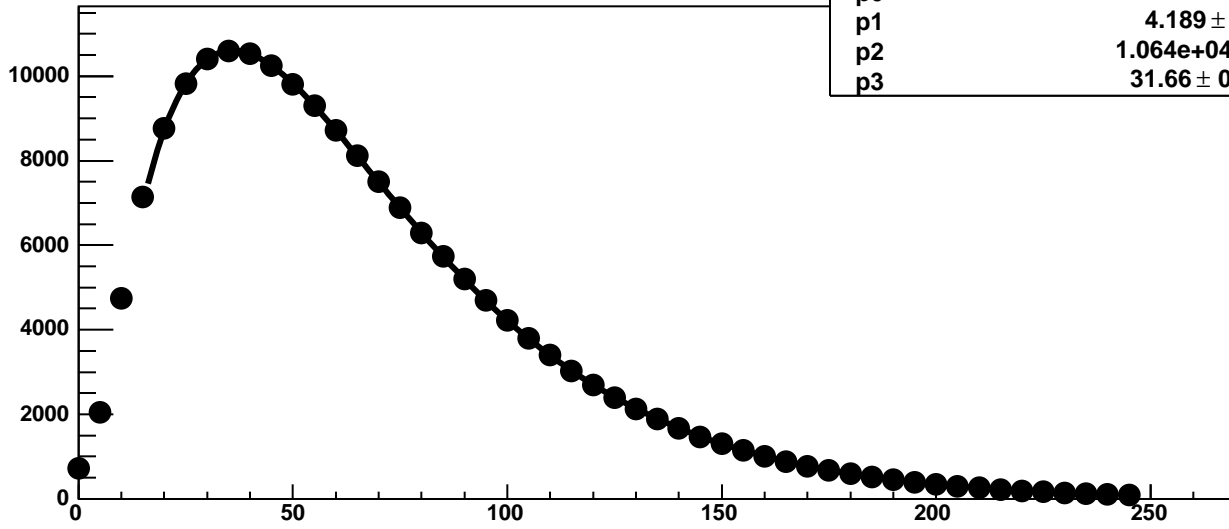
Chip 3, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold

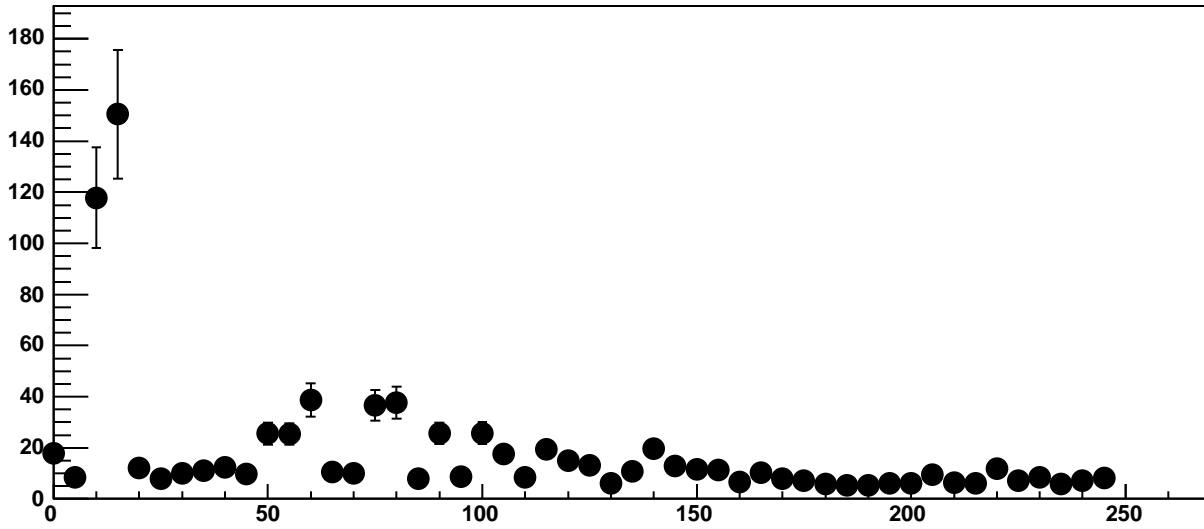


Chip 3, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold

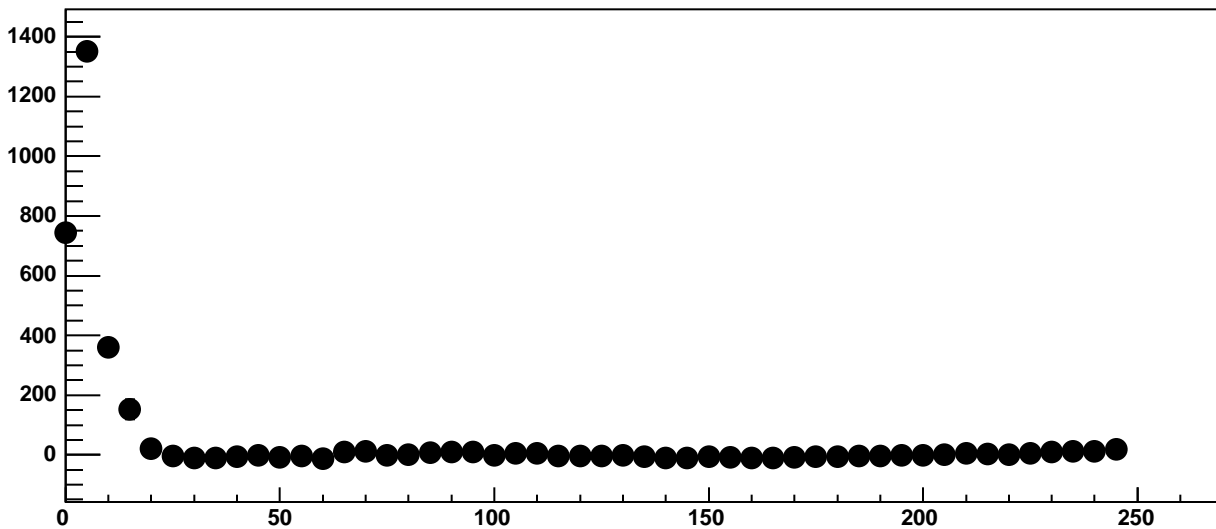


$\chi^2 / \text{ndf}$	571.8 / 42
p0	-29.17 ± 0.5823
p1	4.189 ± 0.01084
p2	1.064e+04 ± 1.026
p3	31.66 ± 0.006823

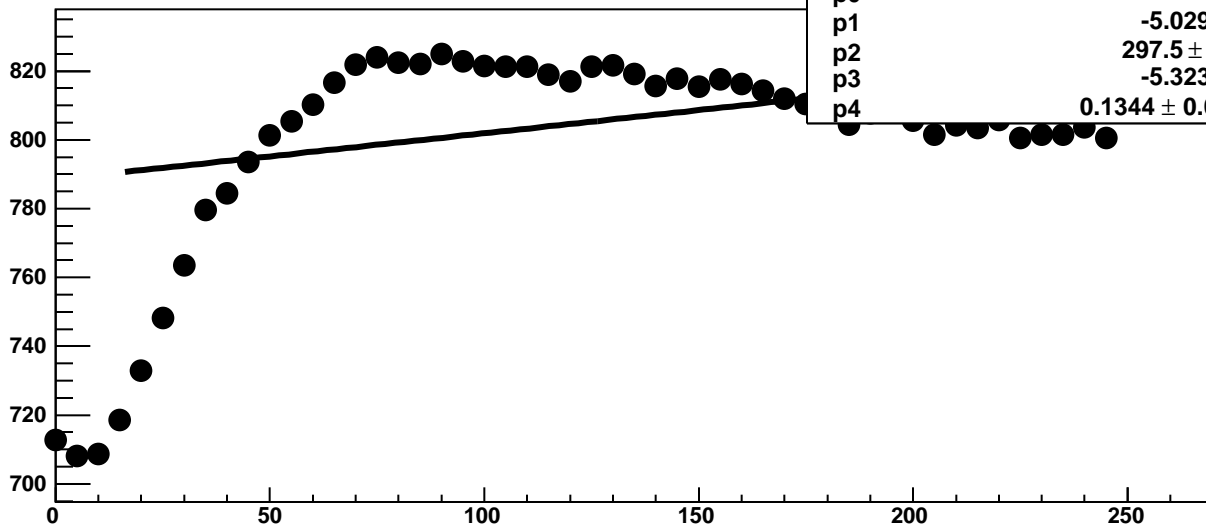
Chip 3, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold

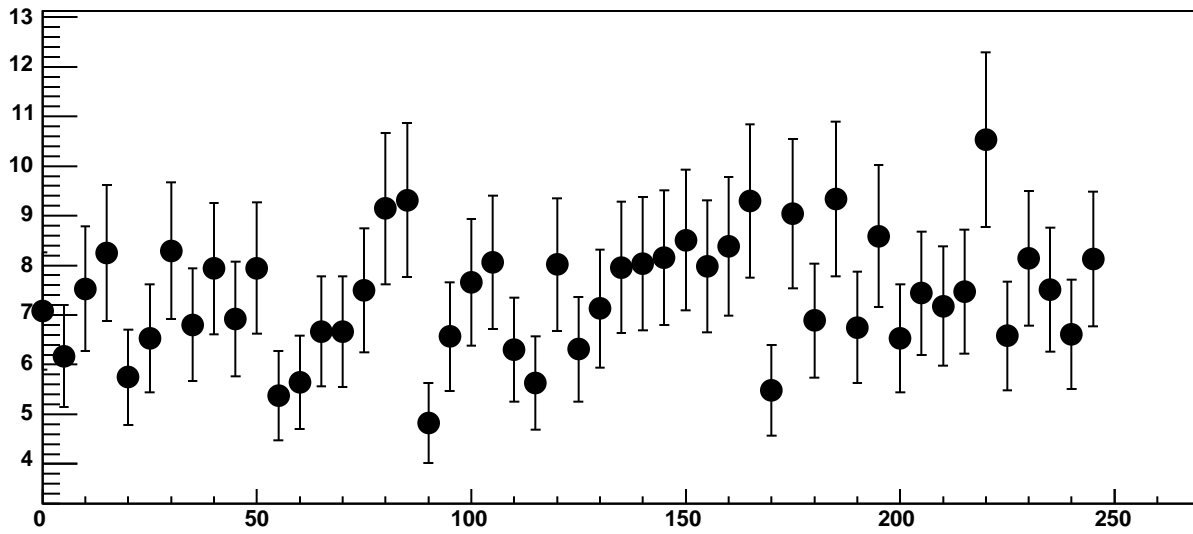


Chip 3, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

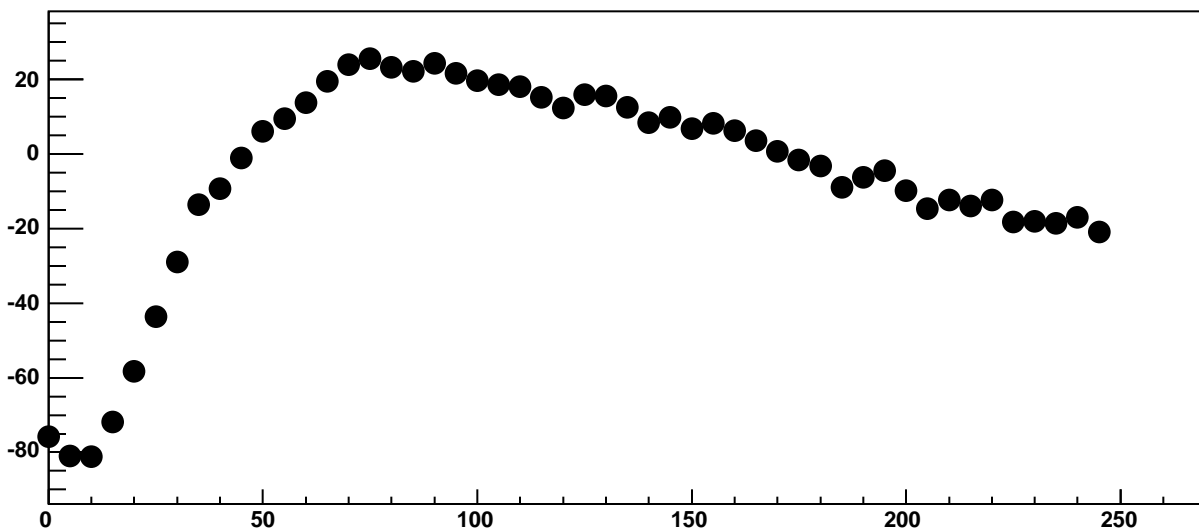


$\chi^2 / \text{ndf}$	8022 / 41
p0	$490.4 \pm 0.05962$
p1	$-5.029 \pm 9.462$
p2	$297.5 \pm 0.06147$
p3	$-5.323 \pm 1.675$
p4	$0.1344 \pm 0.0004786$

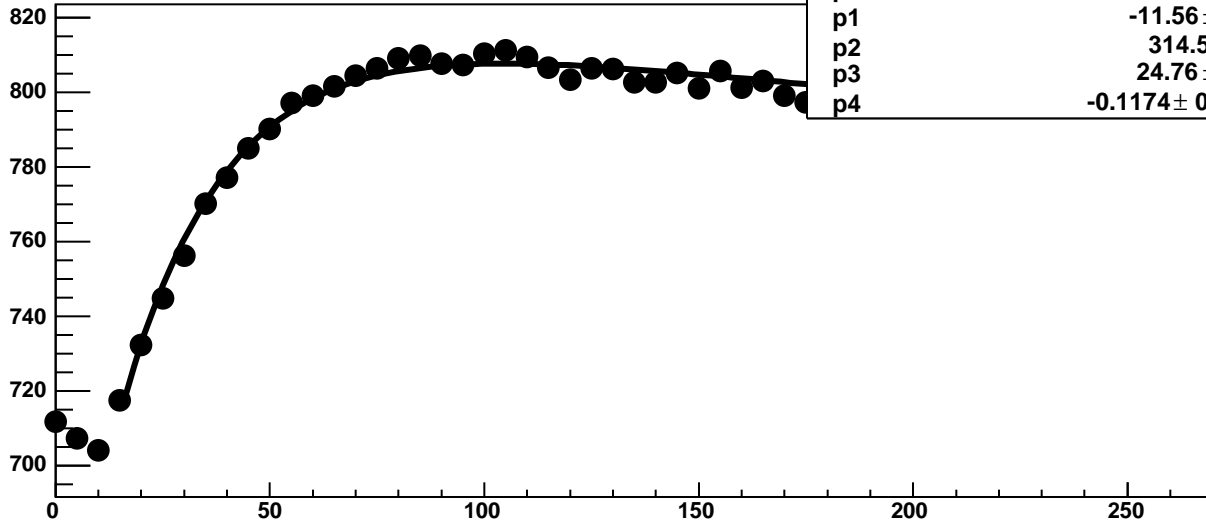
Chip 3, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

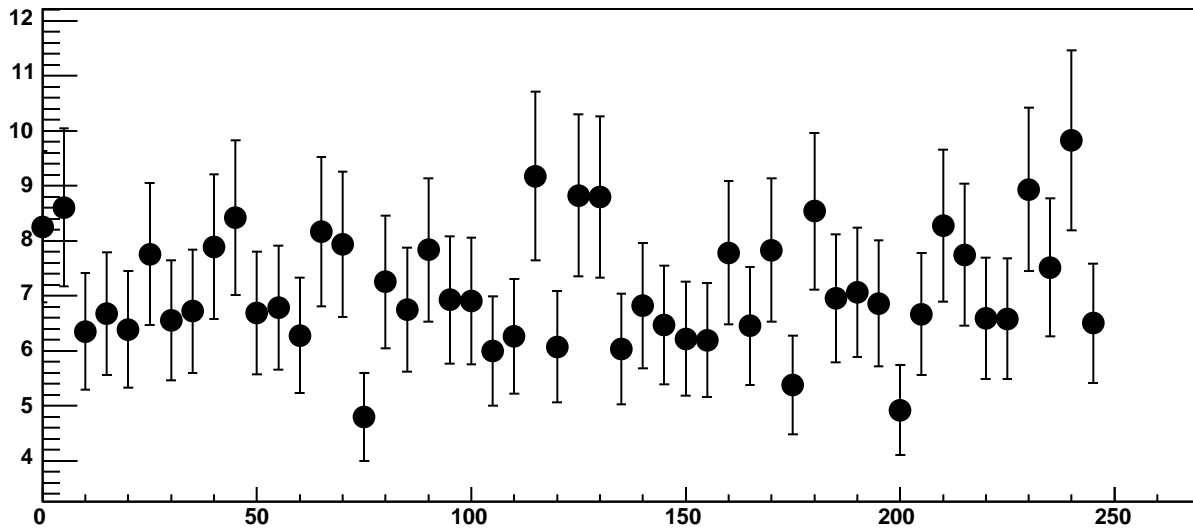


Chip 3, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

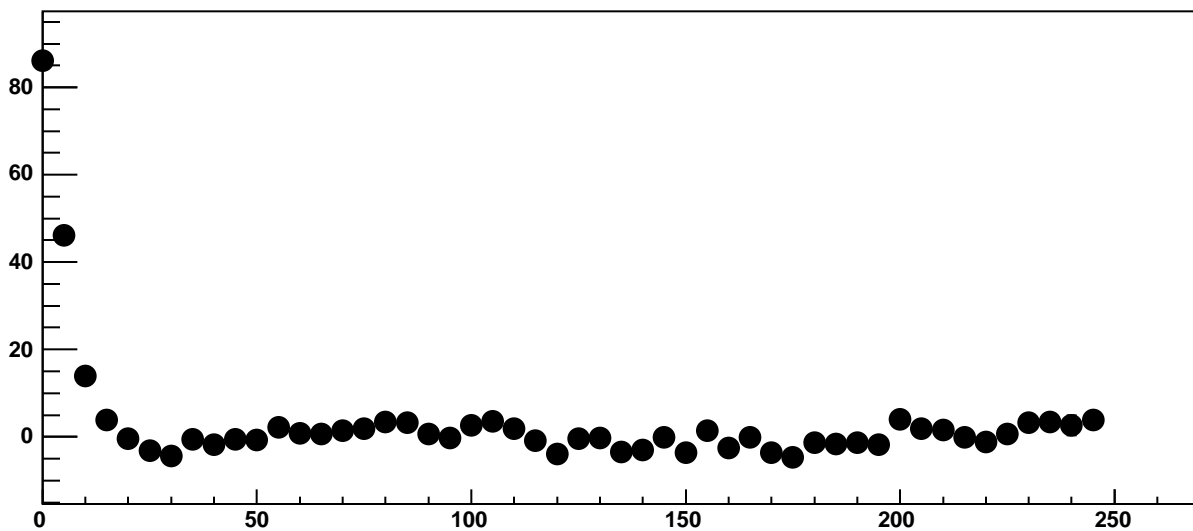


$\chi^2 / \text{ndf}$	119.3 / 41
p0	$509.8 \pm 2.934$
p1	$-11.56 \pm 0.7693$
p2	$314.5 \pm 2.962$
p3	$24.76 \pm 0.6518$
p4	$-0.1174 \pm 0.007033$

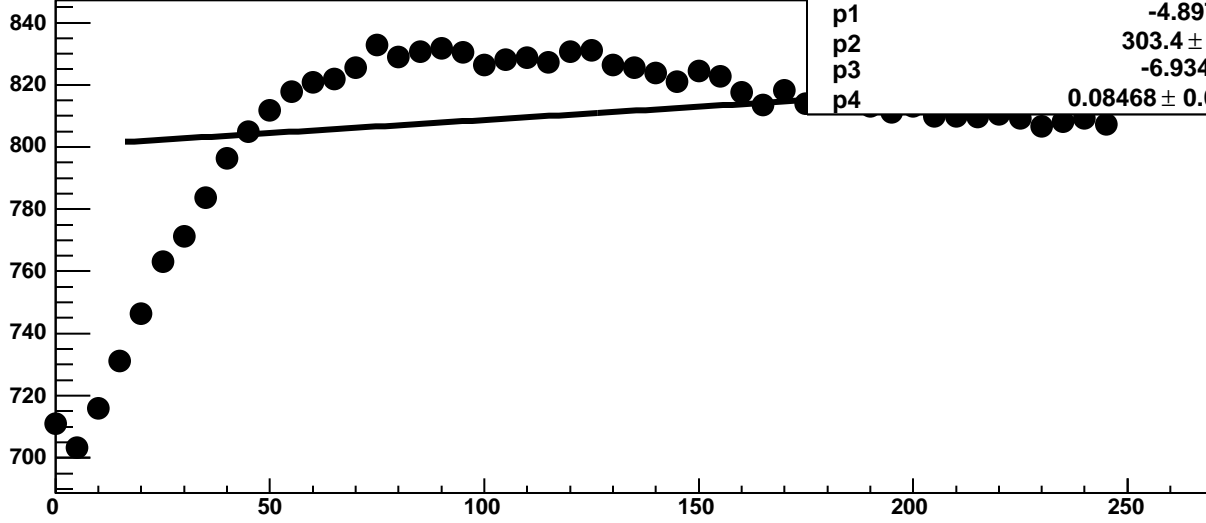
Chip 3, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



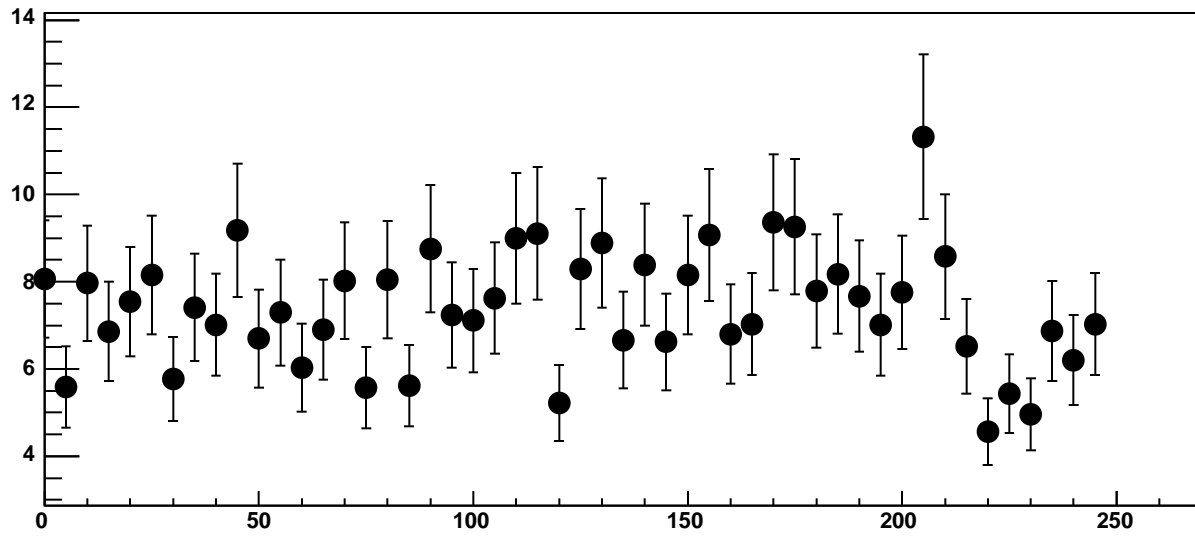
Chip 3, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold



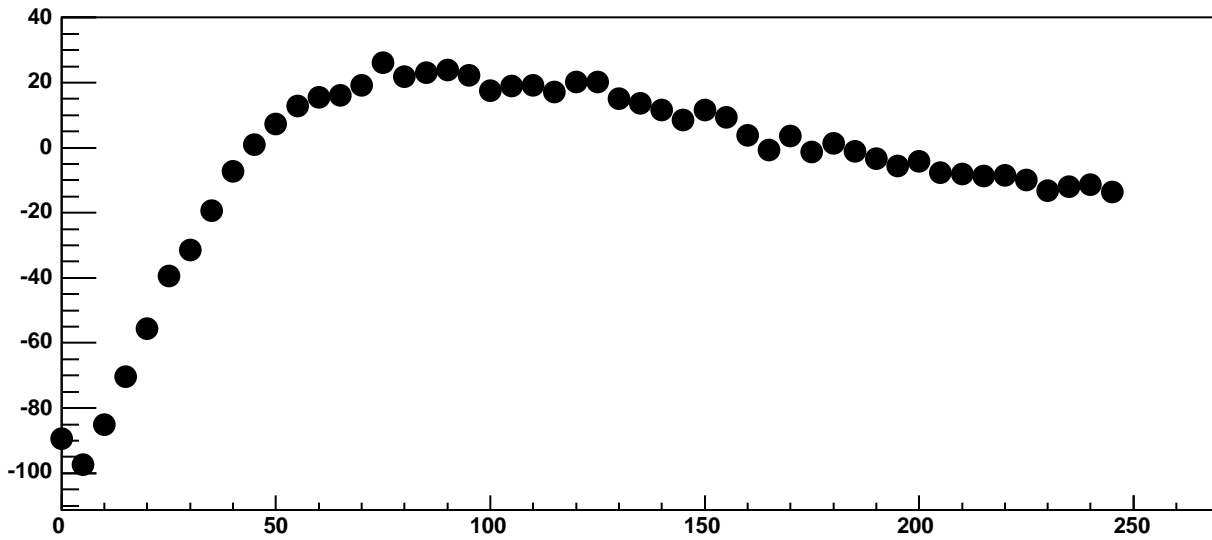
Chip 3, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold



Chip 3, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold

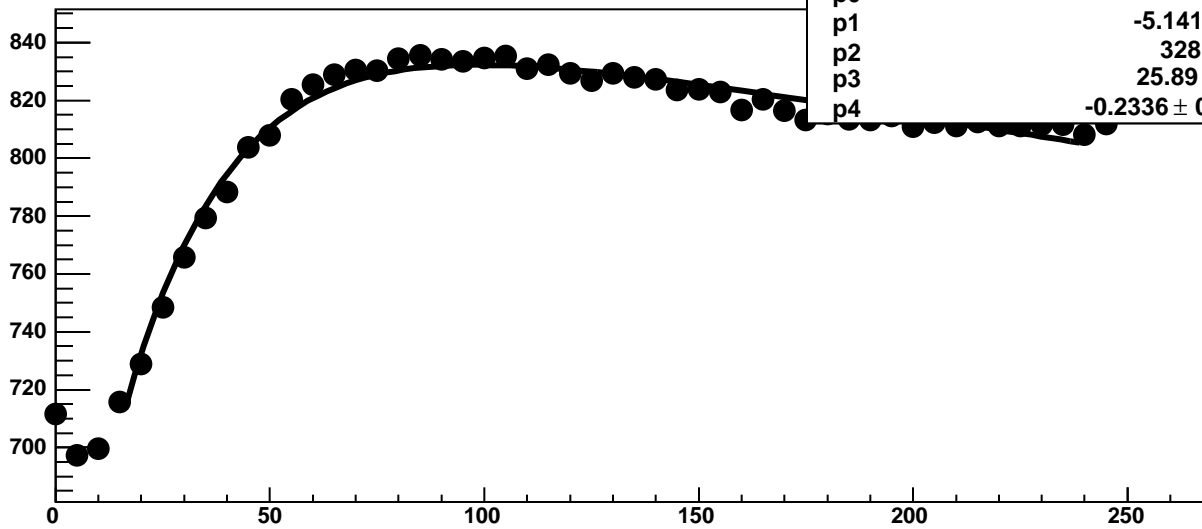


Chip 3, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold



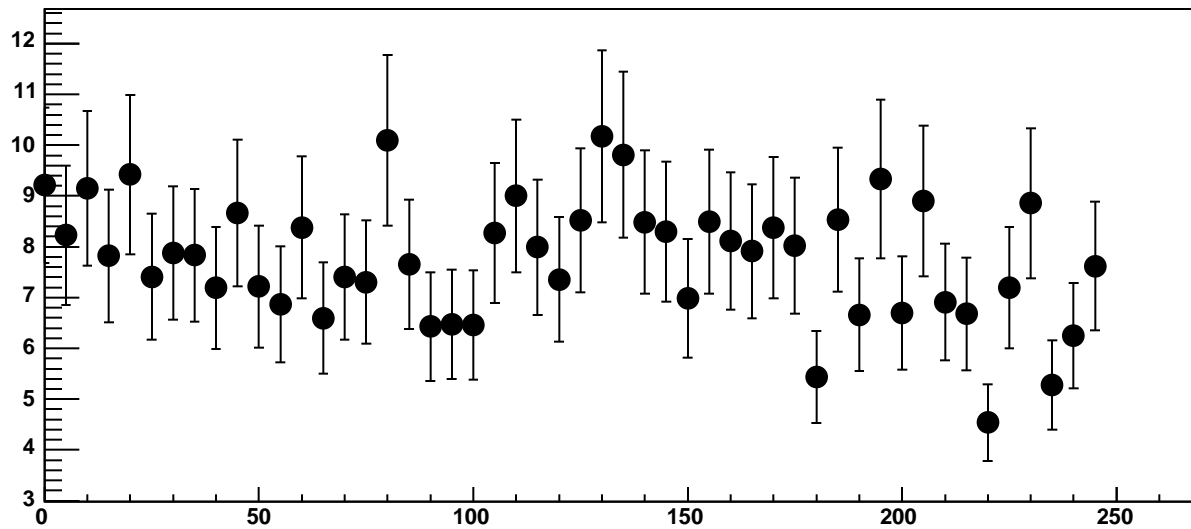


Chip 3, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

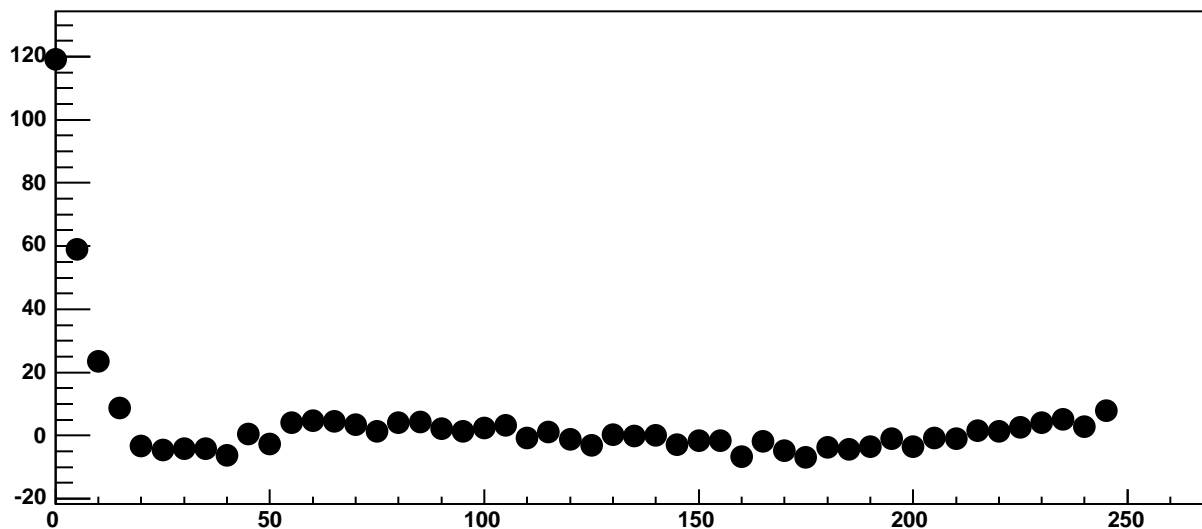


$\chi^2 / \text{ndf}$	202.7 / 41
p0	$534.4 \pm 1.023$
p1	$-5.141 \pm 0.4931$
p2	$328 \pm 0.5971$
p3	$25.89 \pm 0.5779$
p4	$-0.2336 \pm 0.007798$

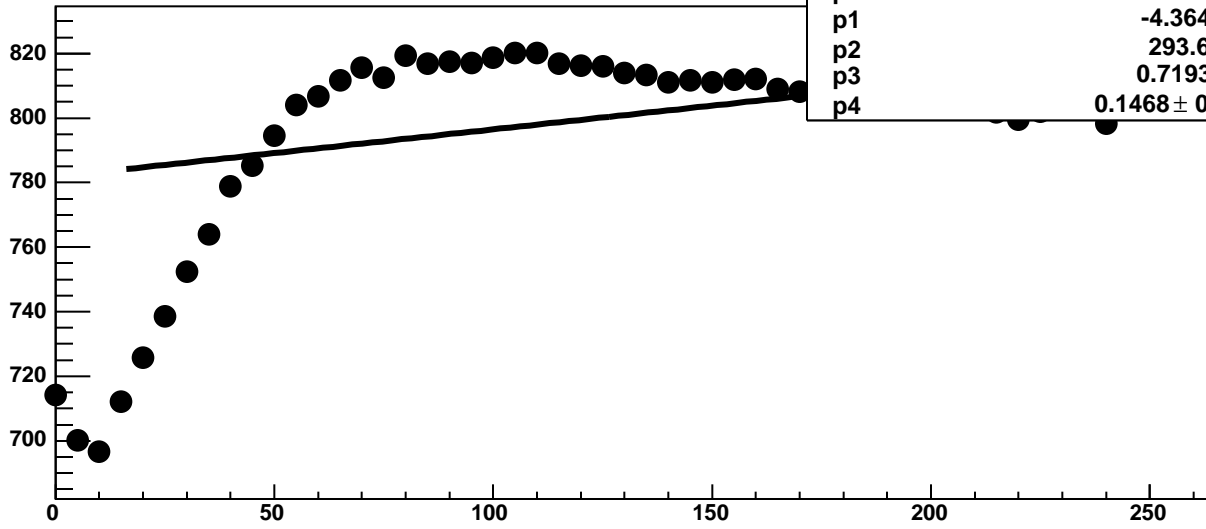
Chip 3, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

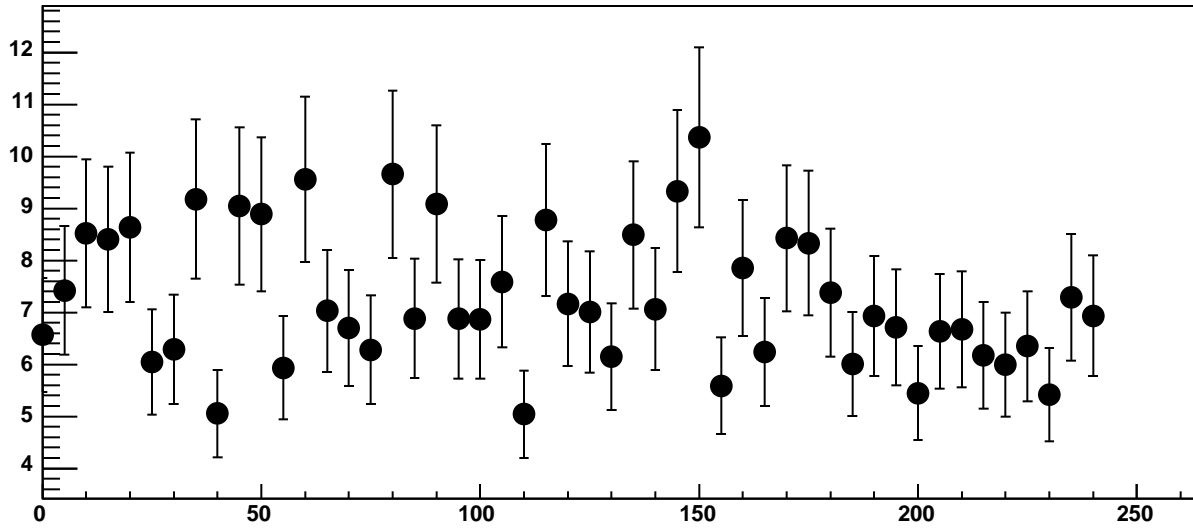


Chip 3, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

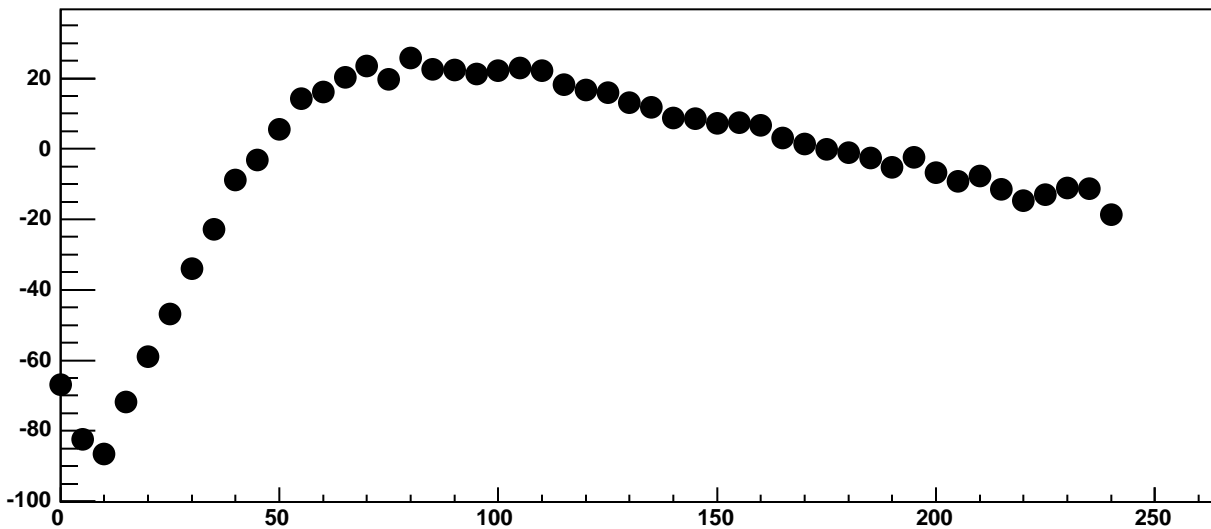


$\chi^2 / \text{ndf}$	7423 / 41
p0	$487.6 \pm 1.036$
p1	$-4.364 \pm 1.407$
p2	$293.6 \pm 1.036$
p3	$0.7193 \pm 1.414$
p4	$0.1468 \pm 0.003364$

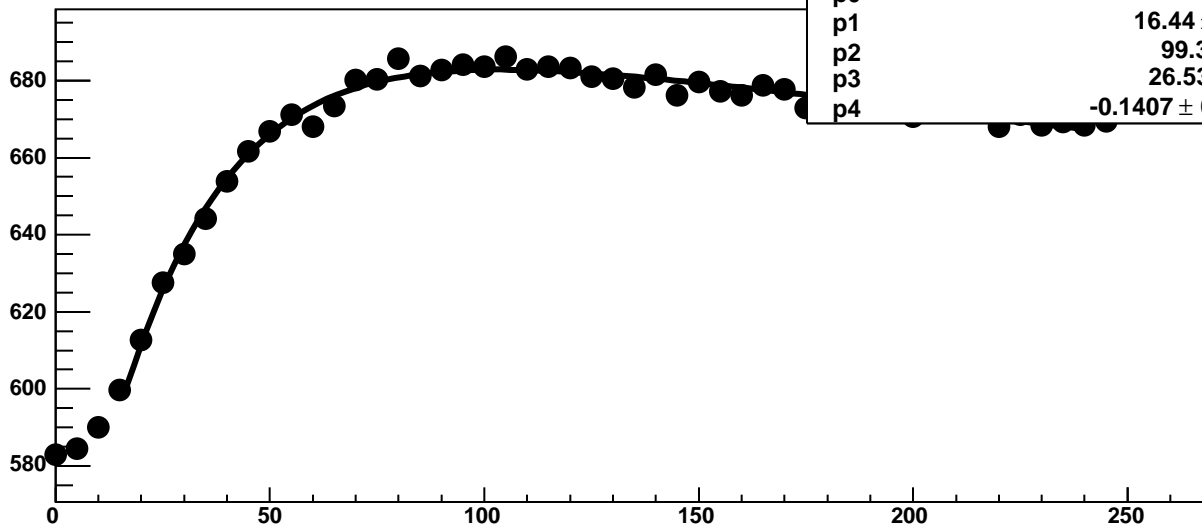
Chip 3, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold

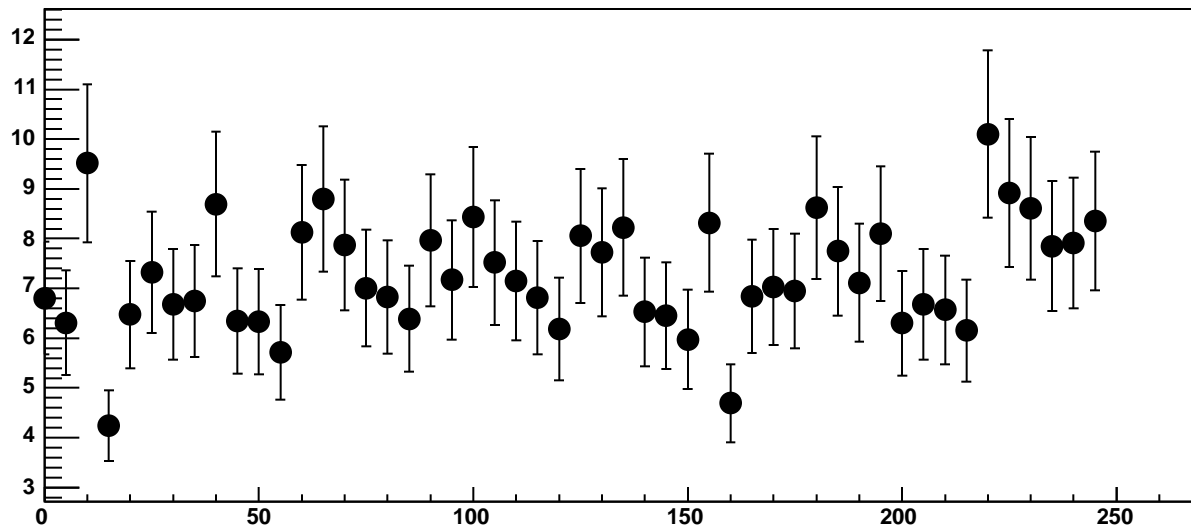


Chip 3, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold

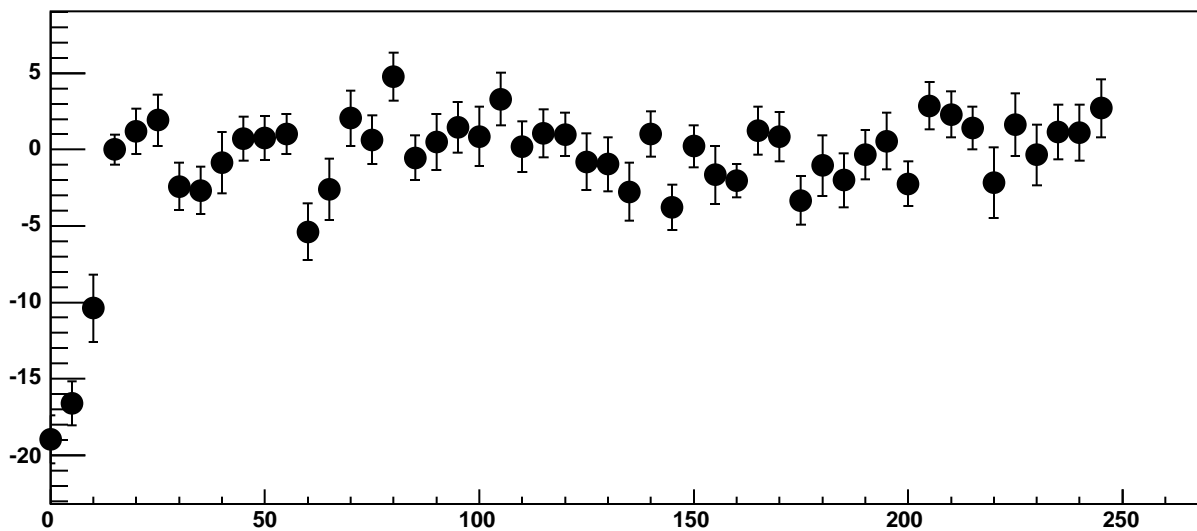


$\chi^2 / \text{ndf}$	67.69 / 41
p0	$599.5 \pm 0.9355$
p1	$16.44 \pm 0.5006$
p2	$99.31 \pm 1.81$
p3	$26.53 \pm 1.024$
p4	$-0.1407 \pm 0.009121$

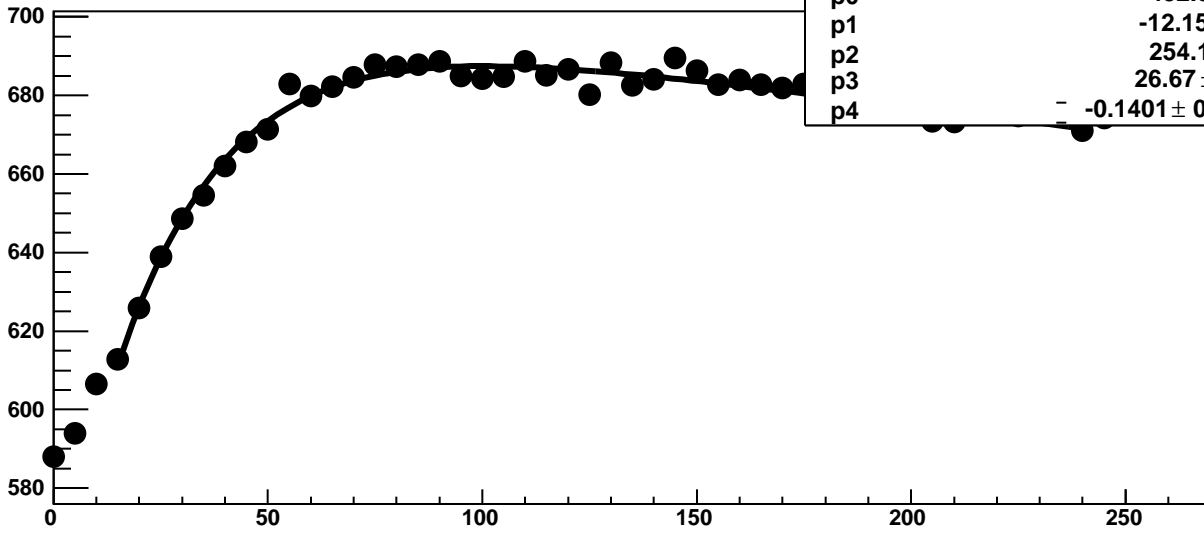
Chip 3, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold

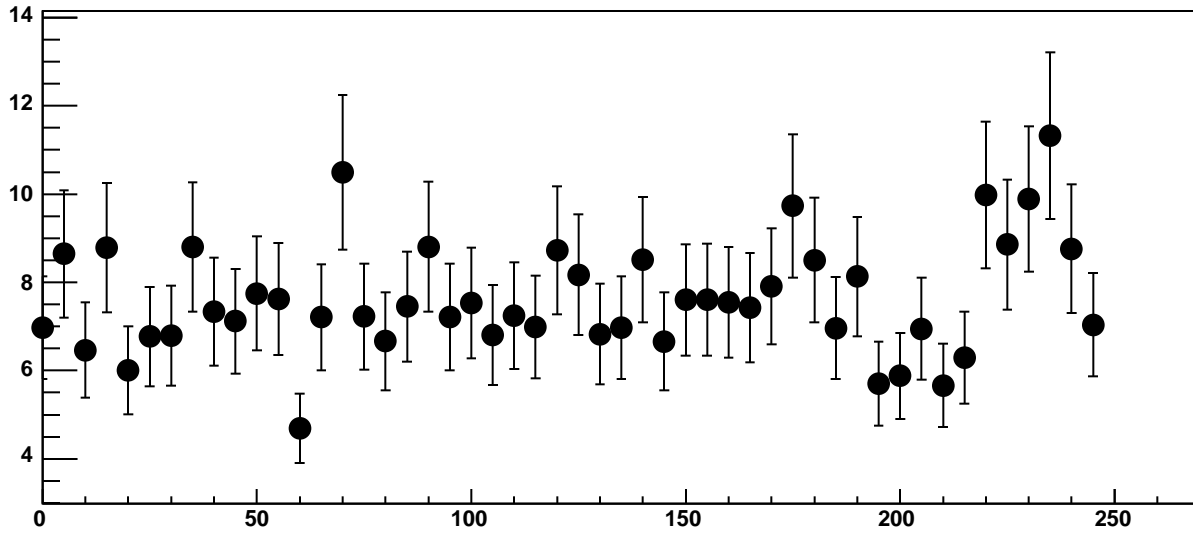


Chip 3, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

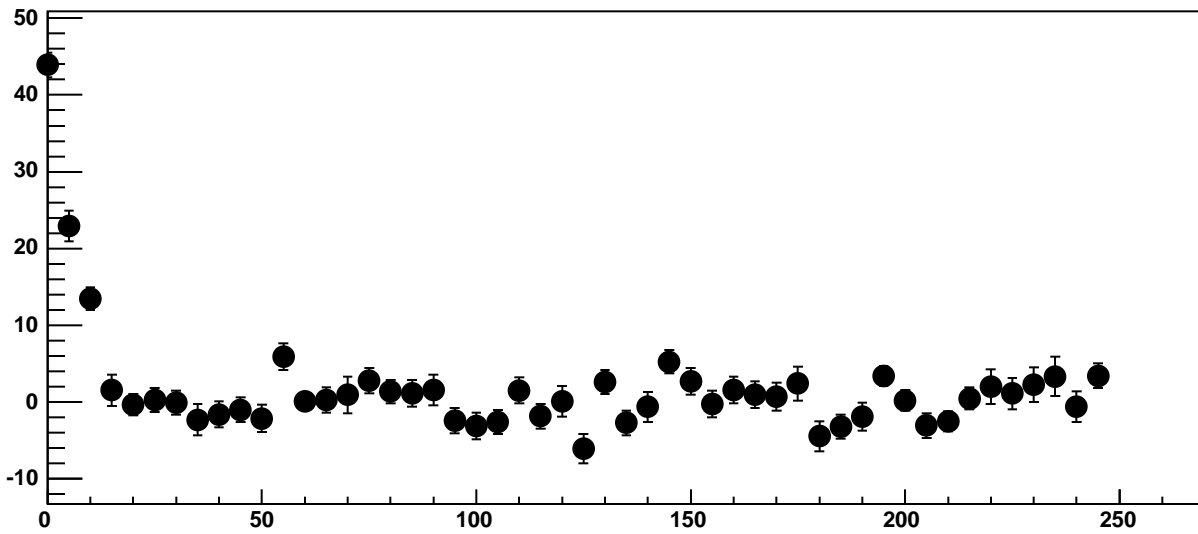


$\chi^2 / \text{ndf}$	92.91 / 41
p0	$452.8 \pm 3.141$
p1	$-12.15 \pm 1.066$
p2	$254.1 \pm 3.189$
p3	$26.67 \pm 0.9548$
p4	$-0.1401 \pm 0.008294$

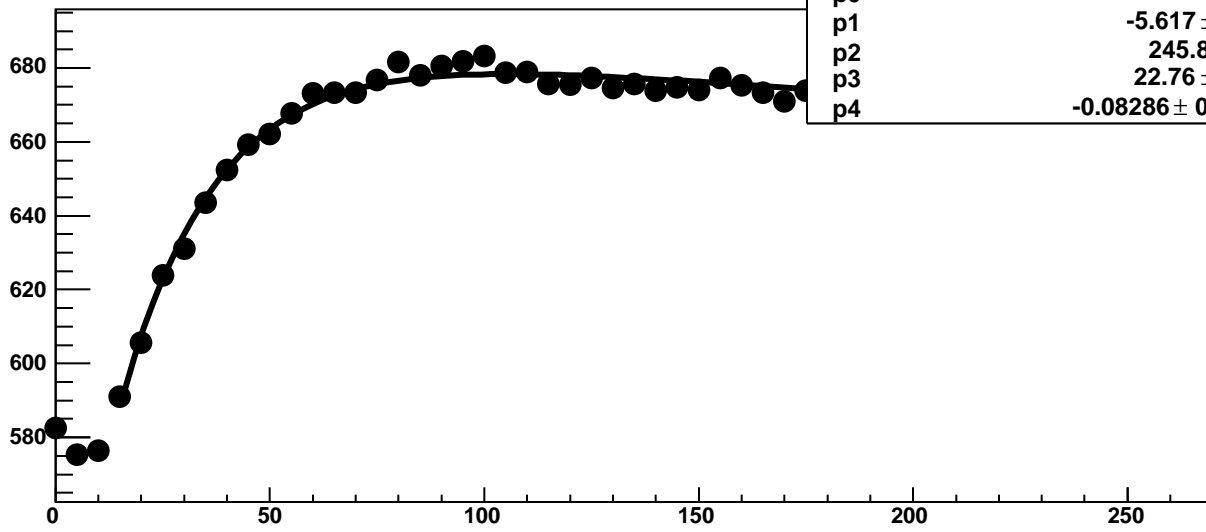
Chip 3, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold

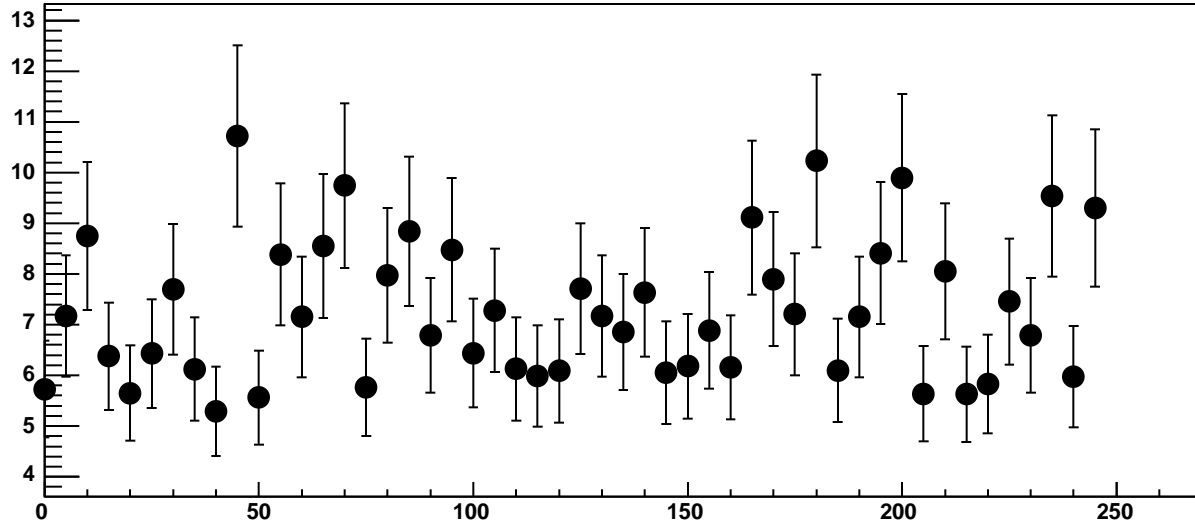


Chip 3, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

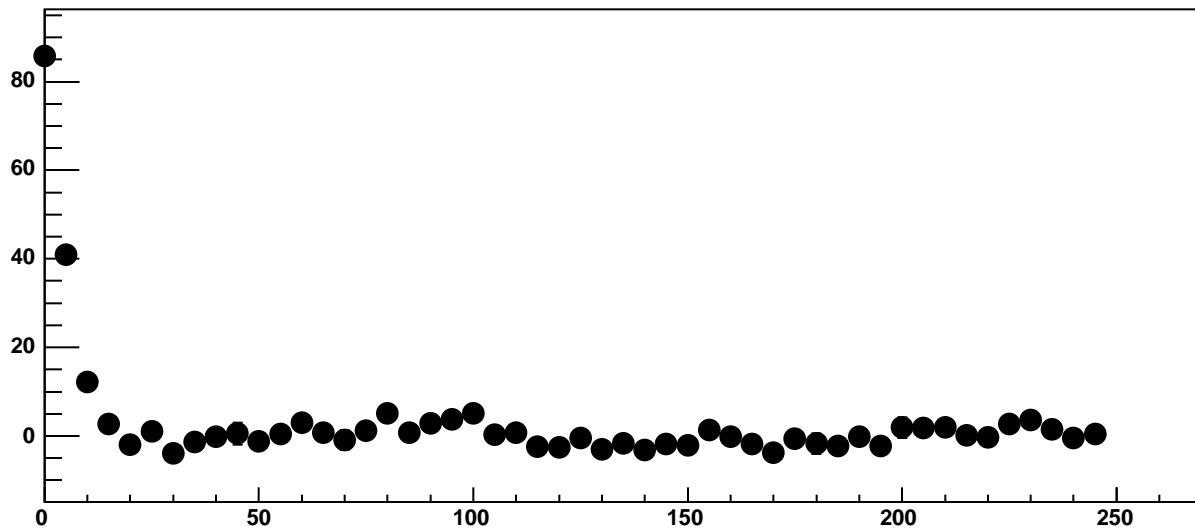


$\chi^2 / \text{ndf}$	85.51 / 41
p0	$443.6 \pm 2.943$
p1	$-5.617 \pm 0.6834$
p2	$245.8 \pm 2.968$
p3	$22.76 \pm 0.6235$
p4	$-0.08286 \pm 0.006458$

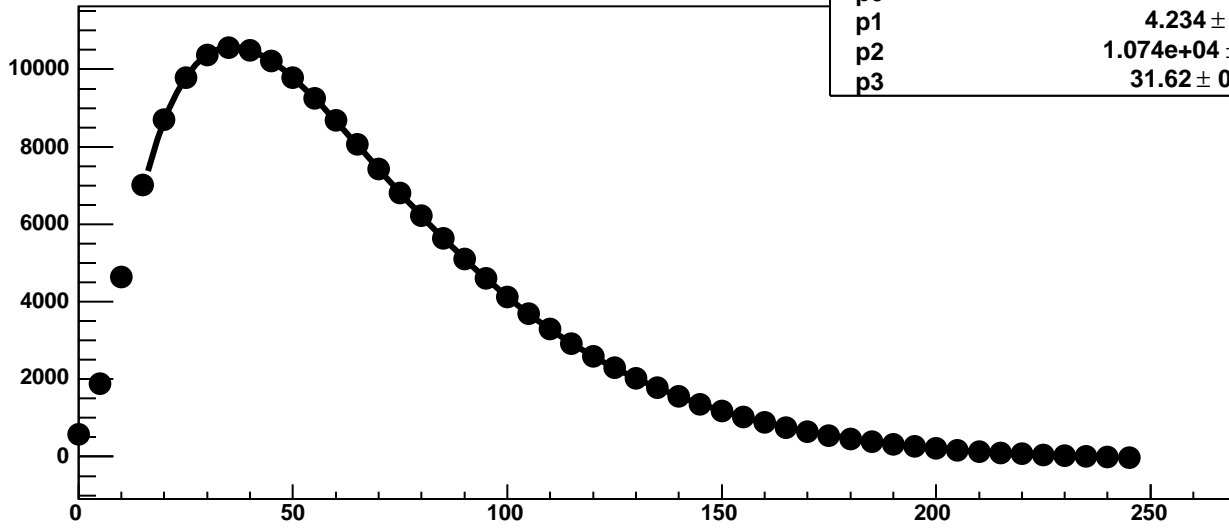
Chip 3, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

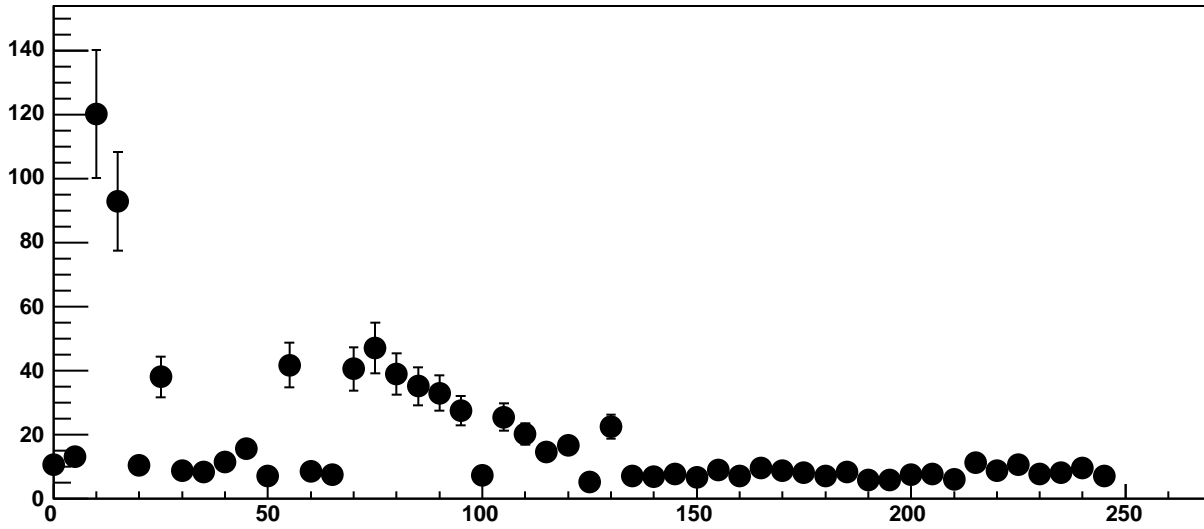


Chip 3, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold

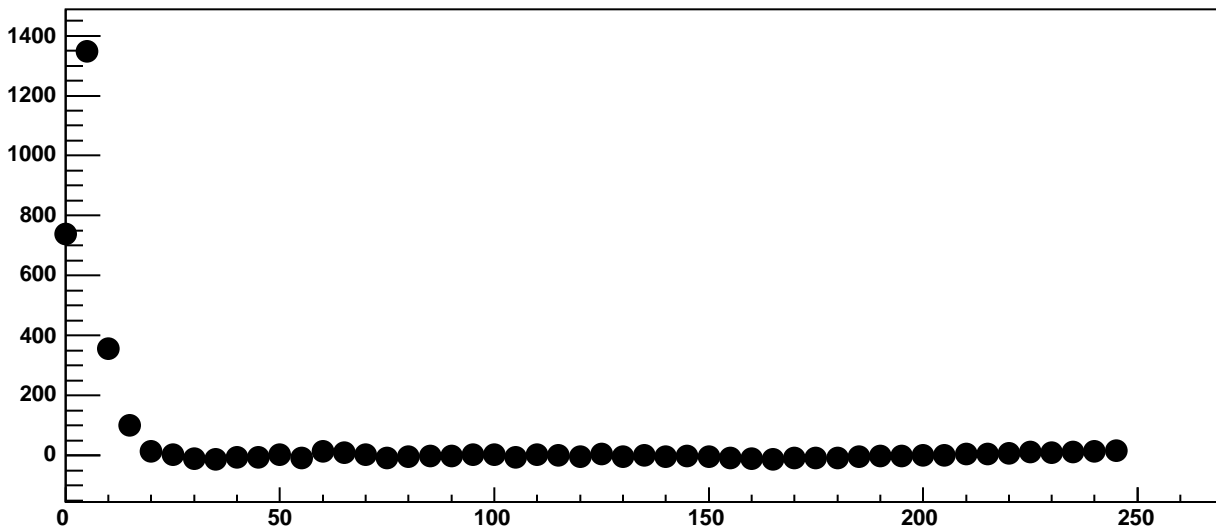


$\chi^2 / \text{ndf}$	555.6 / 42
p0	-155.2 ± 0.6586
p1	4.234 ± 0.00982
p2	1.074e+04 ± 0.9722
p3	31.62 ± 0.006404

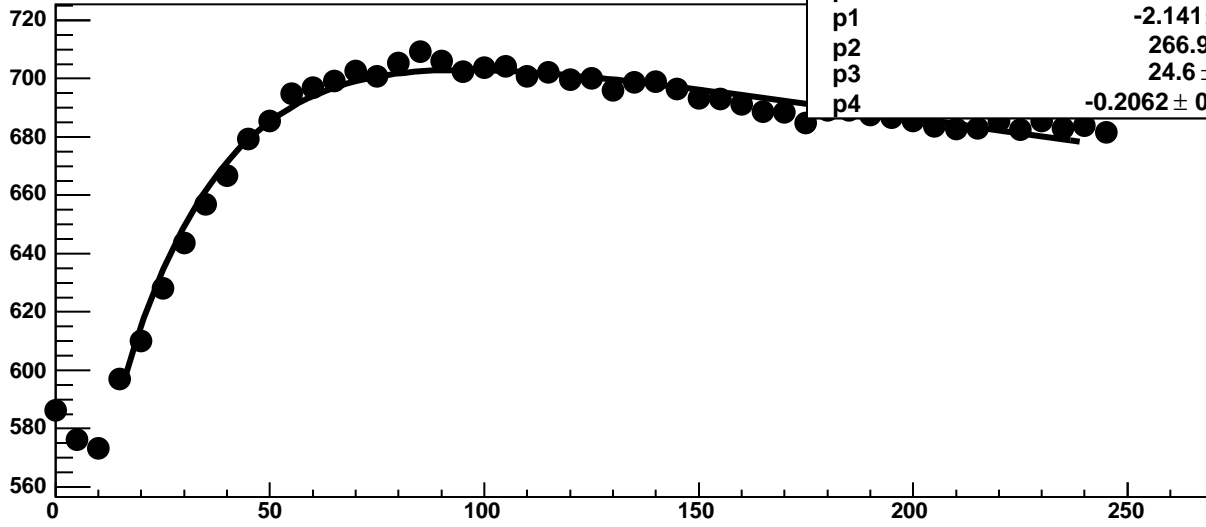
Chip 3, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold



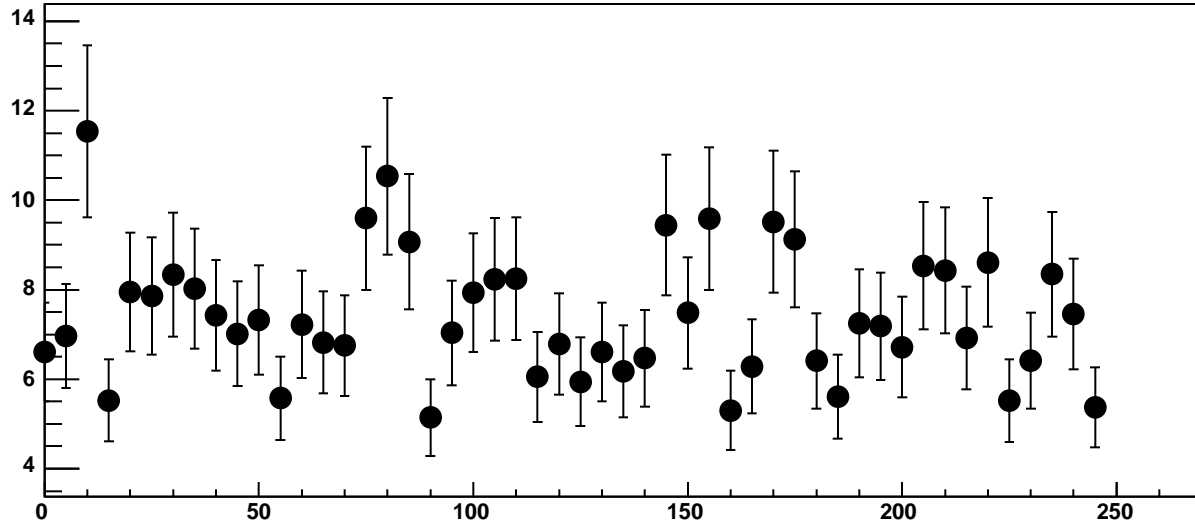
Chip 3, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold



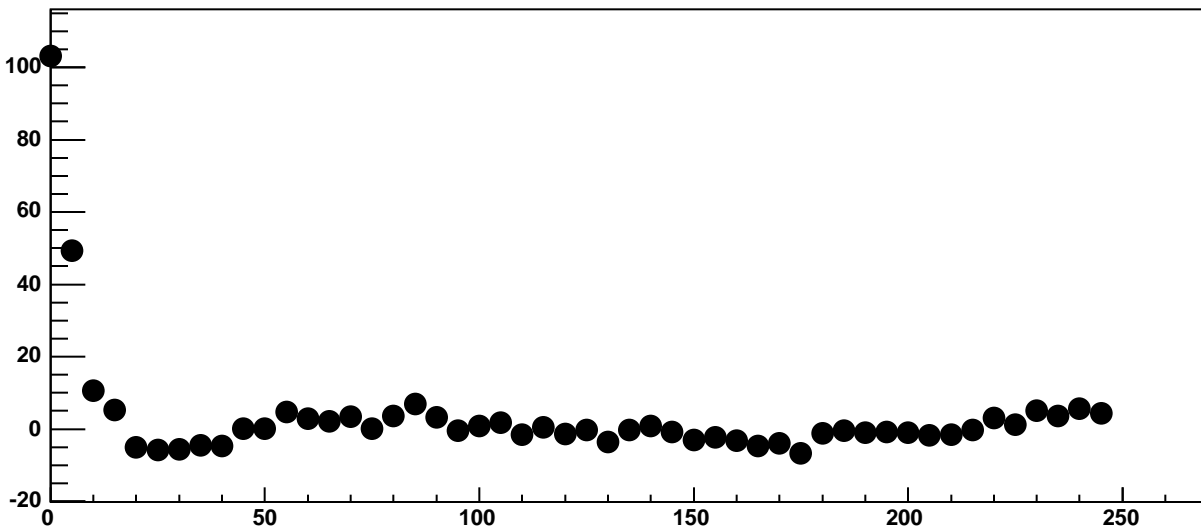
Chip 3, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold



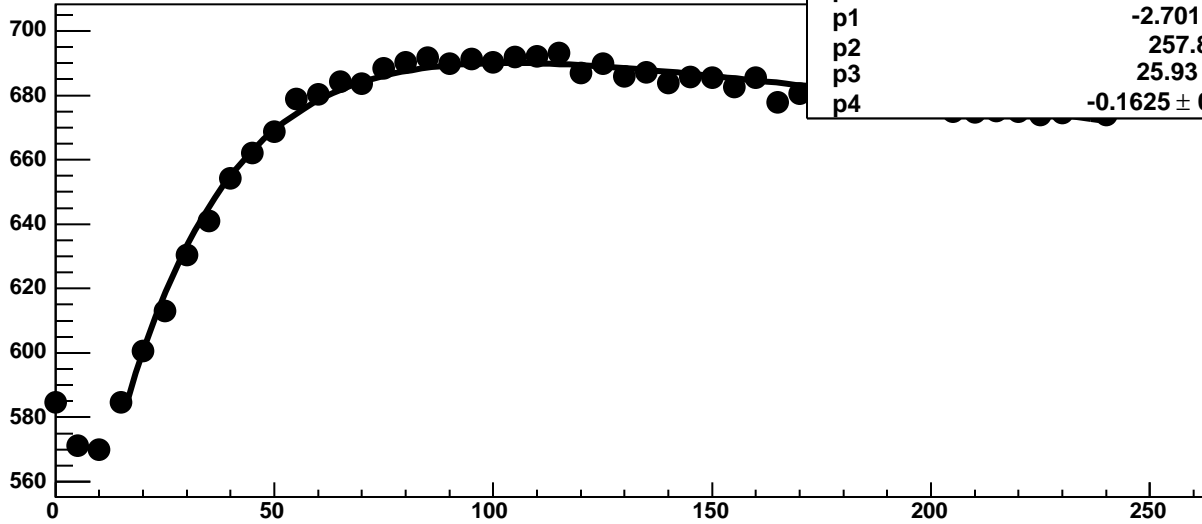
Chip 3, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

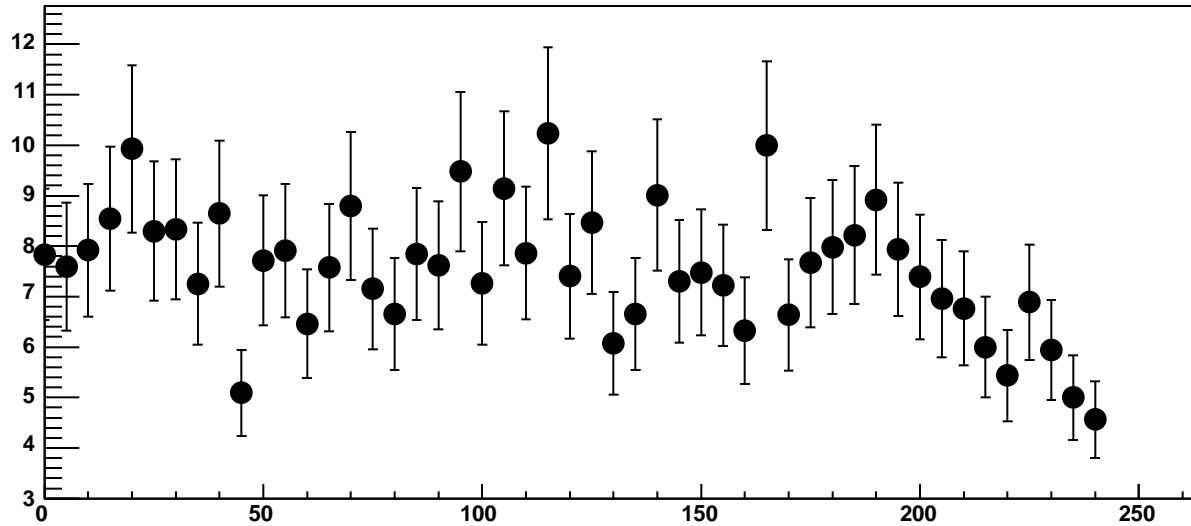


Chip 3, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

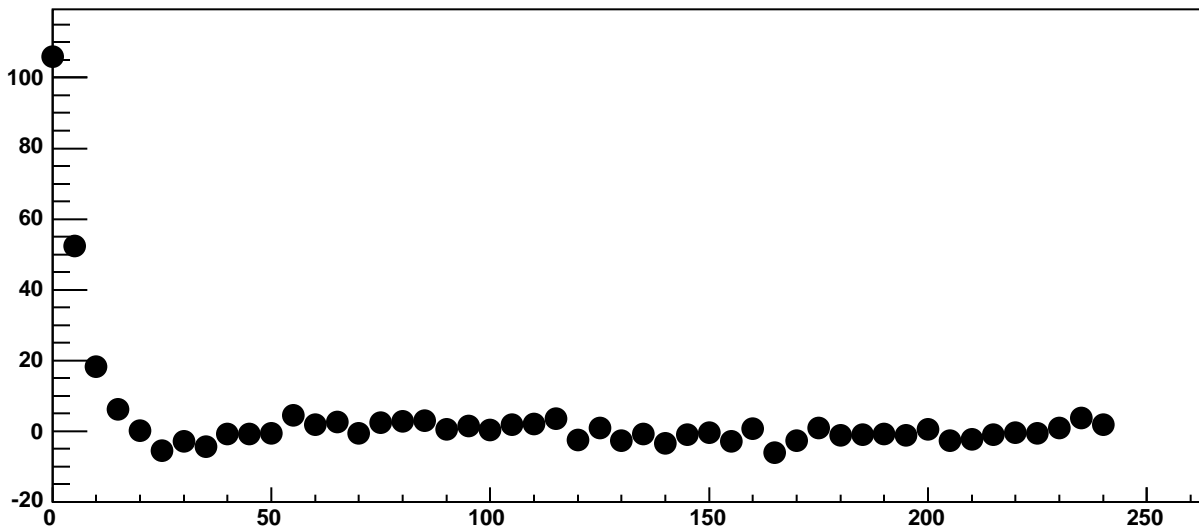


$\chi^2 / \text{ndf}$	95.74 / 41
p0	$453.8 \pm 3.078$
p1	$-2.701 \pm 0.6287$
p2	$257.8 \pm 3.132$
p3	$25.93 \pm 0.6223$
p4	$-0.1625 \pm 0.006922$

Chip 3, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold

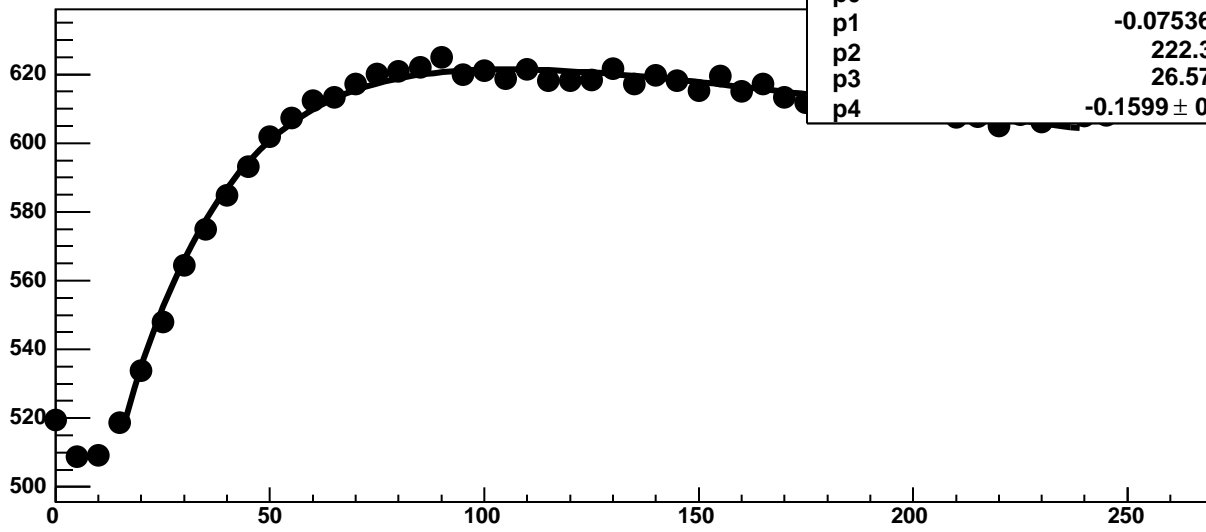


Chip 3, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold



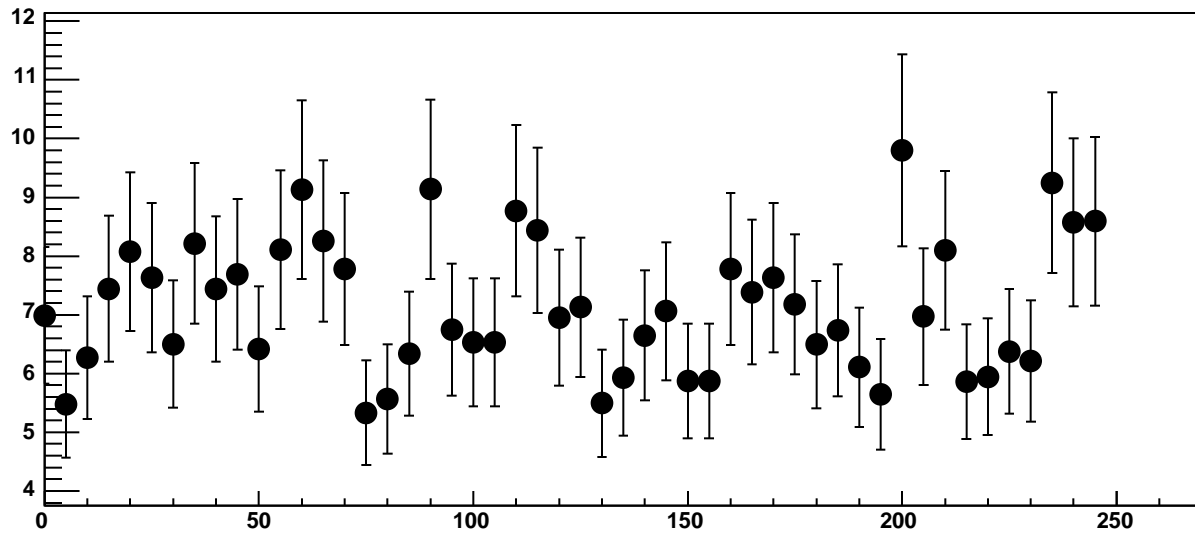


Chip 3, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold

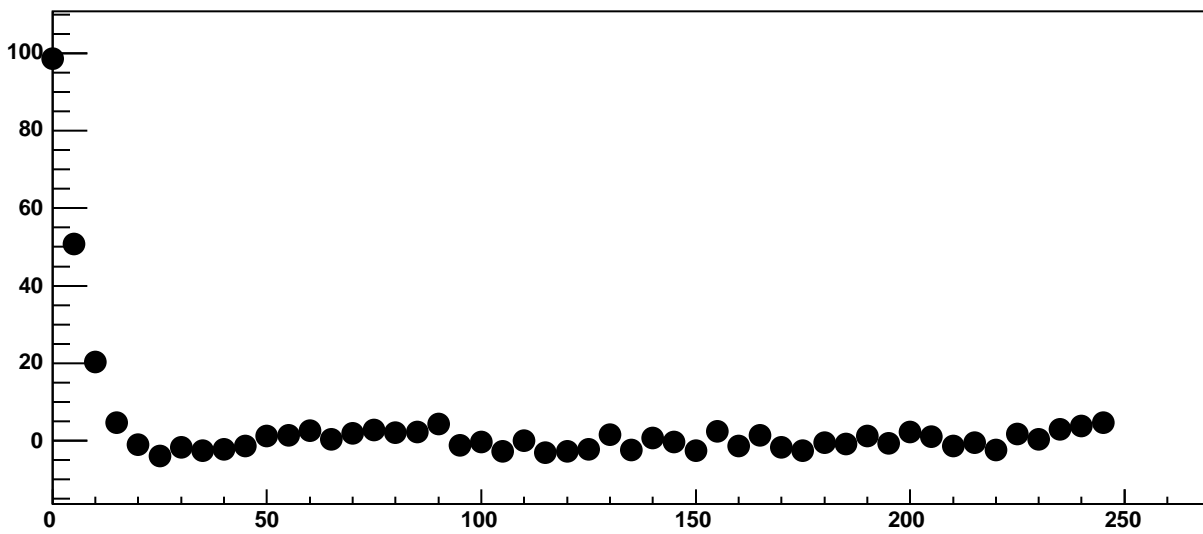


$\chi^2 / \text{ndf}$	76.81 / 41
p0	$420.3 \pm 2.968$
p1	$-0.07536 \pm 0.596$
p2	$222.3 \pm 3.031$
p3	$26.57 \pm 0.671$
p4	$-0.1599 \pm 0.007545$

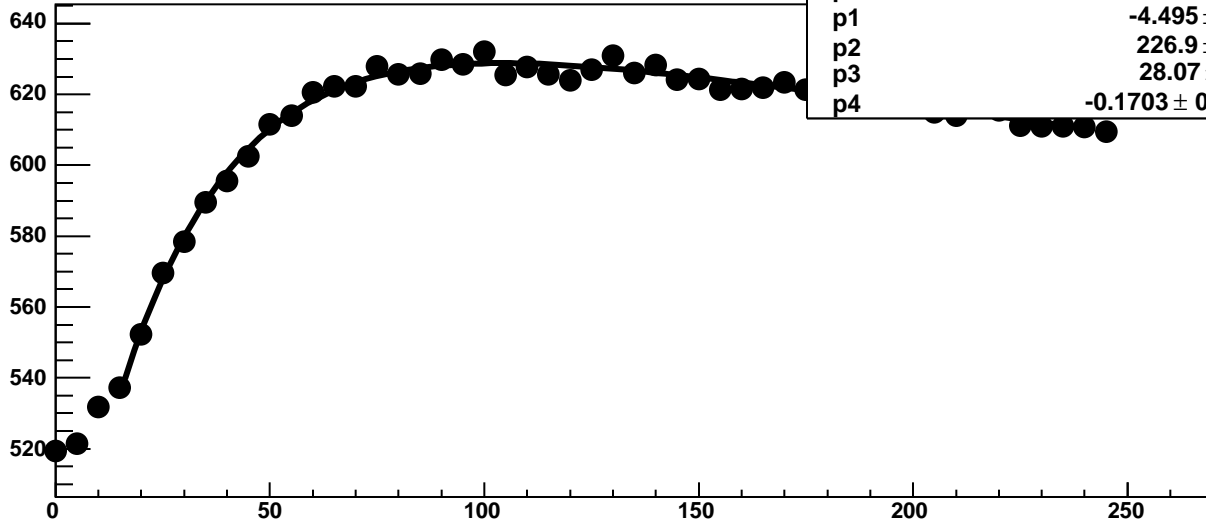
Chip 3, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

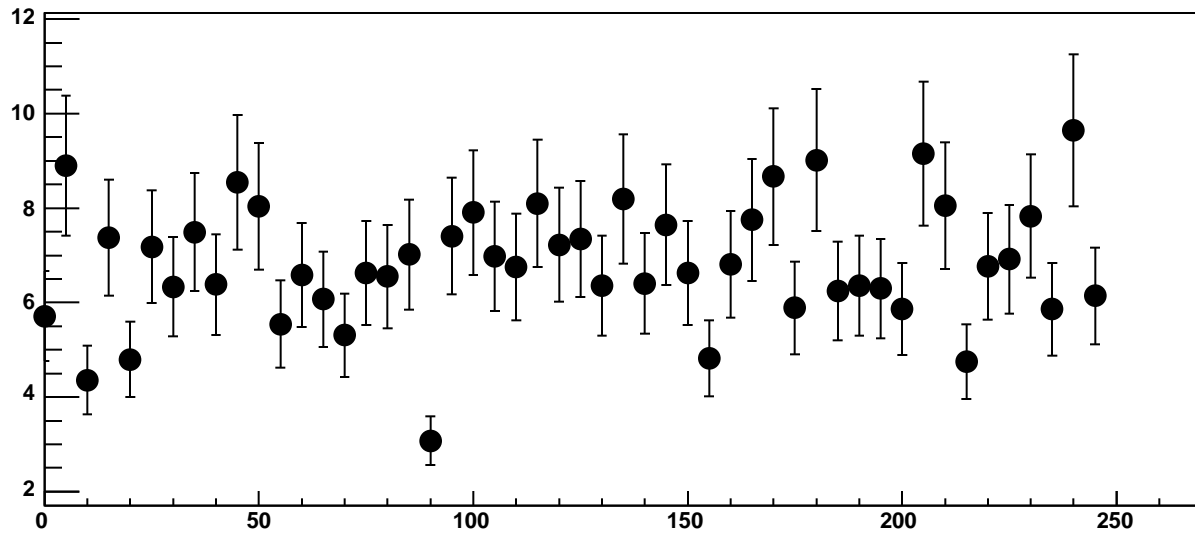


Chip 3, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold

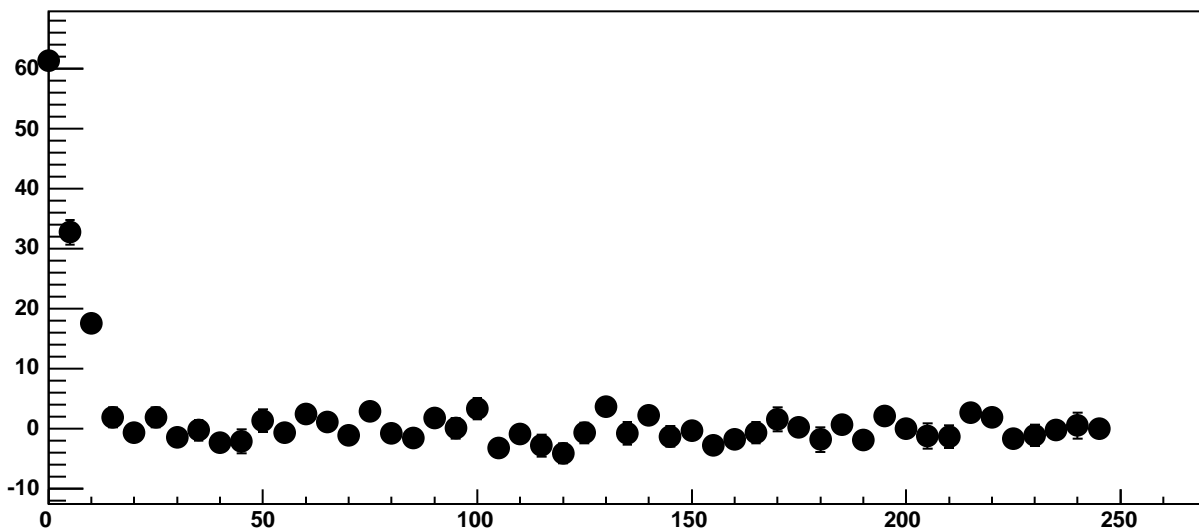


$\chi^2 / \text{ndf}$	72.73 / 41
p0	$425.2 \pm 0.8439$
p1	$-4.495 \pm 0.5943$
p2	$226.9 \pm 0.8729$
p3	$28.07 \pm 0.8461$
p4	$-0.1703 \pm 0.008479$

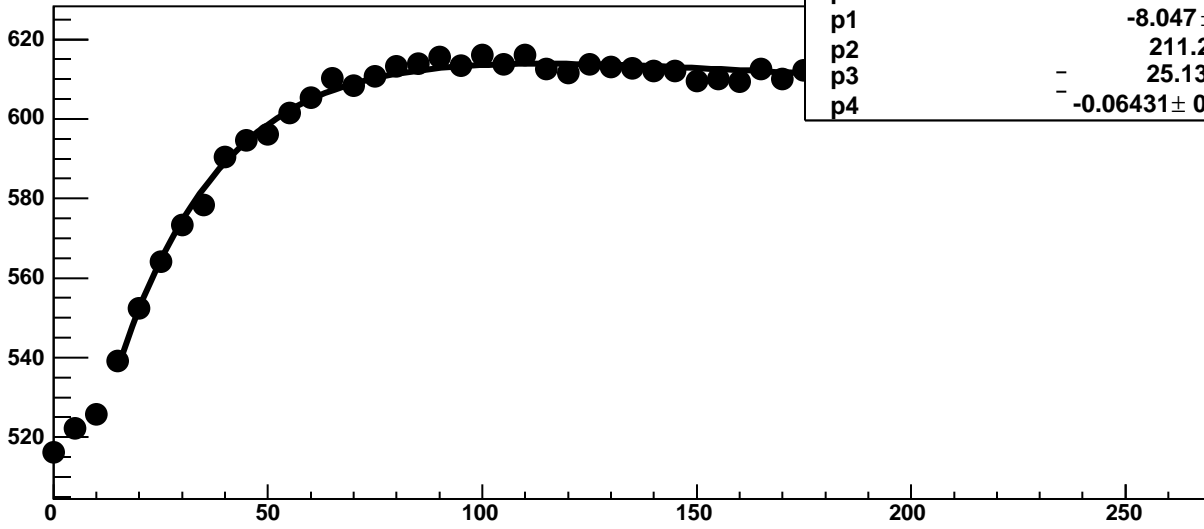
Chip 3, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold

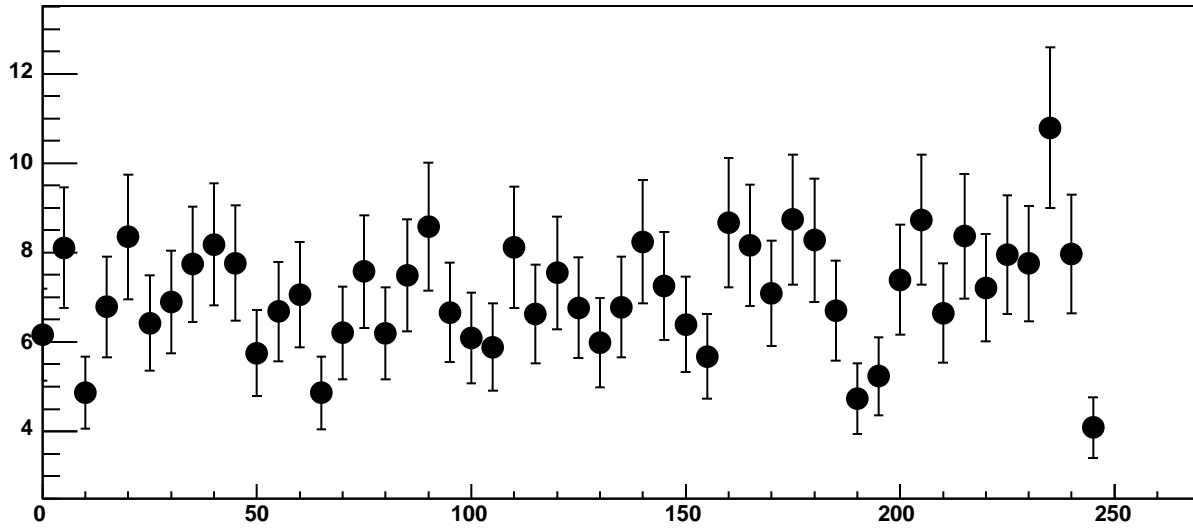


Chip 3, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

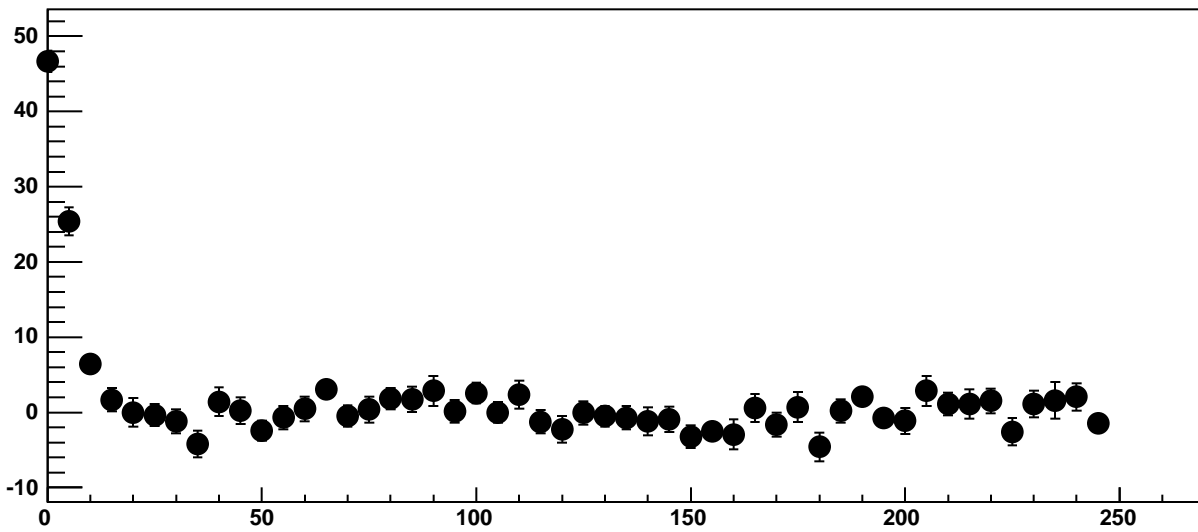


$\chi^2 / \text{ndf}$	62.94 / 41
p0	$412.2 \pm 0.8365$
p1	$-8.047 \pm 0.9473$
p2	$211.2 \pm 0.794$
p3	$25.13 \pm 1.004$
p4	$-0.06431 \pm 0.008298$

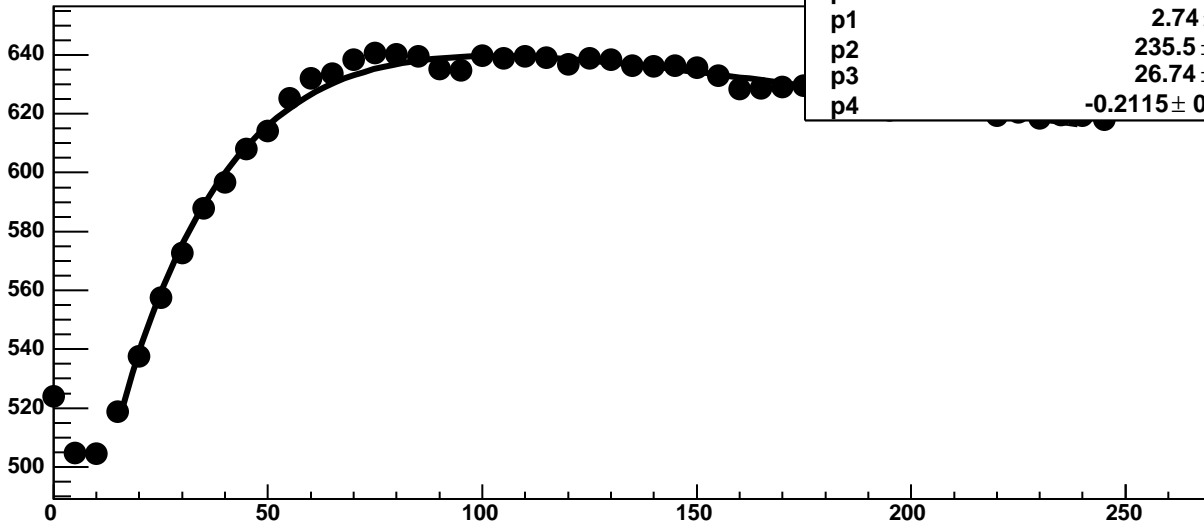
Chip 3, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

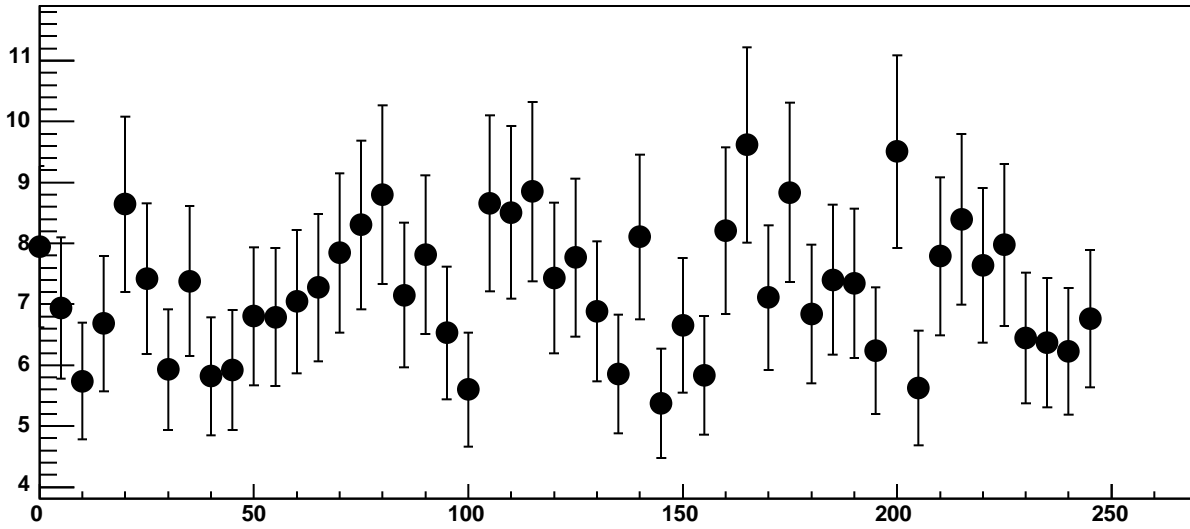


Chip 3, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

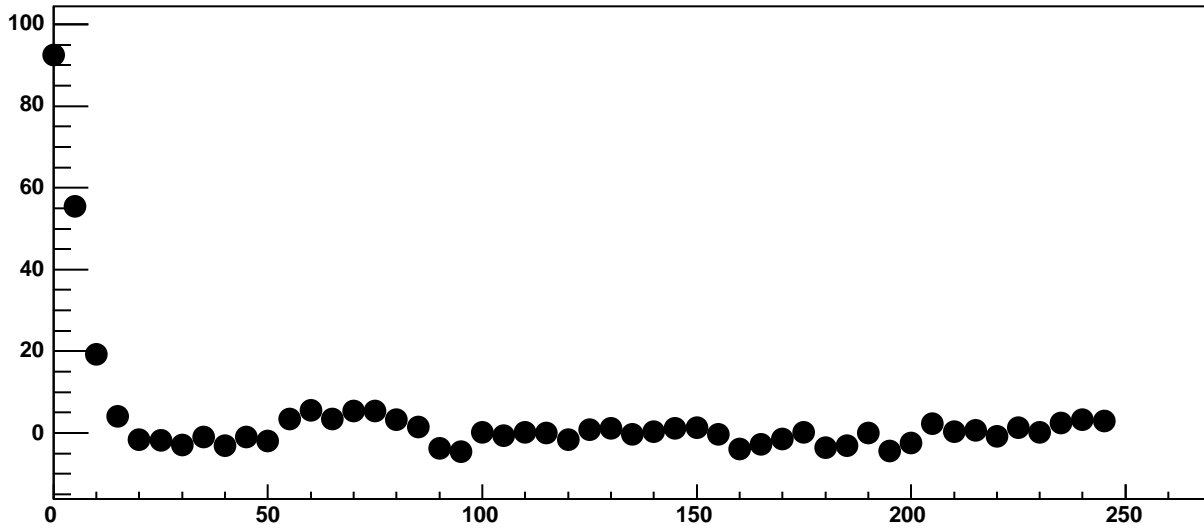


$\chi^2 / \text{ndf}$	118.8 / 41
p0	$430.8 \pm 0.7332$
p1	$2.74 \pm 0.3521$
p2	$235.5 \pm 0.8182$
p3	$26.74 \pm 0.5758$
p4	$-0.2115 \pm 0.008165$

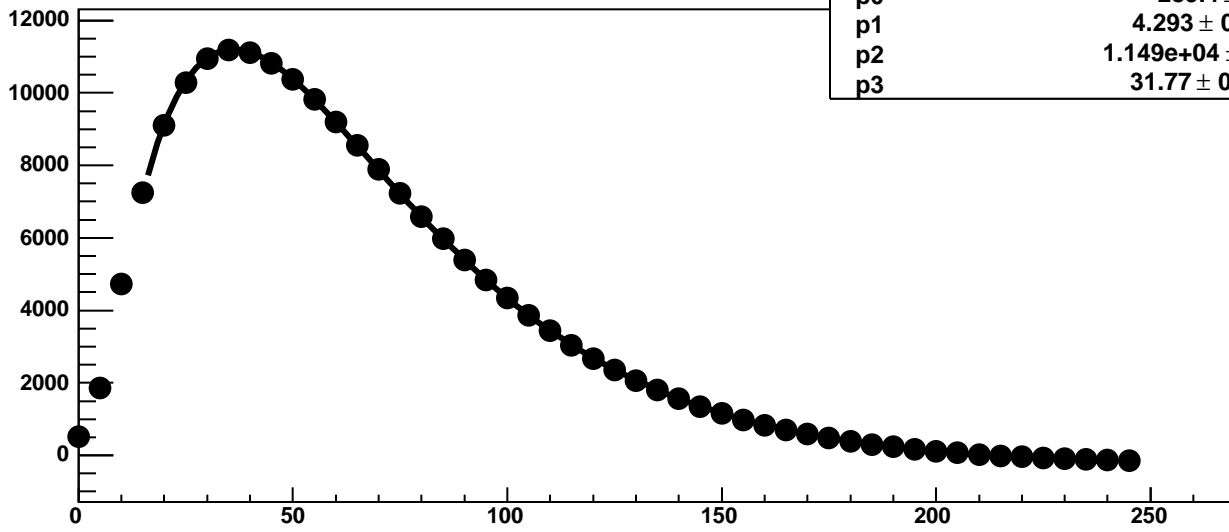
Chip 3, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

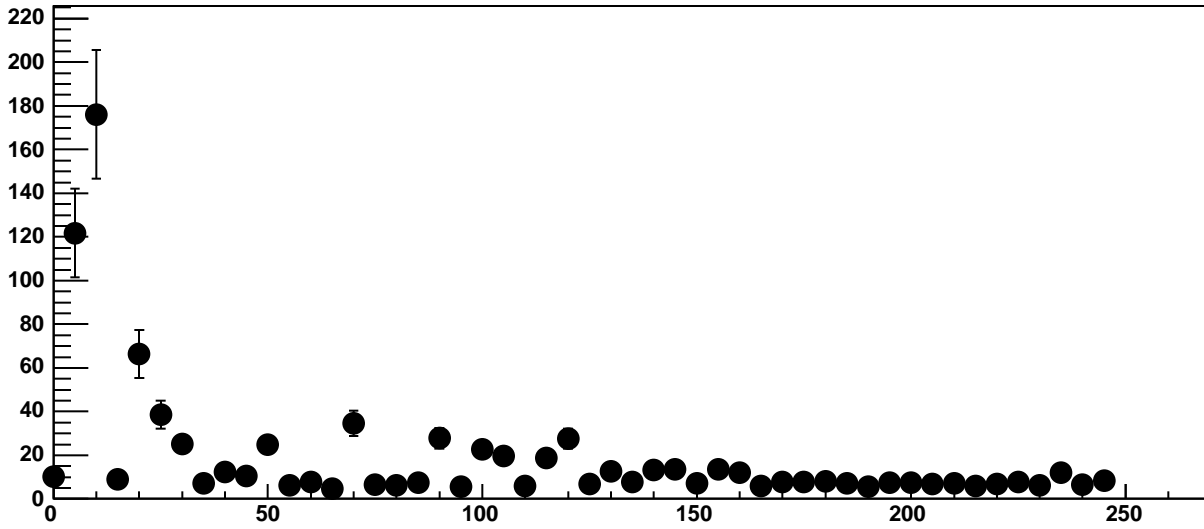


Chip 3, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold

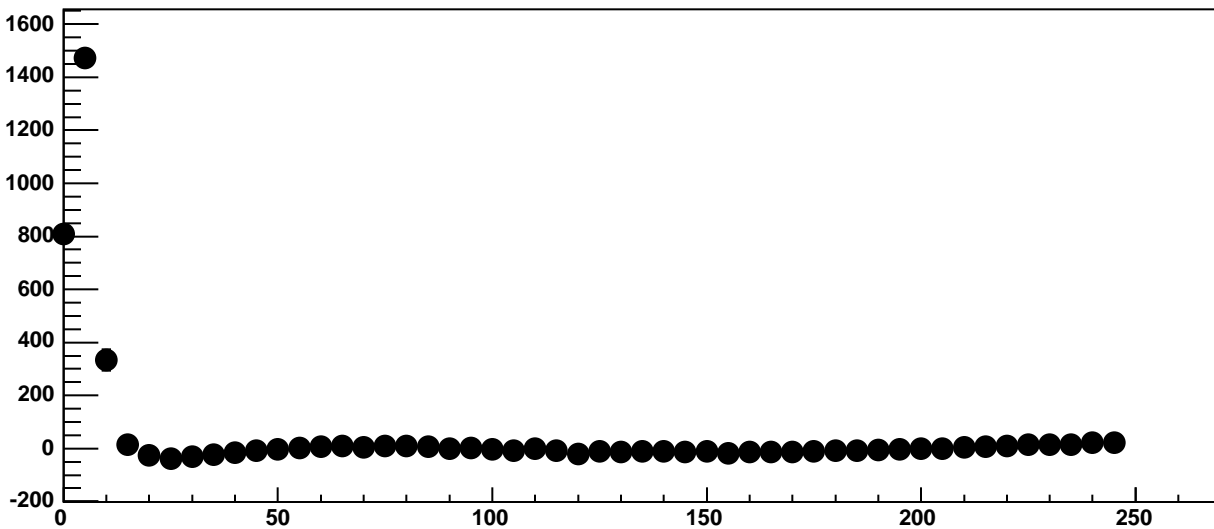


$\chi^2 / \text{ndf}$	1493 / 42
p0	-289.4 ± 0.5455
p1	4.293 ± 0.005372
p2	1.149e+04 ± 0.9162
p3	31.77 ± 0.004455

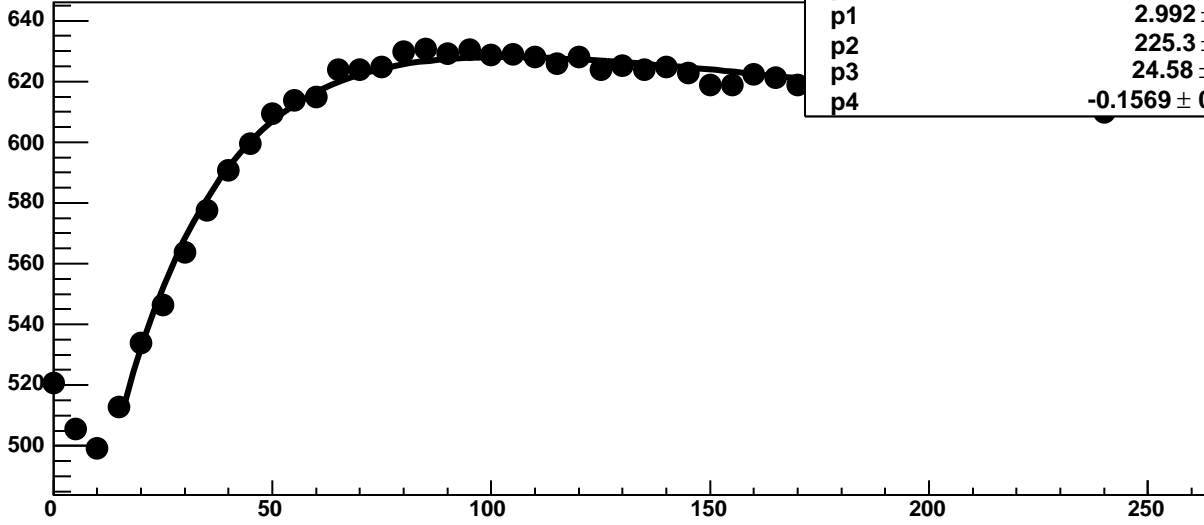
Chip 3, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold

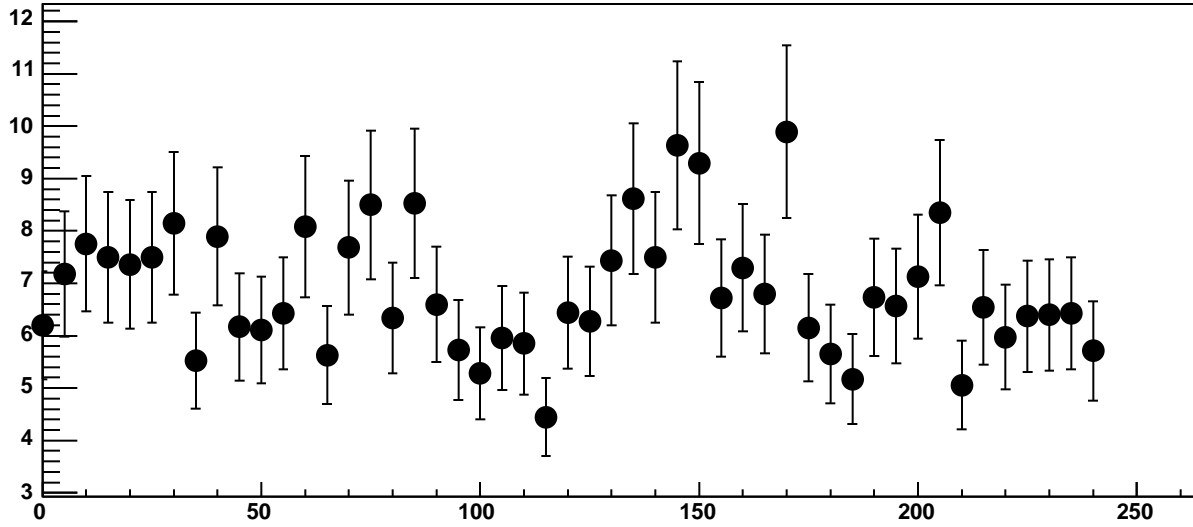


Chip 3, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold

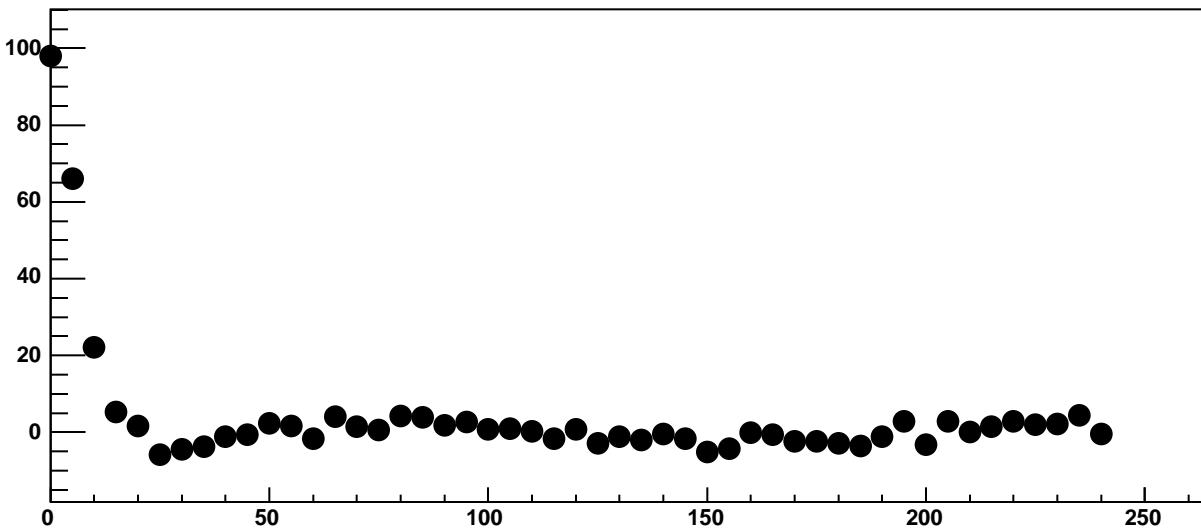


$\chi^2 / \text{ndf}$	142.3 / 41
p0	$422.3 \pm 0.6142$
p1	$2.992 \pm 0.3593$
p2	$225.3 \pm 0.6662$
p3	$24.58 \pm 0.5256$
p4	$-0.1569 \pm 0.006811$

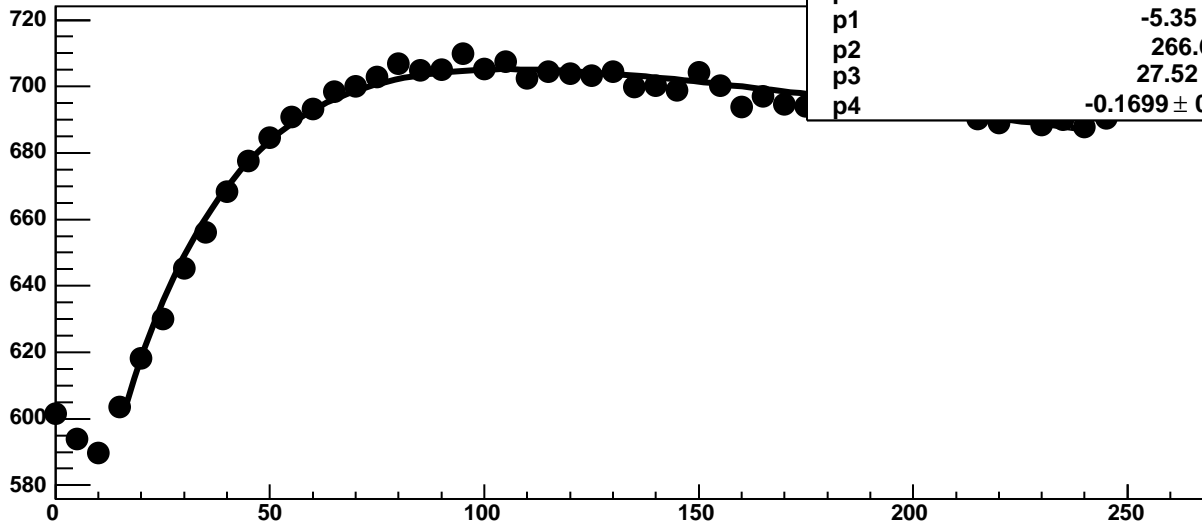
Chip 3, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold

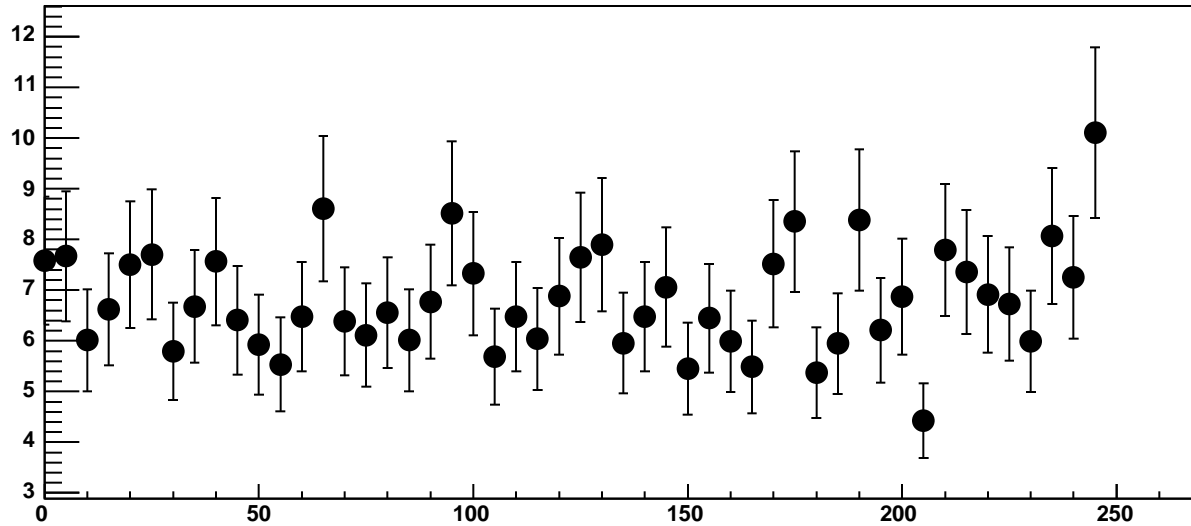


Chip 3, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

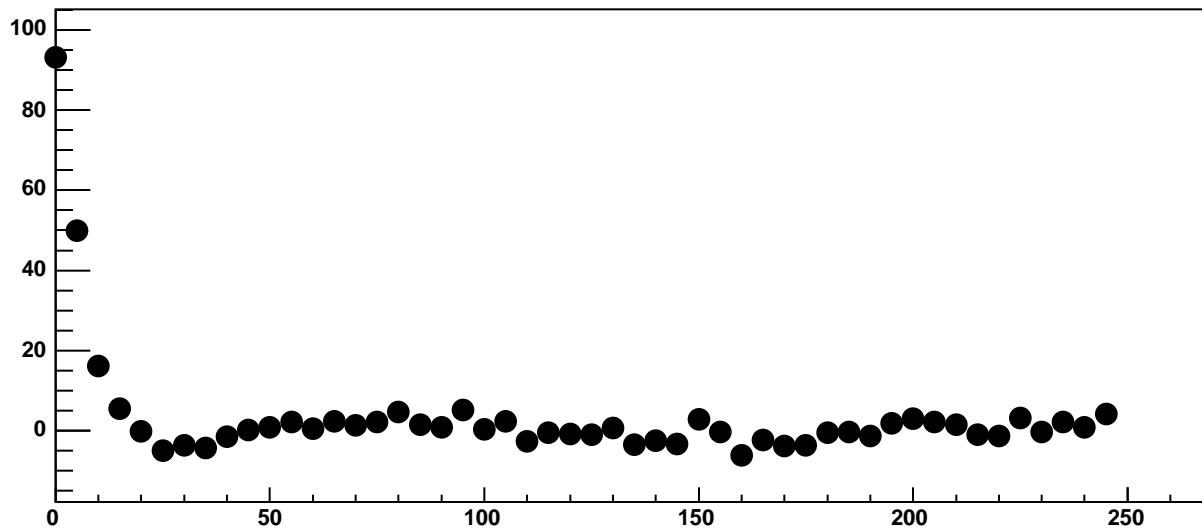


$\chi^2 / \text{ndf}$	139.2 / 41
p0	$462.1 \pm 2.796$
p1	$-5.35 \pm 0.5872$
p2	$266.6 \pm 2.851$
p3	$27.52 \pm 0.6117$
p4	$-0.1699 \pm 0.007254$

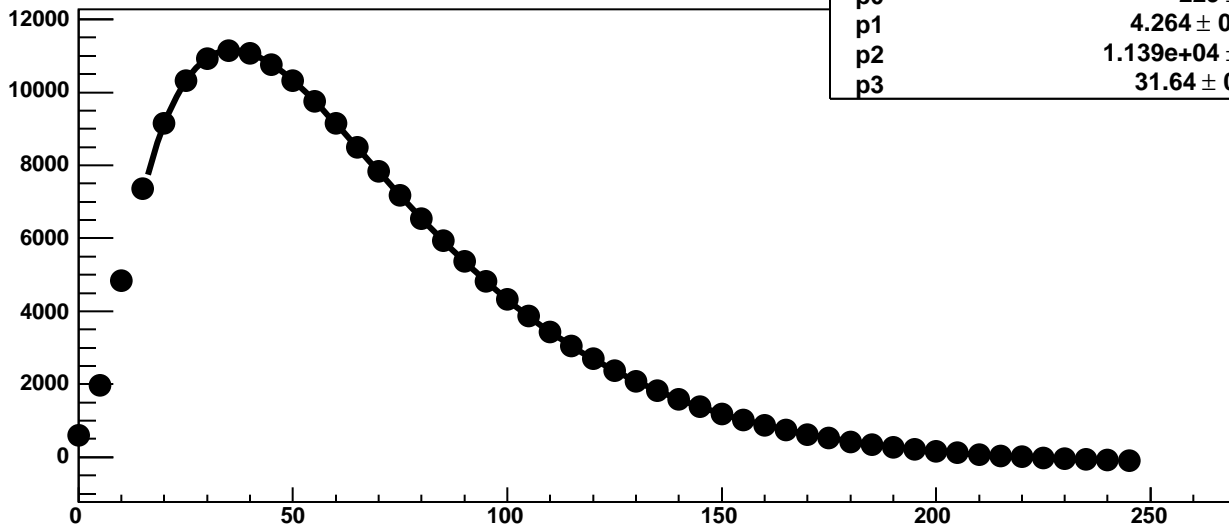
Chip 3, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

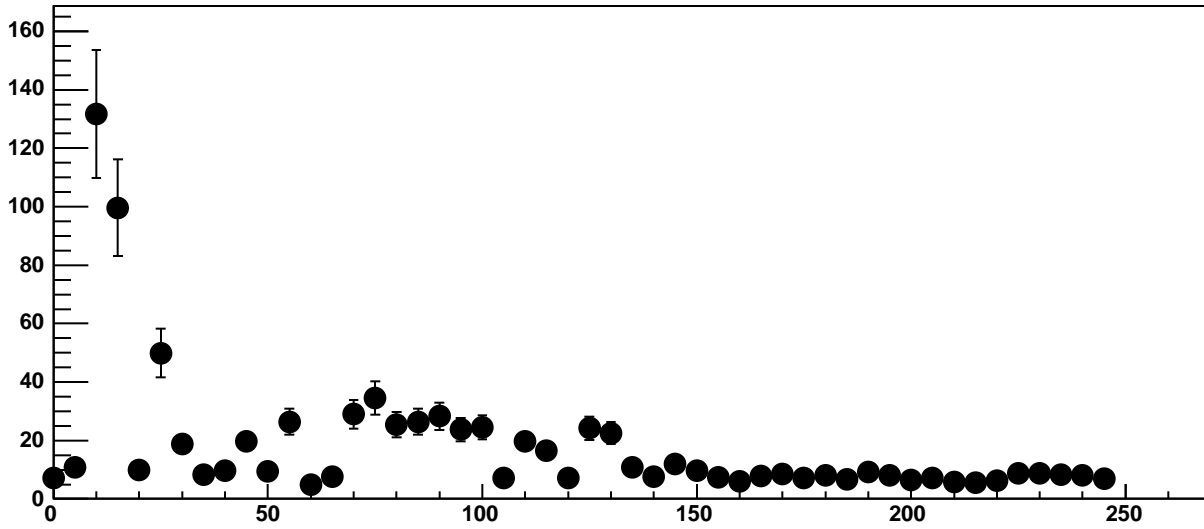


Chip 3, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold

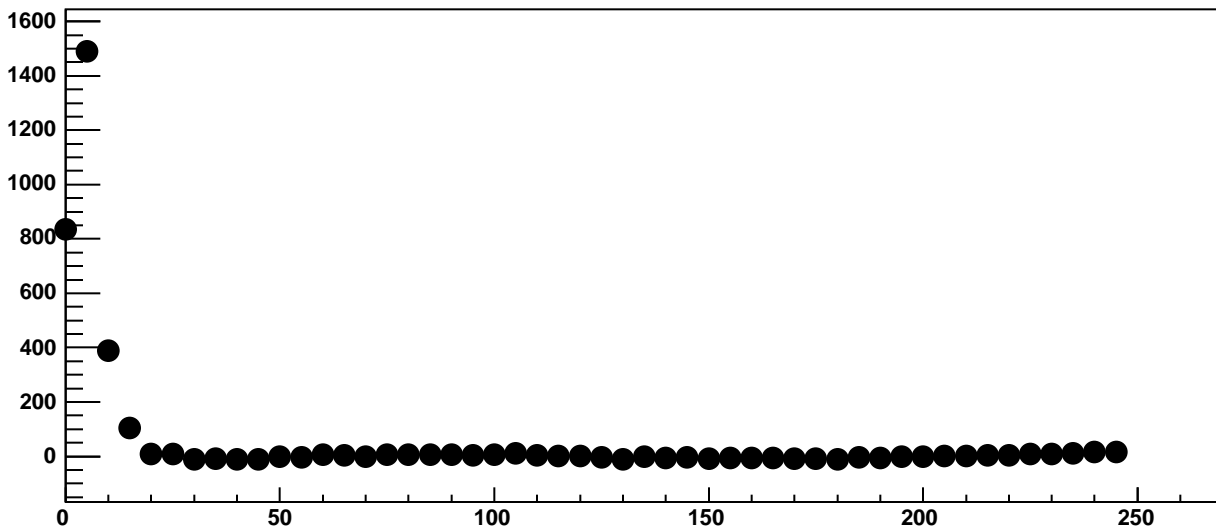


$\chi^2 / \text{ndf}$	529.6 / 42
p0	-229 ± 0.6153
p1	4.264 ± 0.009019
p2	1.139e+04 ± 0.9653
p3	31.64 ± 0.006201

Chip 3, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold

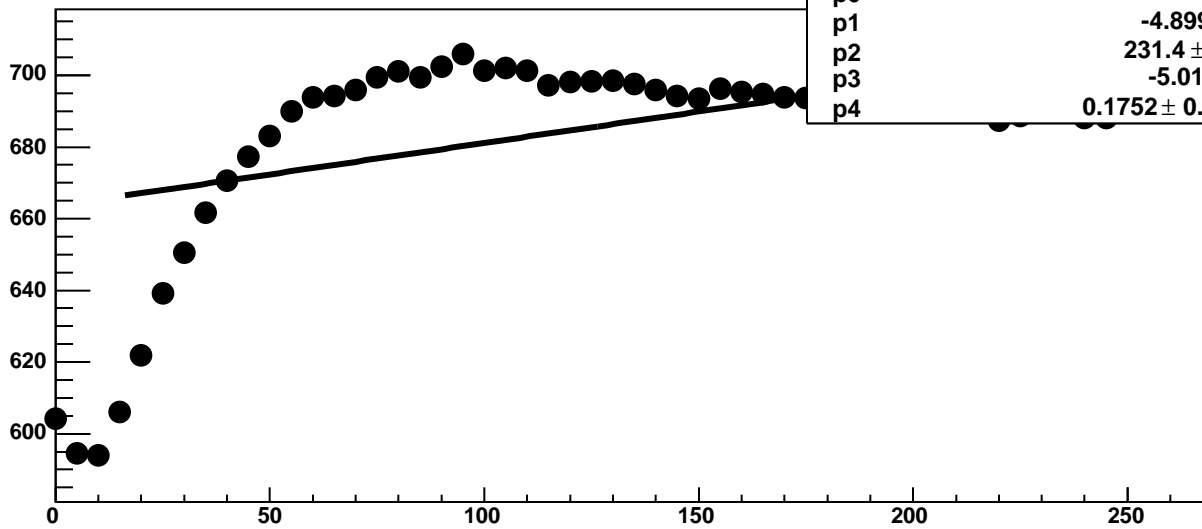


Chip 3, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold



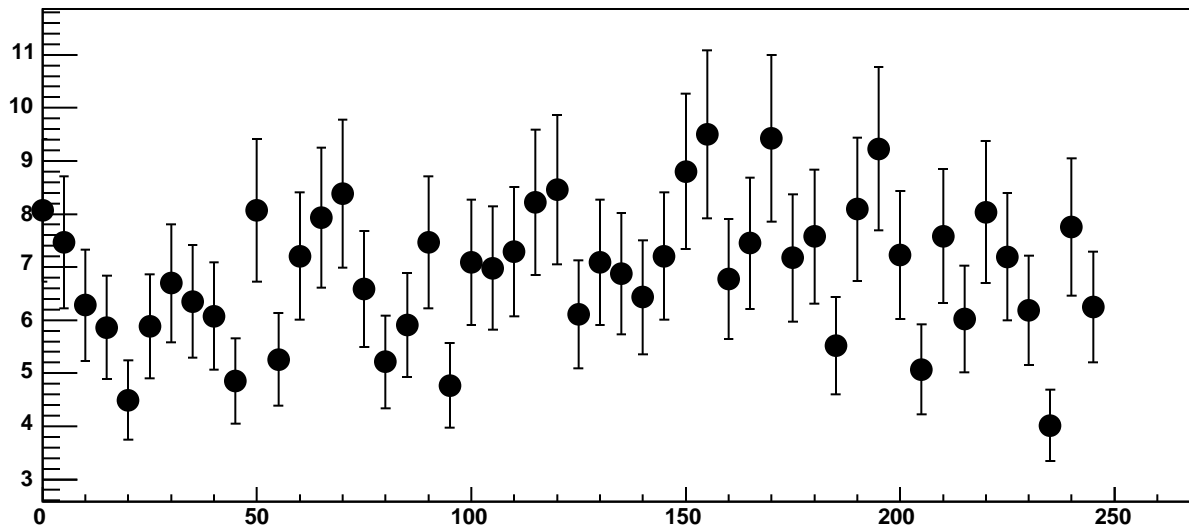


Chip 3, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

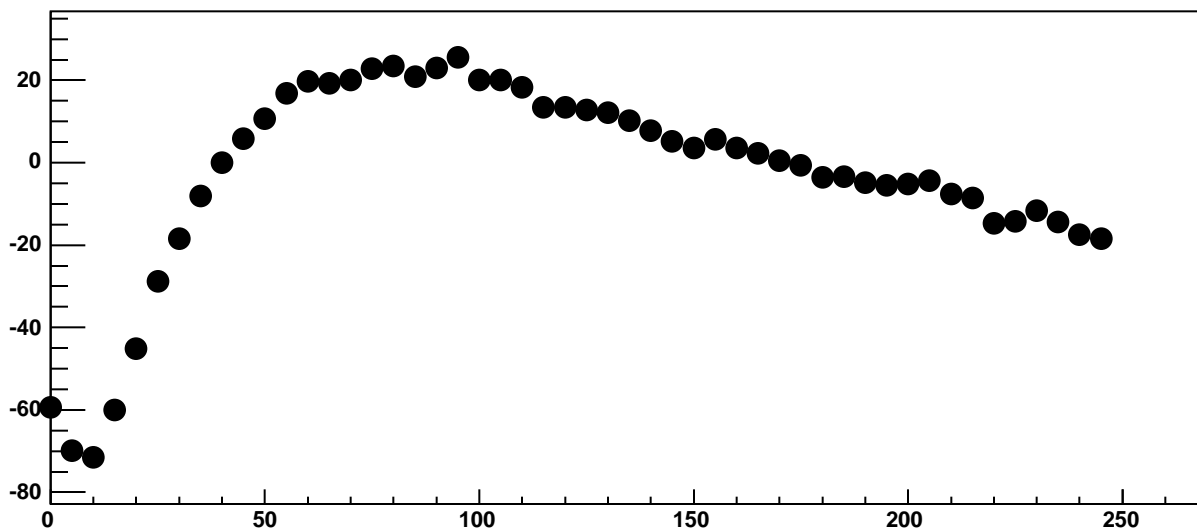


$\chi^2 / \text{ndf}$	8160 / 41
p0	$431.3 \pm 0.07961$
p1	$-4.899 \pm 6.198$
p2	$231.4 \pm 0.06721$
p3	$-5.017 \pm 4.65$
p4	$0.1752 \pm 0.0003866$

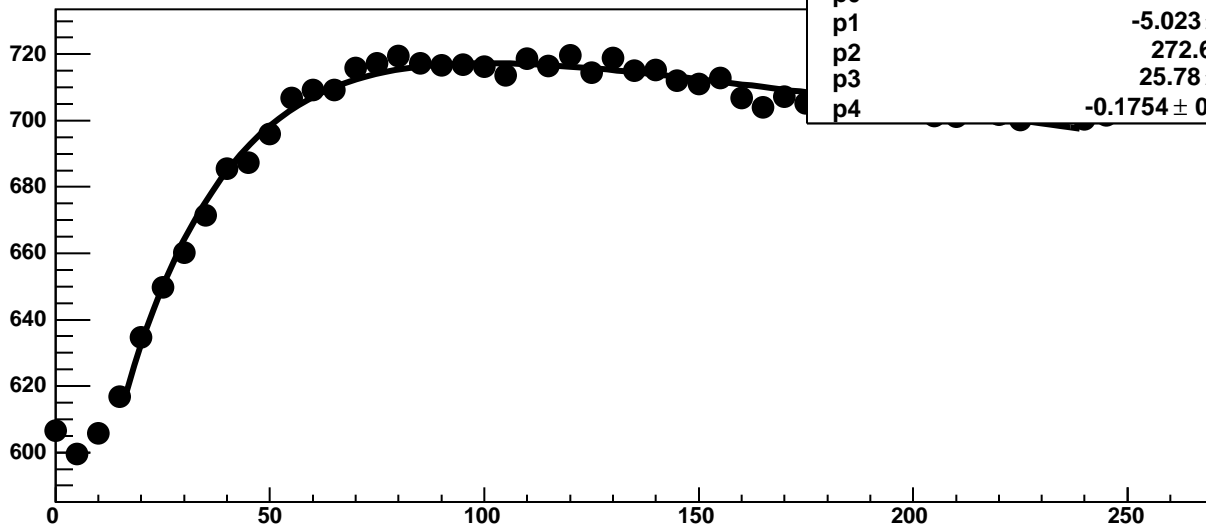
Chip 3, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

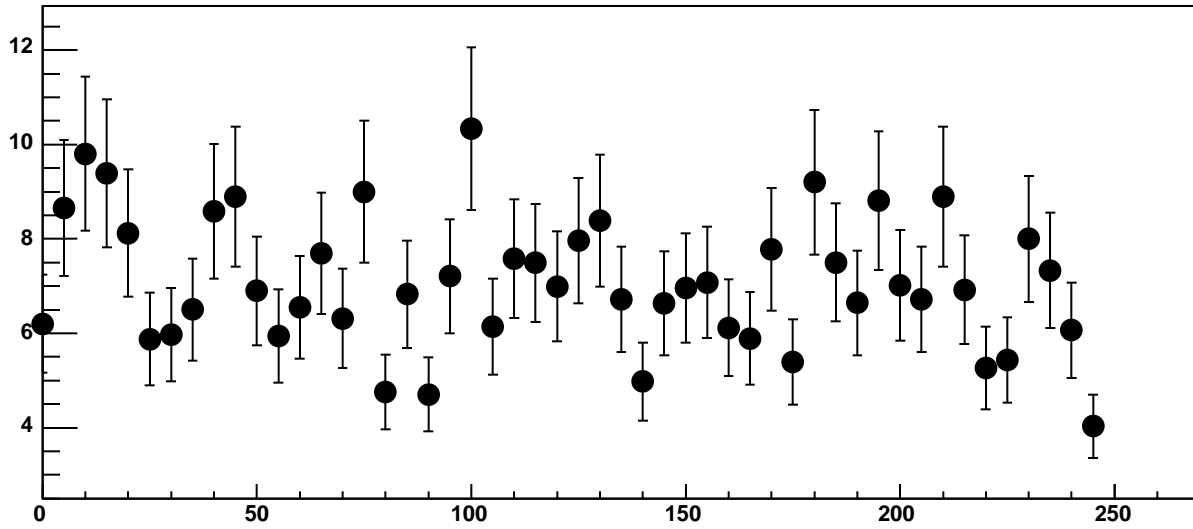


Chip 3, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

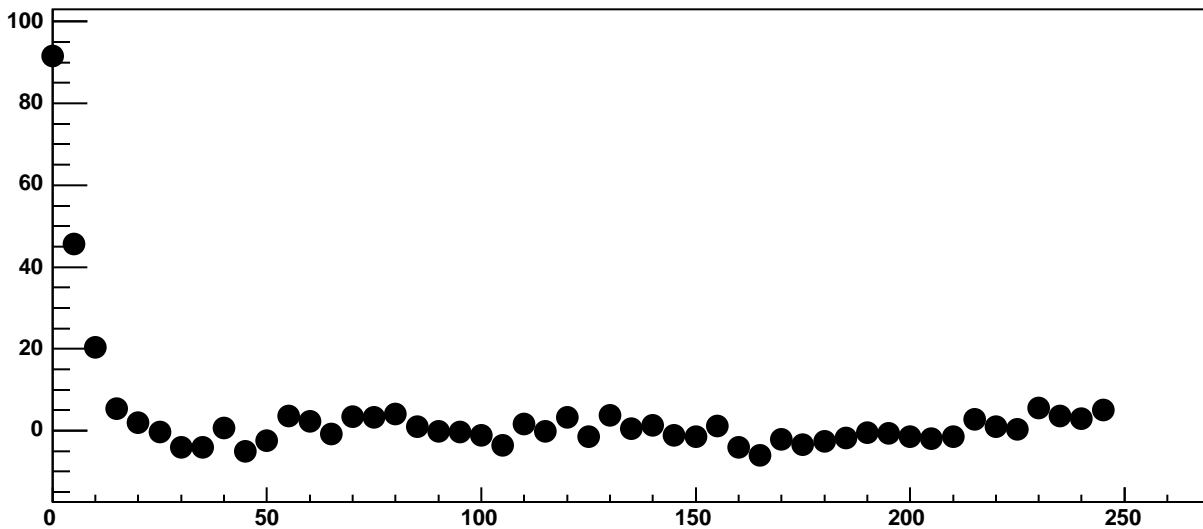


$\chi^2 / \text{ndf}$	149.3 / 41
p0	$467.7 \pm 2.881$
p1	$-5.023 \pm 0.6591$
p2	$272.6 \pm 2.931$
p3	$25.78 \pm 0.6311$
p4	$-0.1754 \pm 0.006924$

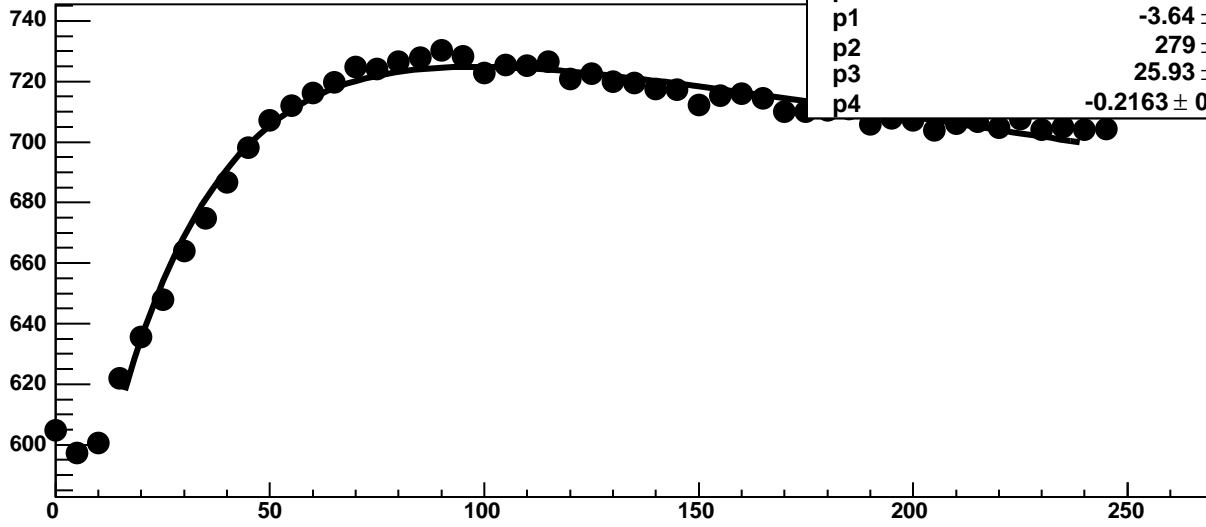
Chip 3, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

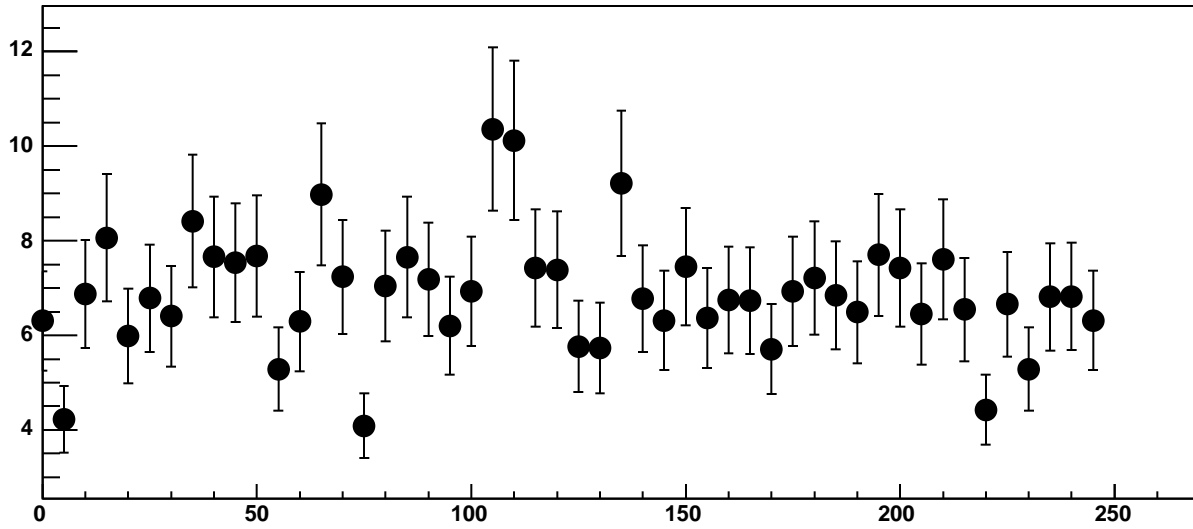


Chip 3, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold

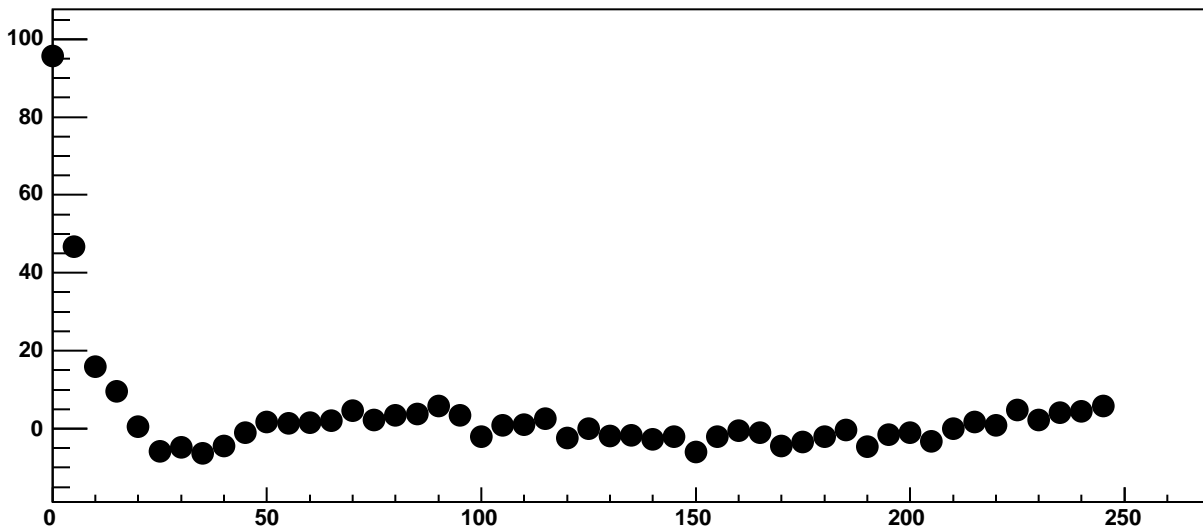


$\chi^2 / \text{ndf}$	208.5 / 41
p0	$473.3 \pm 0.7323$
p1	$-3.64 \pm 0.4864$
p2	$279 \pm 0.7514$
p3	$25.93 \pm 0.5934$
p4	$-0.2163 \pm 0.007334$

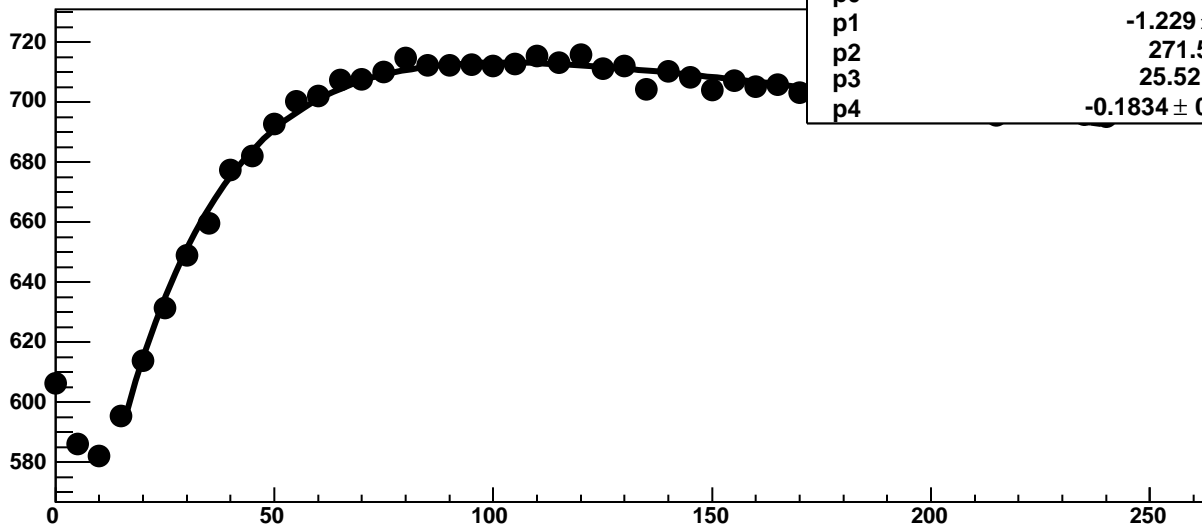
Chip 3, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold

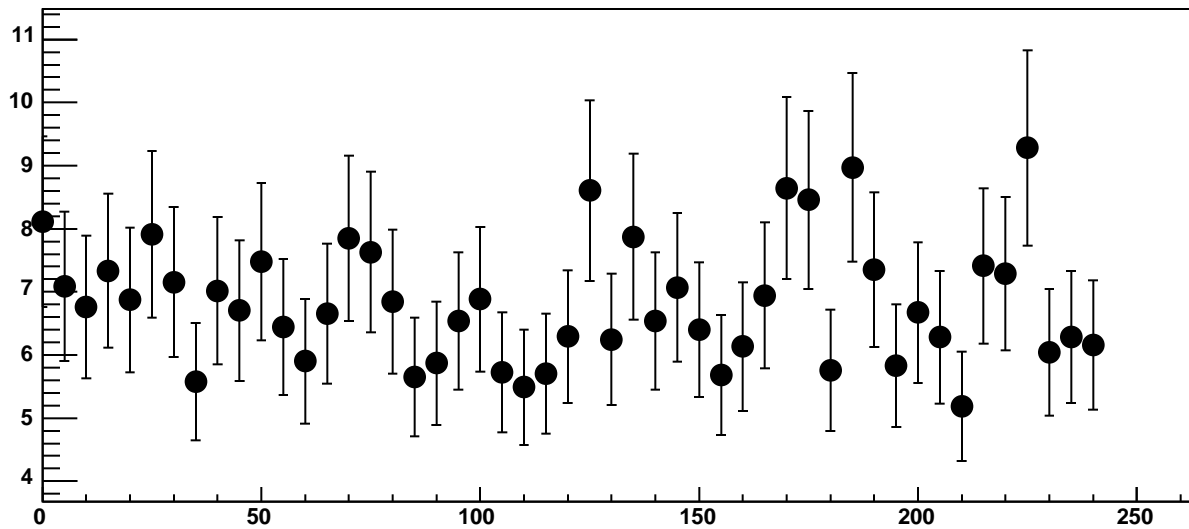


Chip 3, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold

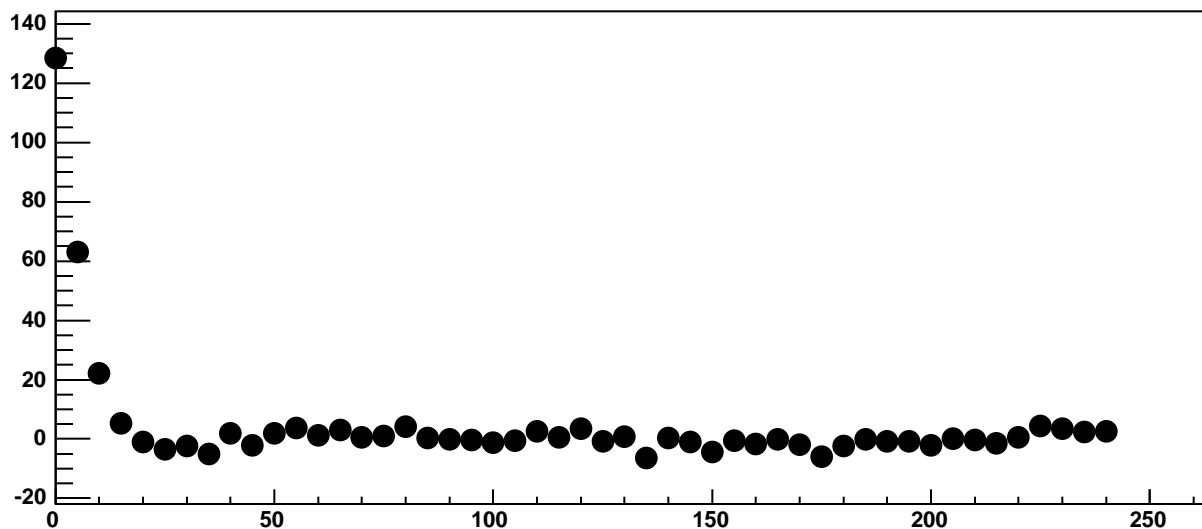


$\chi^2 / \text{ndf}$	120.8 / 41
p0	465.3 ± 2.829
p1	-1.229 ± 0.4885
p2	271.5 ± 2.882
p3	25.52 ± 0.5141
p4	-0.1834 ± 0.006763

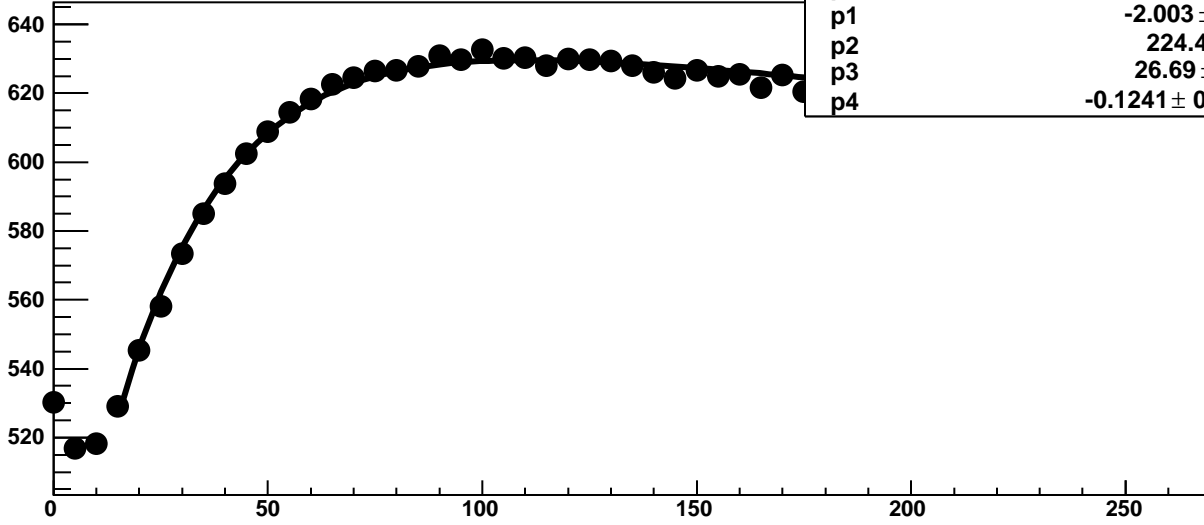
Chip 3, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold

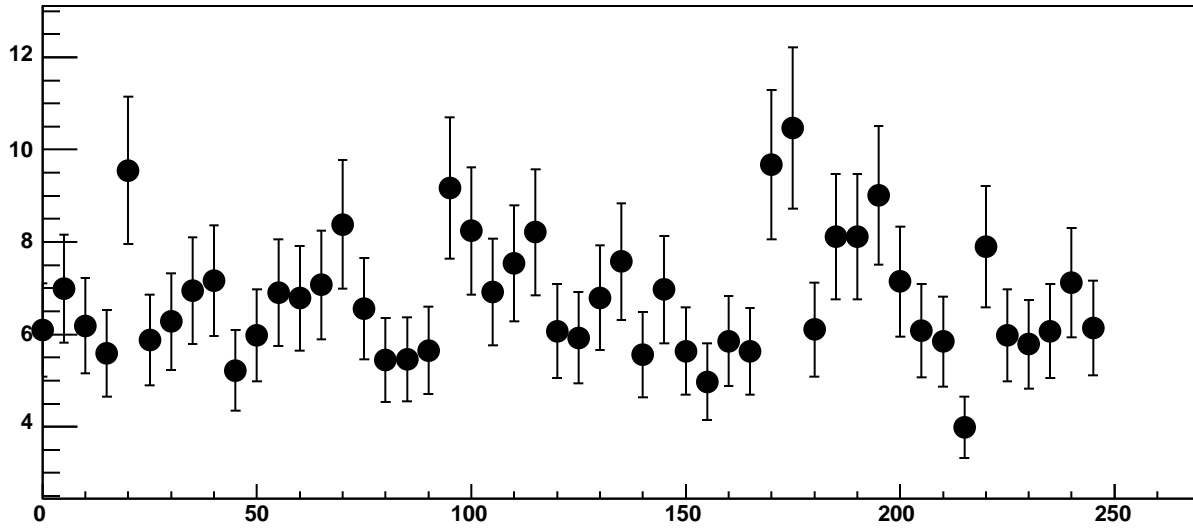


Chip 3, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold

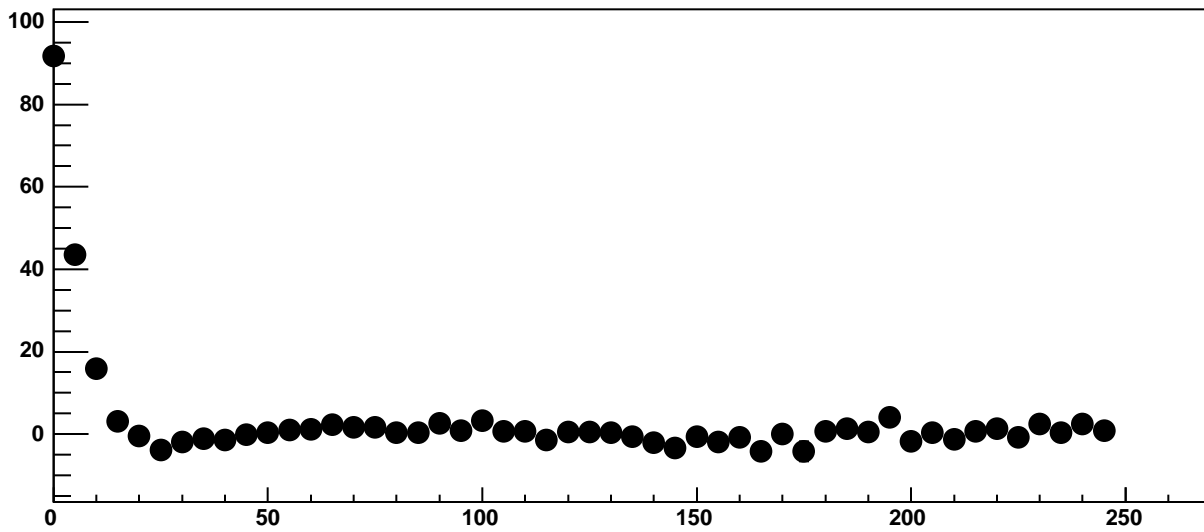


$\chi^2 / \text{ndf}$	68.26 / 41
p0	$422.5 \pm 2.759$
p1	$-2.003 \pm 0.5584$
p2	$224.4 \pm 2.807$
p3	$26.69 \pm 0.6149$
p4	$-0.1241 \pm 0.006789$

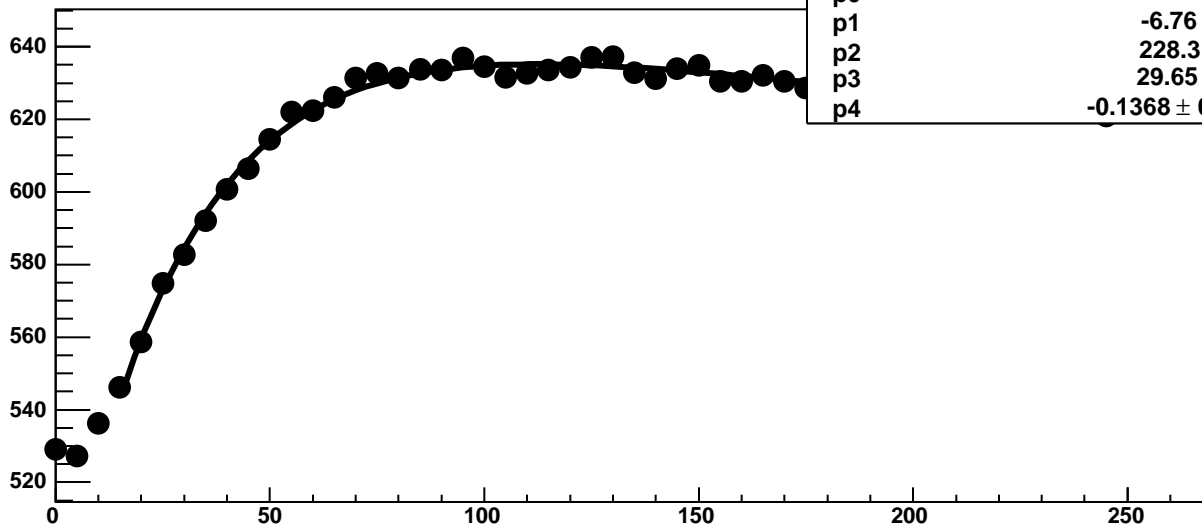
Chip 3, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

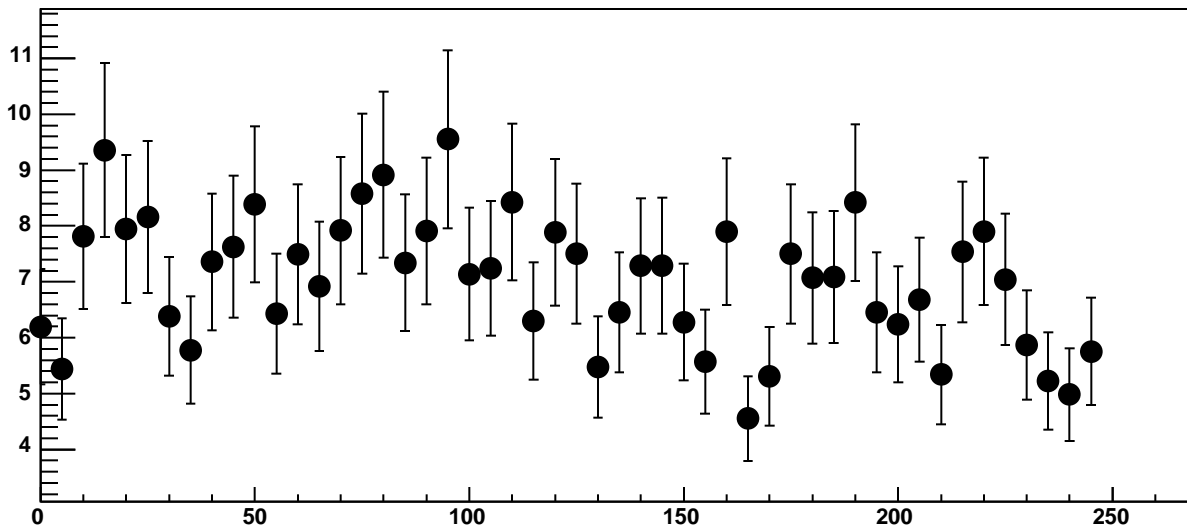


Chip 3, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold

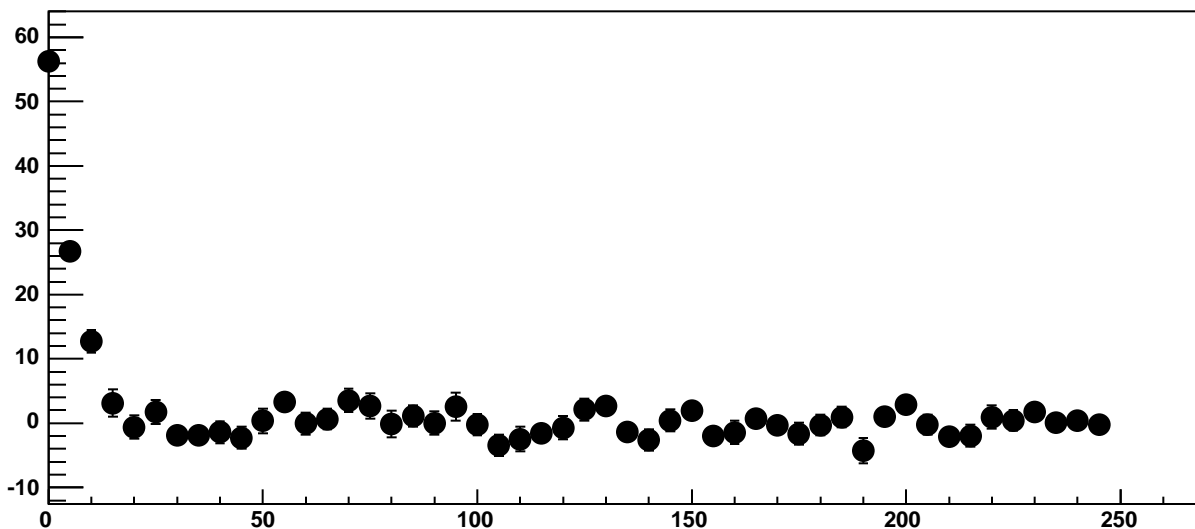


$\chi^2 / \text{ndf}$	60.53 / 41
p0	$427.3 \pm 0.9079$
p1	$-6.76 \pm 0.8287$
p2	$228.3 \pm 0.9221$
p3	$29.65 \pm 0.9902$
p4	$-0.1368 \pm 0.008642$

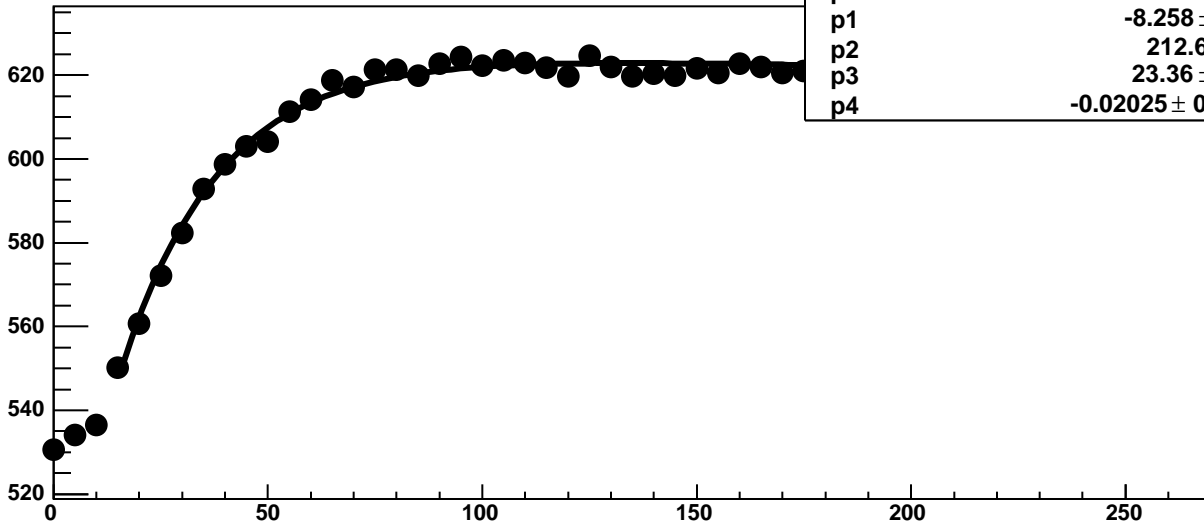
Chip 3, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold

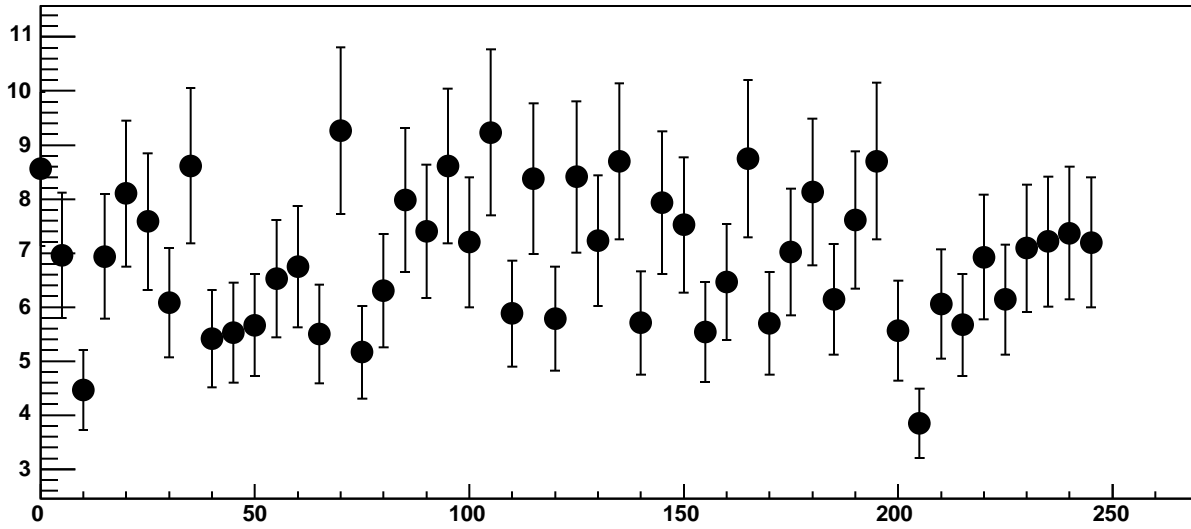


Chip 3, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

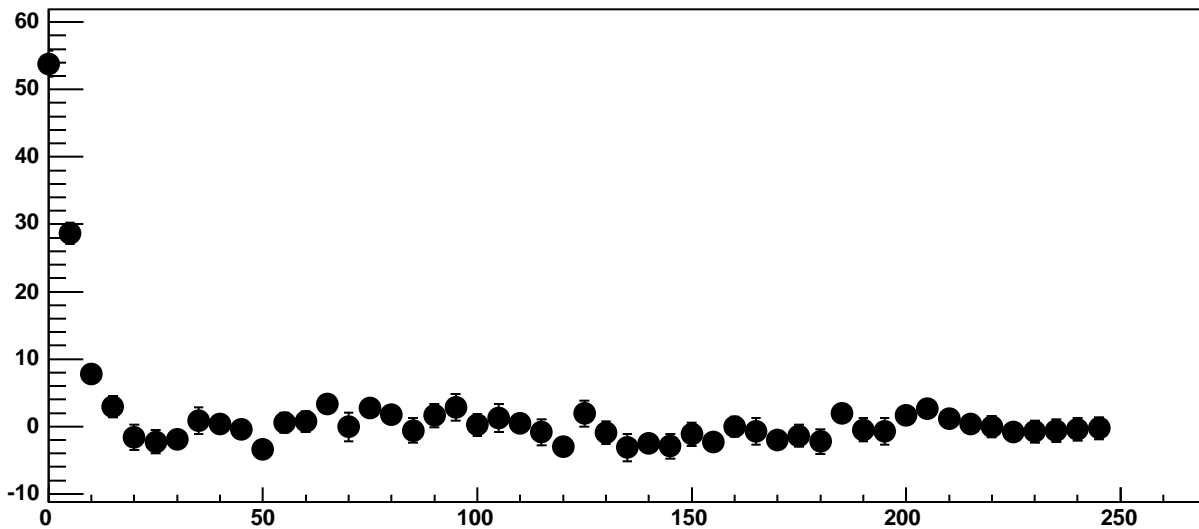


$\chi^2 / \text{ndf}$	70.92 / 41
p0	$413.7 \pm 2.787$
p1	$-8.258 \pm 0.9484$
p2	$212.6 \pm 2.795$
p3	$23.36 \pm 0.8065$
p4	$-0.02025 \pm 0.006394$

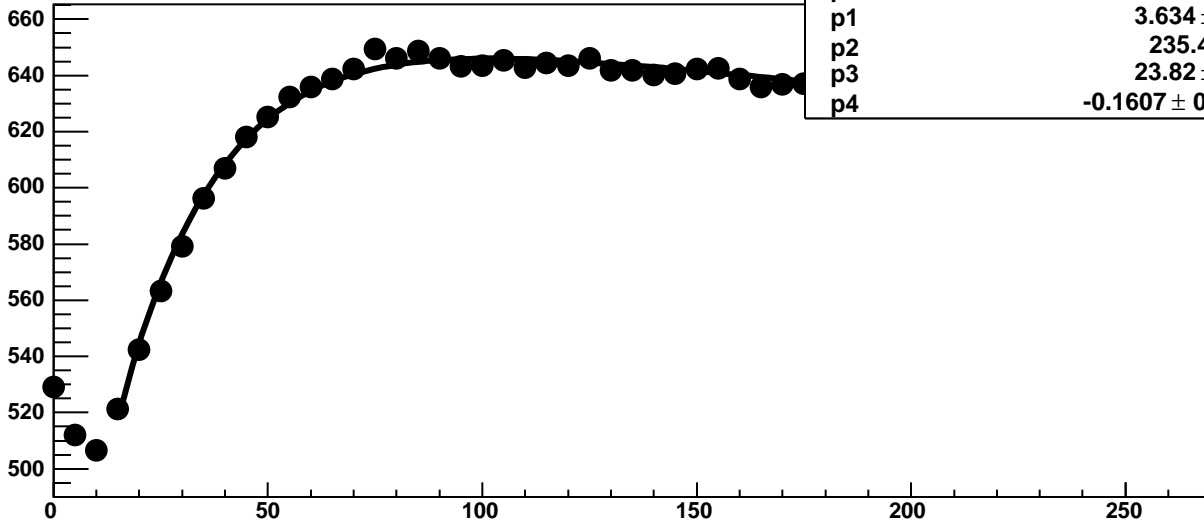
Chip 3, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

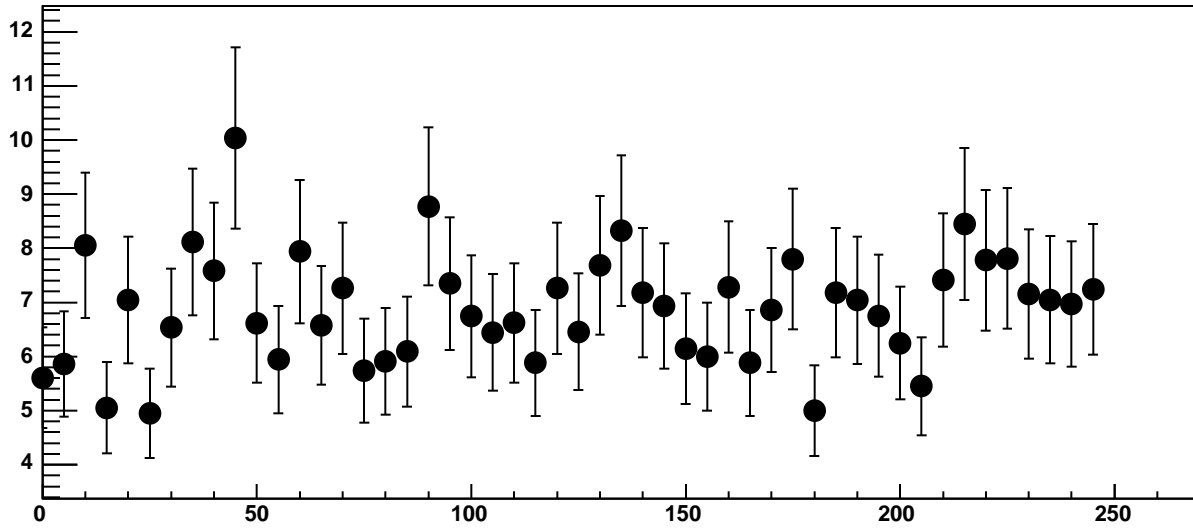


Chip 3, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

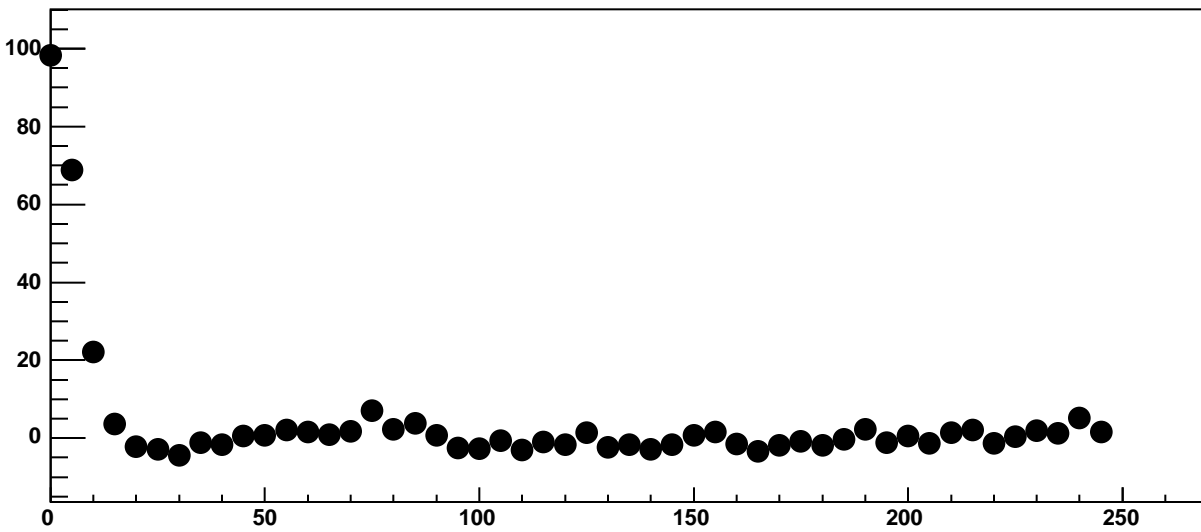


$\chi^2 / \text{ndf}$	122.6 / 41
p0	$430.2 \pm 2.876$
p1	$3.634 \pm 0.3947$
p2	$235.4 \pm 2.931$
p3	$23.82 \pm 0.4573$
p4	$-0.1607 \pm 0.006797$

Chip 3, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold

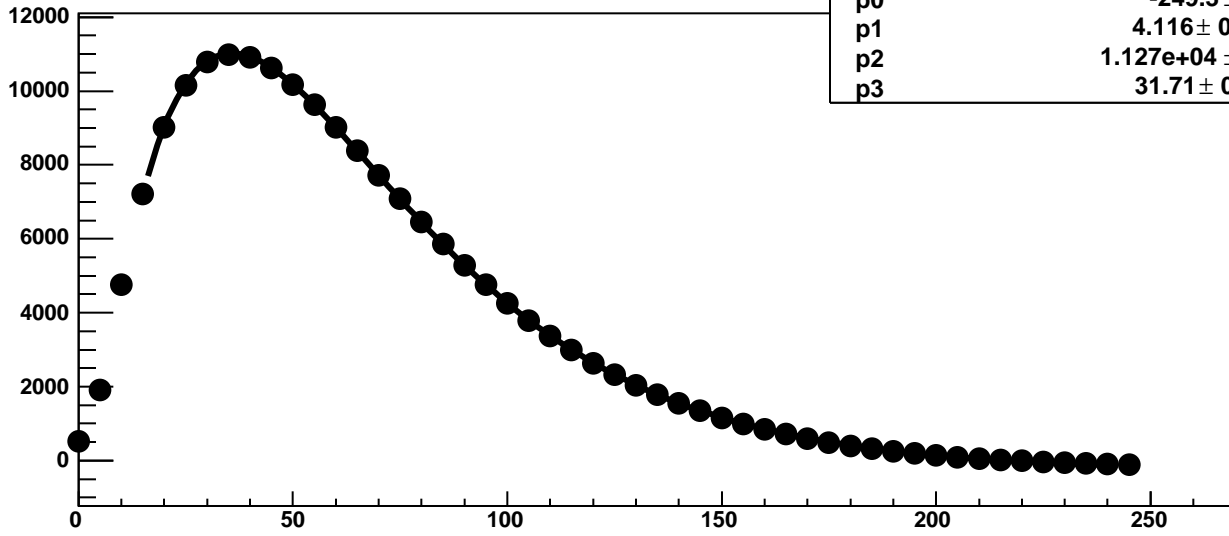


Chip 3, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold



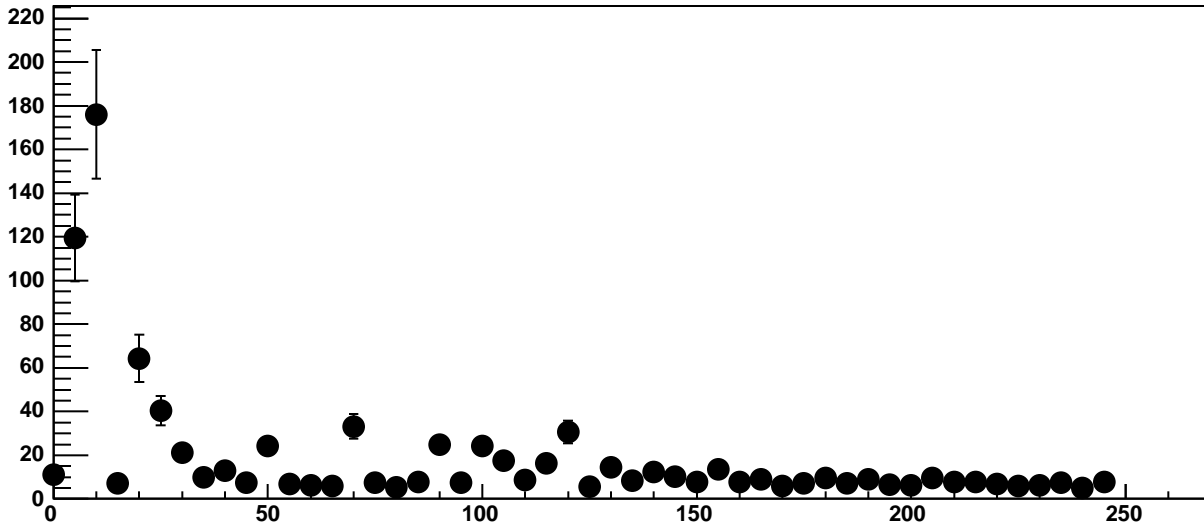


Chip 3, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold

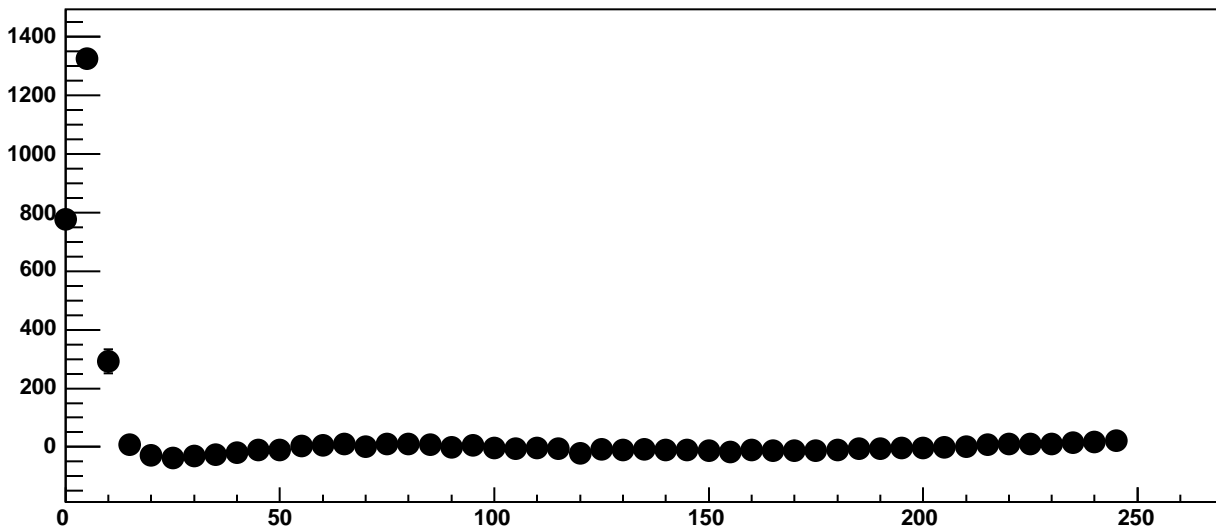


$\chi^2 / \text{ndf}$	1367 / 42
p0	-249.3 ± 0.5305
p1	4.116 ± 0.004847
p2	1.127e+04 ± 0.9324
p3	31.71 ± 0.004582

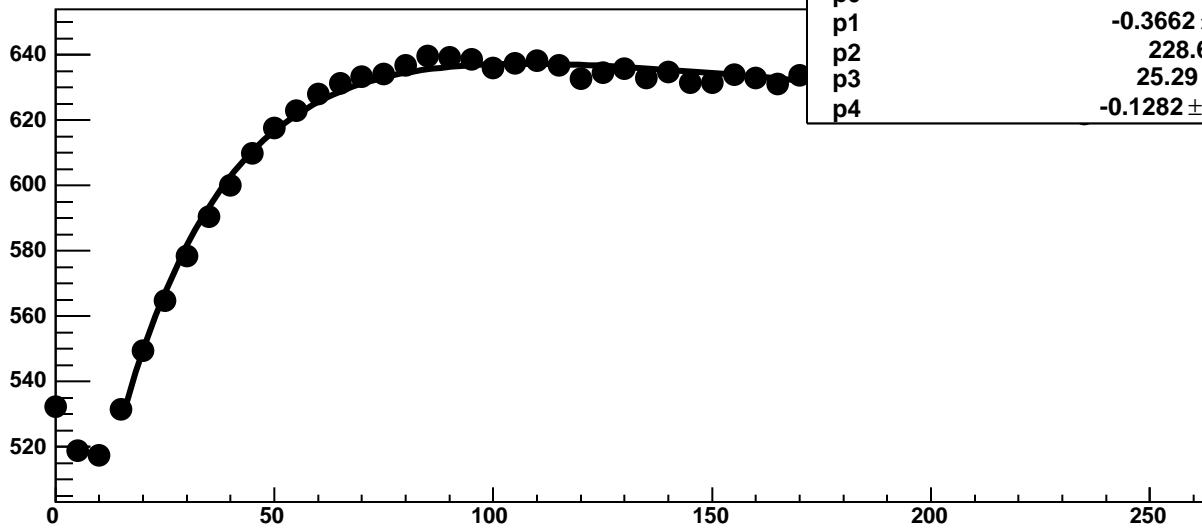
Chip 3, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold

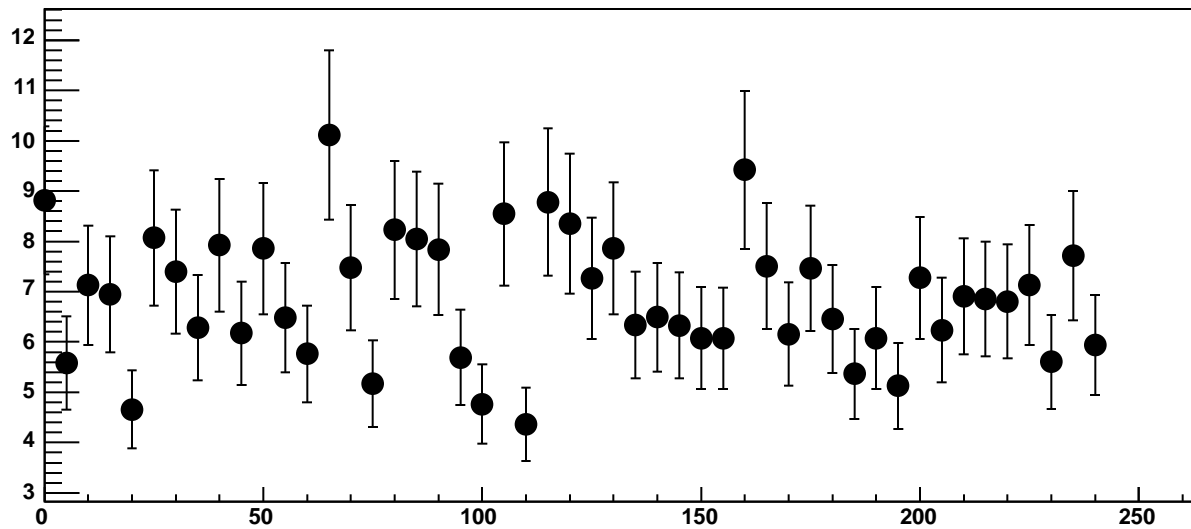


Chip 3, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

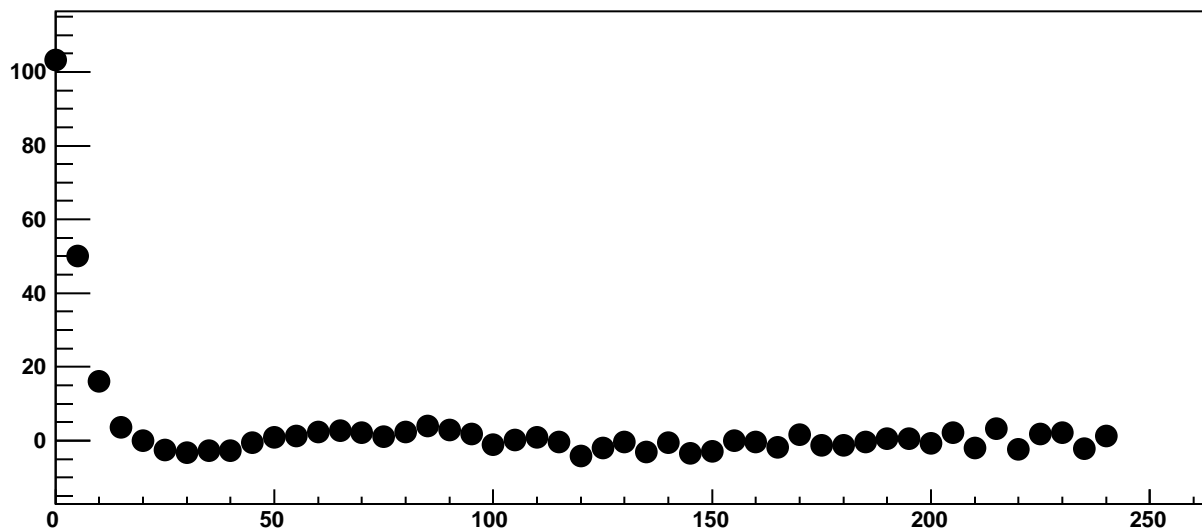


$\chi^2 / \text{ndf}$	79.09 / 41
p0	$425.8 \pm 2.798$
p1	$-0.3662 \pm 0.5205$
p2	$228.6 \pm 2.844$
p3	$25.29 \pm 0.5868$
p4	$-0.1282 \pm 0.00674$

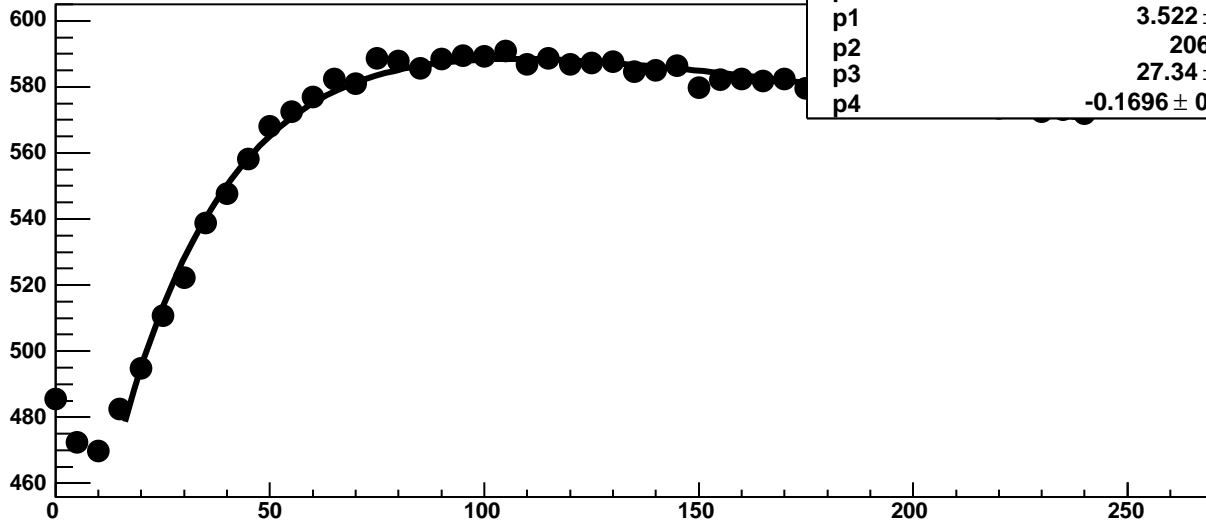
Chip 3, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

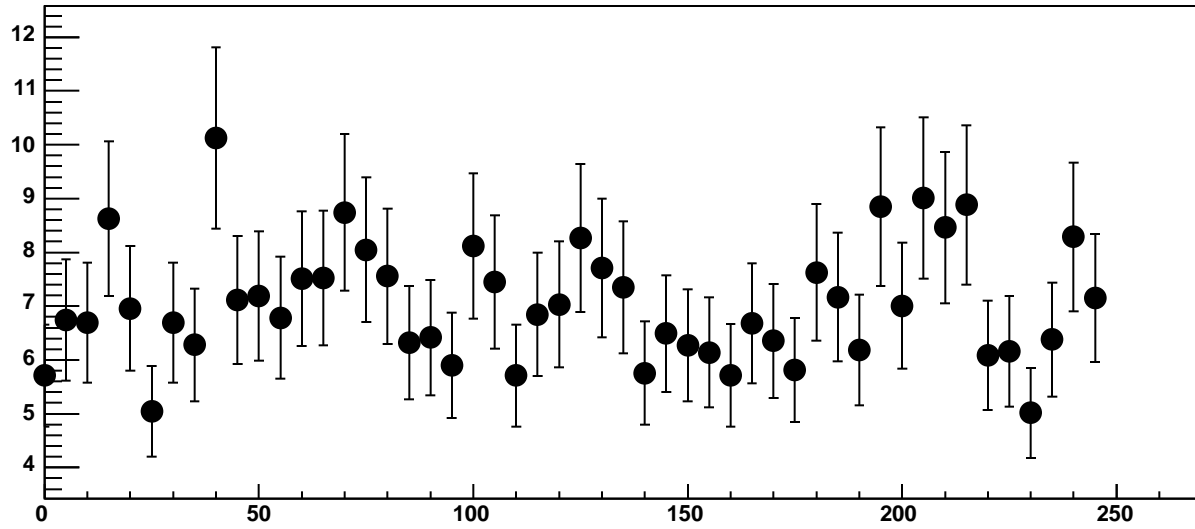


Chip 3, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

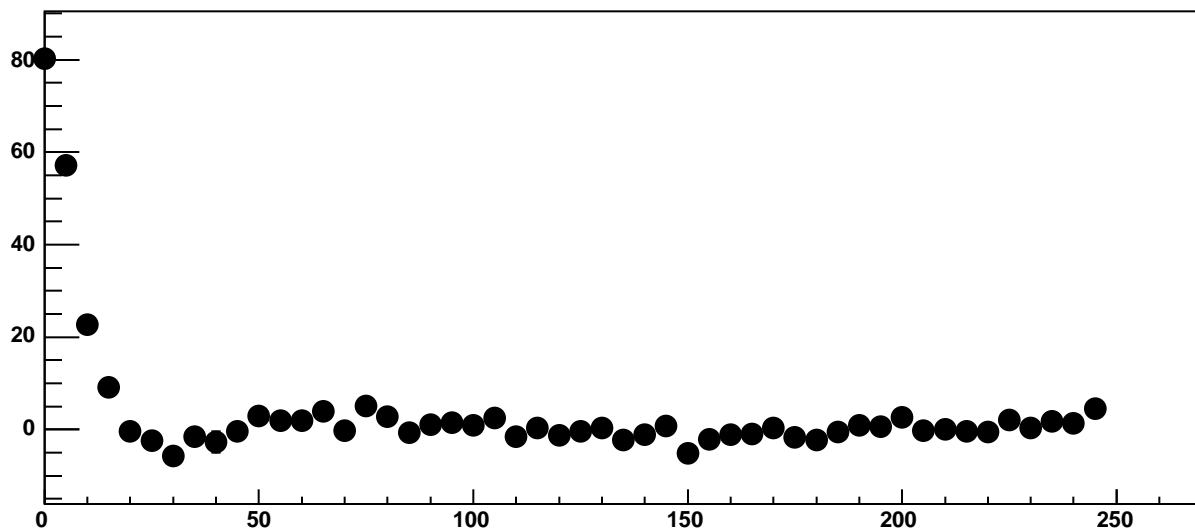


$\chi^2 / \text{ndf}$	99.85 / 41
p0	$404.7 \pm 2.936$
p1	$3.522 \pm 0.5527$
p2	$206 \pm 3.013$
p3	$27.34 \pm 0.6459$
p4	$-0.1696 \pm 0.007497$

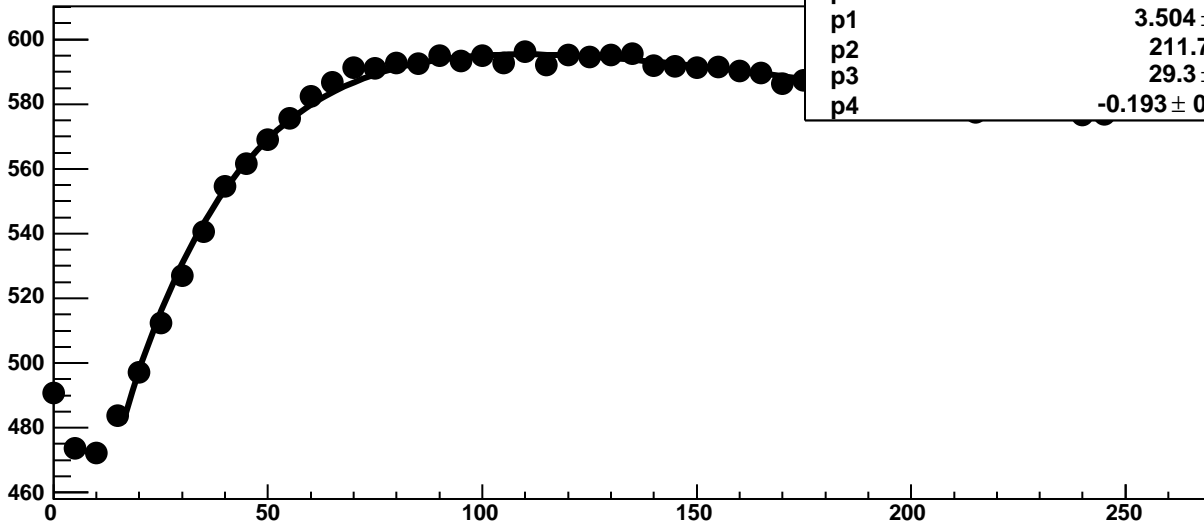
Chip 3, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold

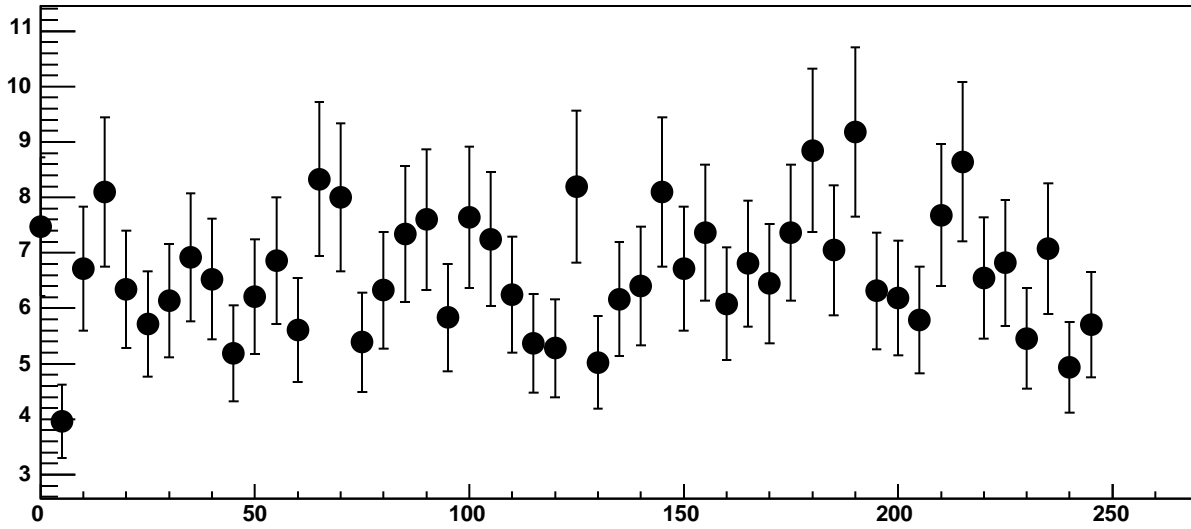


Chip 3, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold

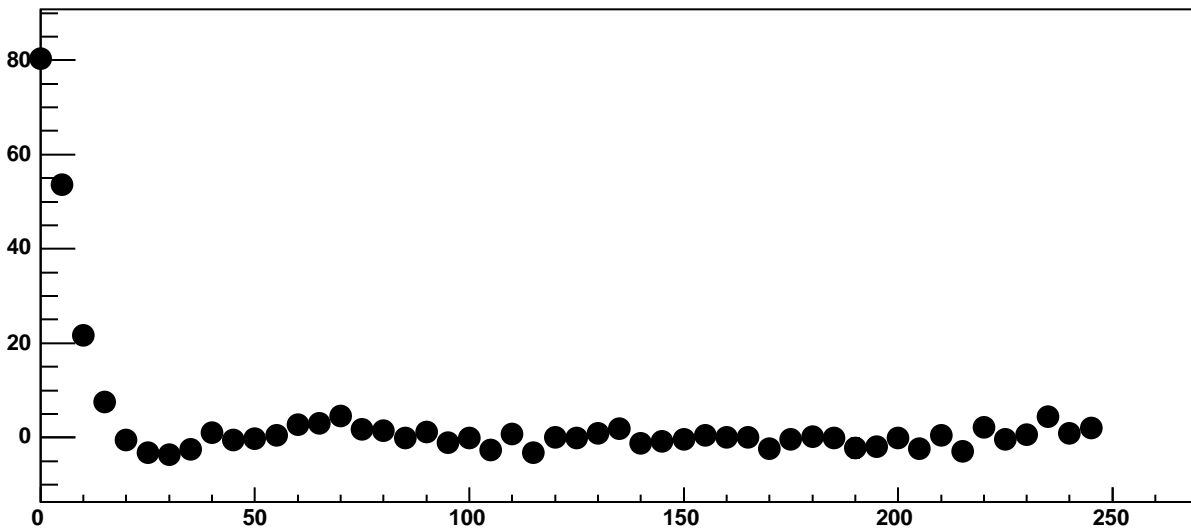


$\chi^2 / \text{ndf}$	84.49 / 41
p0	$409.8 \pm 2.783$
p1	$3.504 \pm 0.5233$
p2	$211.7 \pm 2.872$
p3	$29.3 \pm 0.6212$
p4	$-0.193 \pm 0.007446$

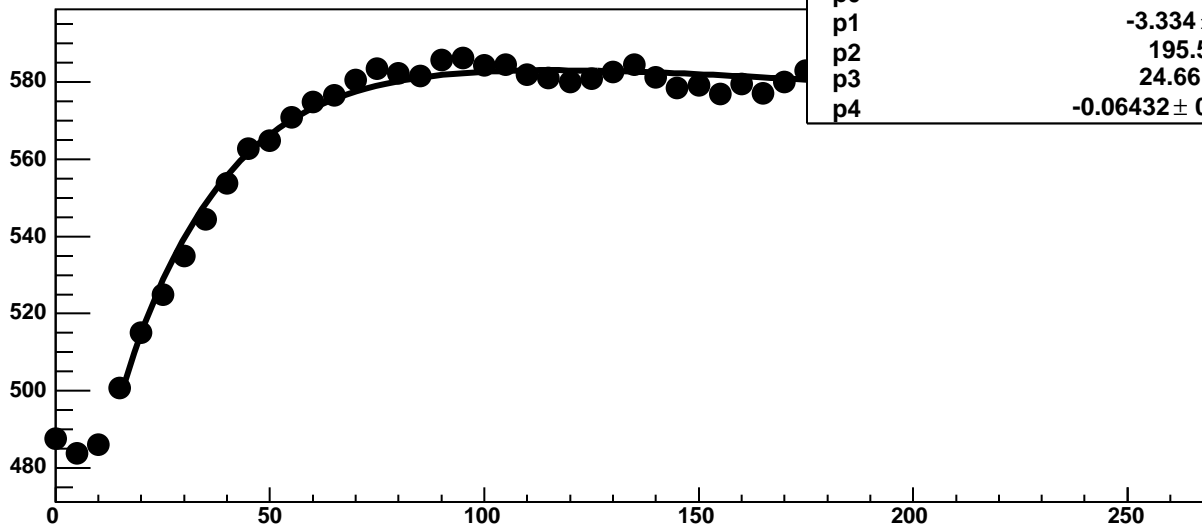
Chip 3, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold

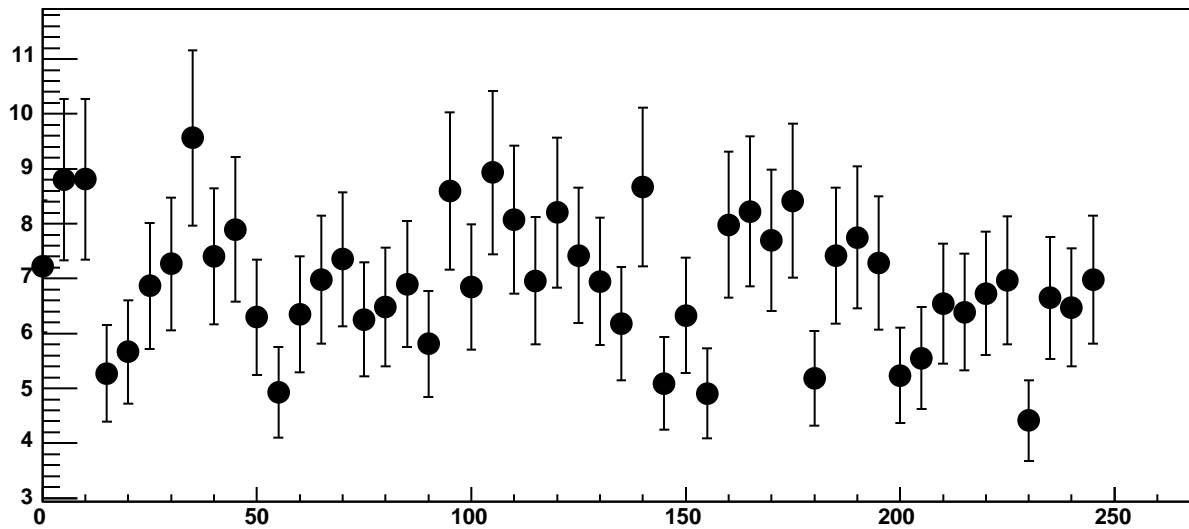


Chip 3, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold

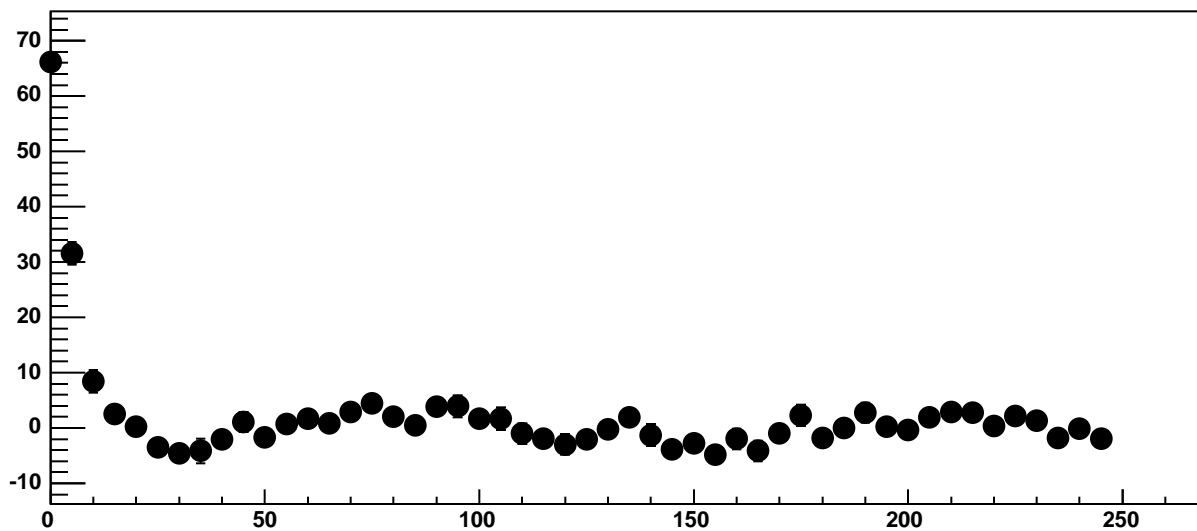


$\chi^2 / \text{ndf}$	122.7 / 41
p0	$396.8 \pm 2.804$
p1	$-3.334 \pm 0.6596$
p2	$195.5 \pm 2.832$
p3	$24.66 \pm 0.7091$
p4	$-0.06432 \pm 0.006433$

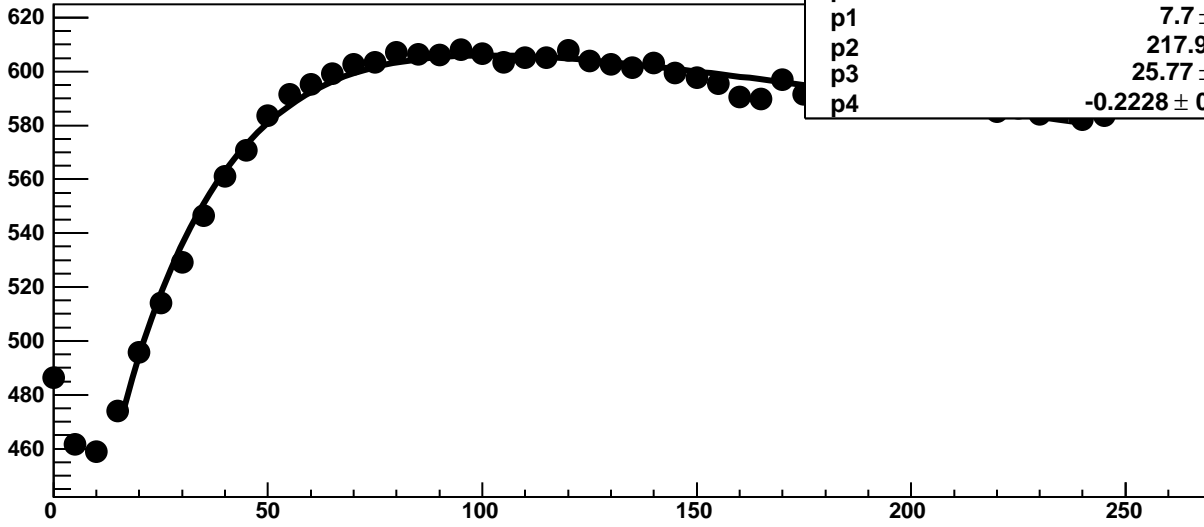
Chip 3, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold

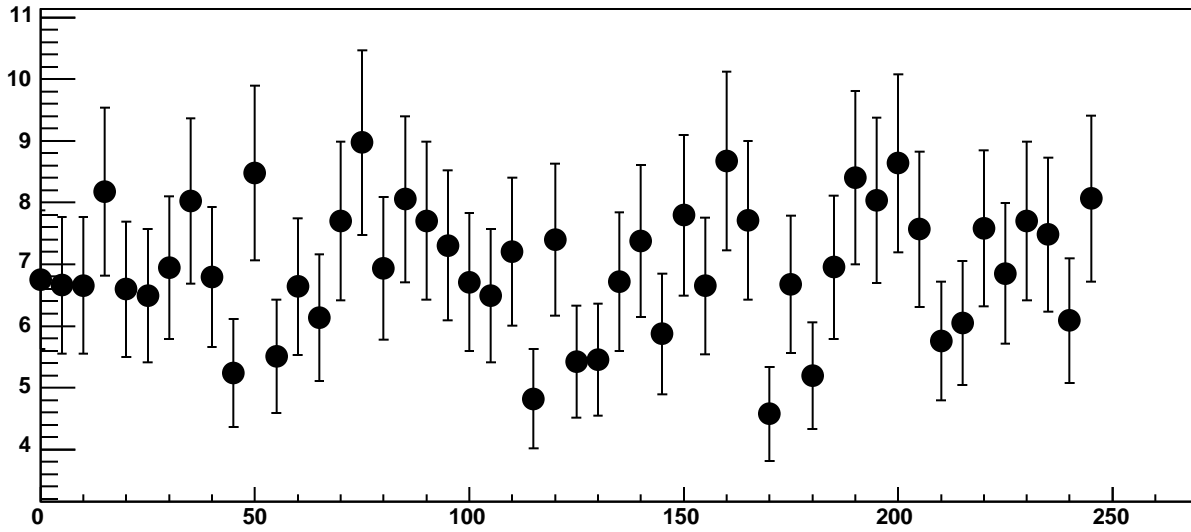


Chip 3, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

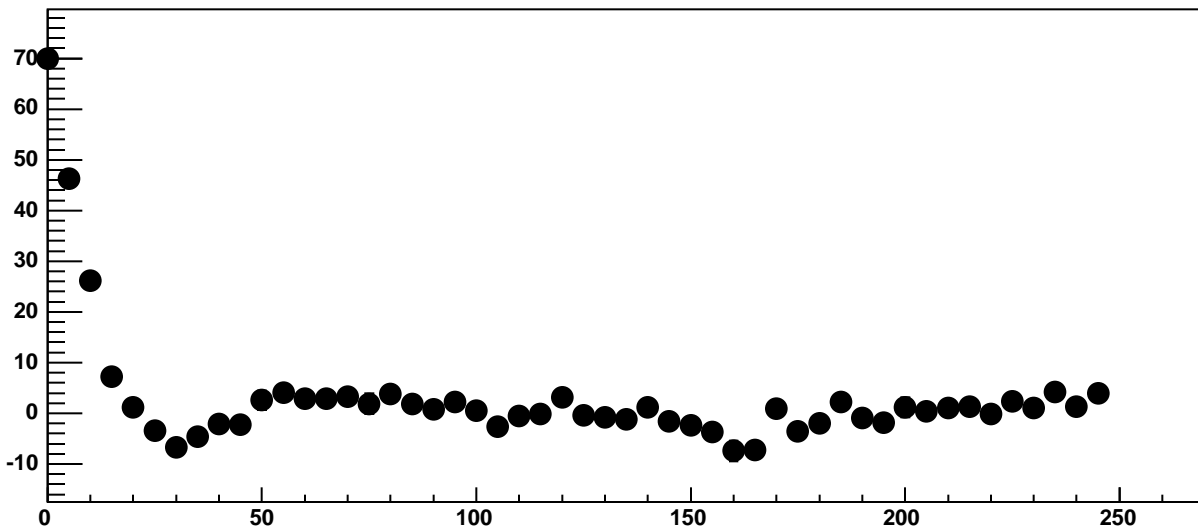


$\chi^2 / \text{ndf}$	153.9 / 41
p0	$414.7 \pm 2.872$
p1	$7.7 \pm 0.4275$
p2	$217.9 \pm 2.959$
p3	$25.77 \pm 0.4686$
p4	$-0.2228 \pm 0.007162$

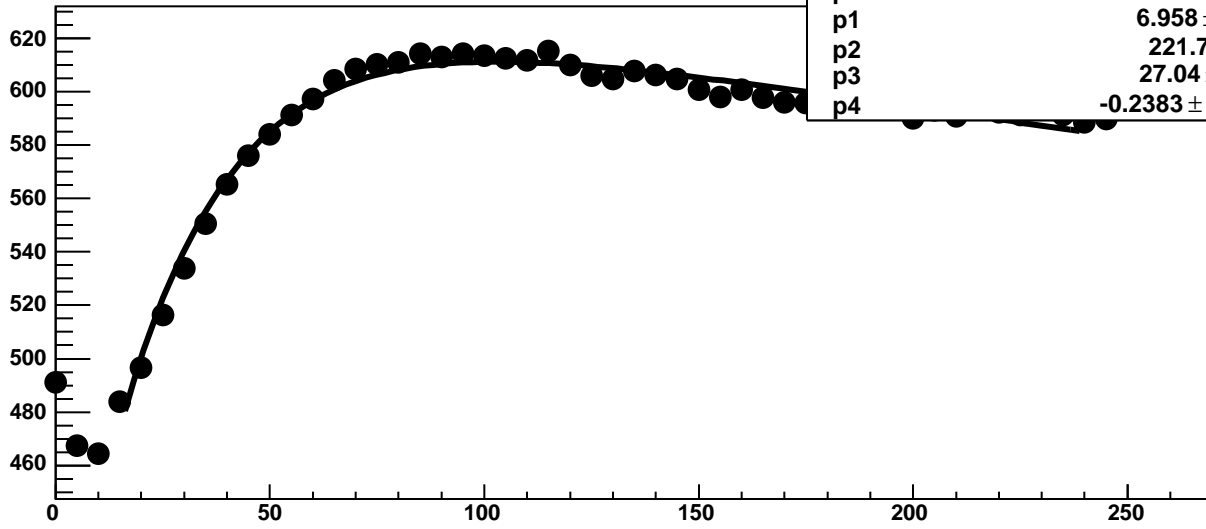
Chip 3, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

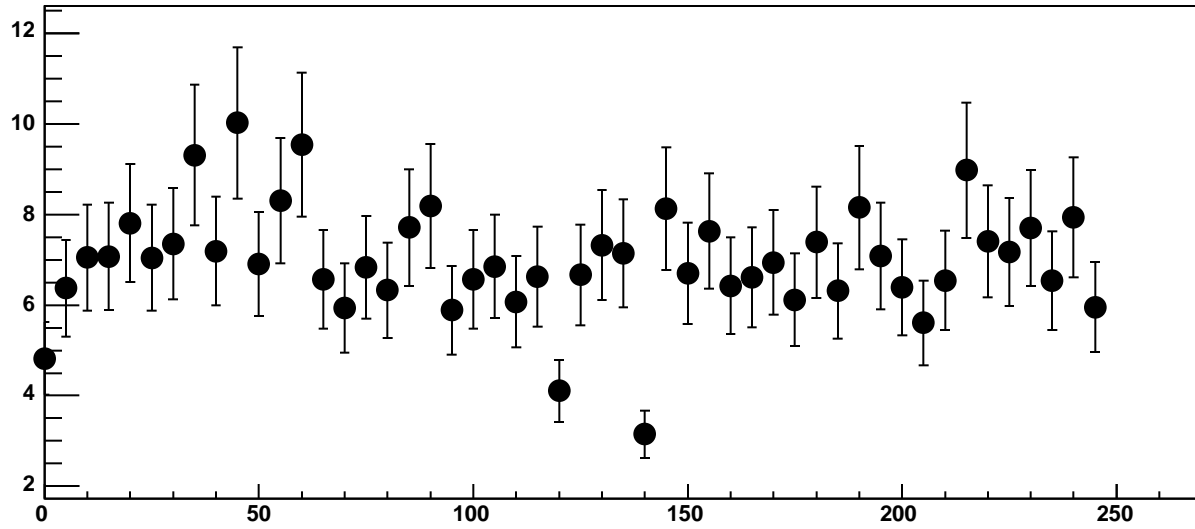


Chip 3, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

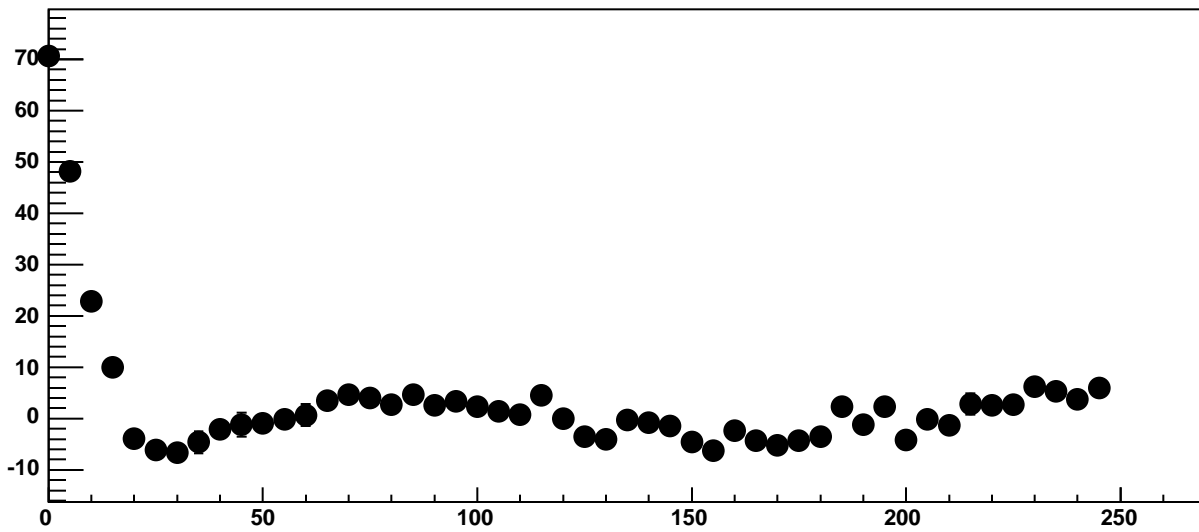


$\chi^2 / \text{ndf}$	253 / 41
p0	$418.8 \pm 2.843$
p1	$6.958 \pm 0.4372$
p2	$221.7 \pm 2.935$
p3	$27.04 \pm 0.4991$
p4	$-0.2383 \pm 0.00733$

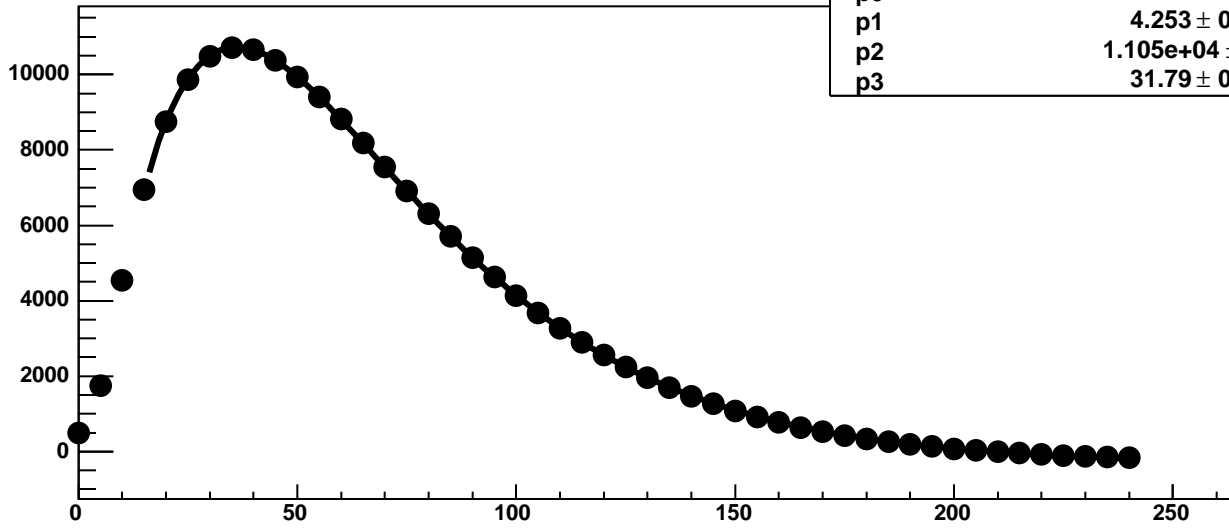
Chip 3, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

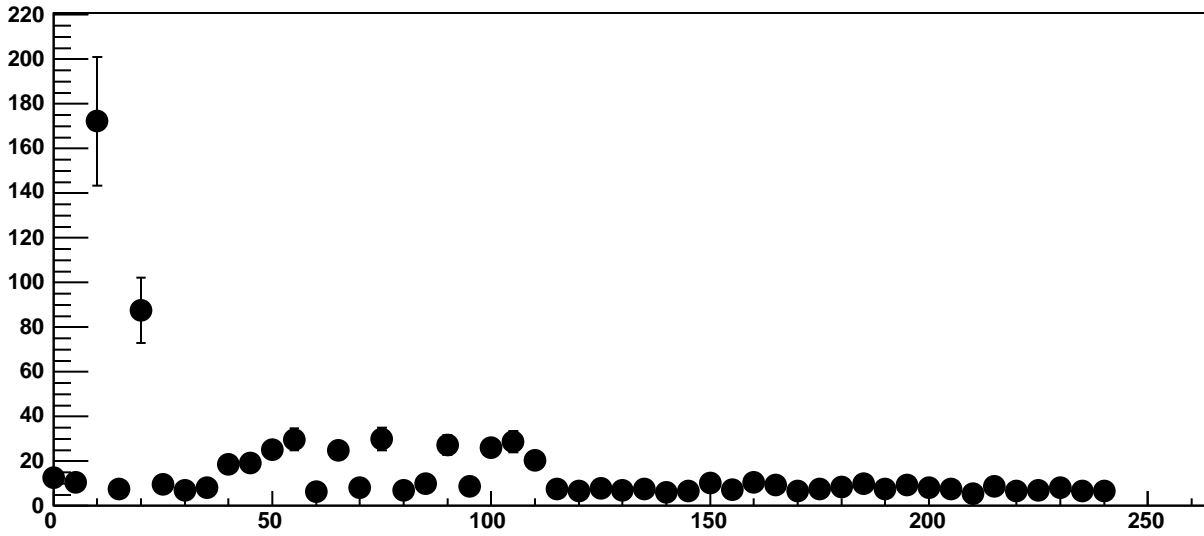


Chip 3, Channel 6, Enable 5!, DAC=4000, ADC Mean vs Hold

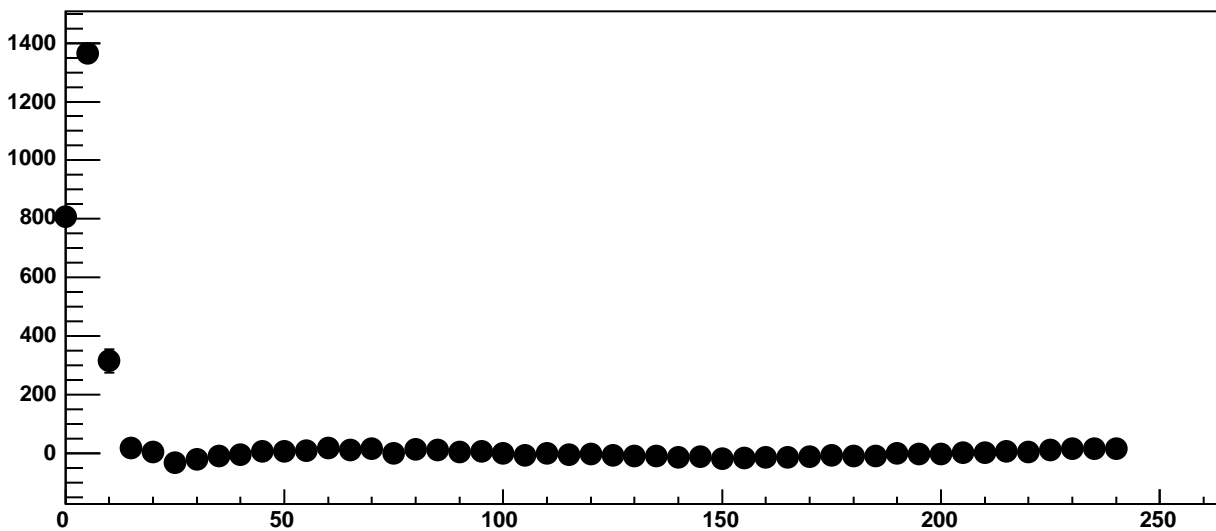


$\chi^2 / \text{ndf}$	1725 / 42
p0	-313.4 ± 0.5628
p1	4.253 ± 0.005207
p2	1.105e+04 ± 0.9467
p3	31.79 ± 0.004524

Chip 3, Channel 6, Enable 5!, DAC=4000, ADC Noise vs Hold

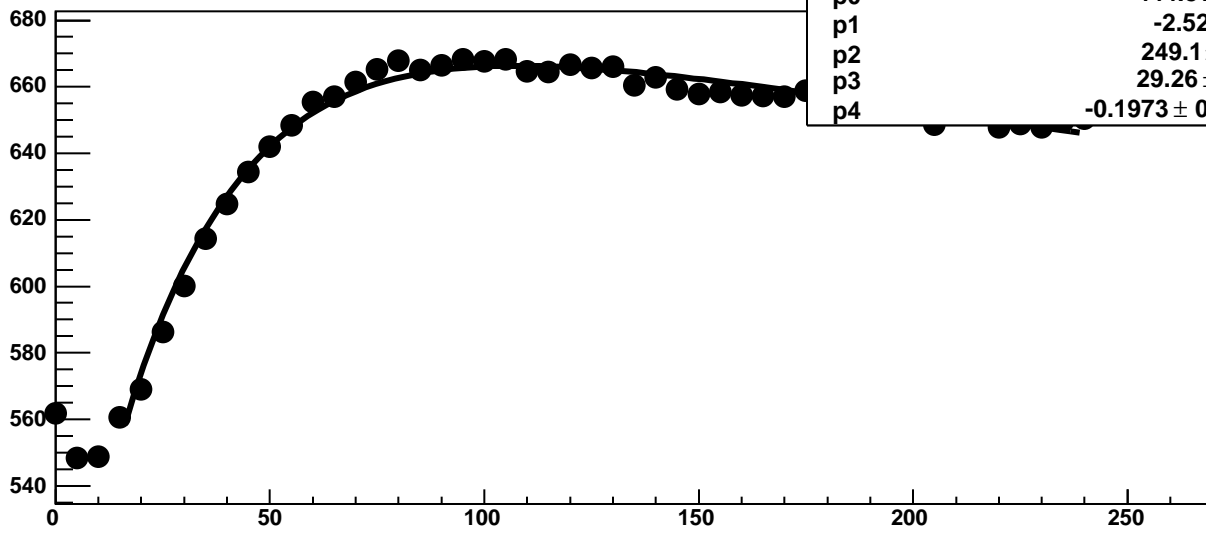


Chip 3, Channel 6, Enable 5!, DAC=4000, ADC Residuals vs Hold



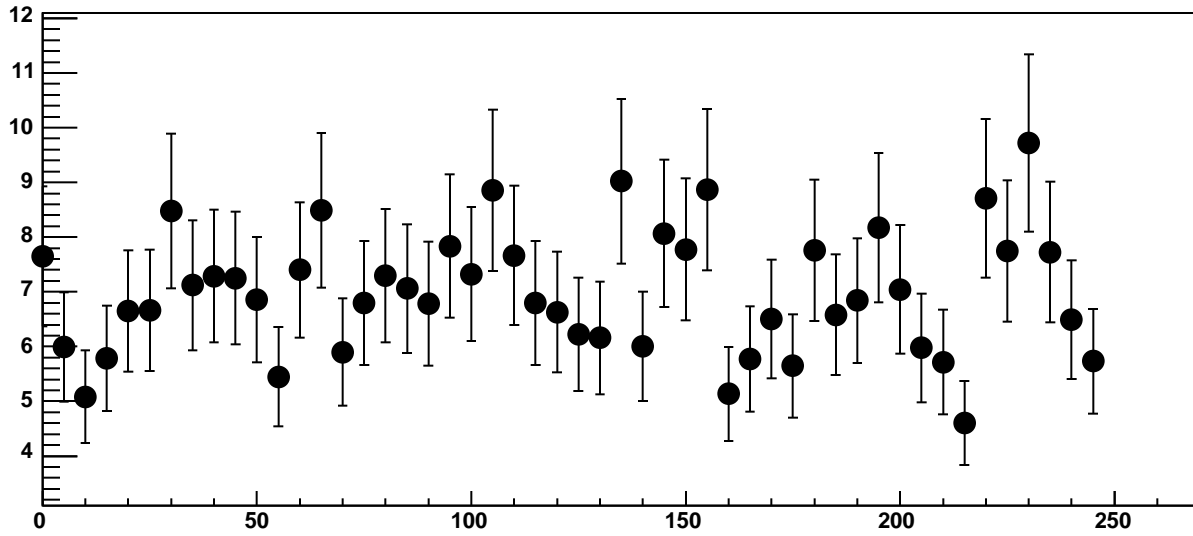


Chip 3, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

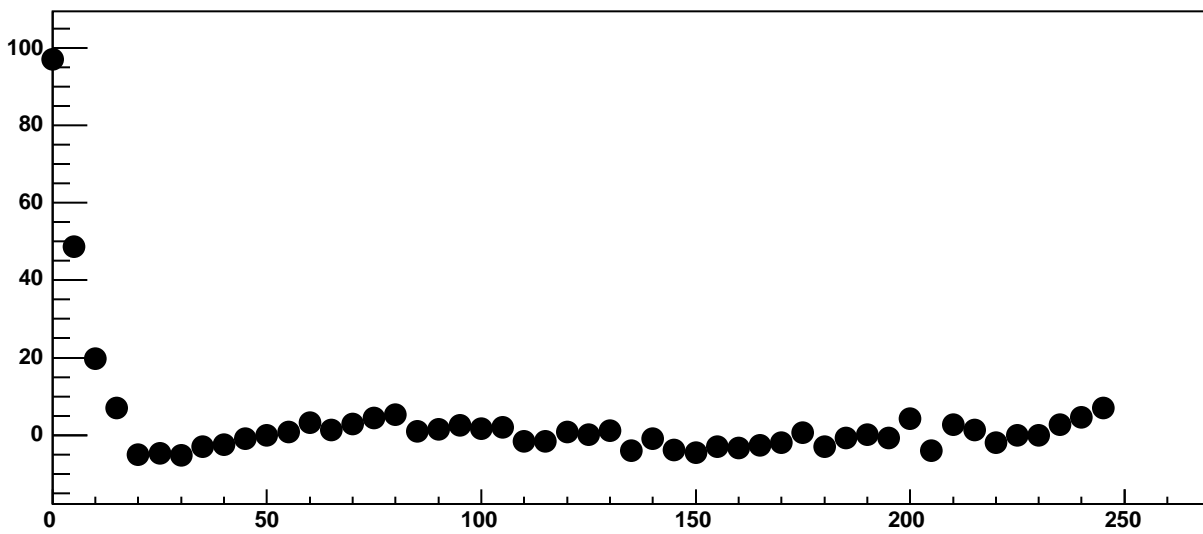


$\chi^2 / \text{ndf}$	165.6 / 41
p0	$444.8 \pm 0.8399$
p1	$-2.52 \pm 0.425$
p2	$249.1 \pm 0.9091$
p3	$29.26 \pm 0.6878$
p4	$-0.1973 \pm 0.008794$

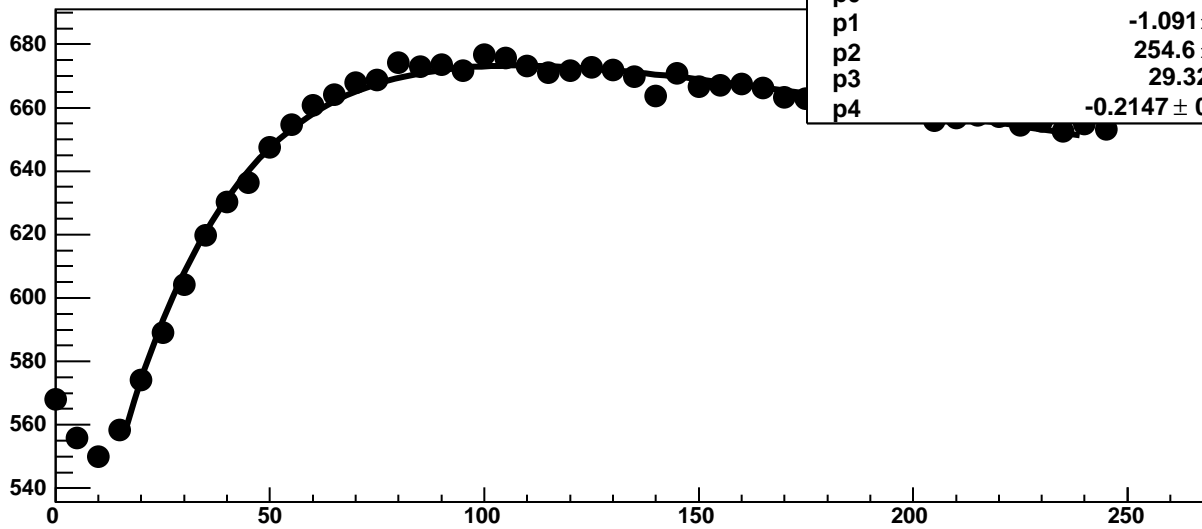
Chip 3, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

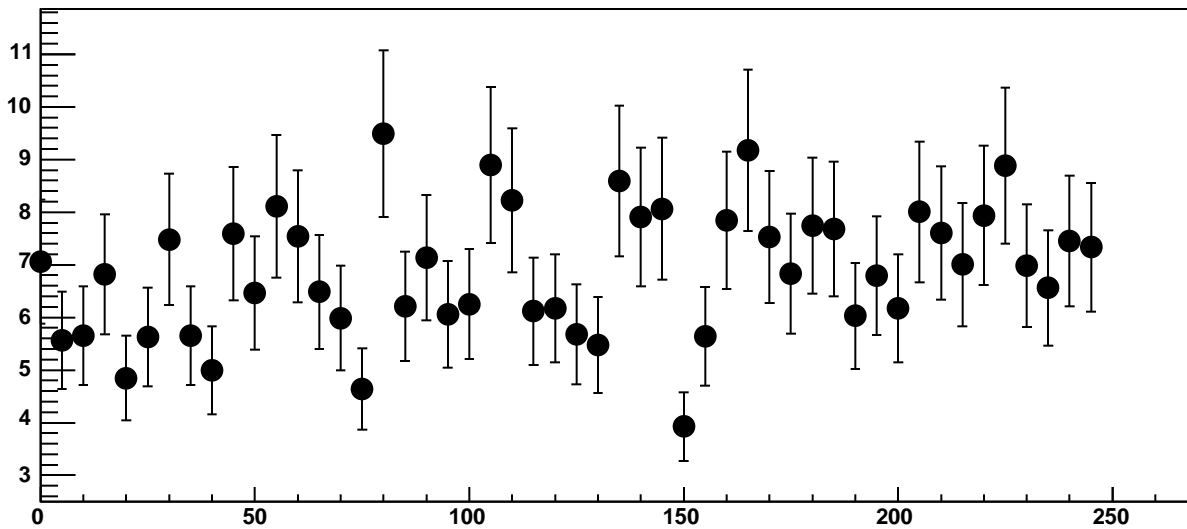


Chip 3, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold

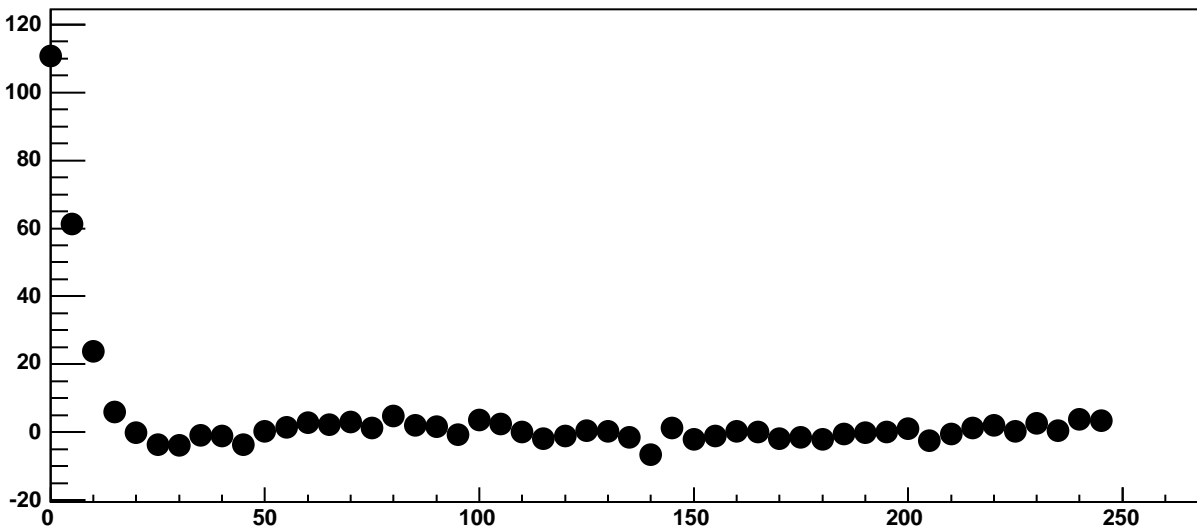


$\chi^2 / \text{ndf}$	99.46 / 41
p0	$448.3 \pm 0.8057$
p1	$-1.091 \pm 0.3794$
p2	$254.6 \pm 0.8966$
p3	$29.32 \pm 0.636$
p4	$-0.2147 \pm 0.008775$

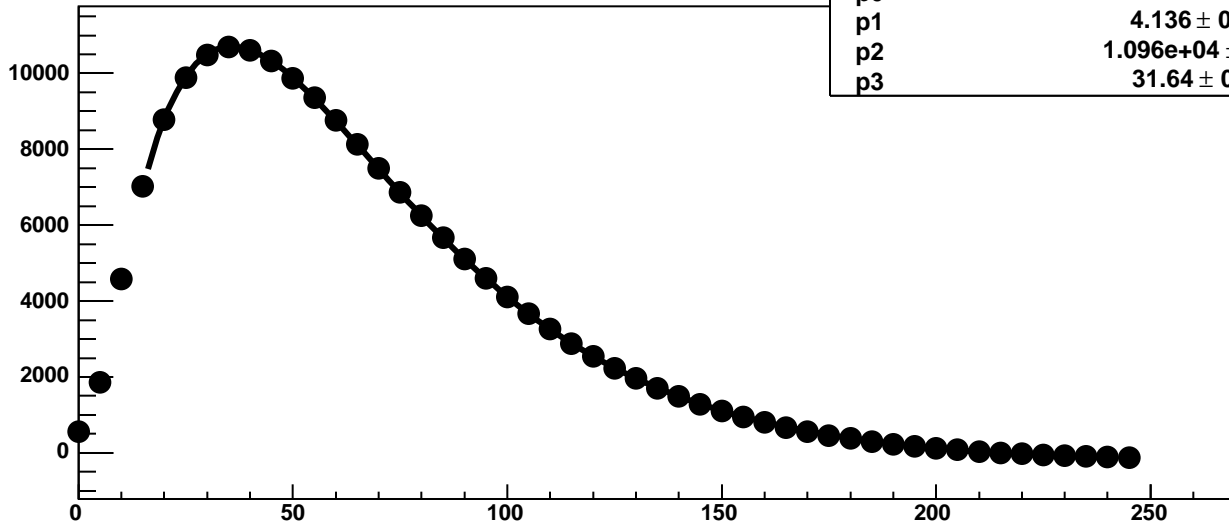
Chip 3, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

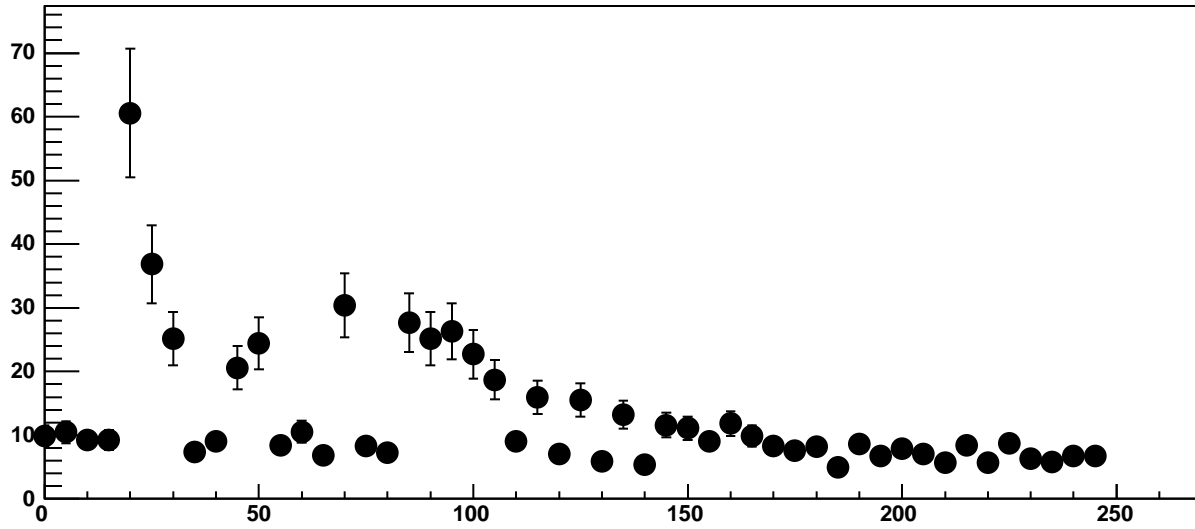


Chip 3, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold

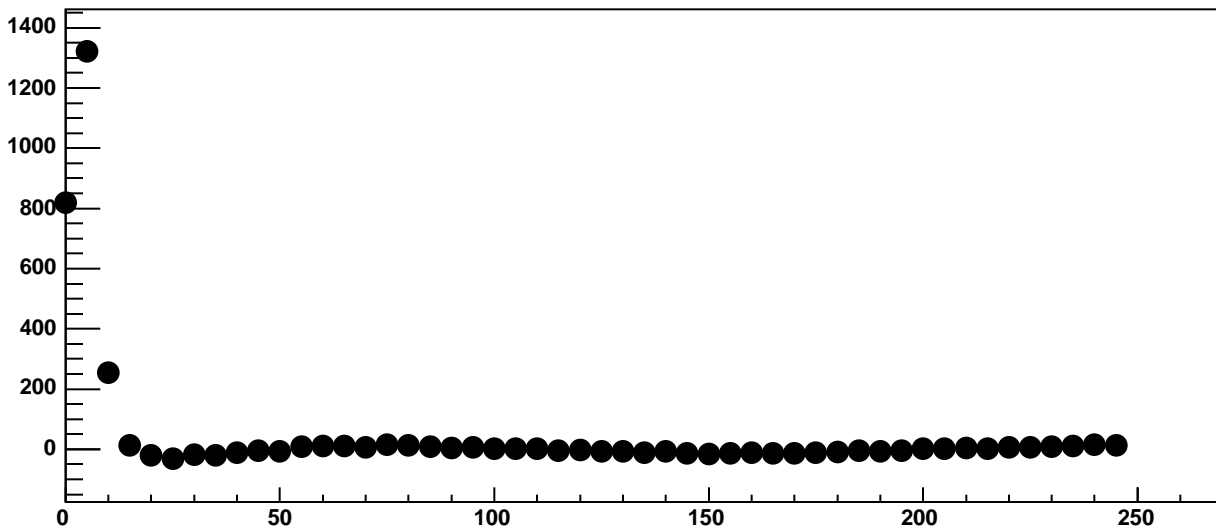


$\chi^2 / \text{ndf}$	1116 / 42
p0	-253.2 ± 0.5402
p1	4.136 ± 0.005994
p2	1.096e+04 ± 0.9922
p3	31.64 ± 0.005002

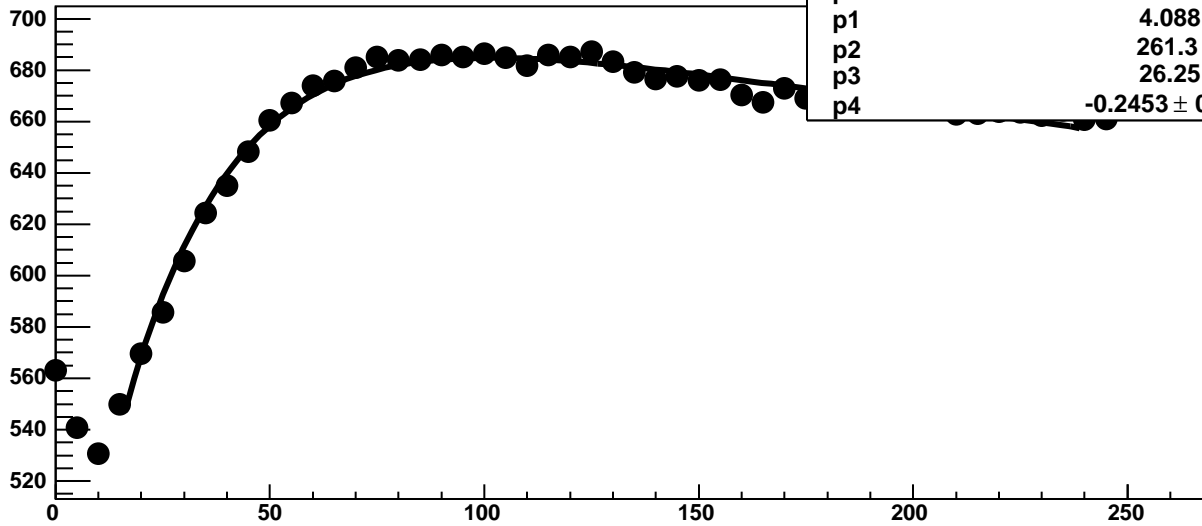
Chip 3, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold

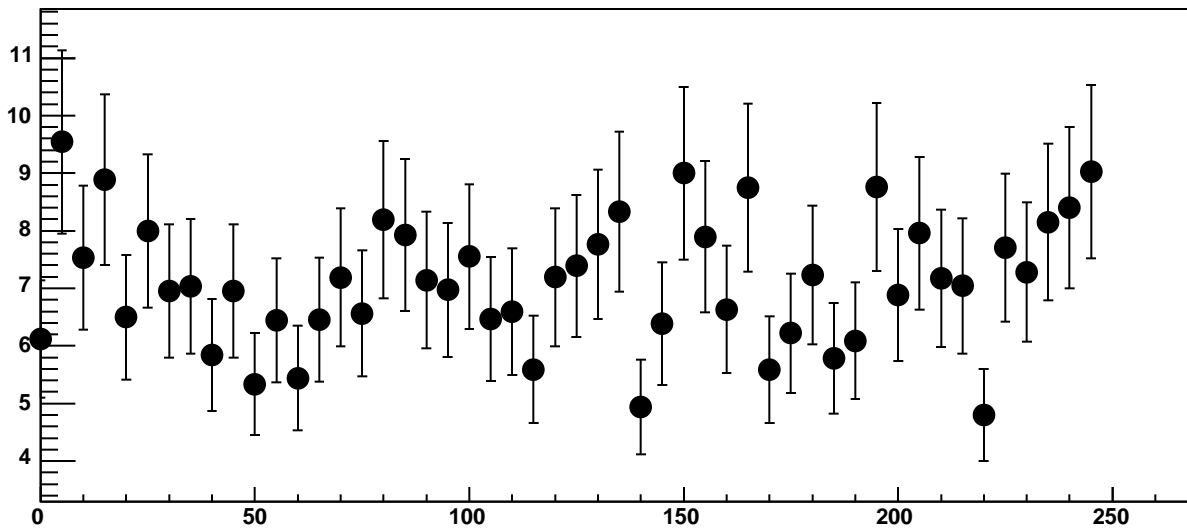


Chip 3, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

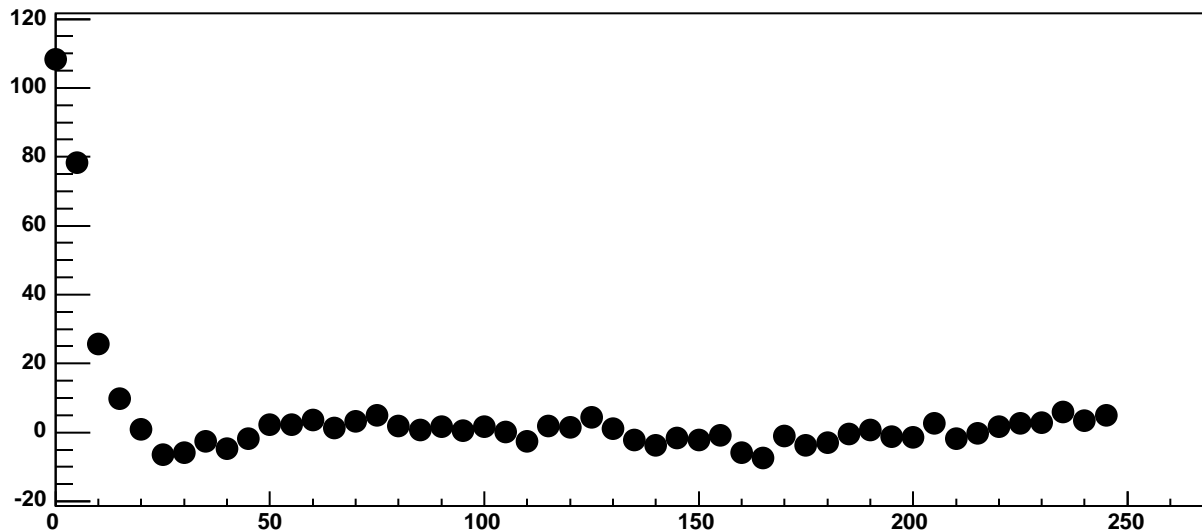


$\chi^2 / \text{ndf}$	190.2 / 41
p0	$453.8 \pm 0.706$
p1	$4.088 \pm 0.3031$
p2	$261.3 \pm 0.7932$
p3	$26.25 \pm 0.4891$
p4	$-0.2453 \pm 0.008054$

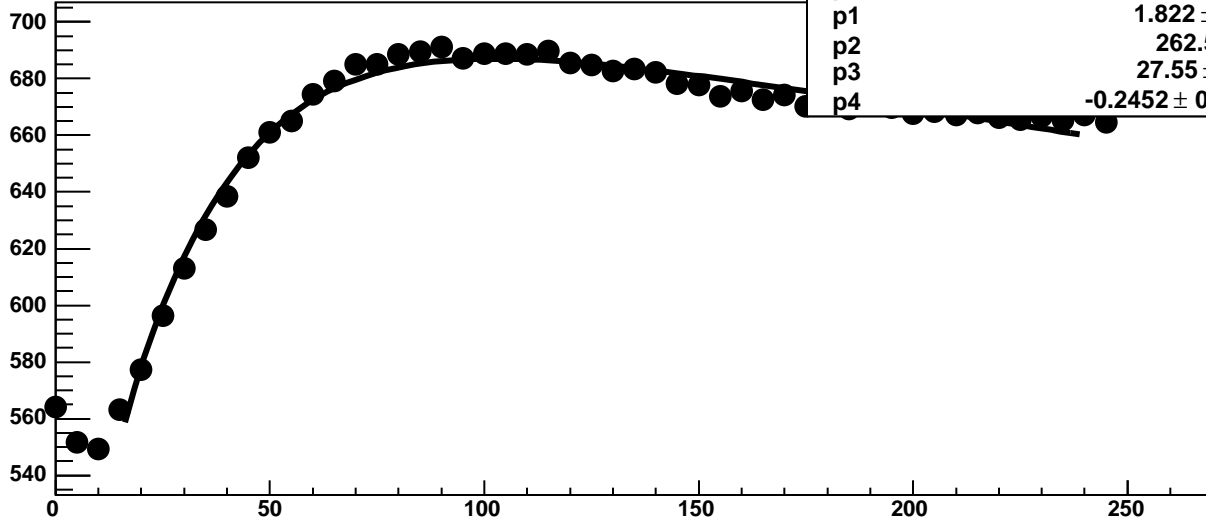
Chip 3, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

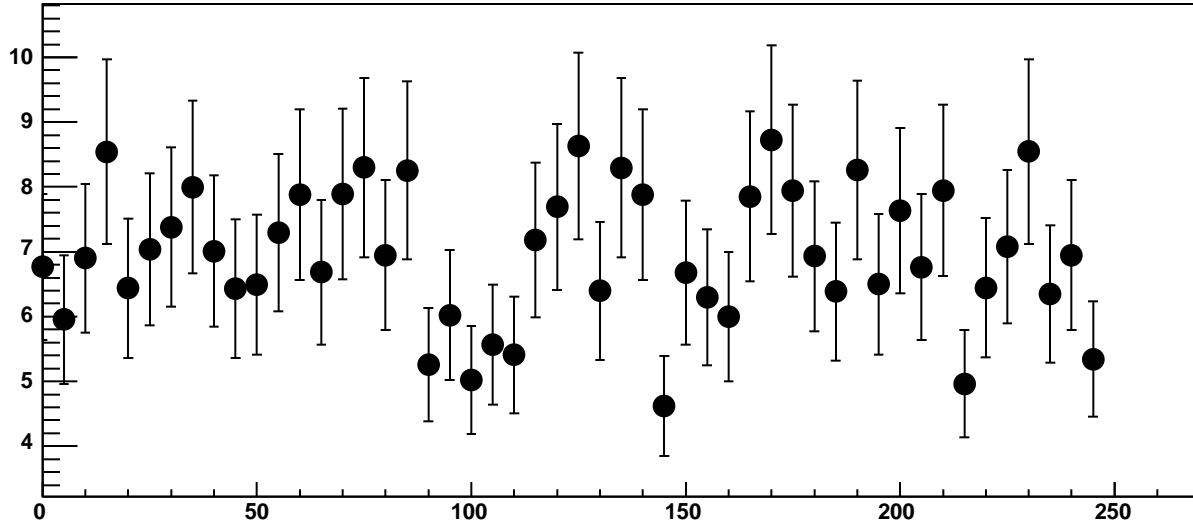


Chip 3, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

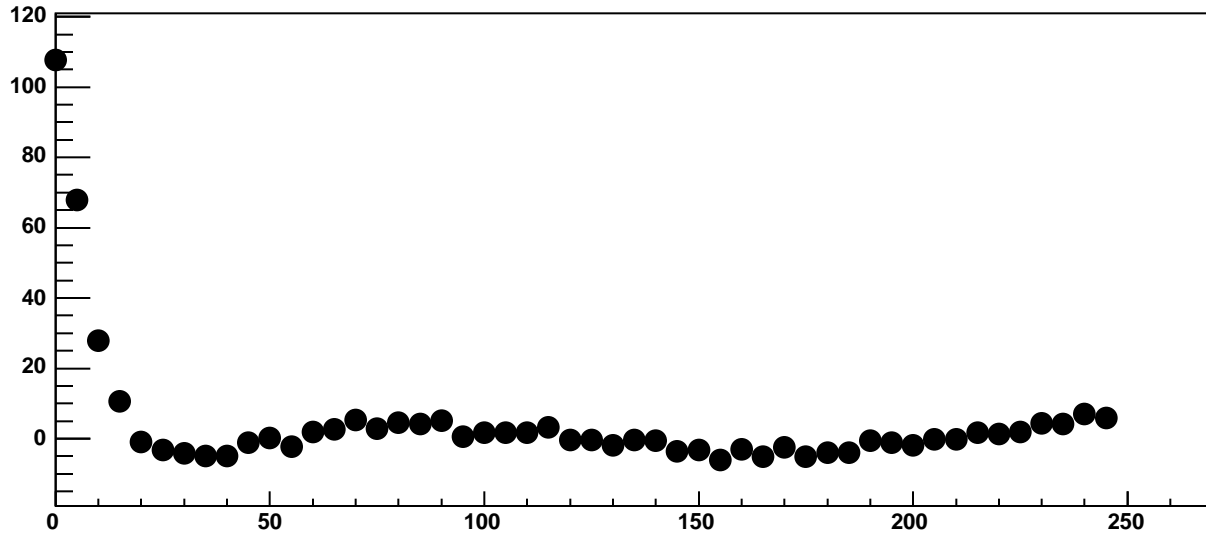


$\chi^2 / \text{ndf}$	224.6 / 41
p0	$456 \pm 2.897$
p1	$1.822 \pm 0.4584$
p2	$262.5 \pm 2.98$
p3	$27.55 \pm 0.5158$
p4	$-0.2452 \pm 0.007303$

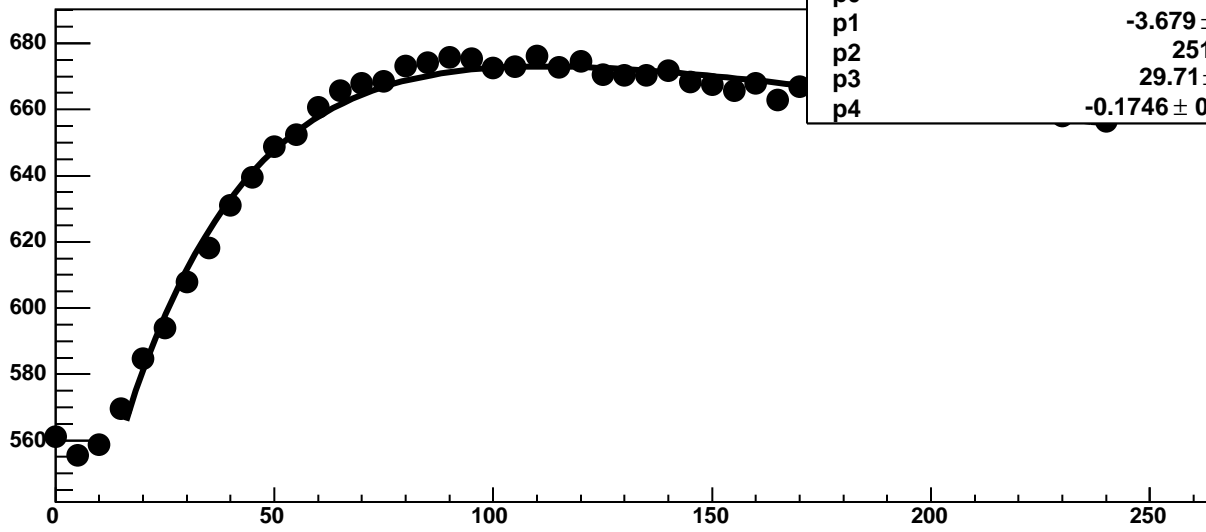
Chip 3, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



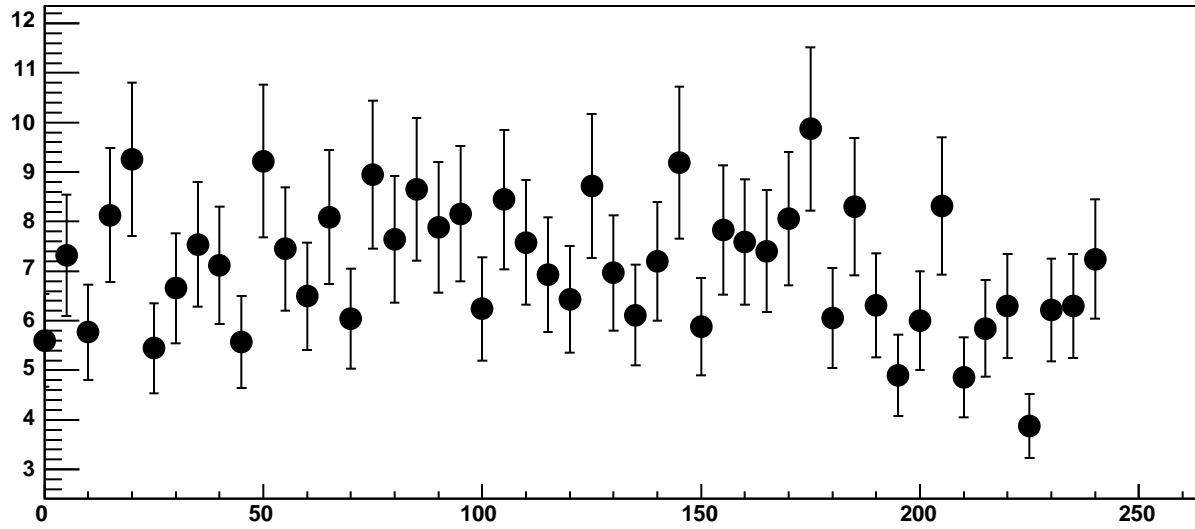
Chip 3, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold



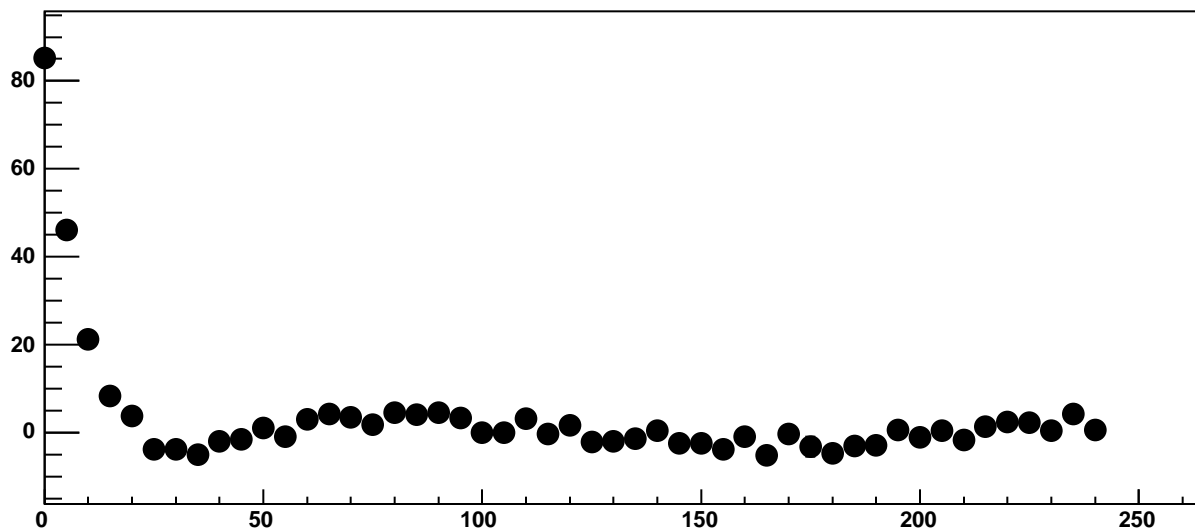
Chip 3, Channel 7, Enable 5, DAC=4000, ADC Mean vs Hold



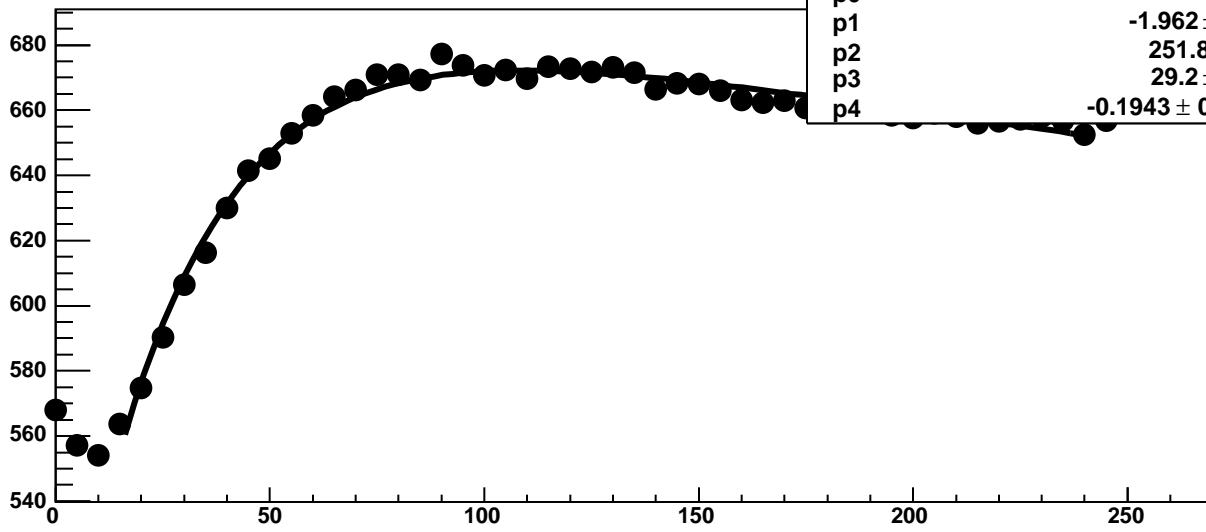
Chip 3, Channel 7, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 7, Enable 5, DAC=4000, ADC Residuals vs Hold

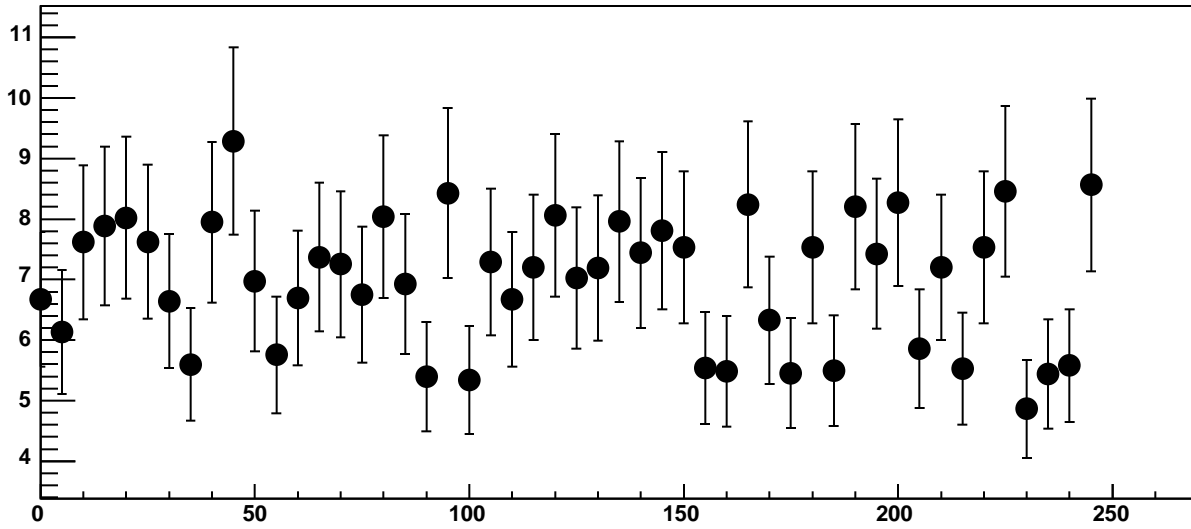


Chip 3, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

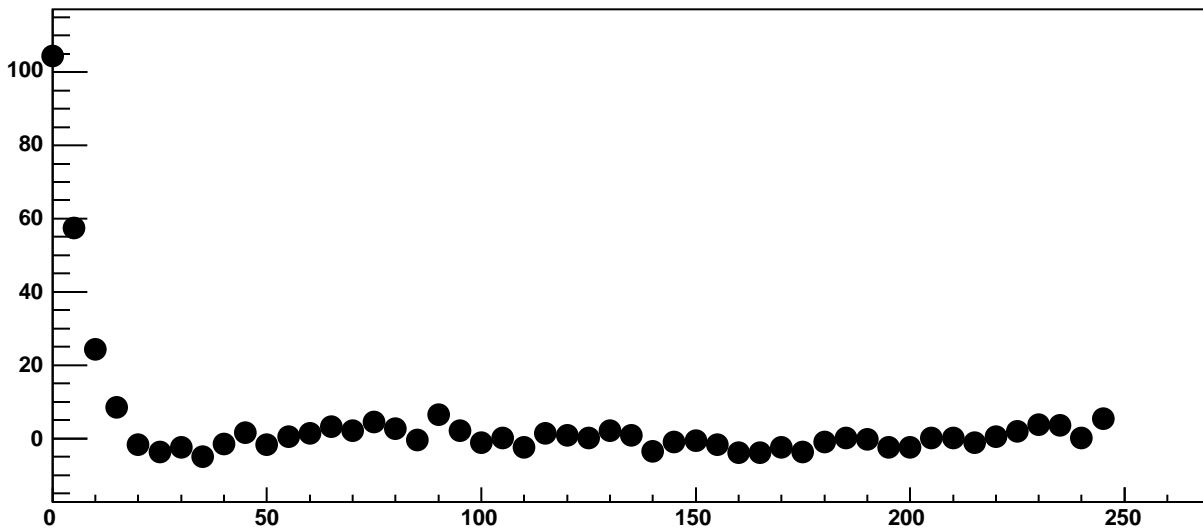


$\chi^2 / \text{ndf}$	155 / 41
p0	$447.6 \pm 2.879$
p1	$-1.962 \pm 0.5707$
p2	$251.8 \pm 2.952$
p3	$29.2 \pm 0.6352$
p4	$-0.1943 \pm 0.007402$

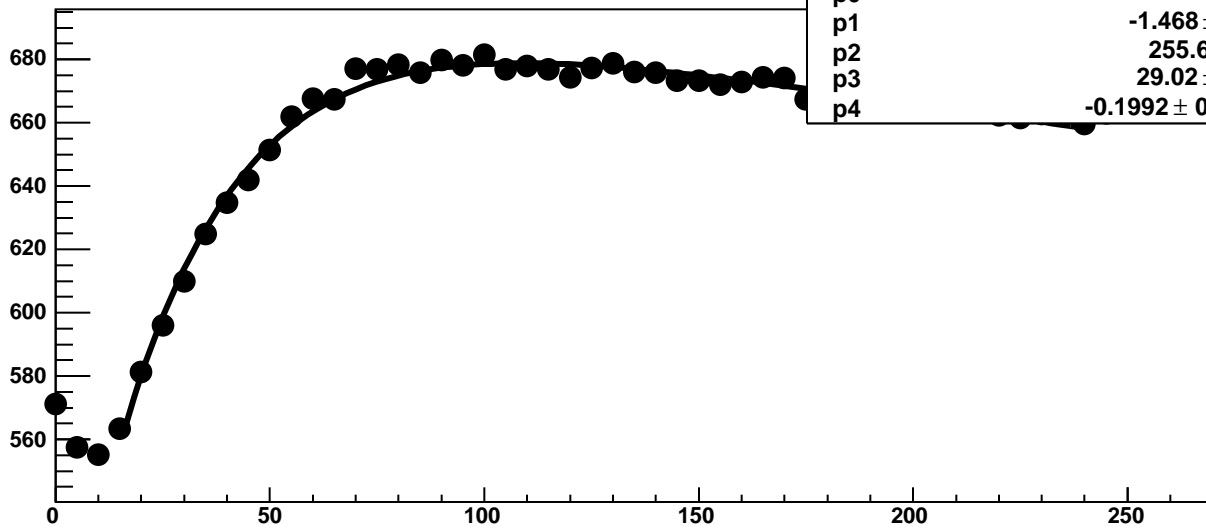
Chip 3, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

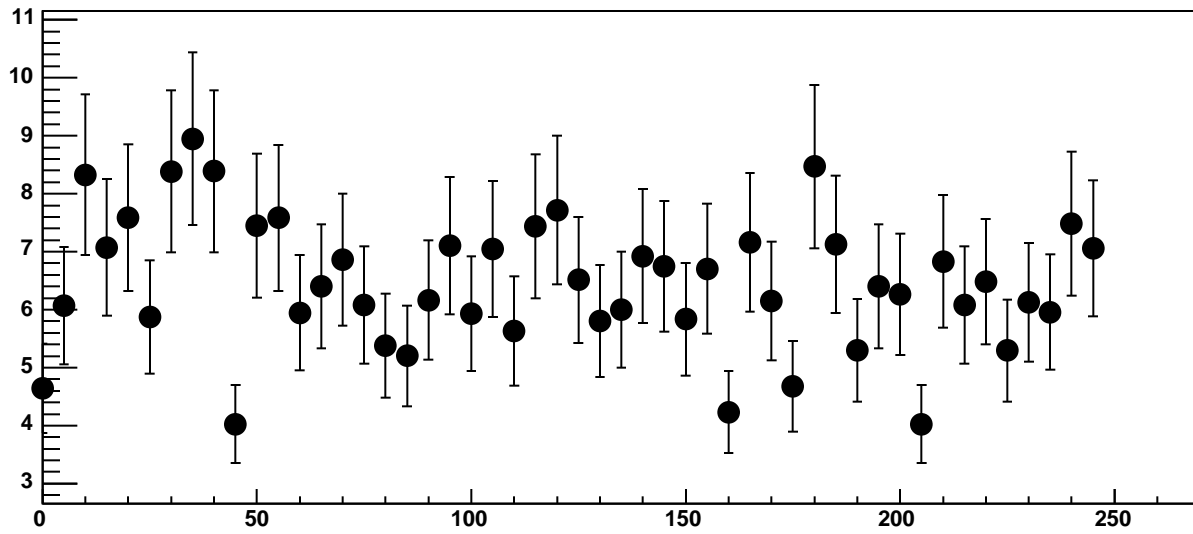


Chip 3, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

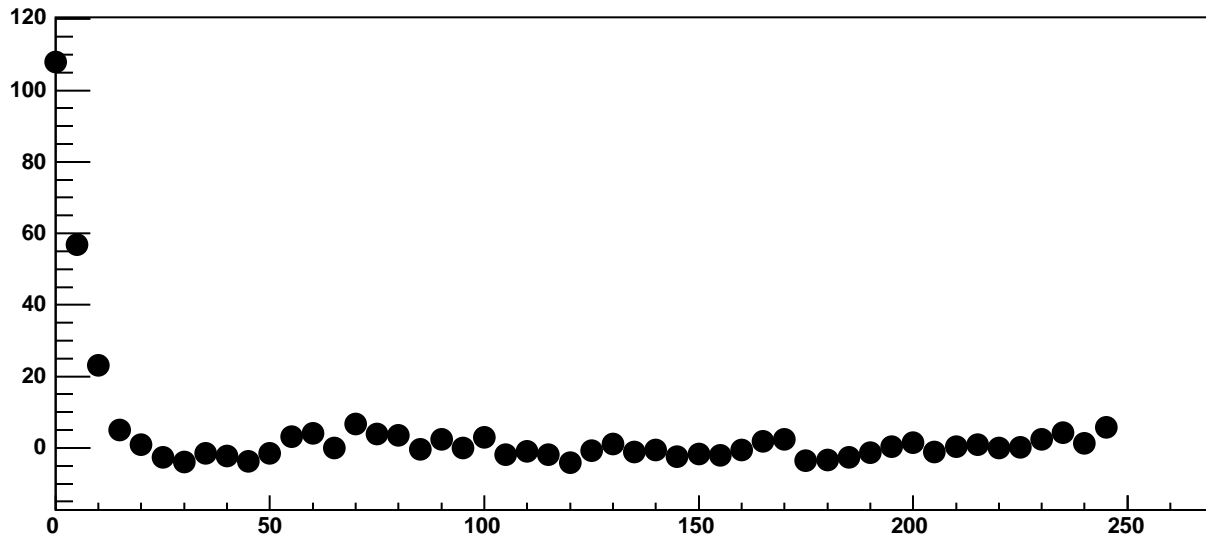


$\chi^2 / \text{ndf}$	144.4 / 41
p0	$450.9 \pm 2.64$
p1	$-1.468 \pm 0.5008$
p2	$255.6 \pm 2.708$
p3	$29.02 \pm 0.5739$
p4	$-0.1992 \pm 0.007169$

Chip 3, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold

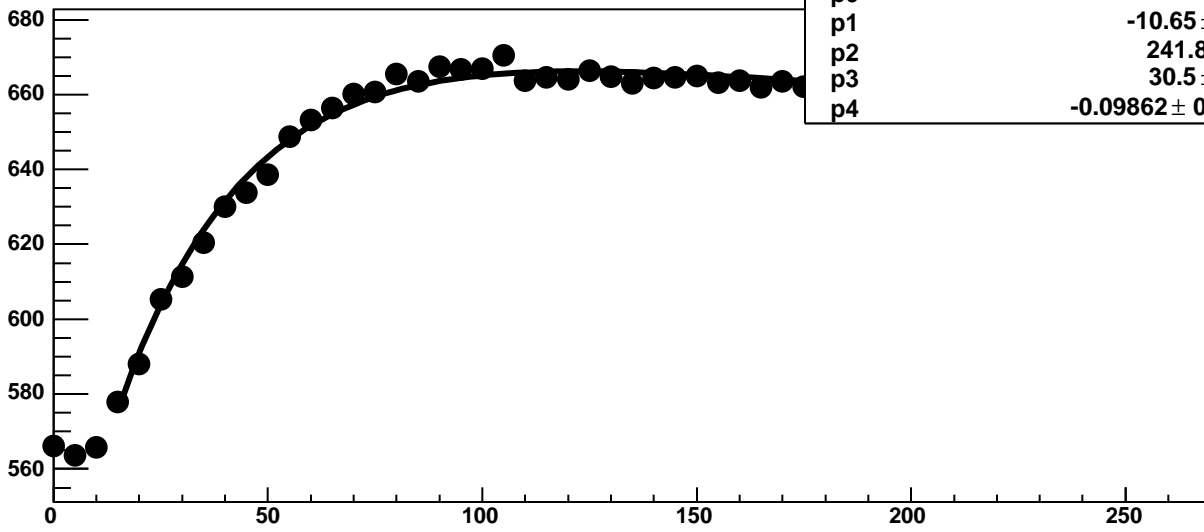


Chip 3, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold



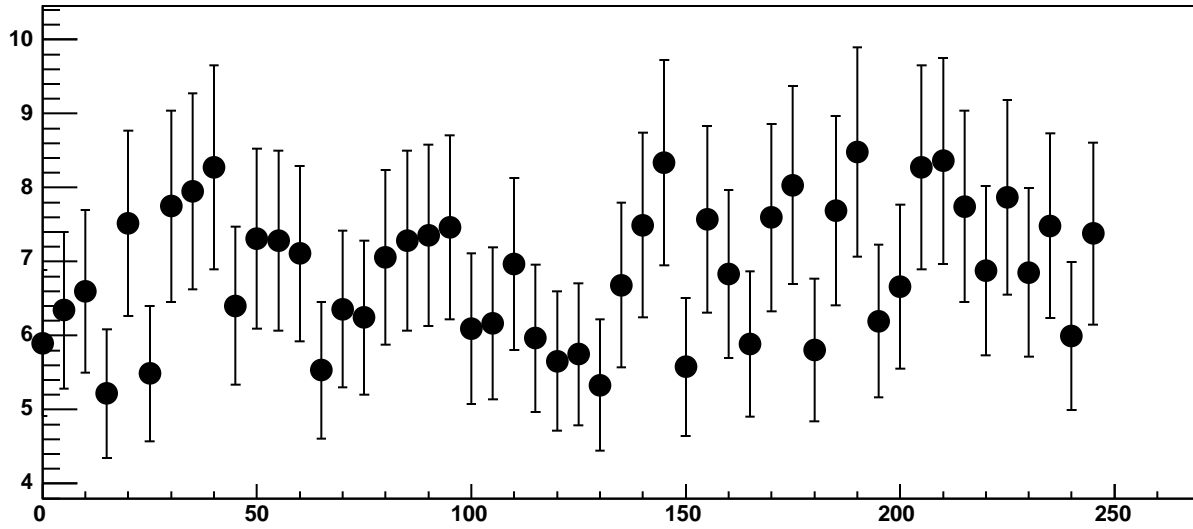


Chip 3, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold

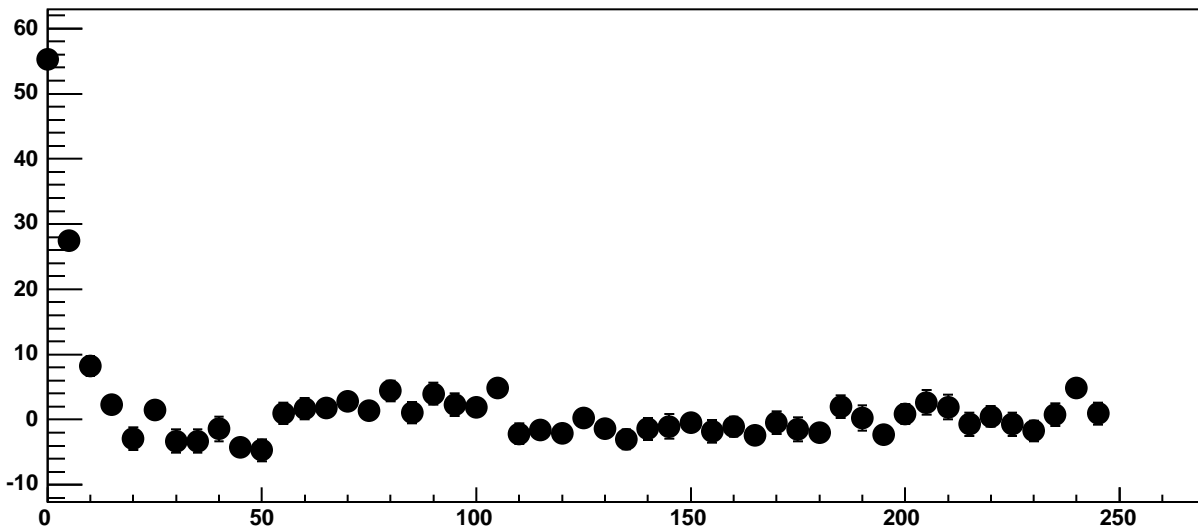


$\chi^2 / \text{ndf}$	108.8 / 41
p0	$440.7 \pm 2.862$
p1	$-10.65 \pm 0.7668$
p2	$241.8 \pm 2.907$
p3	$30.5 \pm 0.8342$
p4	$-0.09862 \pm 0.007847$

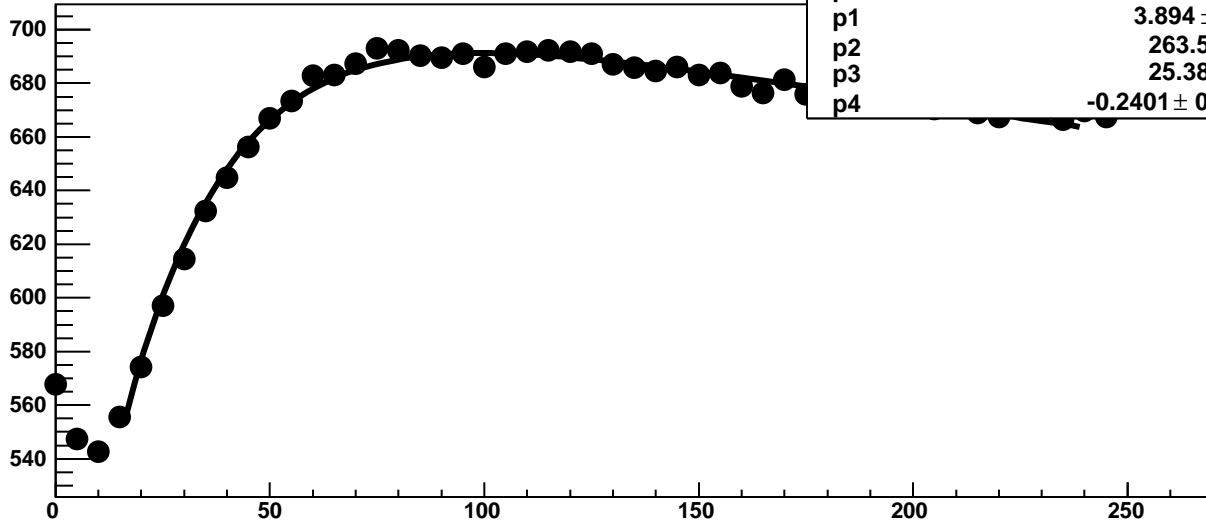
Chip 3, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold

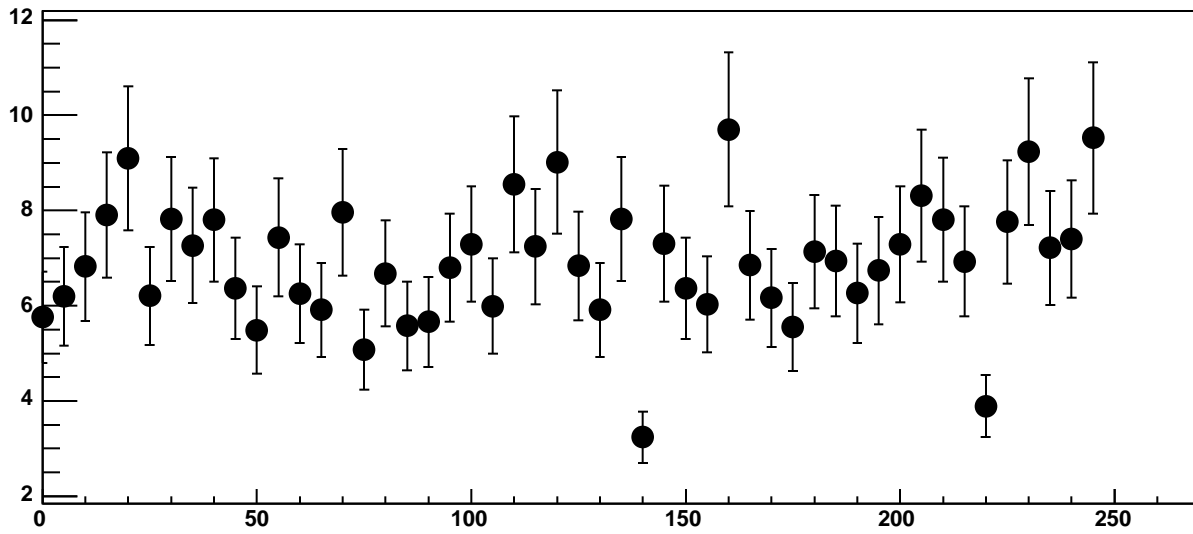


Chip 3, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

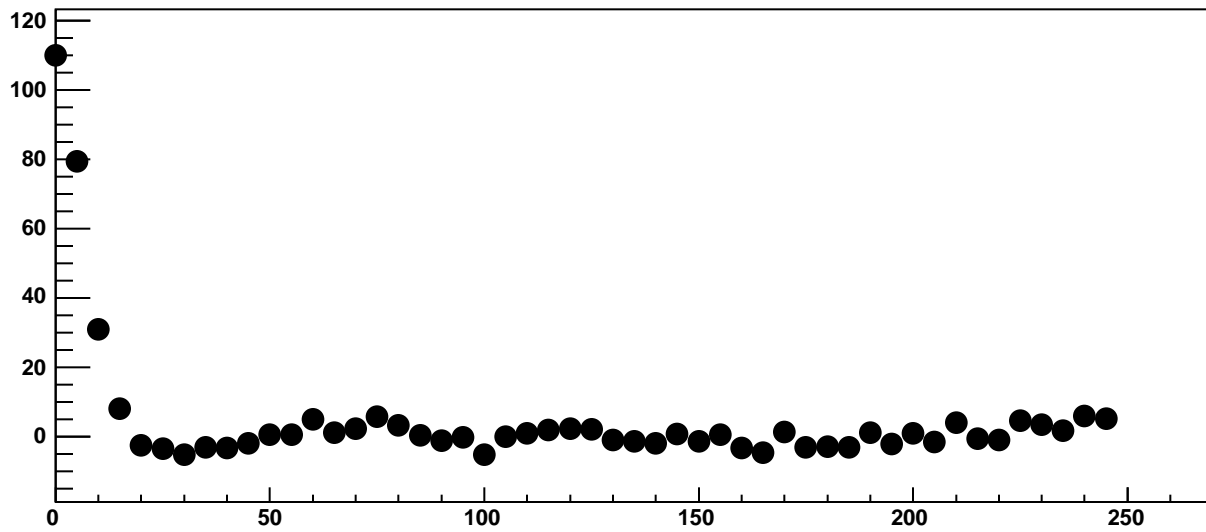


$\chi^2 / \text{ndf}$	169.4 / 41
p0	$456.7 \pm 2.774$
p1	$3.894 \pm 0.4014$
p2	$263.5 \pm 2.845$
p3	$25.38 \pm 0.443$
p4	$-0.2401 \pm 0.006895$

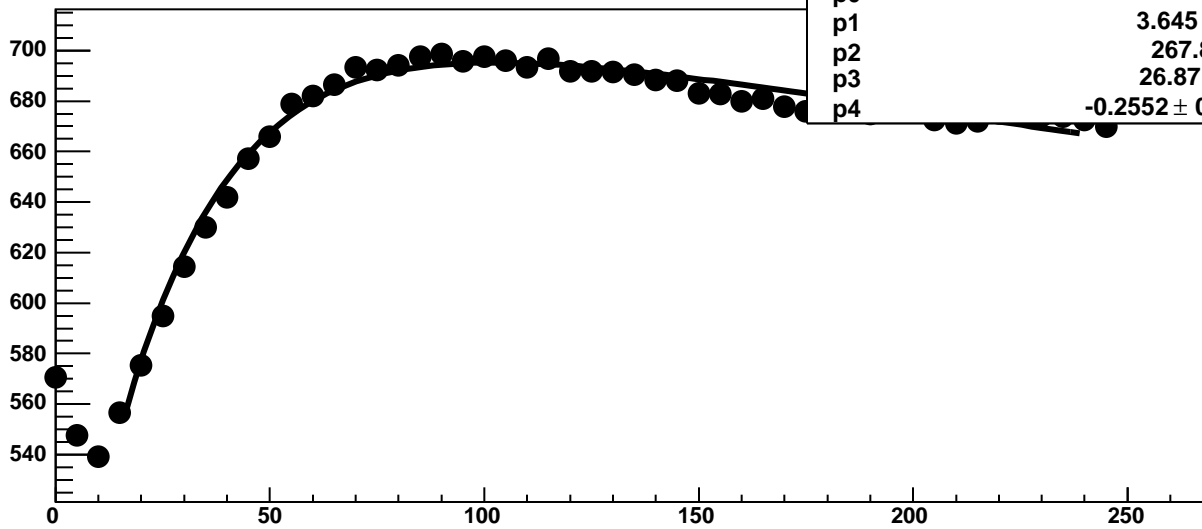
Chip 3, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

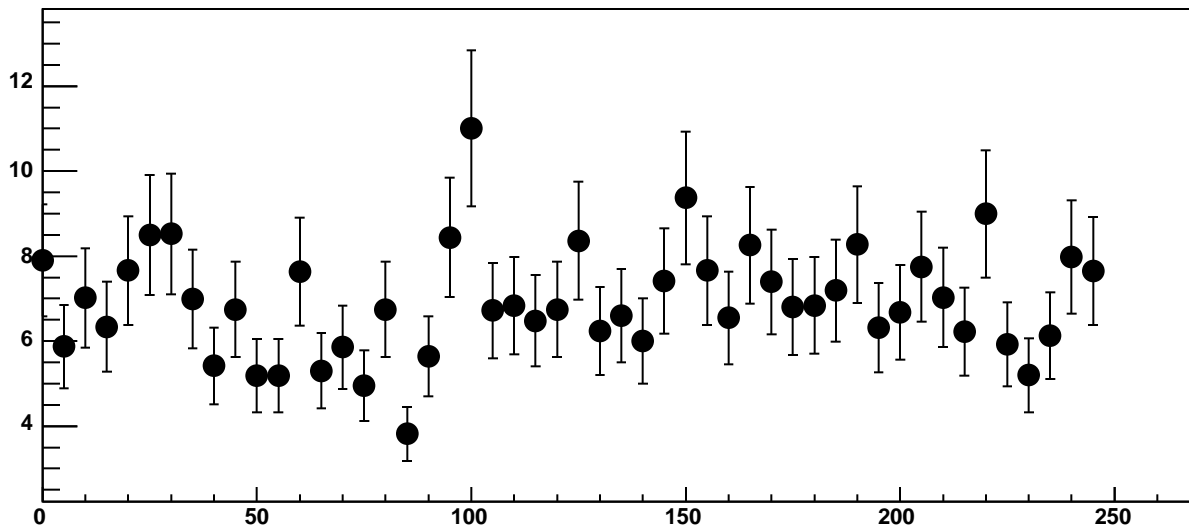


Chip 3, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

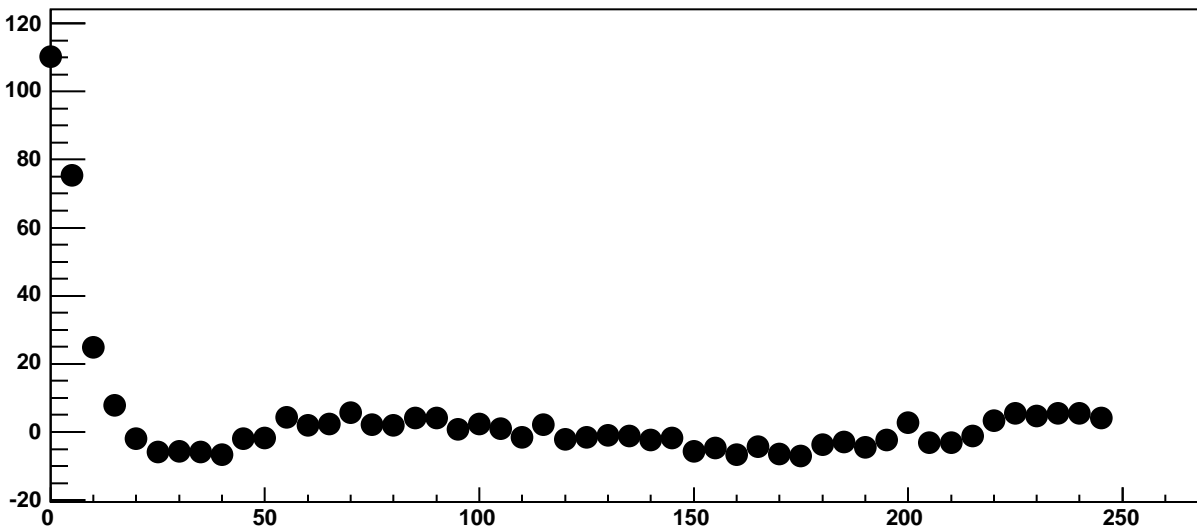


$\chi^2 / \text{ndf}$	340.9 / 41
p0	$459.4 \pm 2.788$
p1	$3.645 \pm 0.3783$
p2	$267.8 \pm 2.871$
p3	$26.87 \pm 0.4191$
p4	$-0.2552 \pm 0.006975$

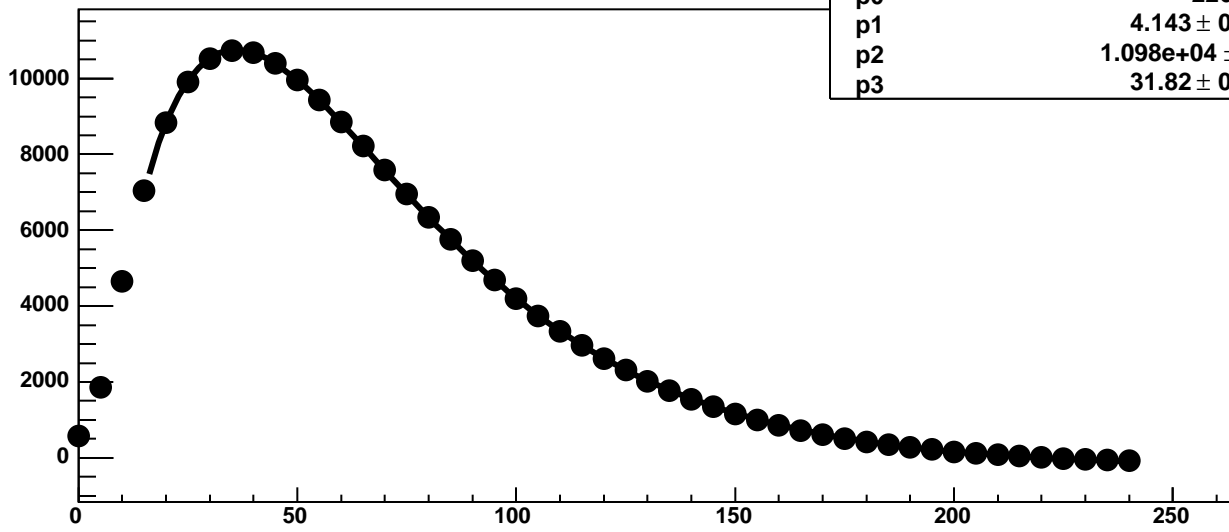
Chip 3, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

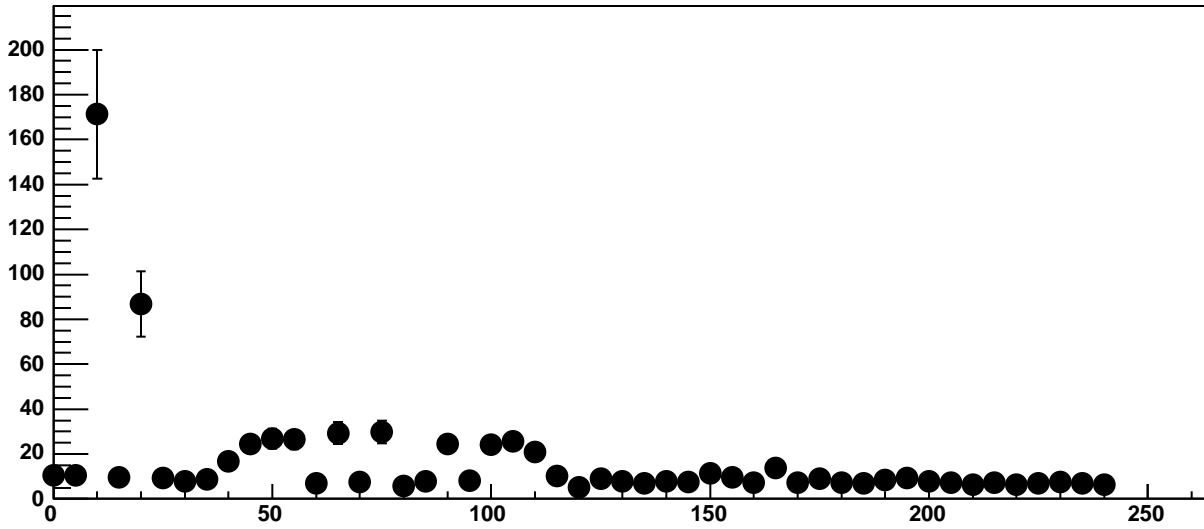


Chip 3, Channel 8, Enable 5!, DAC=4000, ADC Mean vs Hold

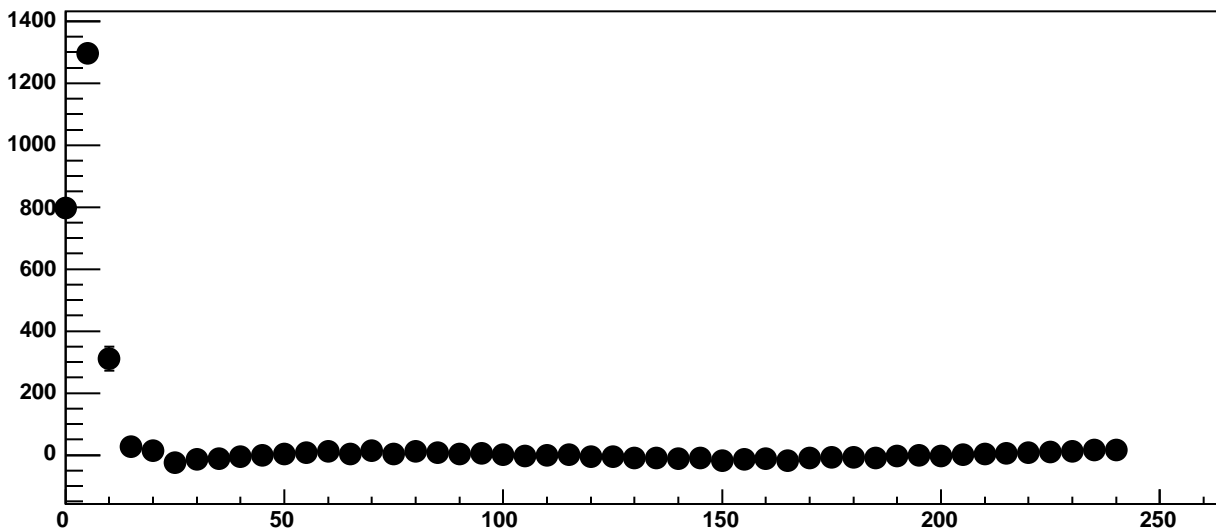


$\chi^2 / \text{ndf}$	1386 / 42
p0	-226 ± 0.553
p1	4.143 ± 0.006347
p2	1.098e+04 ± 0.9704
p3	31.82 ± 0.004819

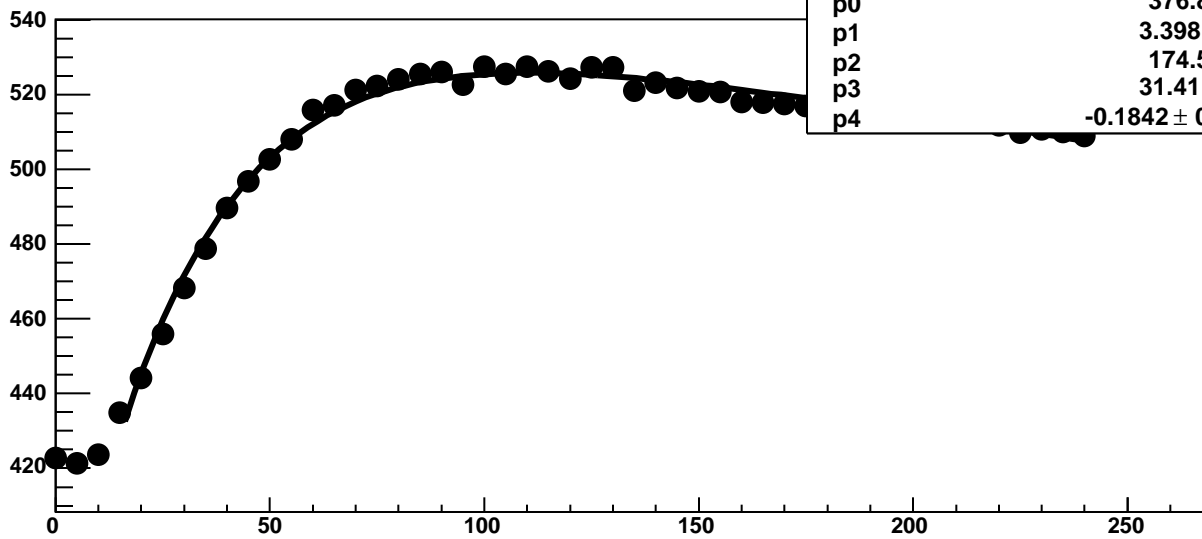
Chip 3, Channel 8, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 8, Enable 5!, DAC=4000, ADC Residuals vs Hold

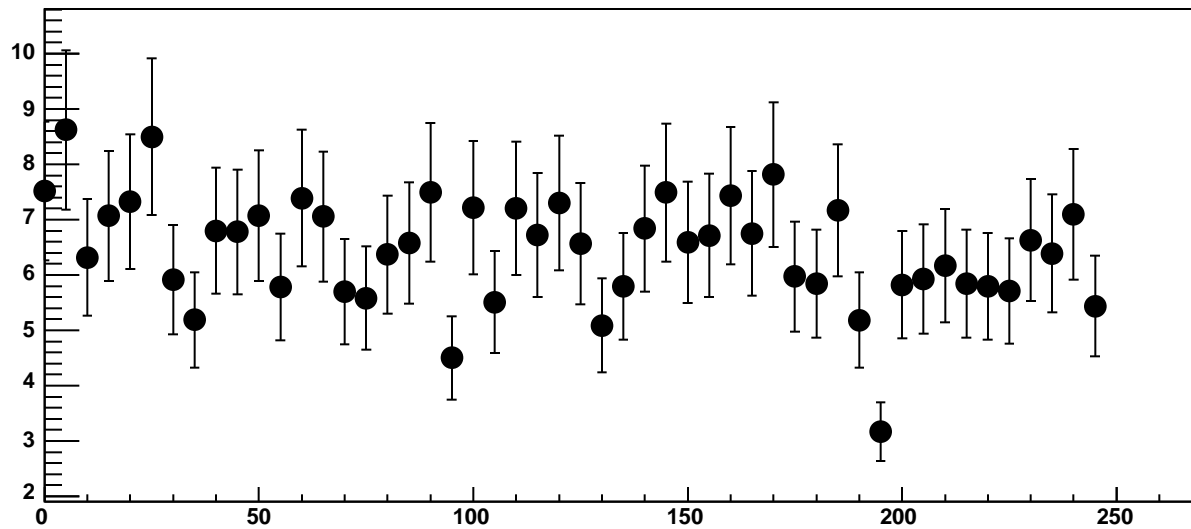


Chip 3, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold

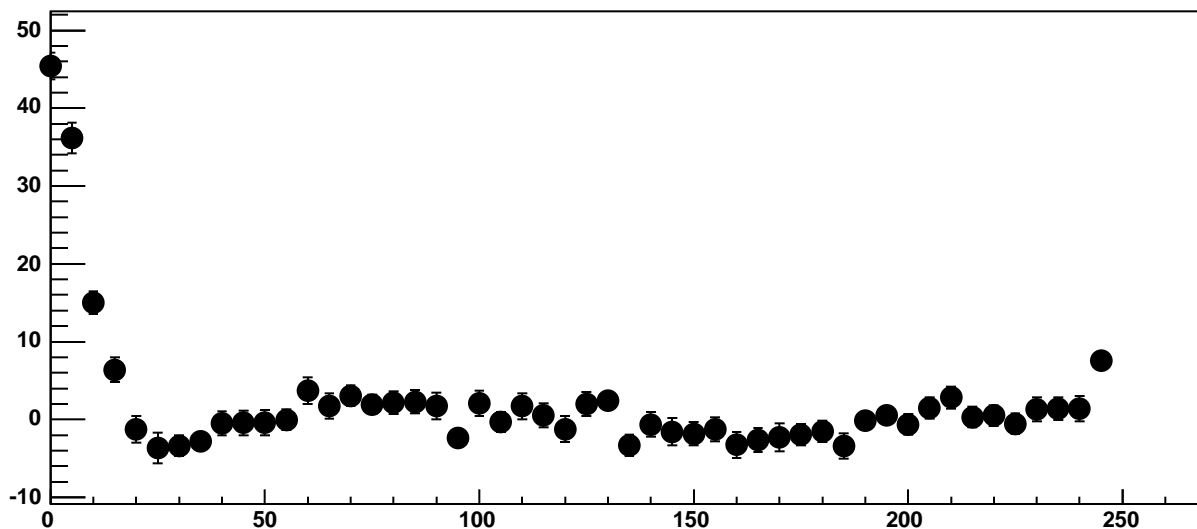


$\chi^2 / \text{ndf}$	99.52 / 41
p0	$376.8 \pm 2.644$
p1	$3.398 \pm 0.6361$
p2	$174.5 \pm 2.748$
p3	$31.41 \pm 0.7898$
p4	$-0.1842 \pm 0.007768$

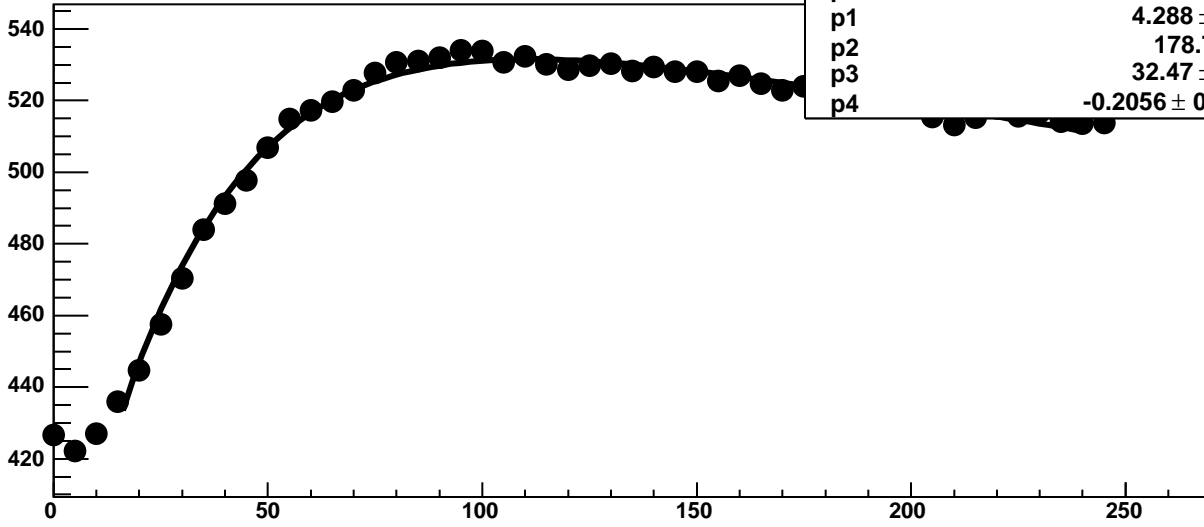
Chip 3, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold

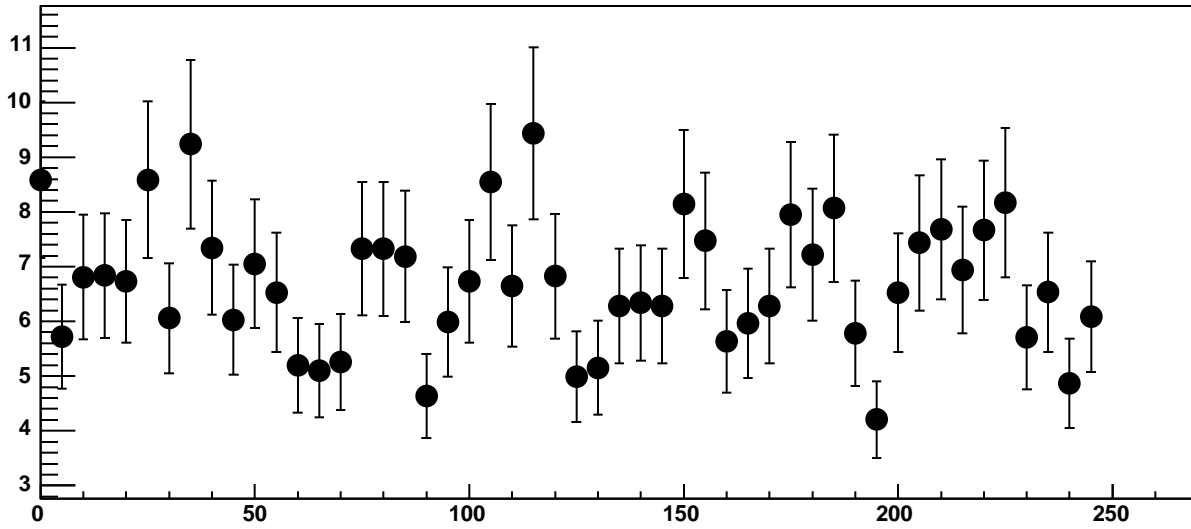


Chip 3, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

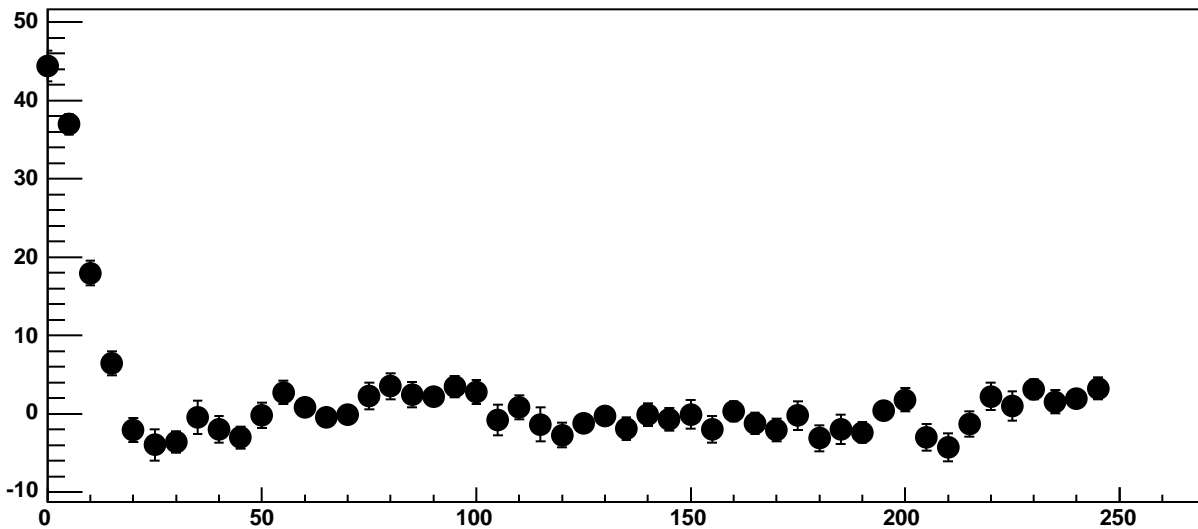


$\chi^2 / \text{ndf}$	103.9 / 41
p0	$381.5 \pm 2.735$
p1	$4.288 \pm 0.6235$
p2	$178.7 \pm 2.86$
p3	$32.47 \pm 0.7664$
p4	$-0.2056 \pm 0.008088$

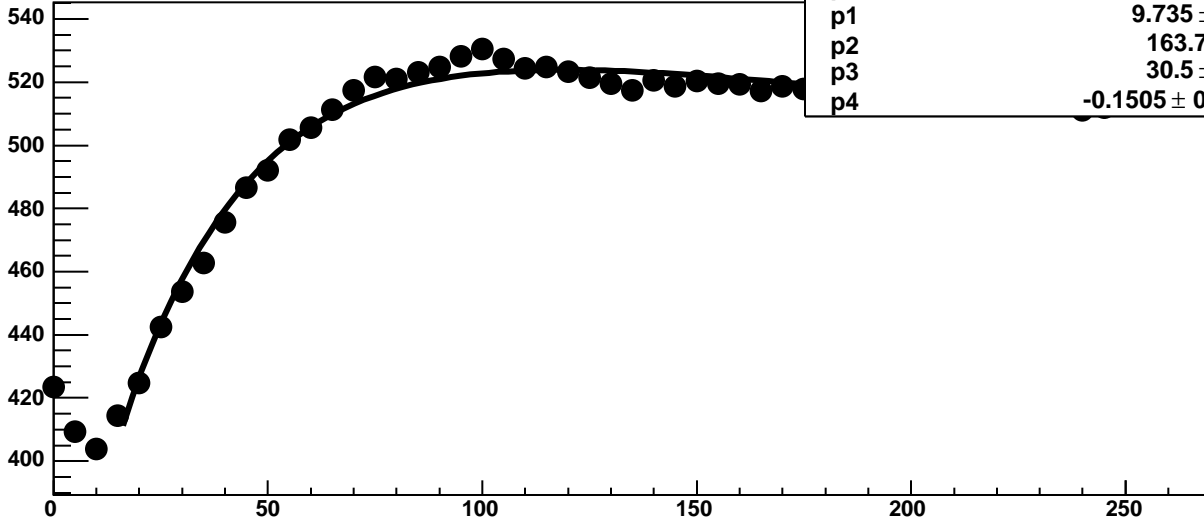
Chip 3, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

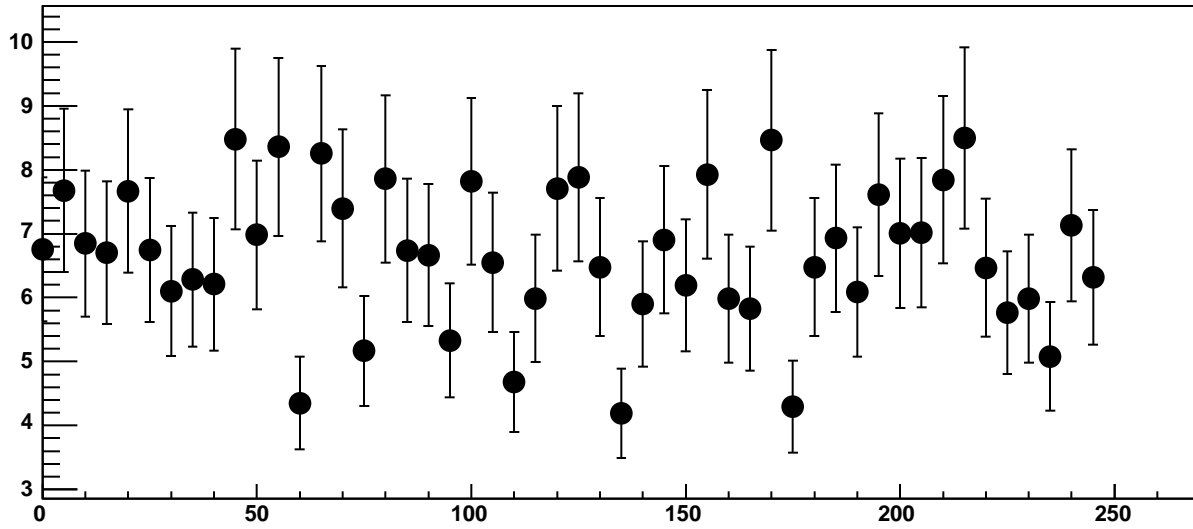


Chip 3, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

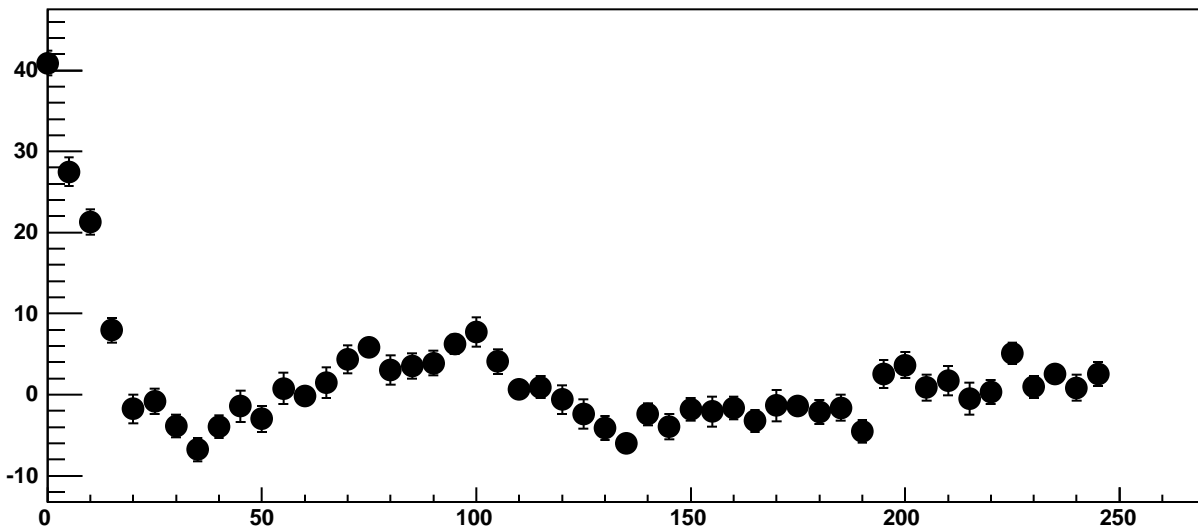


$\chi^2 / \text{ndf}$	281.2 / 41
p0	$381.2 \pm 2.706$
p1	$9.735 \pm 0.5776$
p2	$163.7 \pm 2.806$
p3	$30.5 \pm 0.6395$
p4	$-0.1505 \pm 0.007504$

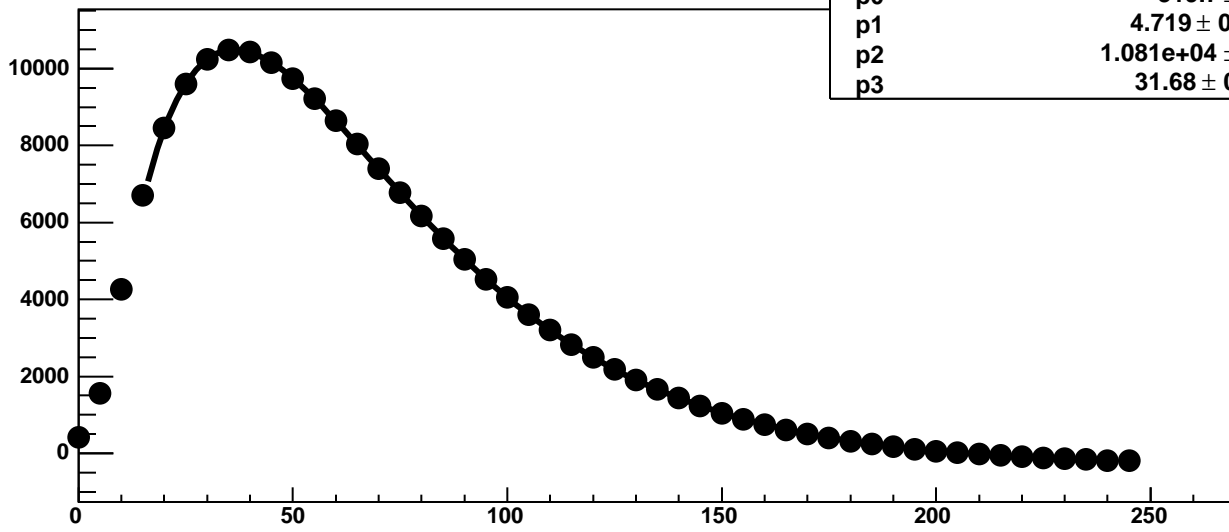
Chip 3, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold

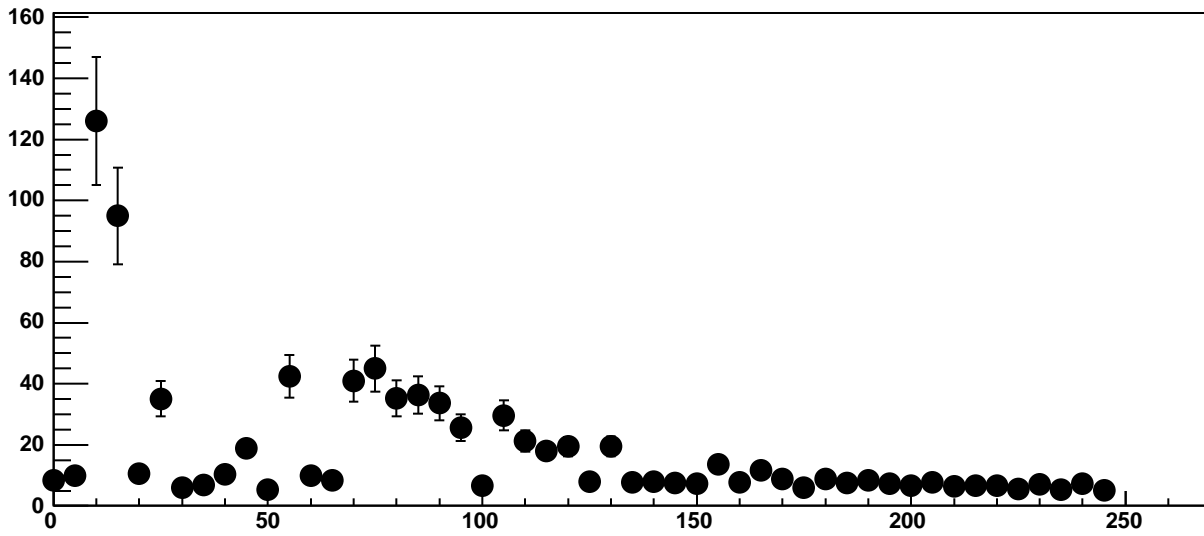


Chip 3, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold

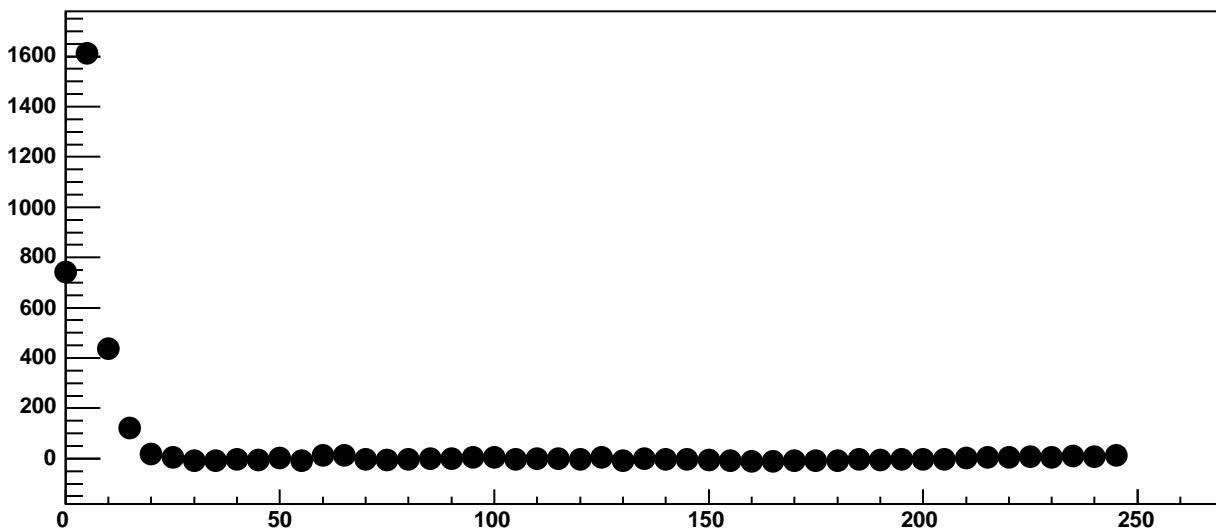


$\chi^2 / \text{ndf}$	583.1 / 42
p0	$-319.7 \pm 0.5706$
p1	$4.719 \pm 0.009097$
p2	$1.081\text{e}+04 \pm 0.8284$
p3	$31.68 \pm 0.006041$

Chip 3, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold

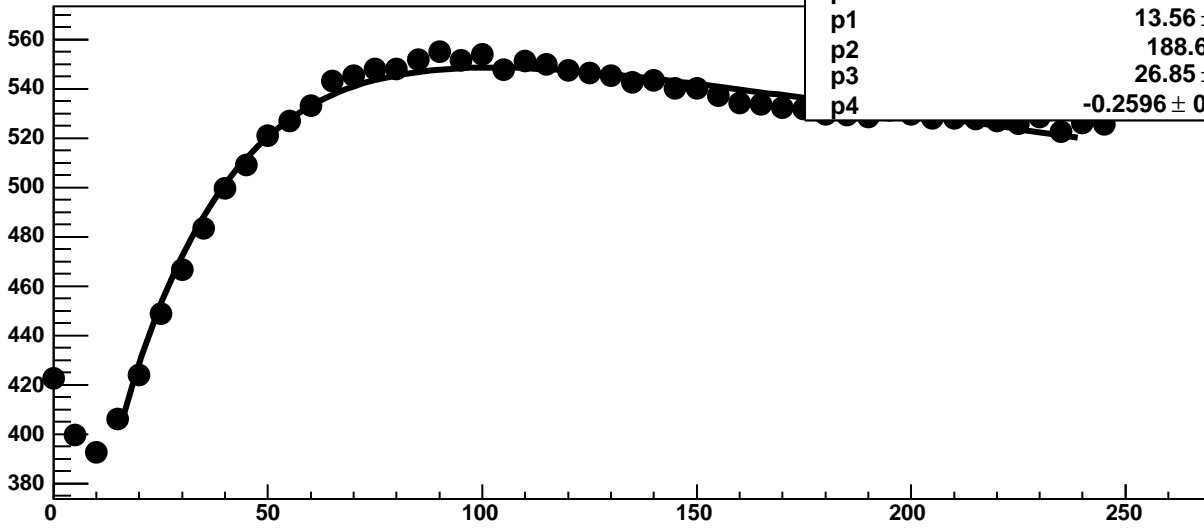


Chip 3, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold



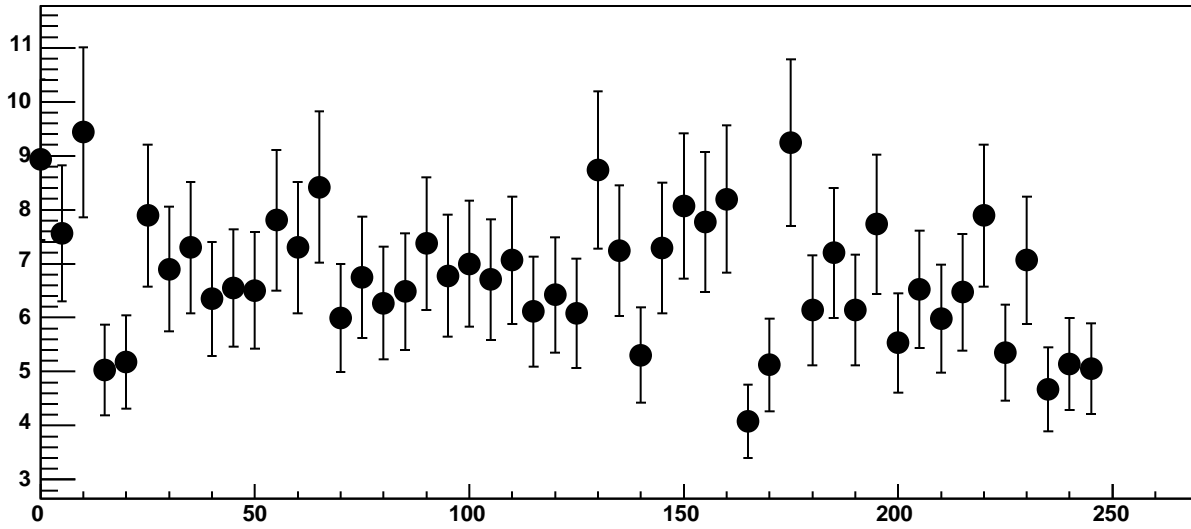


Chip 3, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

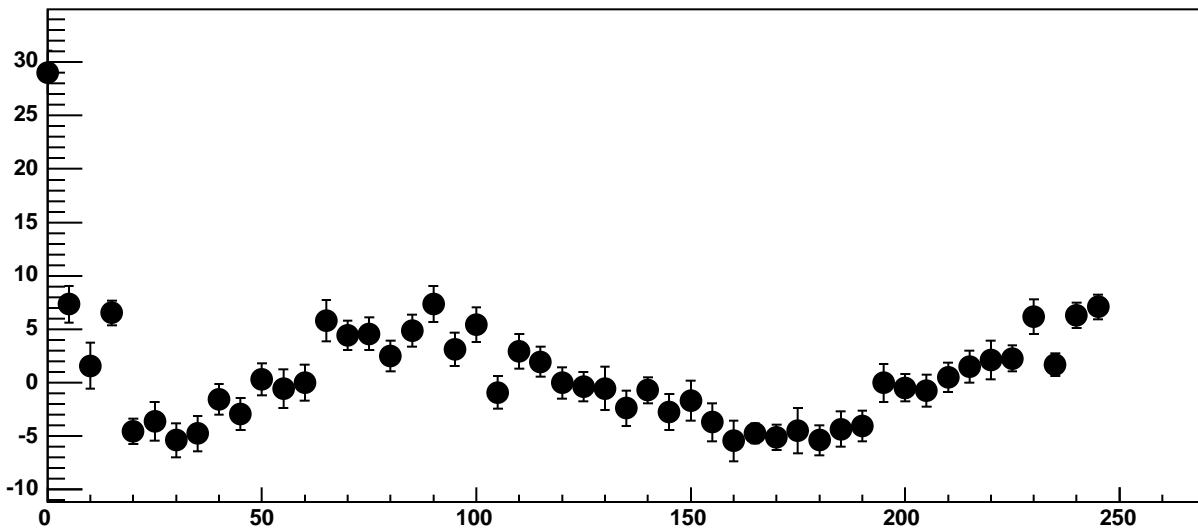


$\chi^2 / \text{ndf}$	306.8 / 41
p0	$390.1 \pm 2.763$
p1	$13.56 \pm 0.4263$
p2	$188.6 \pm 2.887$
p3	$26.85 \pm 0.4097$
p4	$-0.2596 \pm 0.006699$

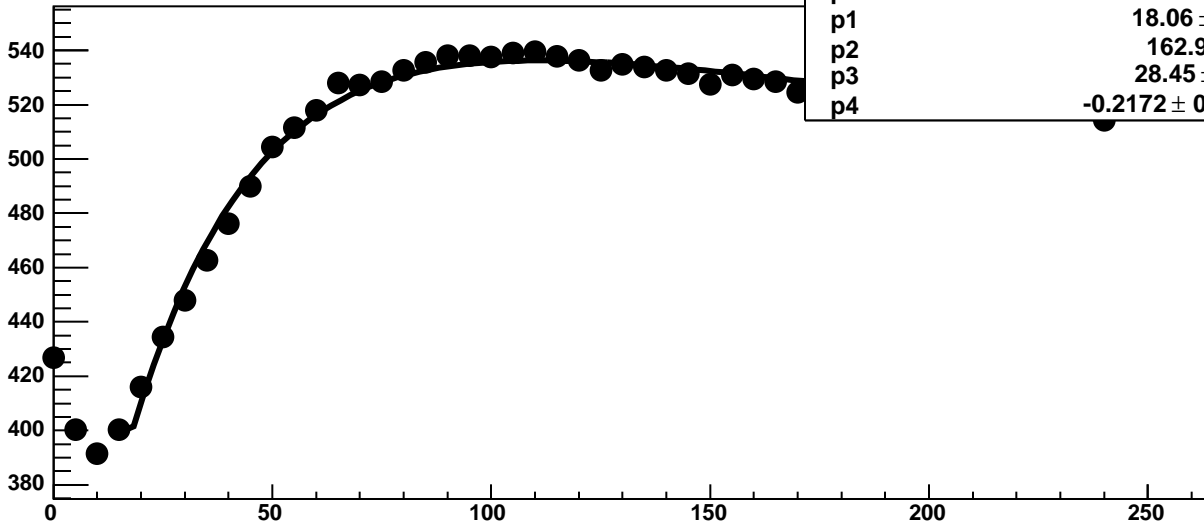
Chip 3, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

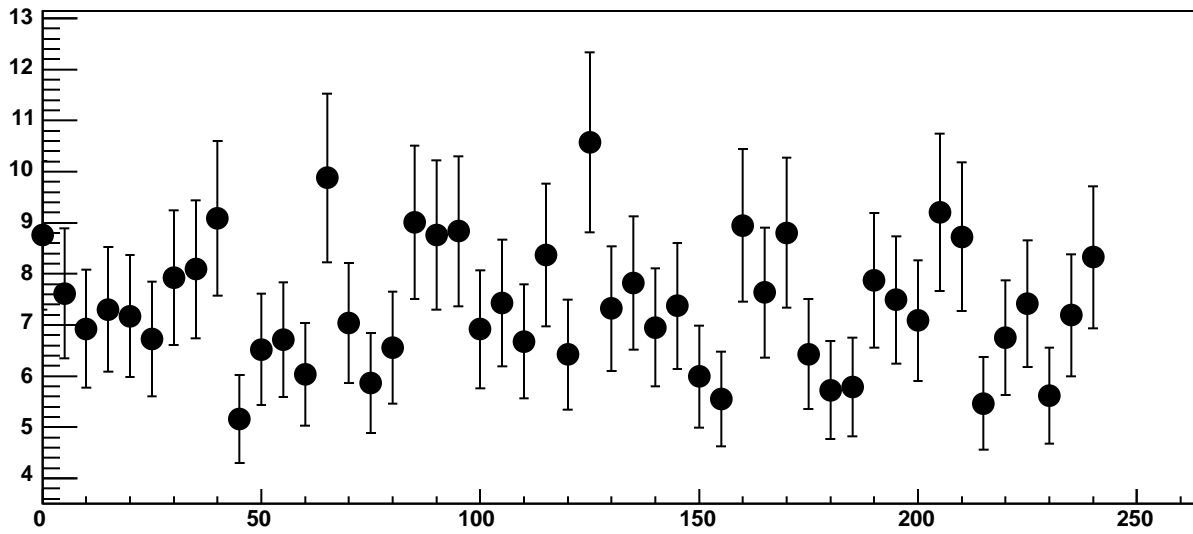


Chip 3, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold

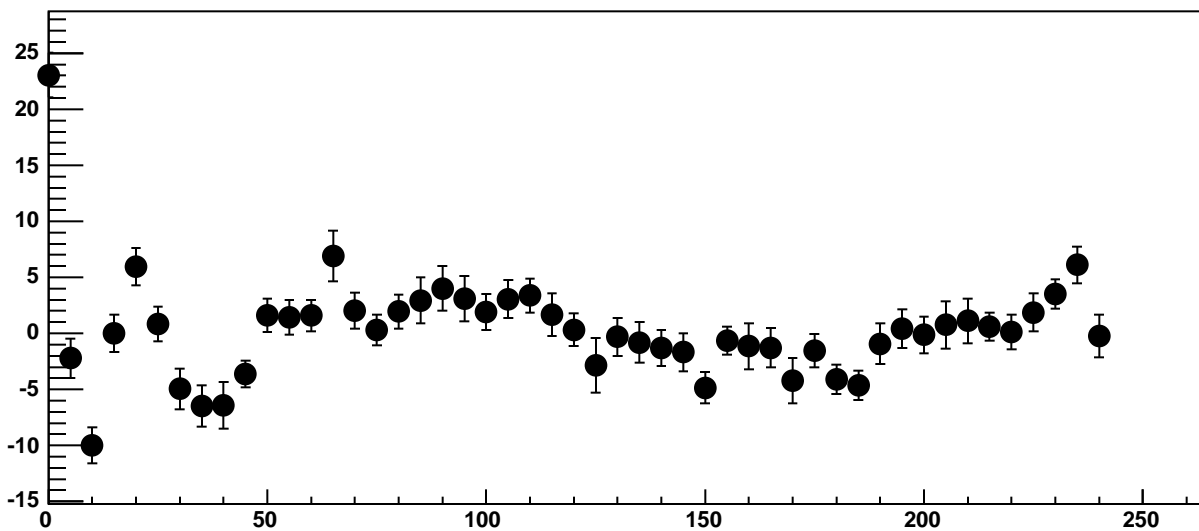


$\chi^2 / \text{ndf}$	154 / 41
p0	$399.8 \pm 1.617$
p1	$18.06 \pm 0.3846$
p2	$162.9 \pm 2.338$
p3	$28.45 \pm 0.6319$
p4	$-0.2172 \pm 0.009375$

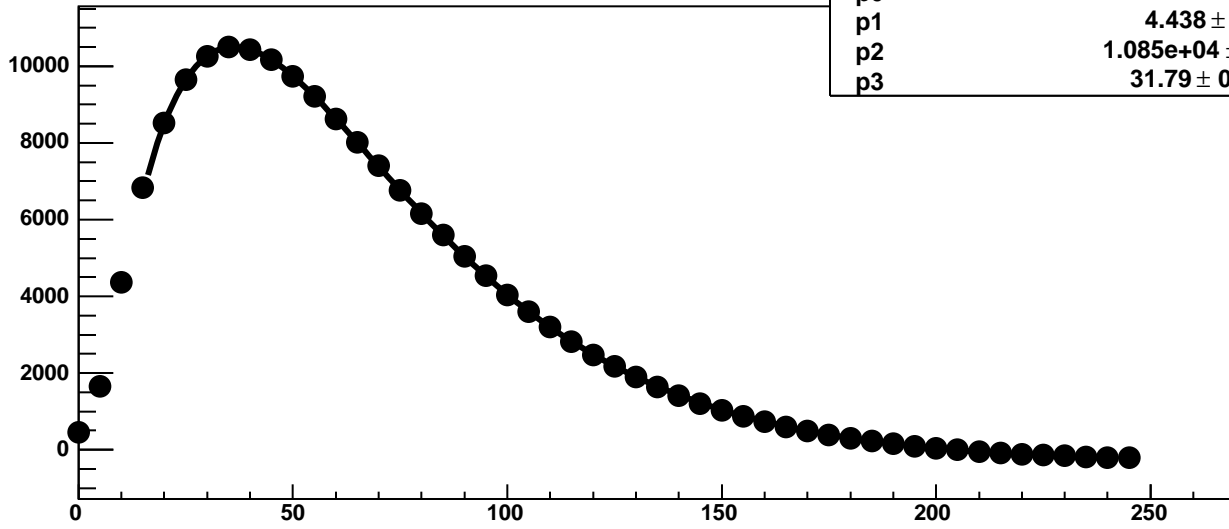
Chip 3, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold

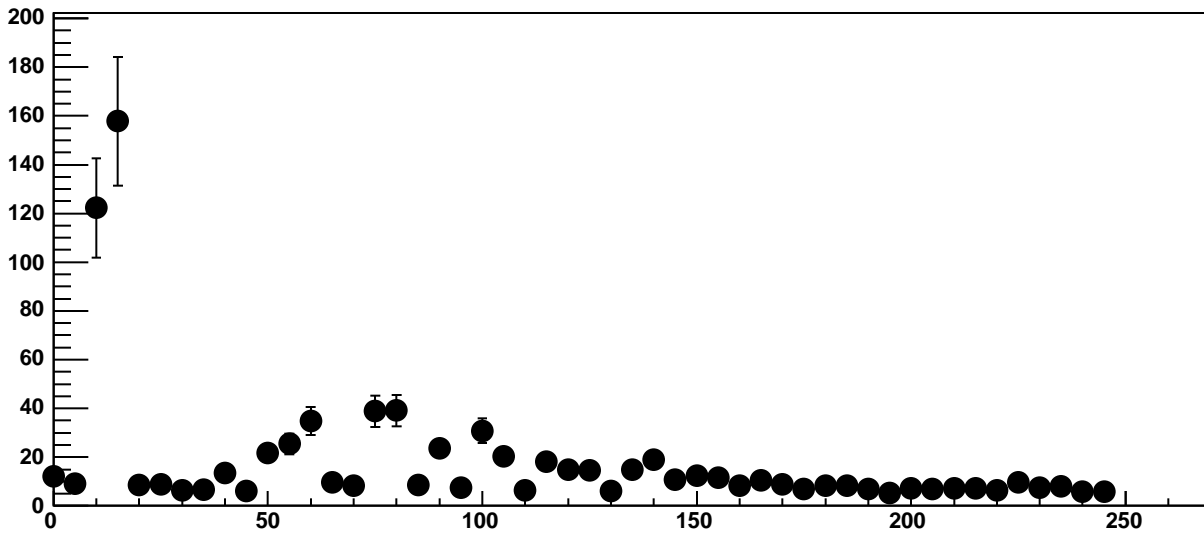


Chip 3, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold

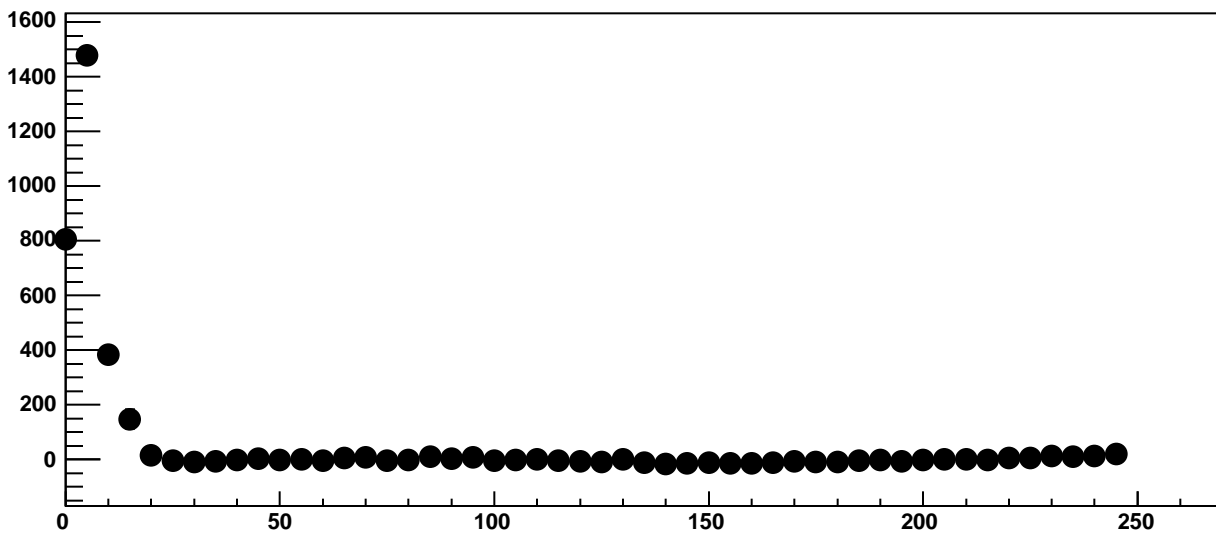


$\chi^2 / \text{ndf}$	690 / 42
p0	-343.4 ± 0.5761
p1	4.438 ± 0.00827
p2	1.085e+04 ± 0.8402
p3	31.79 ± 0.005639

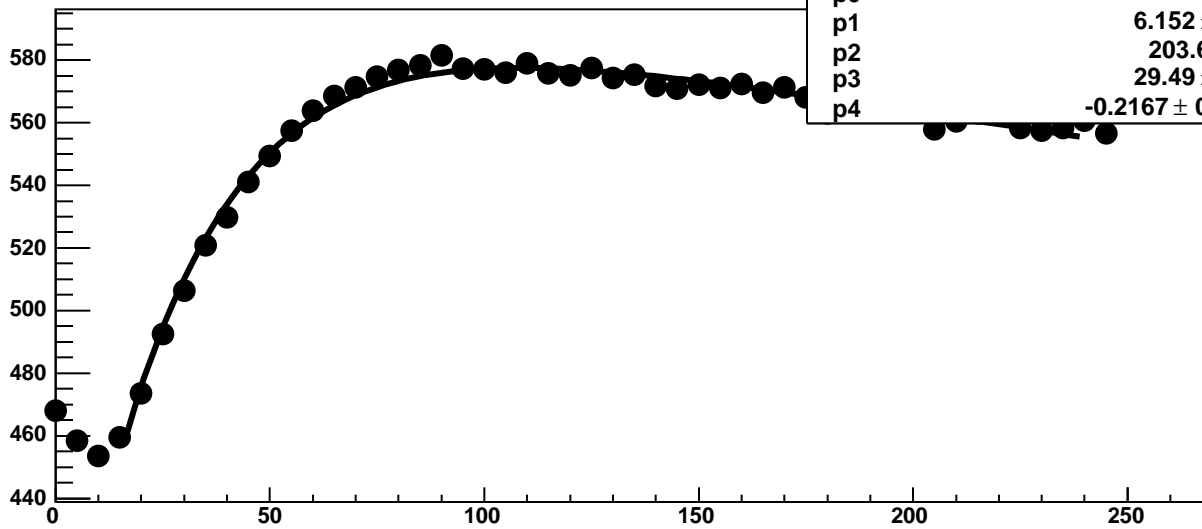
Chip 3, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold

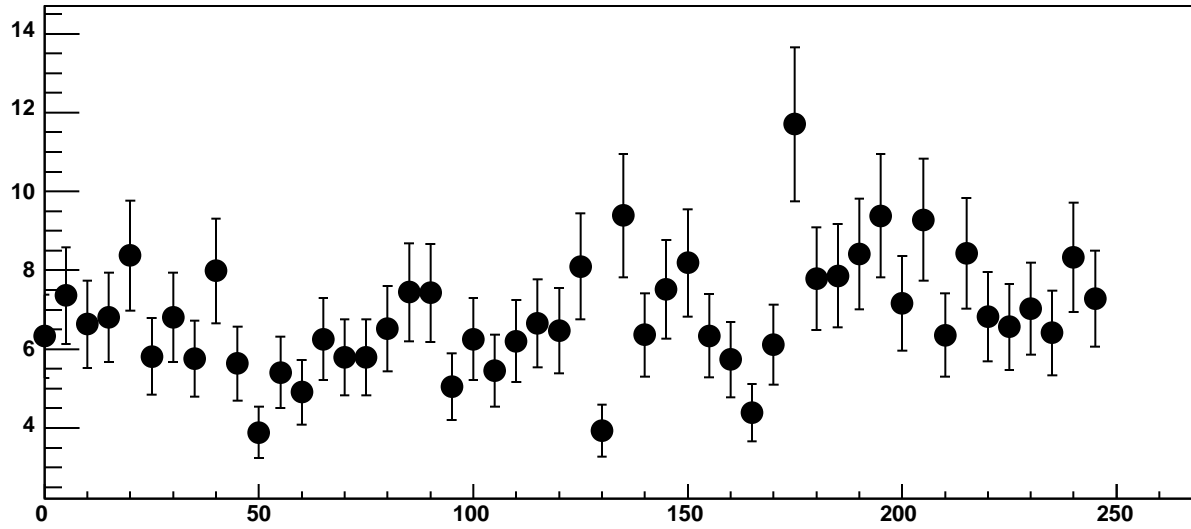


Chip 3, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

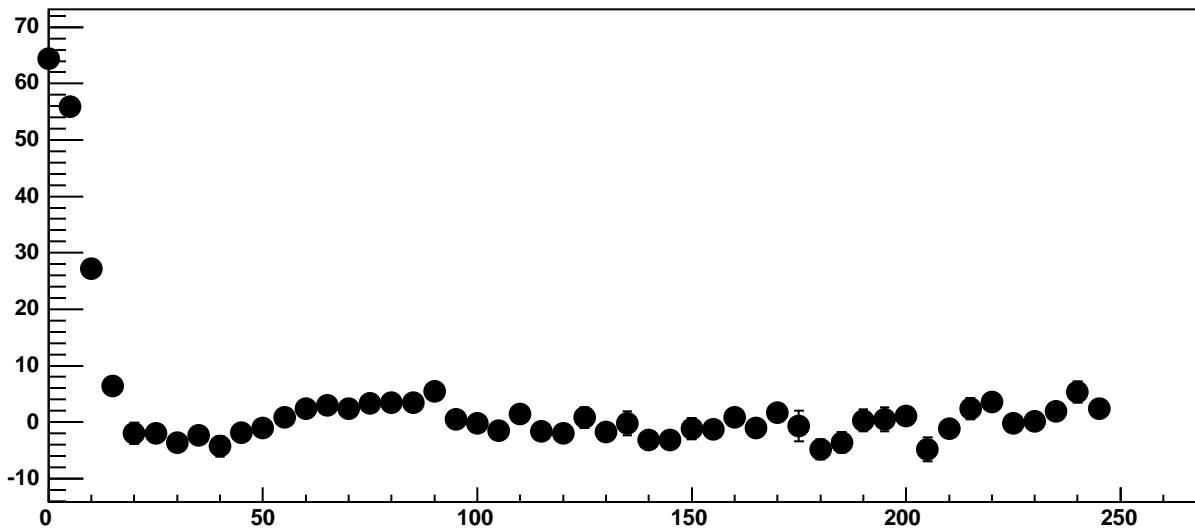


$\chi^2 / \text{ndf}$	132.2 / 41
p0	$402.4 \pm 2.706$
p1	$6.152 \pm 0.4866$
p2	$203.6 \pm 2.814$
p3	$29.49 \pm 0.5534$
p4	$-0.2167 \pm 0.007834$

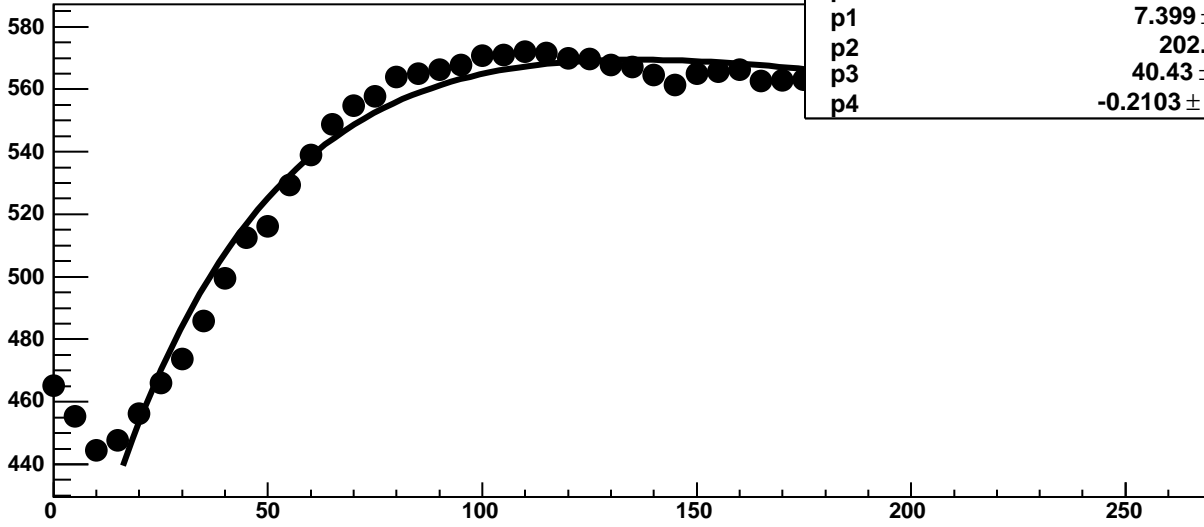
Chip 3, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

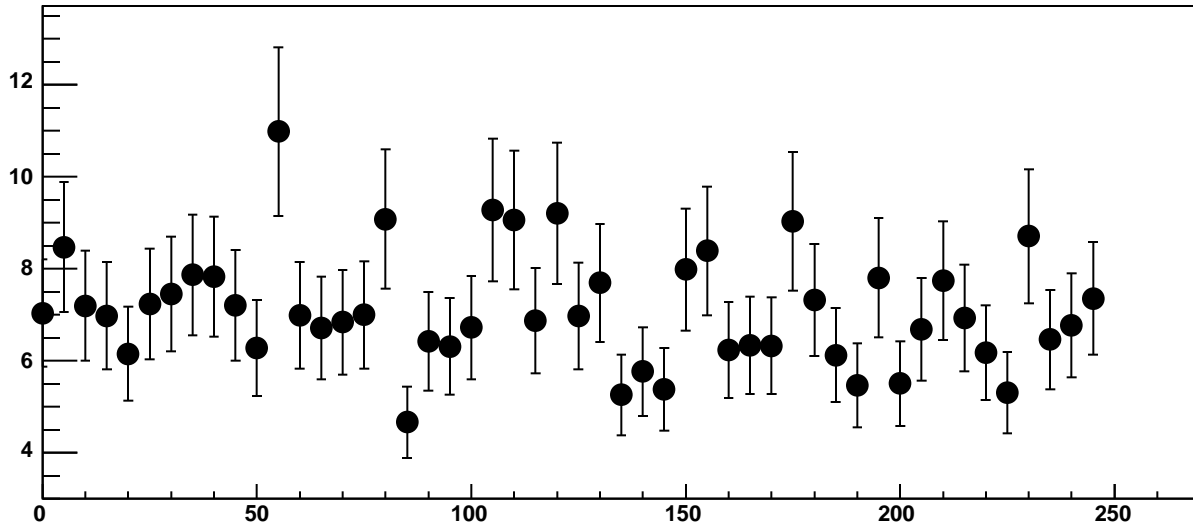


Chip 3, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

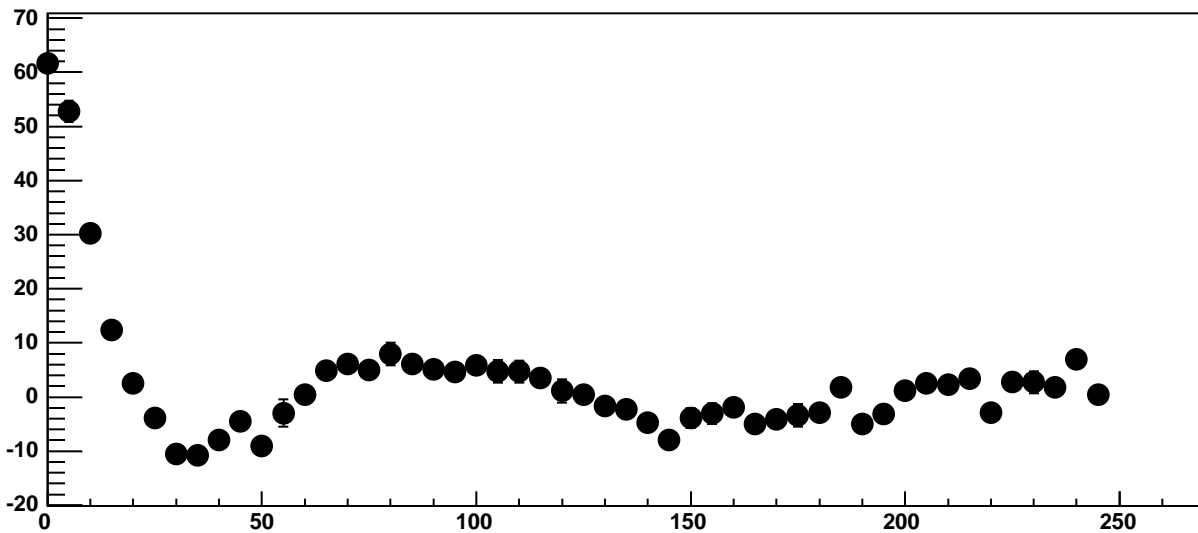


$\chi^2 / \text{ndf}$	500.1 / 41
p0	$402 \pm 2.918$
p1	$7.399 \pm 0.6462$
p2	$202.9 \pm 3.11$
p3	$40.43 \pm 0.8266$
p4	$-0.2103 \pm 0.01027$

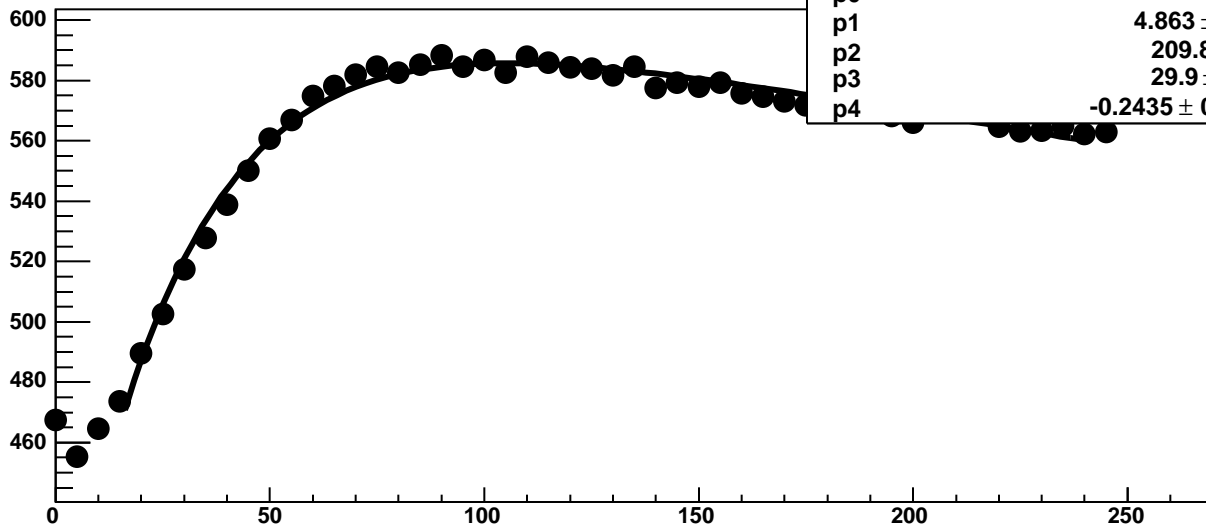
Chip 3, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

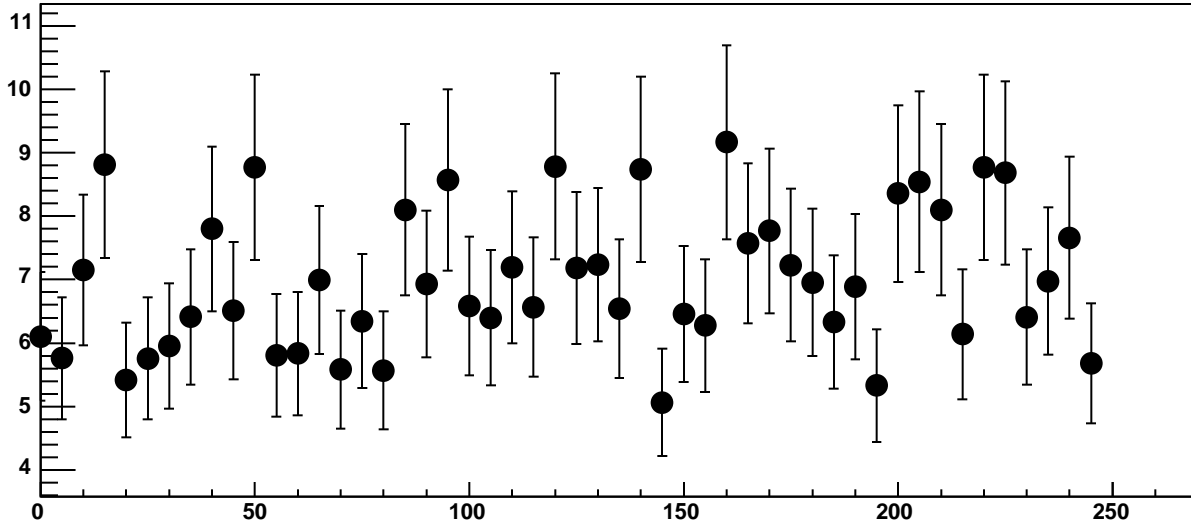


Chip 3, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold

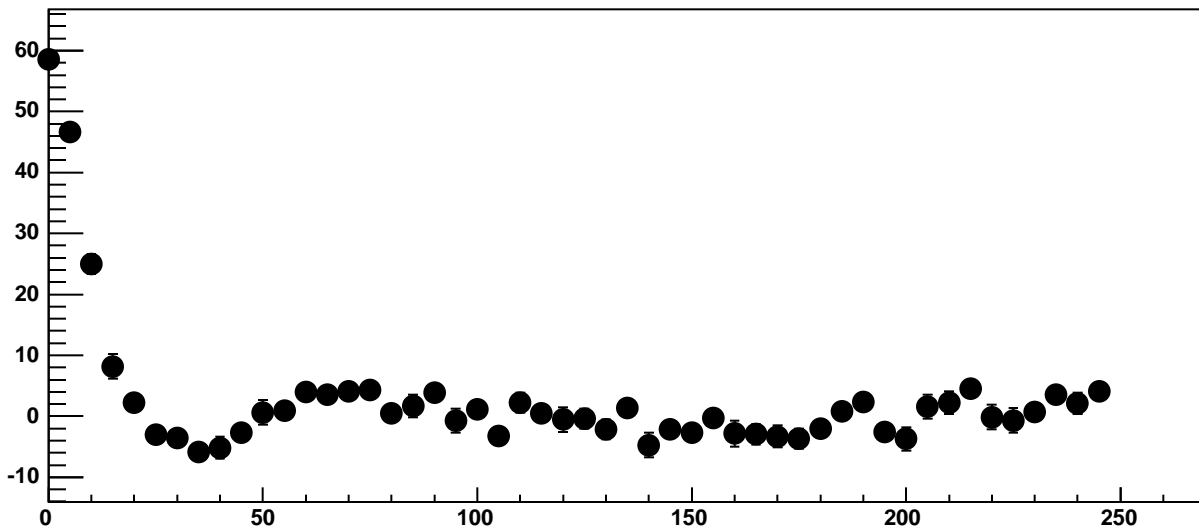


$\chi^2 / \text{ndf}$	165.5 / 41
p0	$407.7 \pm 2.926$
p1	$4.863 \pm 0.5244$
p2	$209.8 \pm 3.051$
p3	$29.9 \pm 0.6358$
p4	$-0.2435 \pm 0.008411$

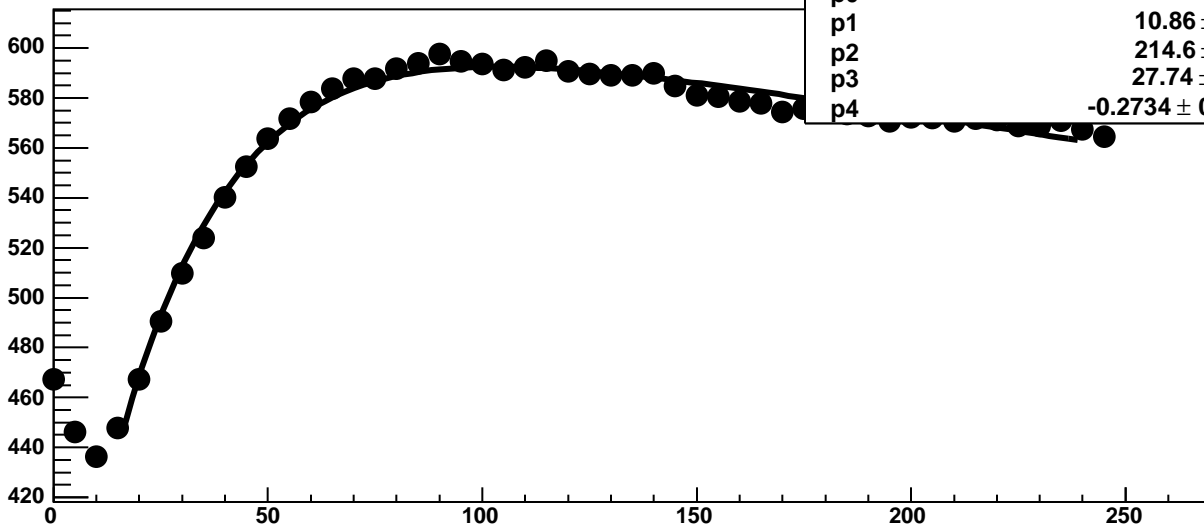
Chip 3, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold

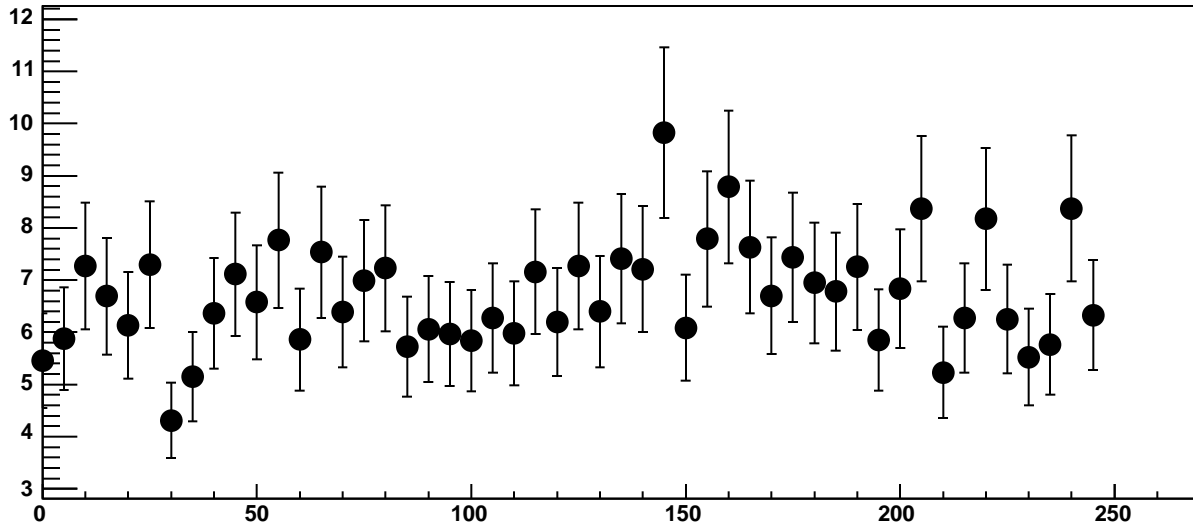


Chip 3, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

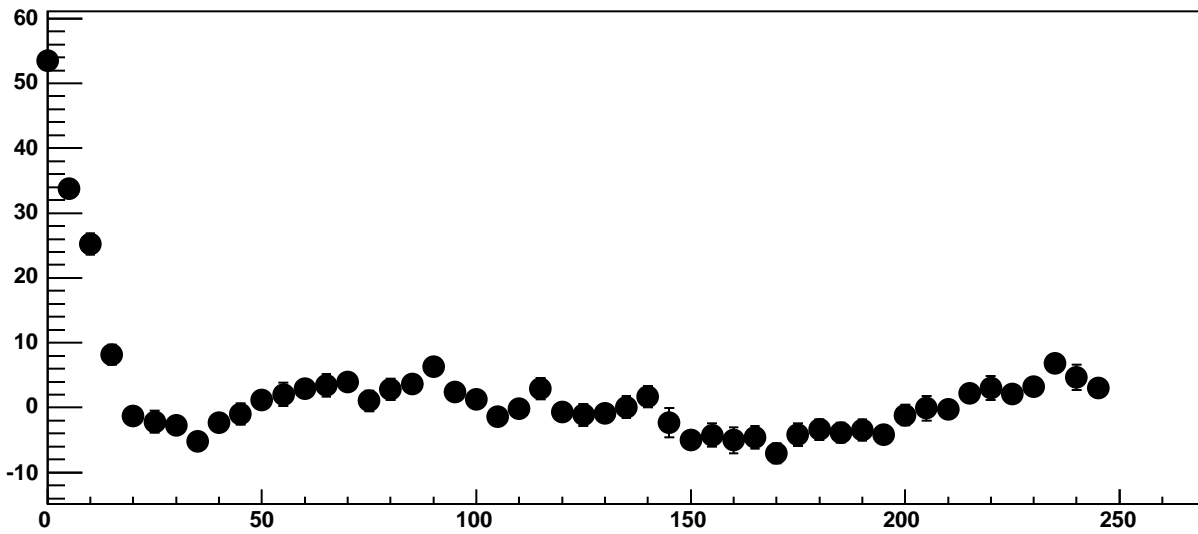


$\chi^2 / \text{ndf}$	250.5 / 41
p0	$410.8 \pm 0.6399$
p1	$10.86 \pm 0.1873$
p2	$214.6 \pm 0.7383$
p3	$27.74 \pm 0.4496$
p4	$-0.2734 \pm 0.007621$

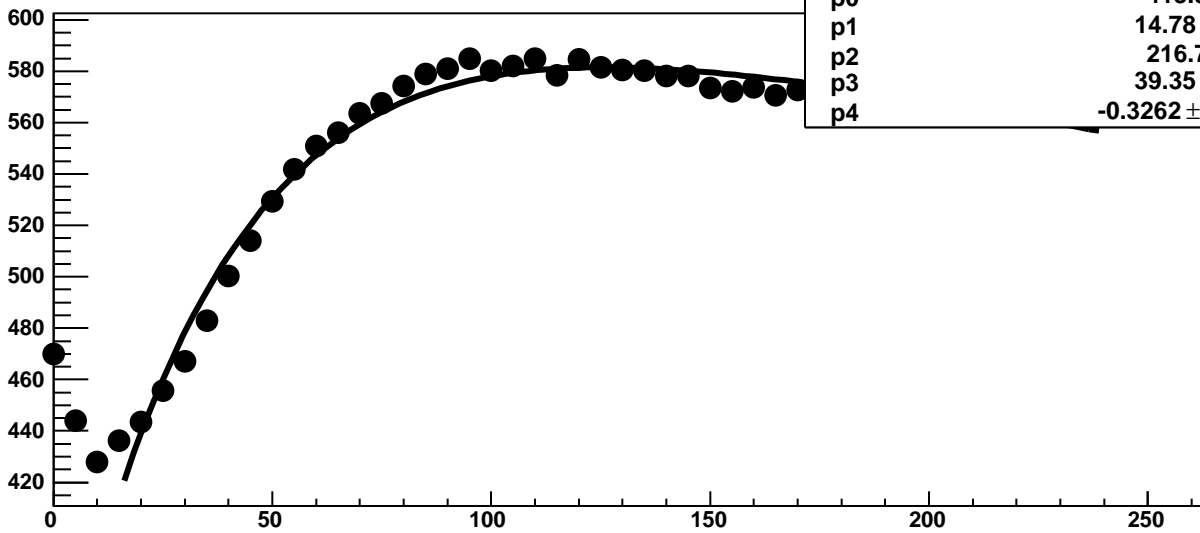
Chip 3, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

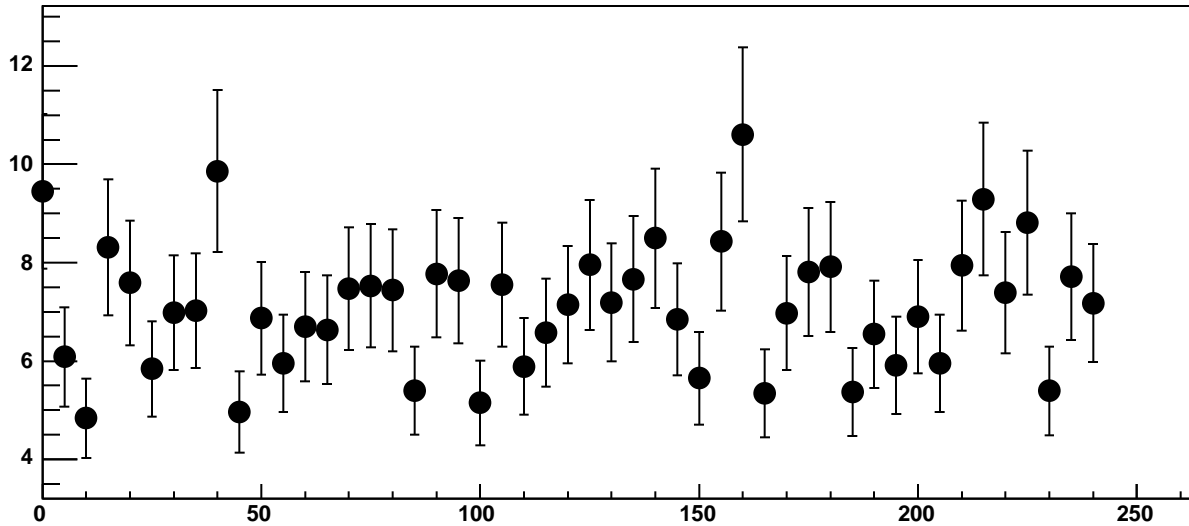


Chip 3, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold

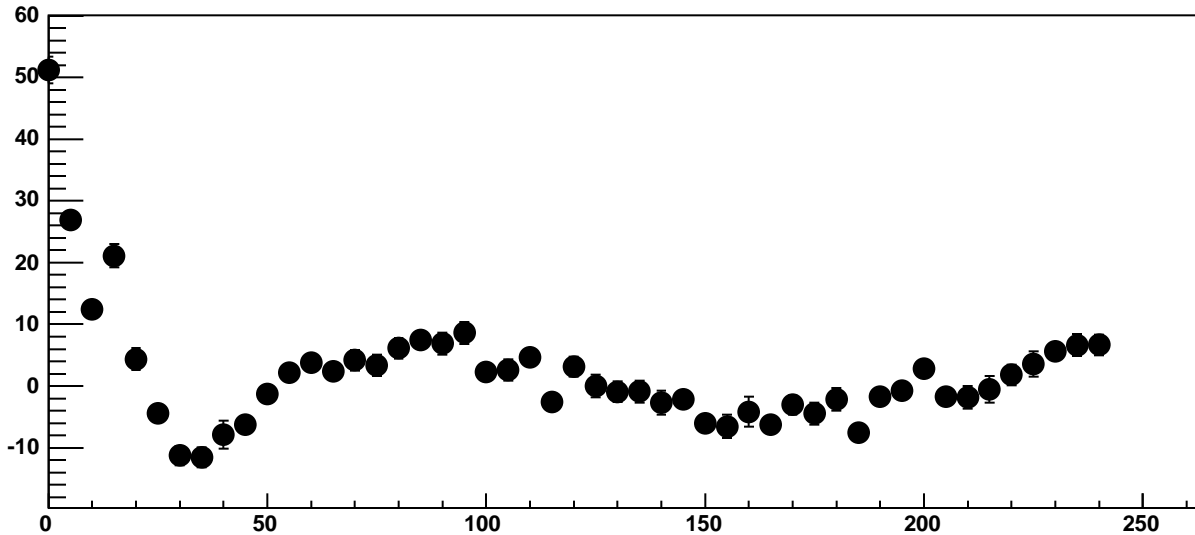


$\chi^2 / \text{ndf}$	594.6 / 41
p0	$413.9 \pm 2.934$
p1	$14.78 \pm 0.5838$
p2	$216.7 \pm 3.195$
p3	$39.35 \pm 0.6493$
p4	$-0.3262 \pm 0.01058$

Chip 3, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold

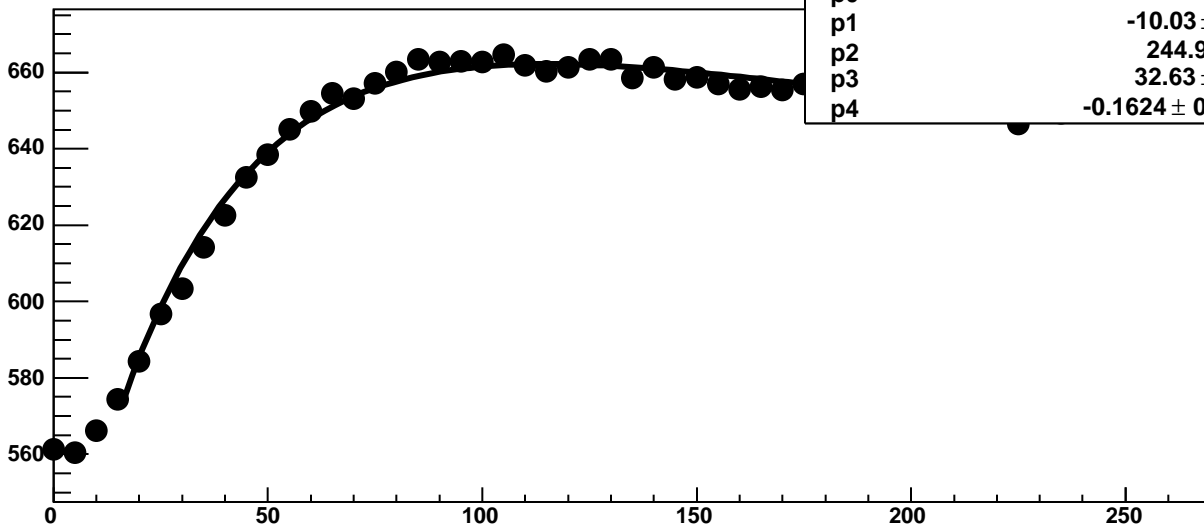


Chip 3, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold



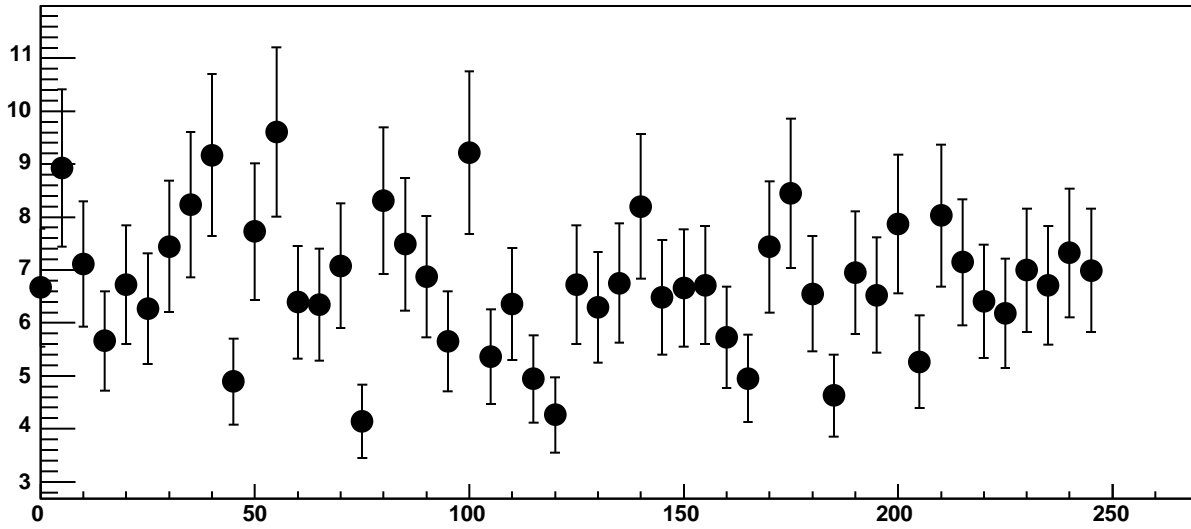


Chip 3, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold

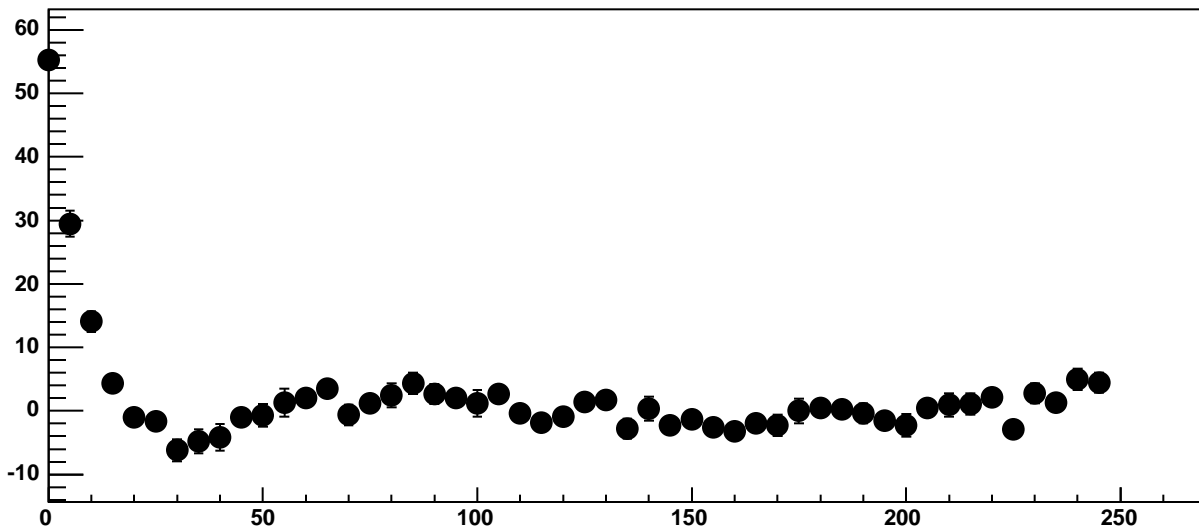


$\chi^2 / \text{ndf}$	114.2 / 41
p0	$442.9 \pm 0.9598$
p1	$-10.03 \pm 0.6769$
p2	$244.9 \pm 1.028$
p3	$32.63 \pm 0.9582$
p4	$-0.1624 \pm 0.009305$

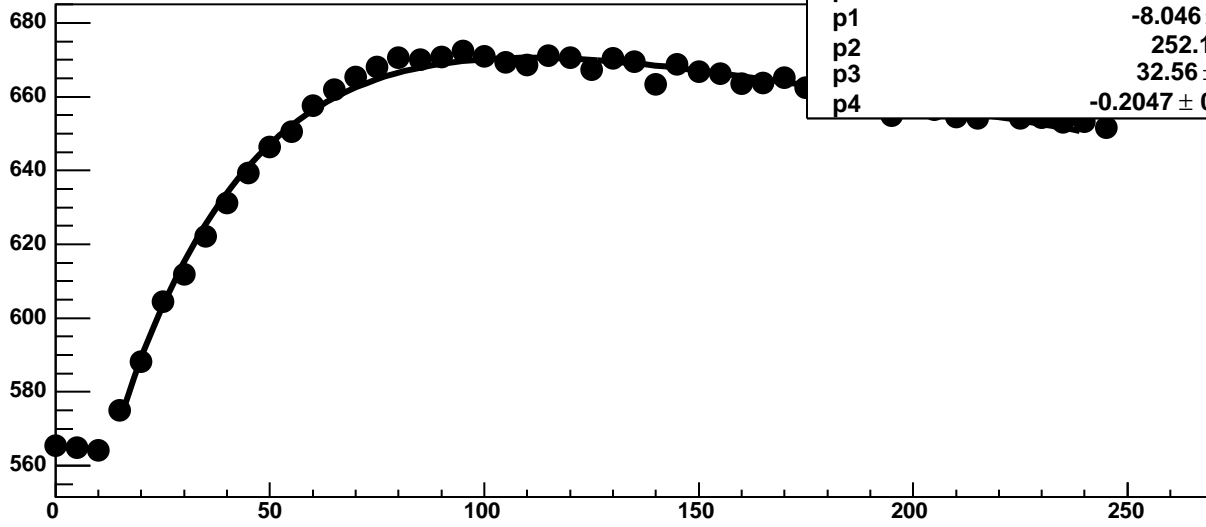
Chip 3, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold

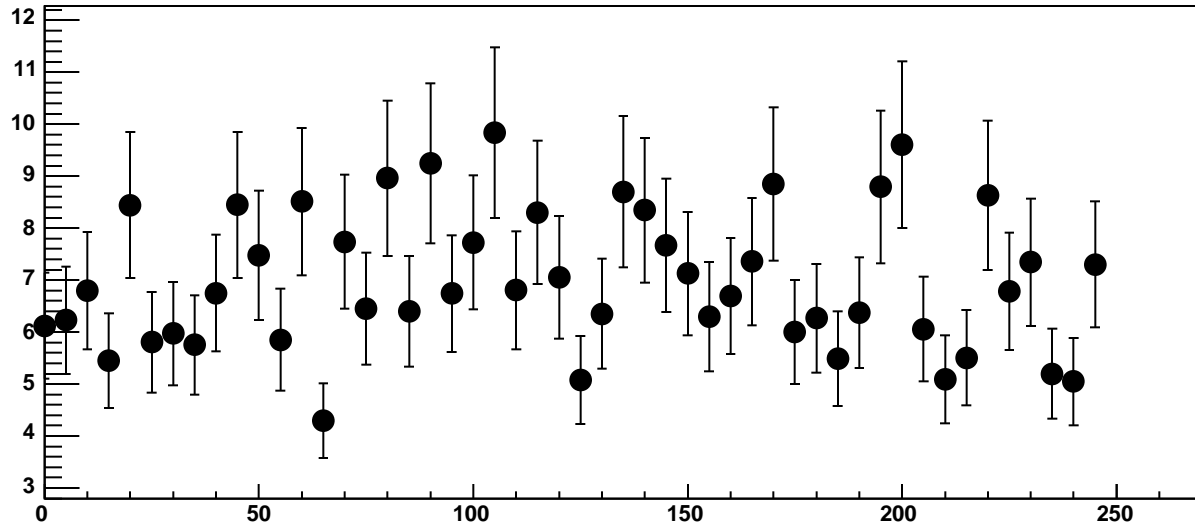


Chip 3, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

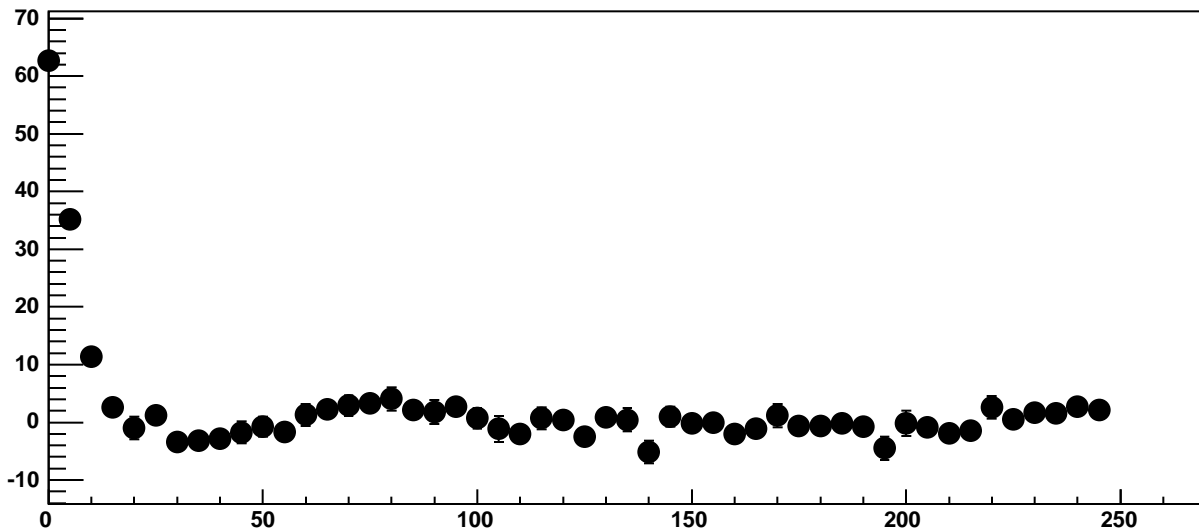


$\chi^2 / \text{ndf}$	83.11 / 41
p0	$449.3 \pm 2.854$
p1	$-8.046 \pm 0.6551$
p2	$252.1 \pm 2.943$
p3	$32.56 \pm 0.7659$
p4	$-0.2047 \pm 0.008021$

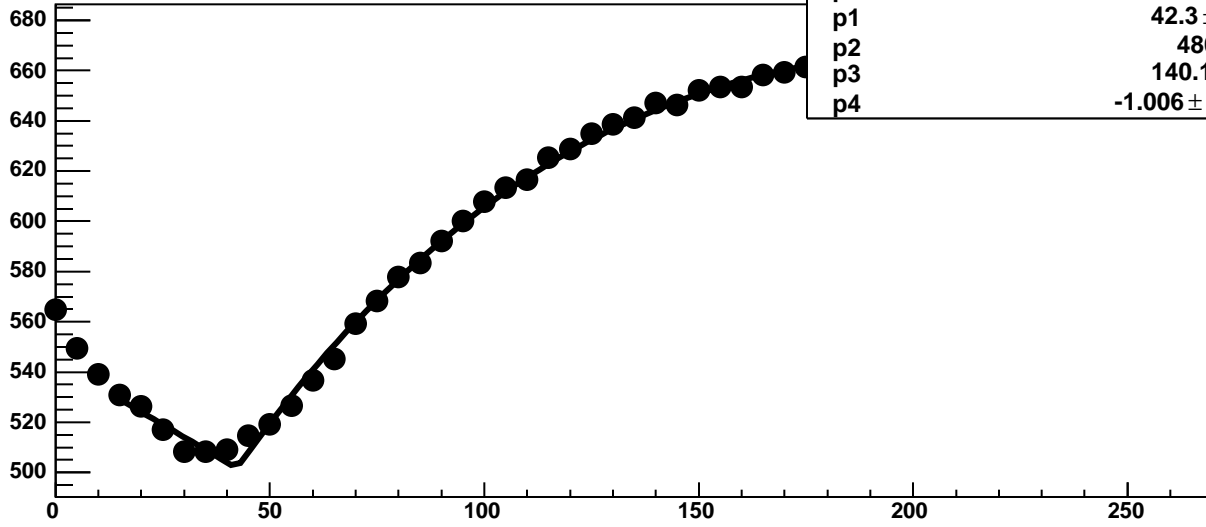
Chip 3, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold

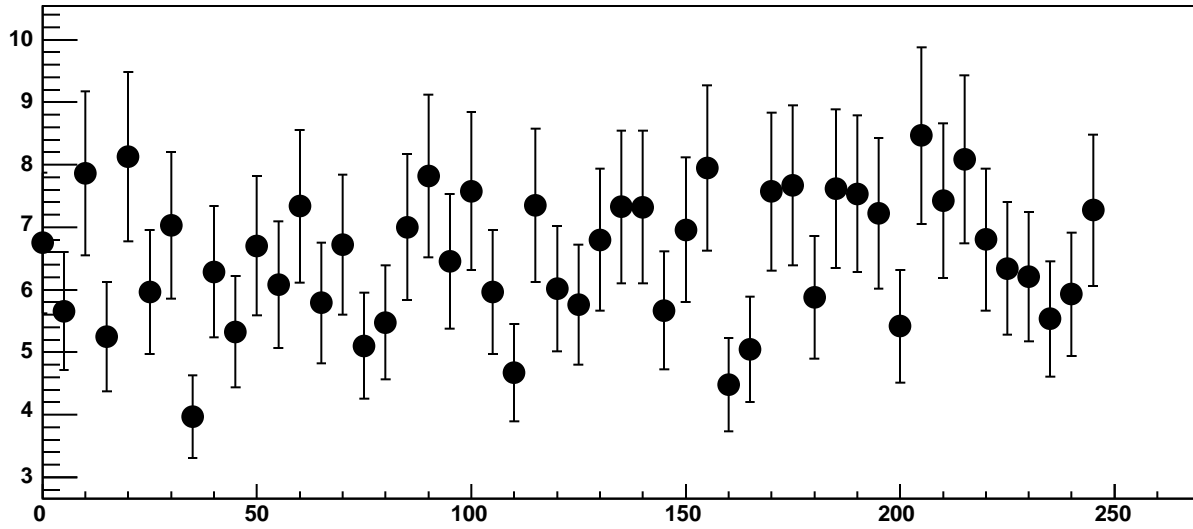


Chip 3, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

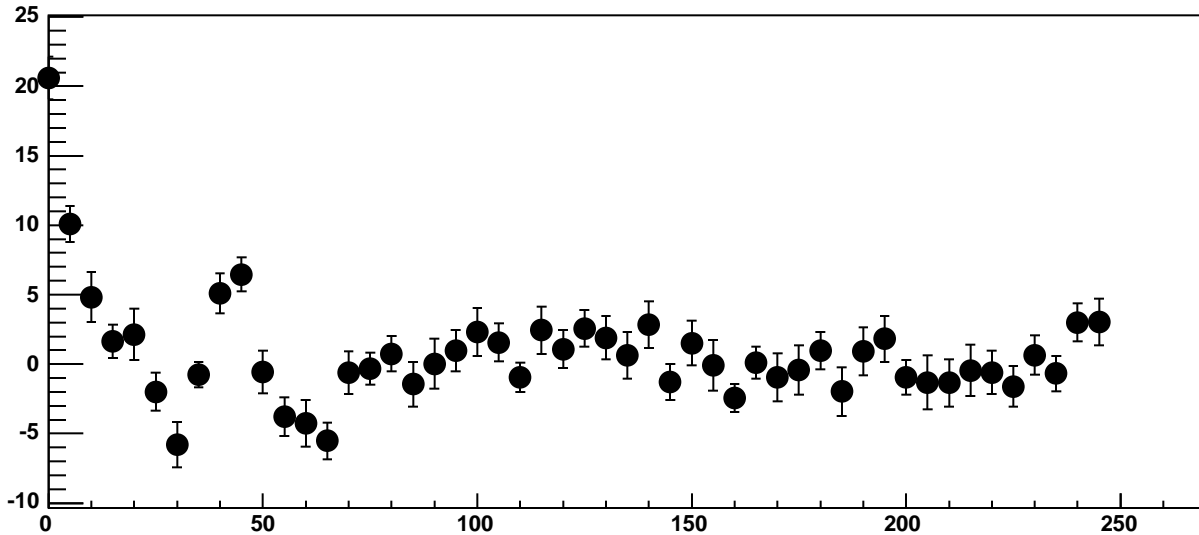


$\chi^2 / \text{ndf}$	126.7 / 41
p0	$501.7 \pm 0.6743$
p1	$42.3 \pm 0.4012$
p2	$480 \pm 23.4$
p3	$140.1 \pm 4.993$
p4	$-1.006 \pm 0.05883$

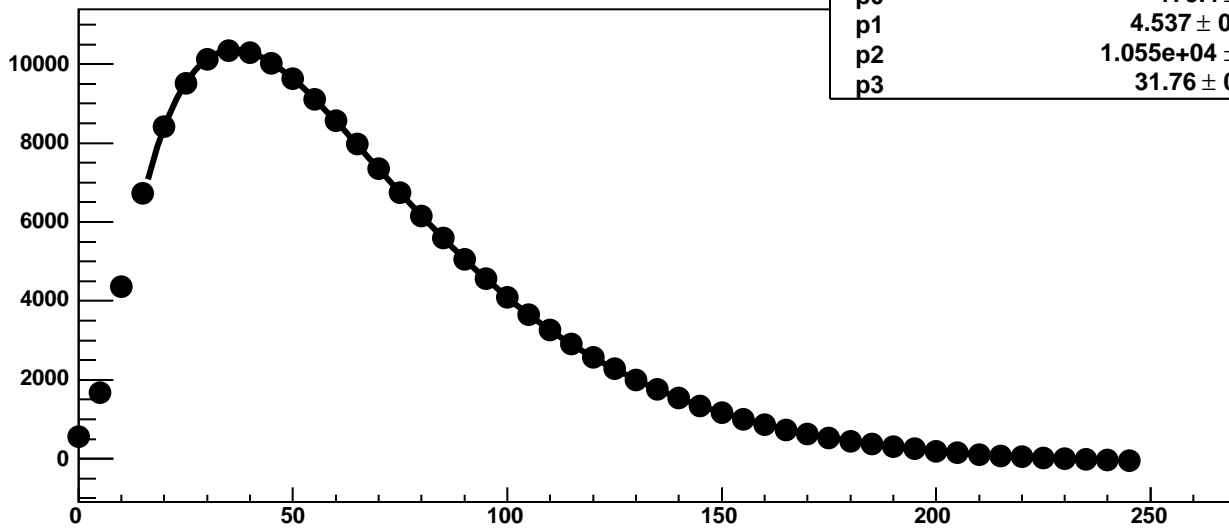
Chip 3, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

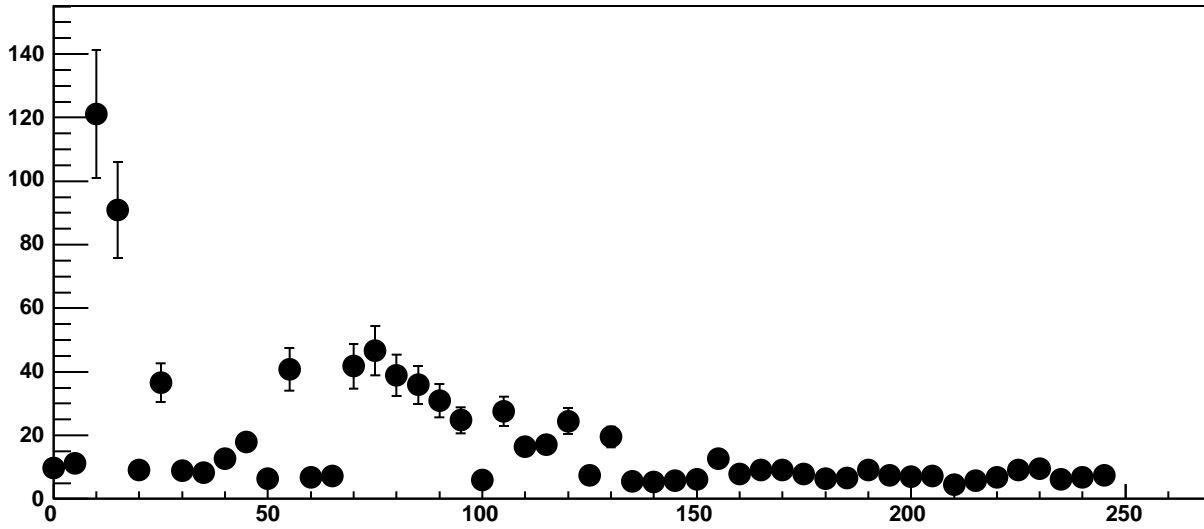


Chip 3, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold

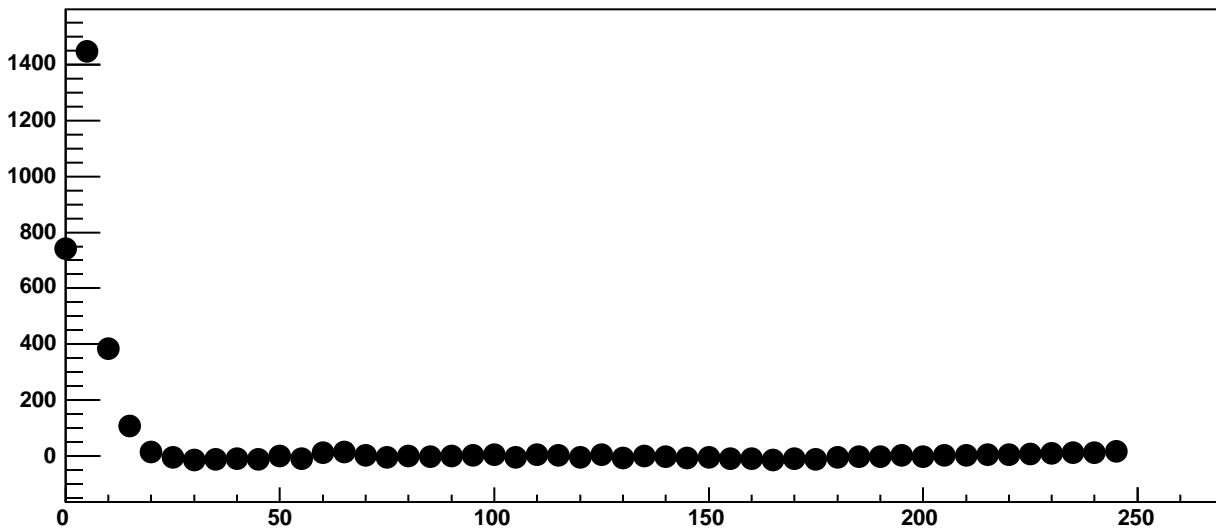


$\chi^2 / \text{ndf}$	740.3 / 42
p0	-175.4 ± 0.5735
p1	4.537 ± 0.008624
p2	1.055e+04 ± 0.8966
p3	31.76 ± 0.005851

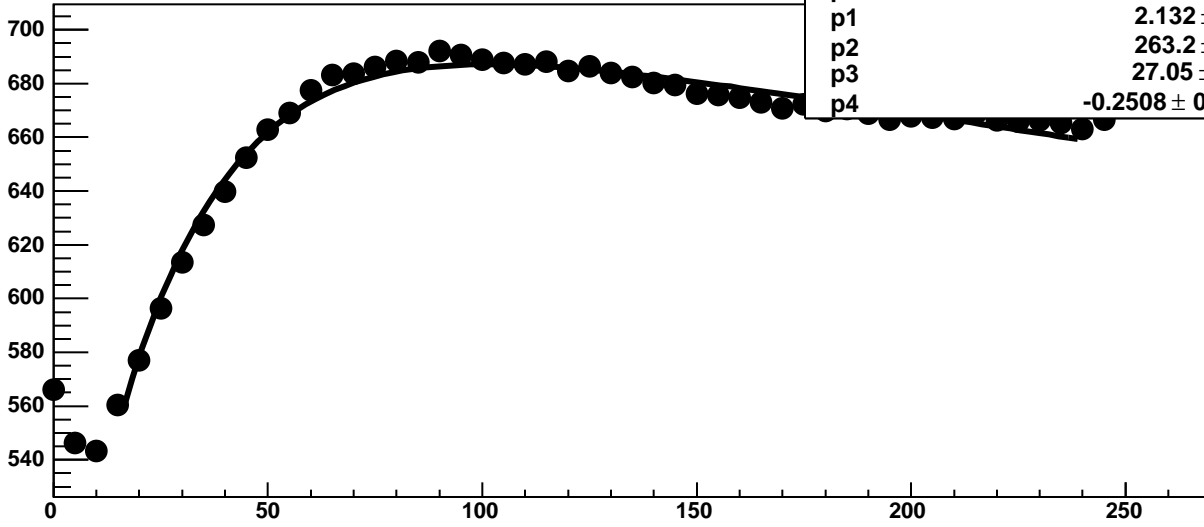
Chip 3, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold

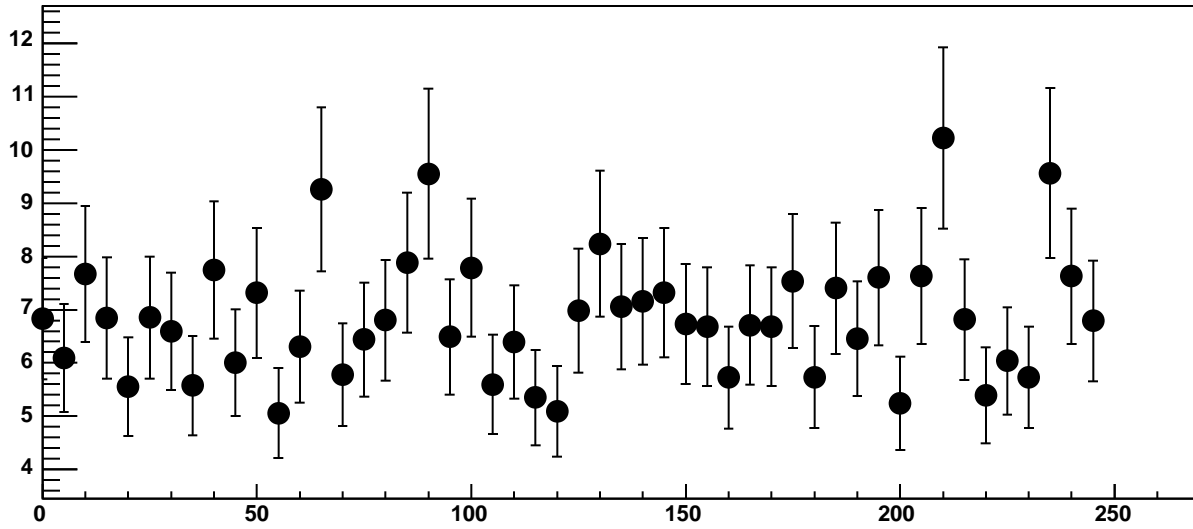


Chip 3, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

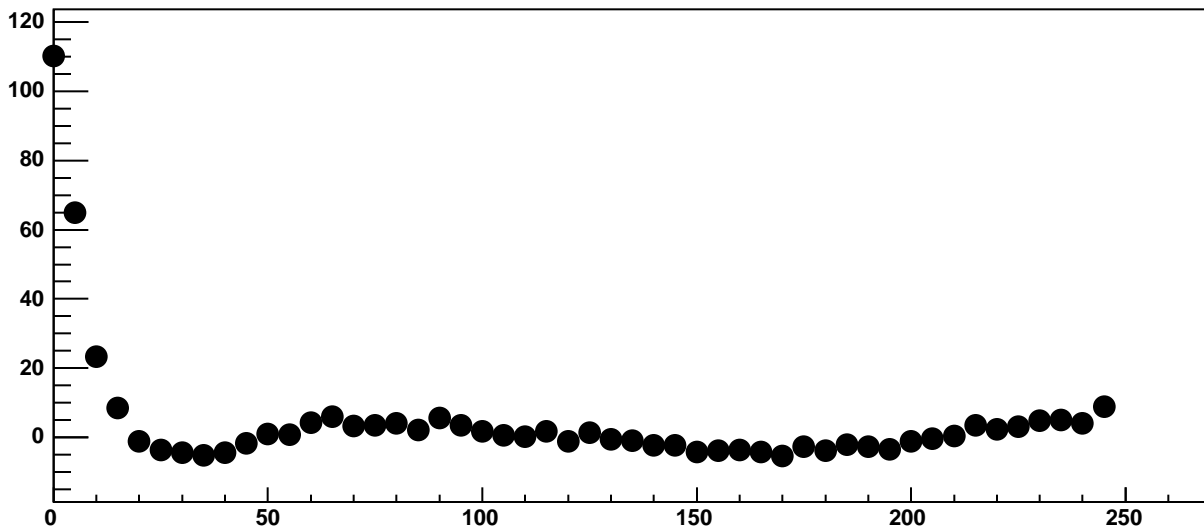


$\chi^2 / \text{ndf}$	224.8 / 41
p0	$455.5 \pm 0.6747$
p1	$2.132 \pm 0.2968$
p2	$263.2 \pm 0.7643$
p3	$27.05 \pm 0.4876$
p4	$-0.2508 \pm 0.007634$

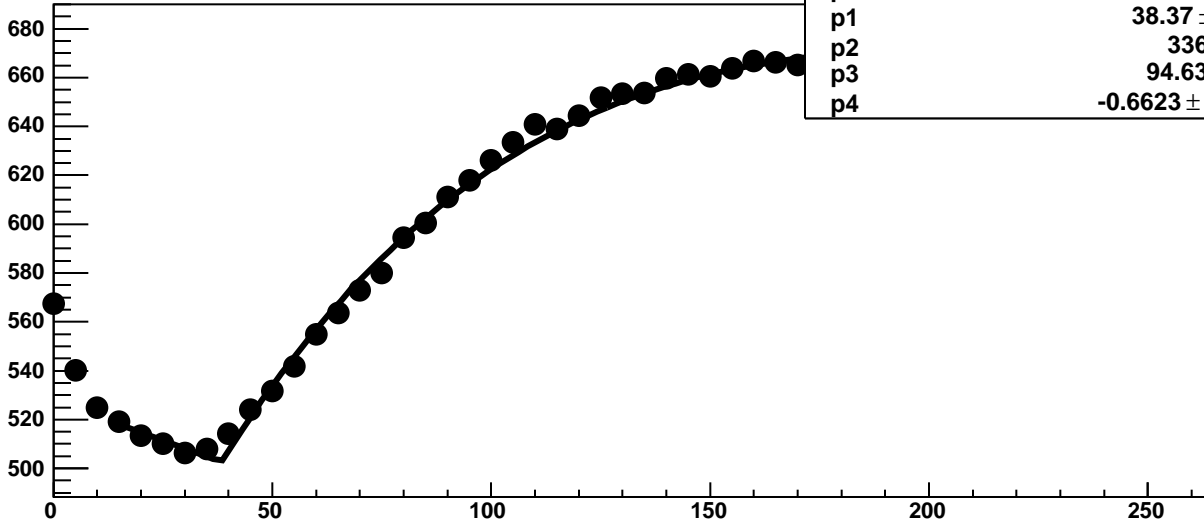
Chip 3, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

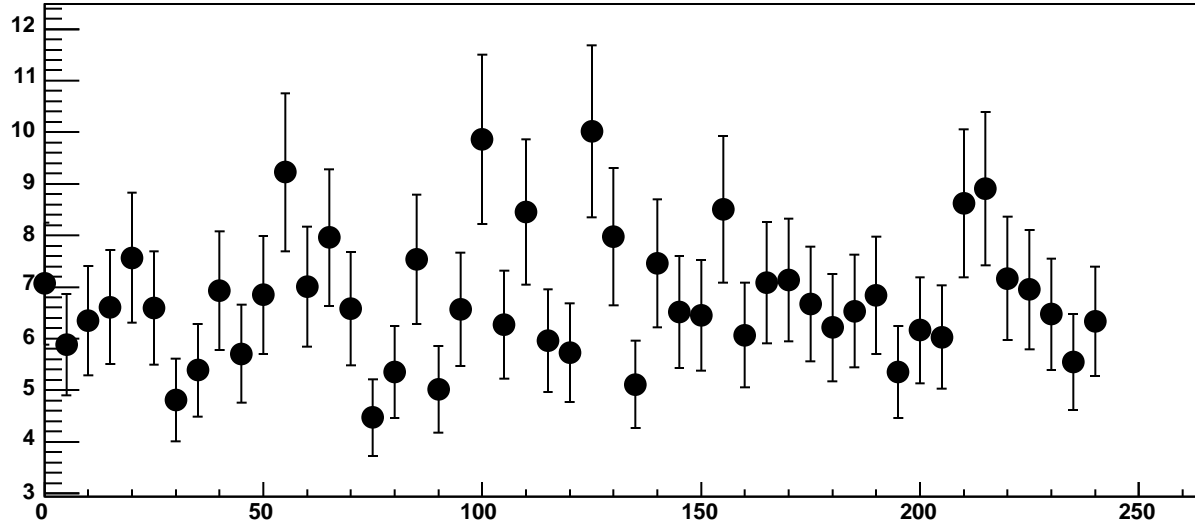


Chip 3, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold

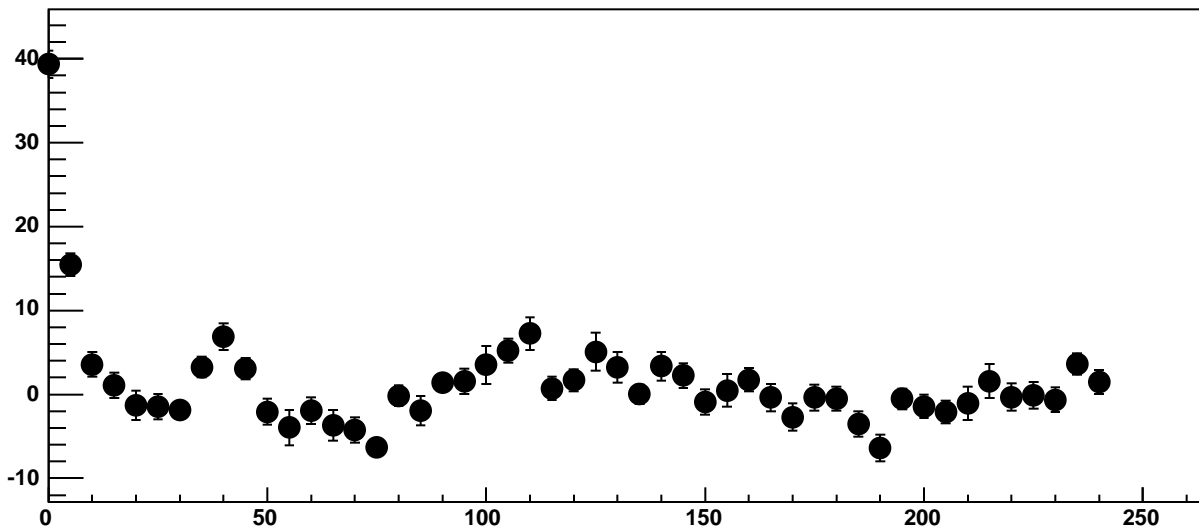


$\chi^2 / \text{ndf}$	182.2 / 41
p0	$502.6 \pm 0.6906$
p1	$38.37 \pm 0.4144$
p2	$336 \pm 16.69$
p3	$94.63 \pm 3.923$
p4	$-0.6623 \pm 0.05506$

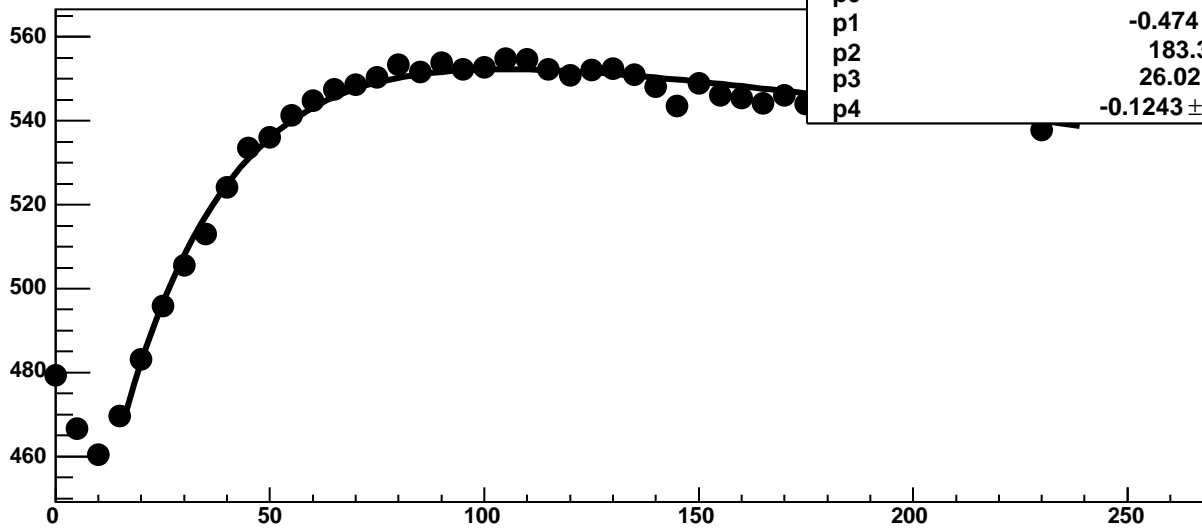
Chip 3, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

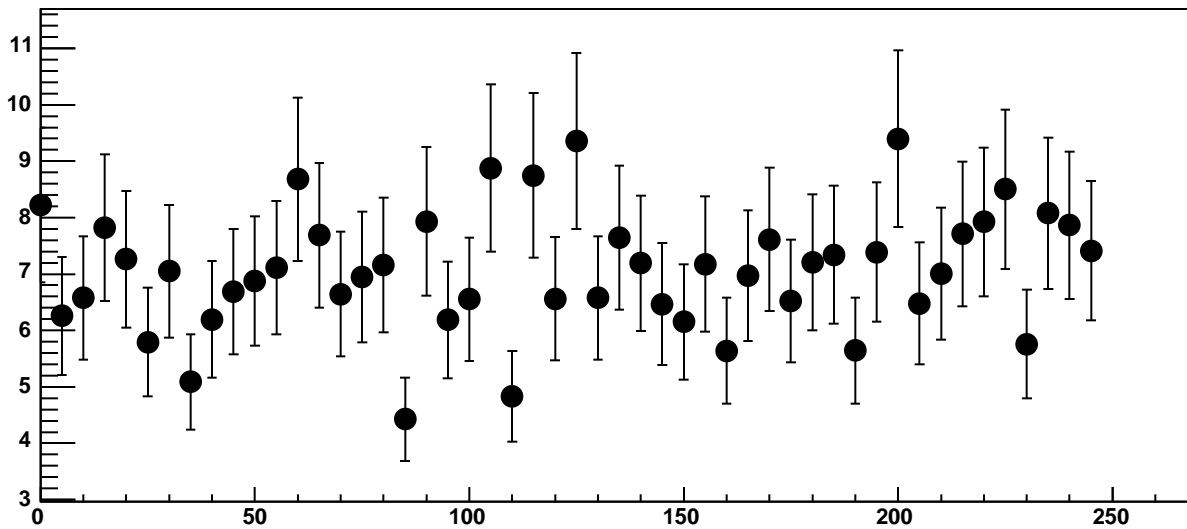


Chip 3, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

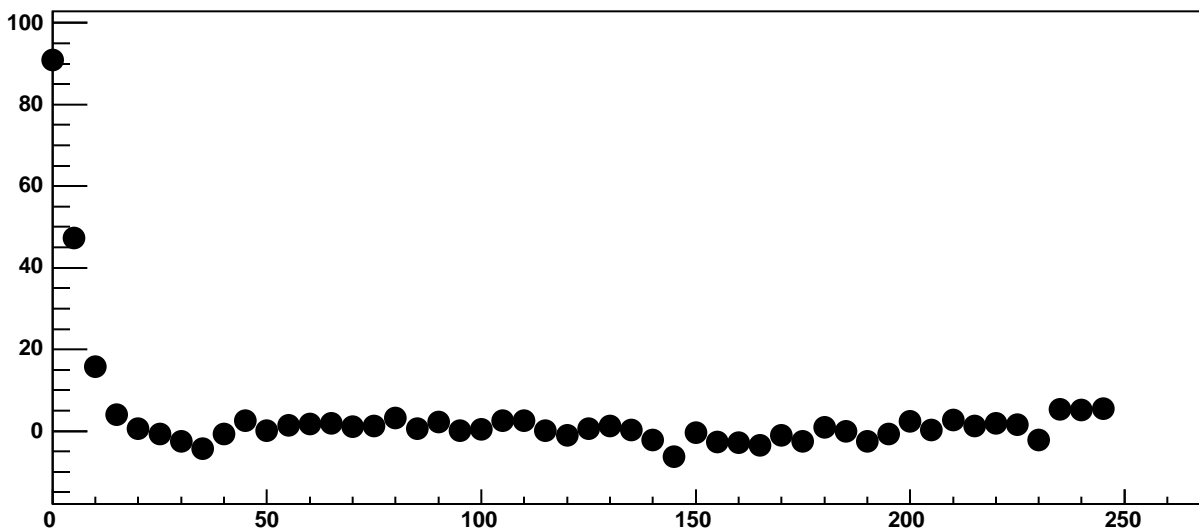


$\chi^2 / \text{ndf}$	106.6 / 41
p0	$385.3 \pm 2.899$
p1	$-0.474 \pm 0.7097$
p2	$183.3 \pm 2.957$
p3	$26.02 \pm 0.7561$
p4	$-0.1243 \pm 0.00726$

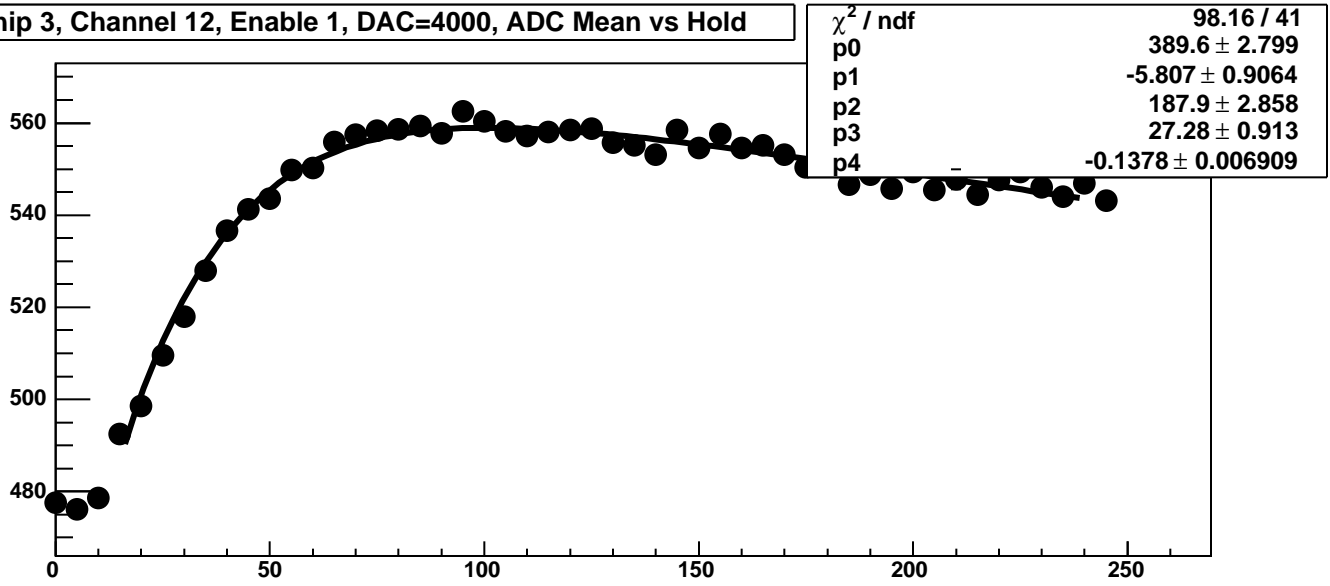
Chip 3, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



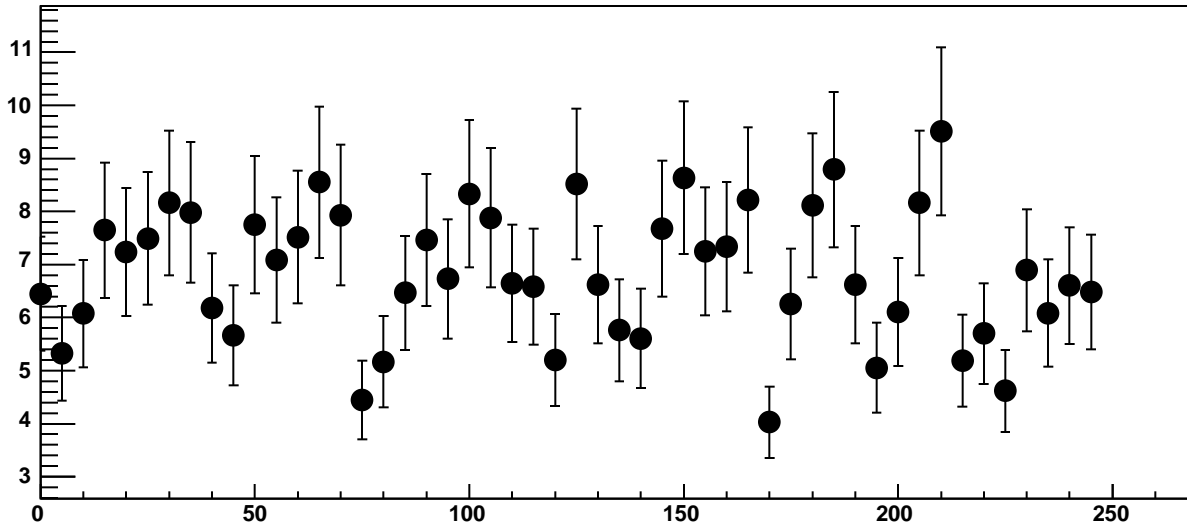
Chip 3, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold



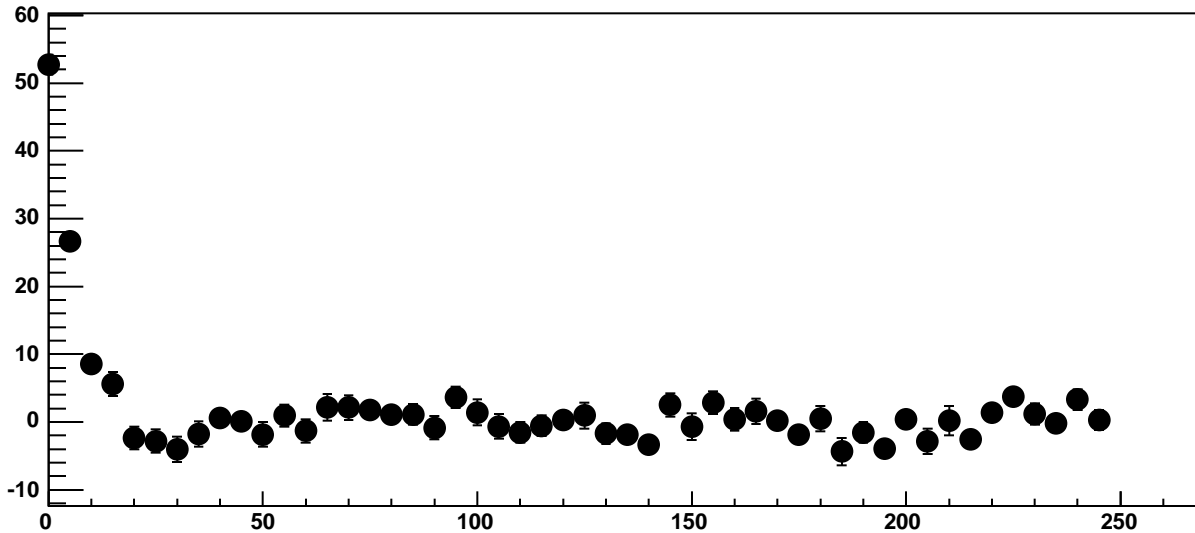
Chip 3, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 3, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold

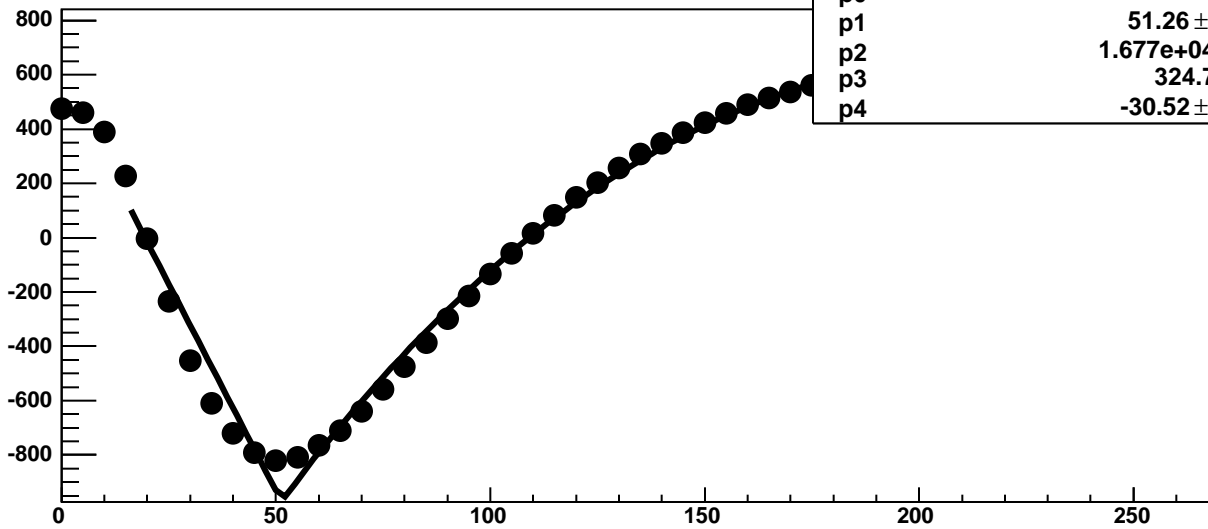


Chip 3, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold

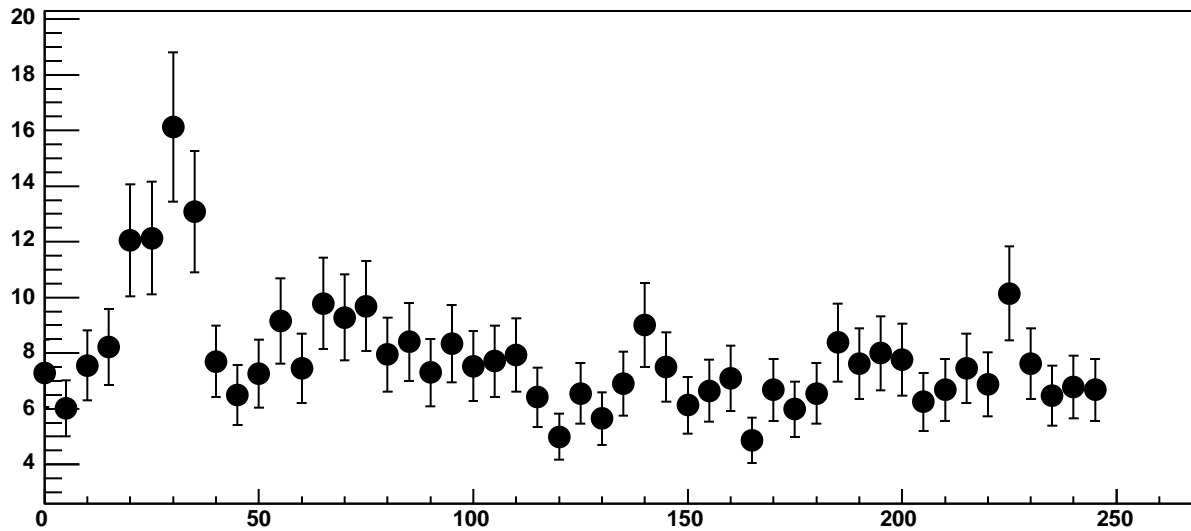




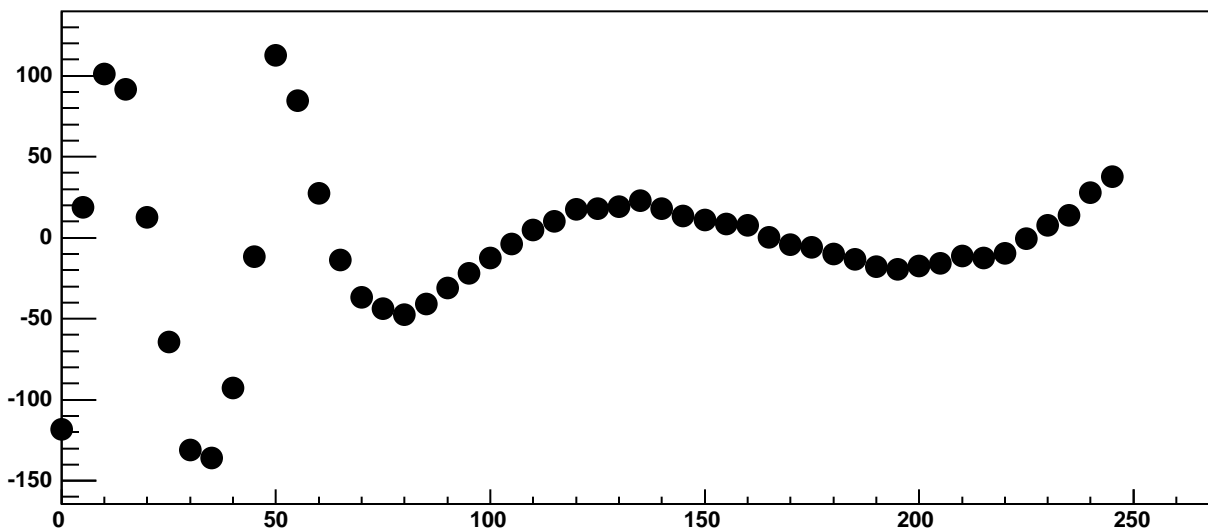
Chip 3, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold



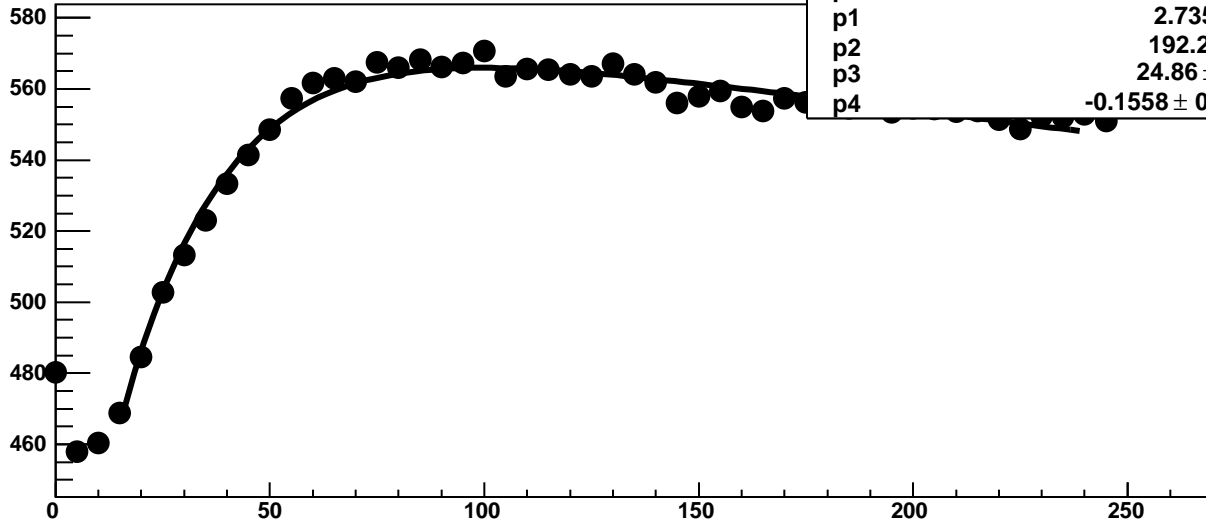
Chip 3, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

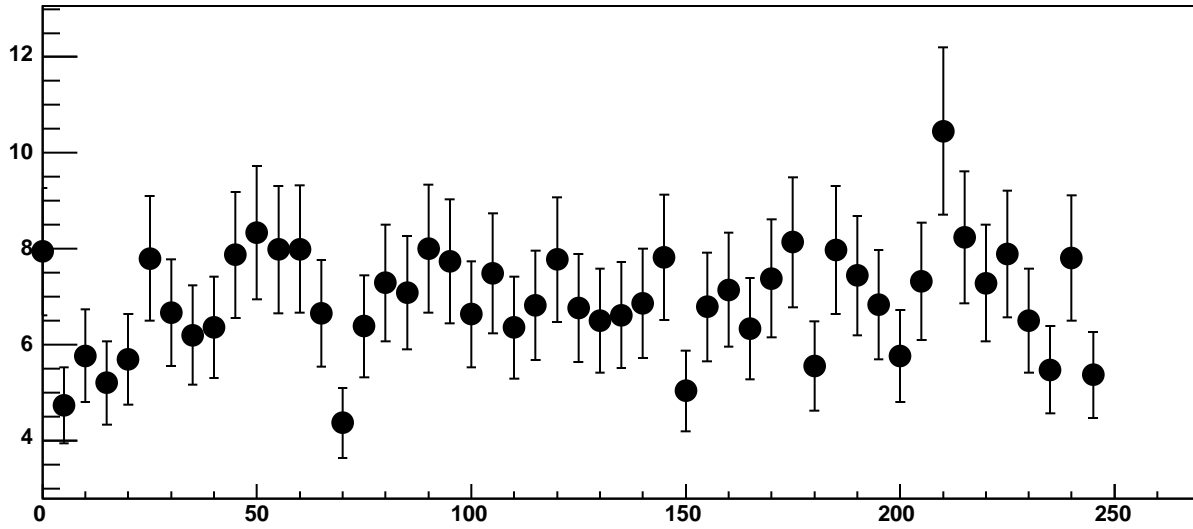


Chip 3, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

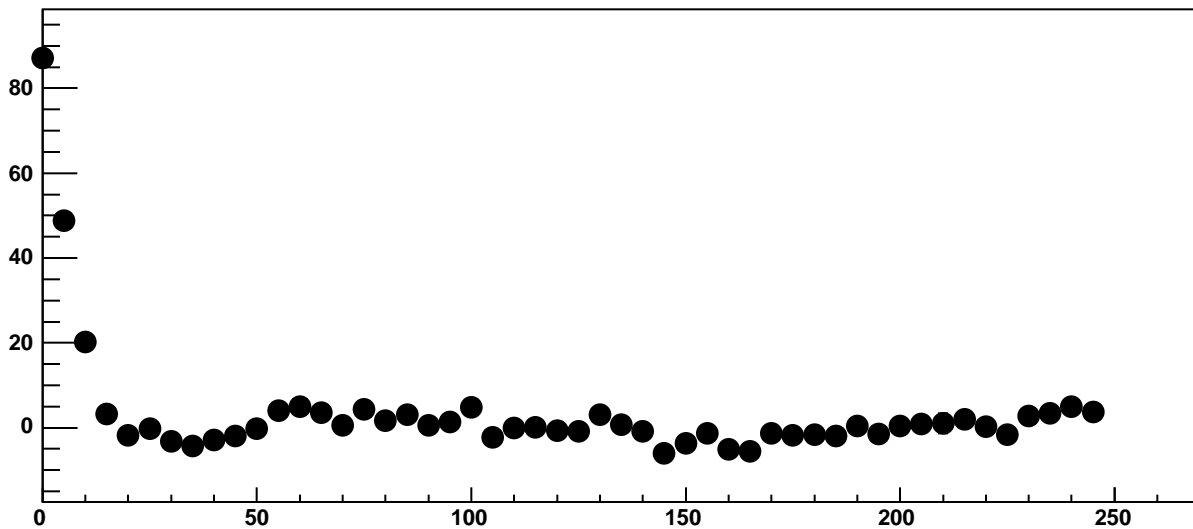


$\chi^2 / \text{ndf}$	148.7 / 41
p0	$392.7 \pm 2.908$
p1	$2.735 \pm 0.511$
p2	$192.2 \pm 2.975$
p3	$24.86 \pm 0.5807$
p4	$-0.1558 \pm 0.006907$

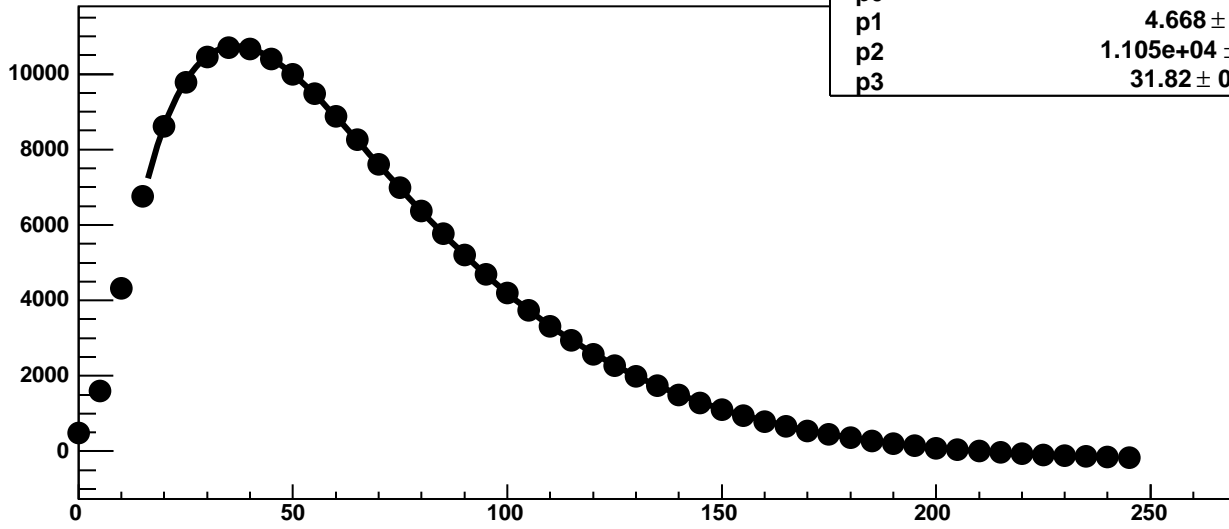
Chip 3, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

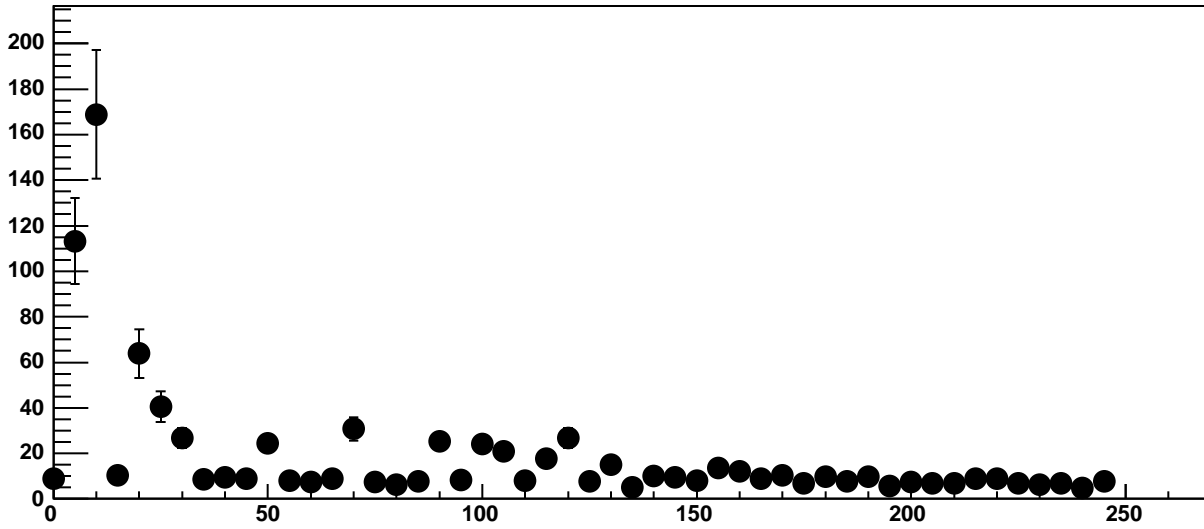


Chip 3, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold

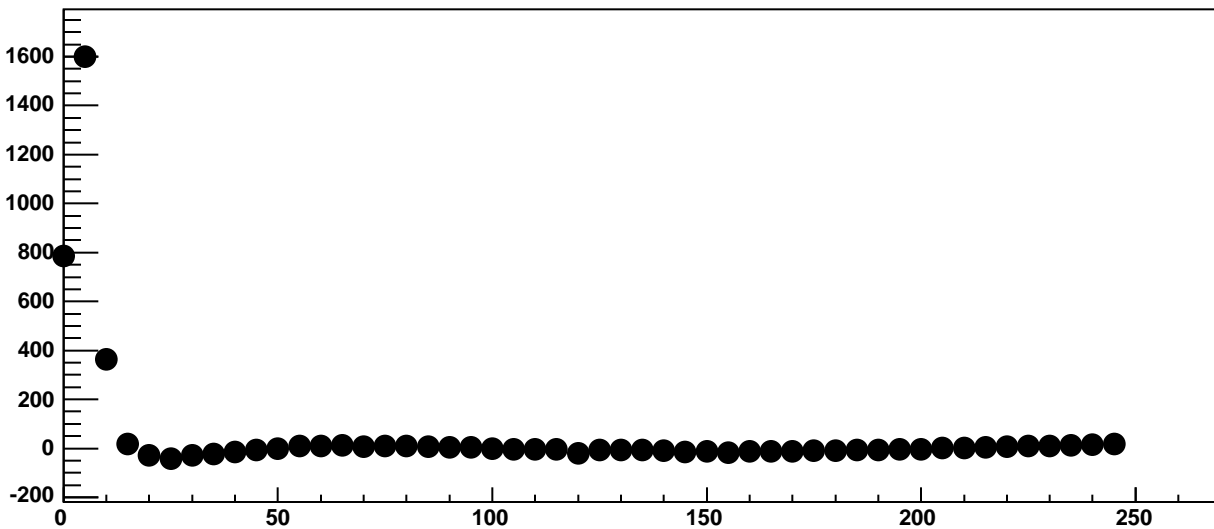


$\chi^2 / \text{ndf}$	1268 / 42
p0	-307.8 ± 0.5408
p1	4.668 ± 0.00607
p2	1.105e+04 ± 0.9664
p3	31.82 ± 0.004738

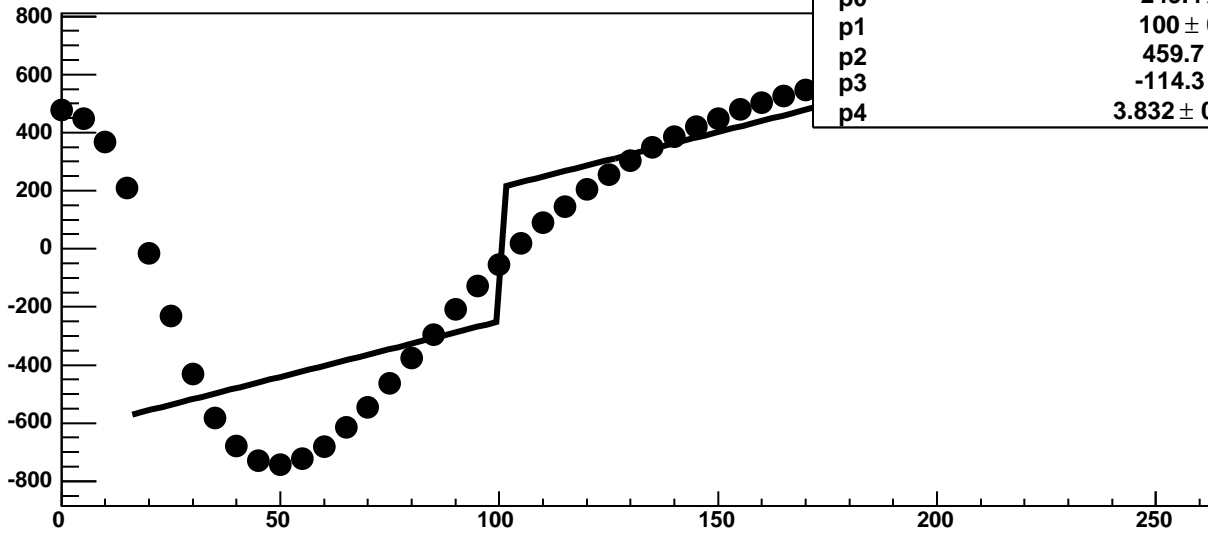
Chip 3, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold

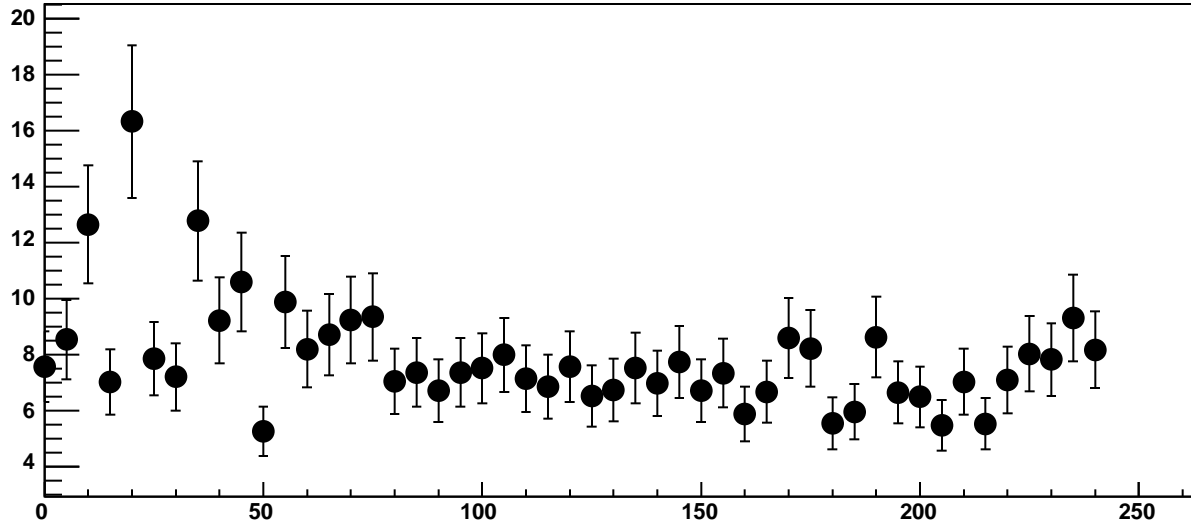


Chip 3, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

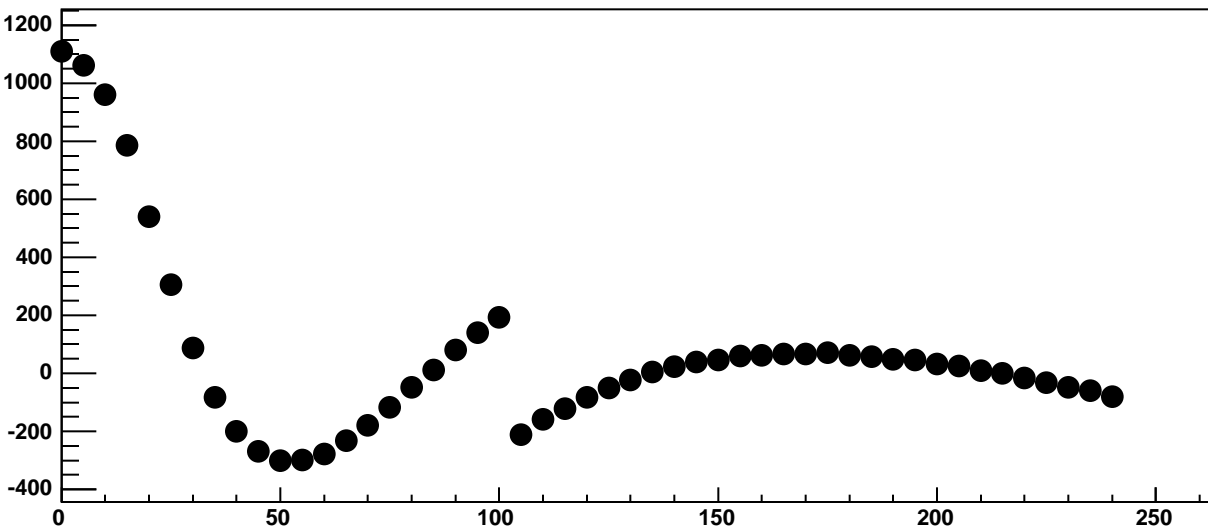


$\chi^2 / \text{ndf}$	5.124e+05 / 41
p0	-249.4 ± 0.1924
p1	100 ± 0.003701
p2	459.7 ± 0.2592
p3	-114.3 ± 0.1359
p4	3.832 ± 0.002784

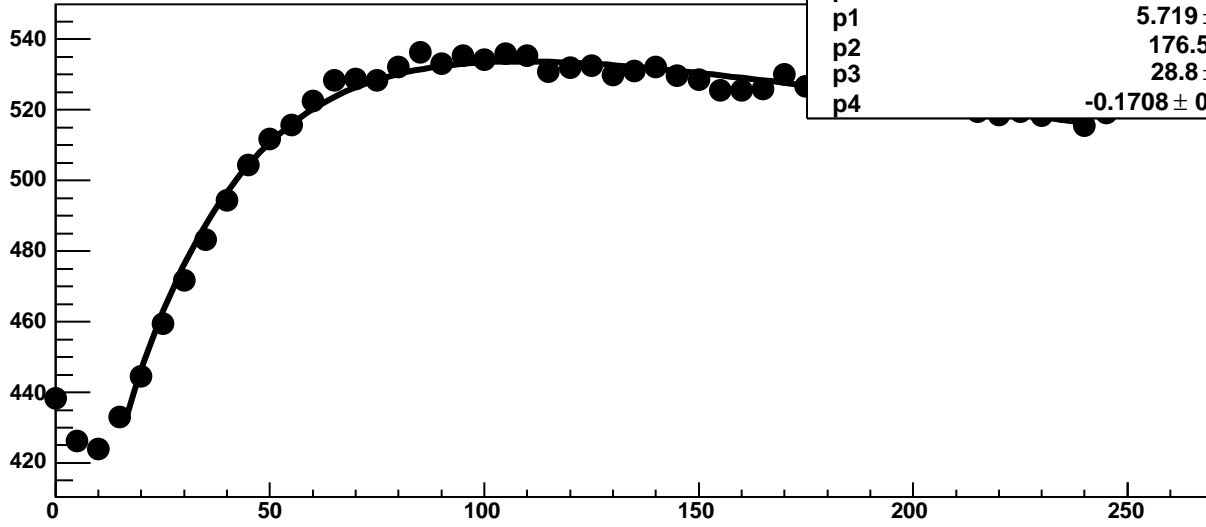
Chip 3, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold

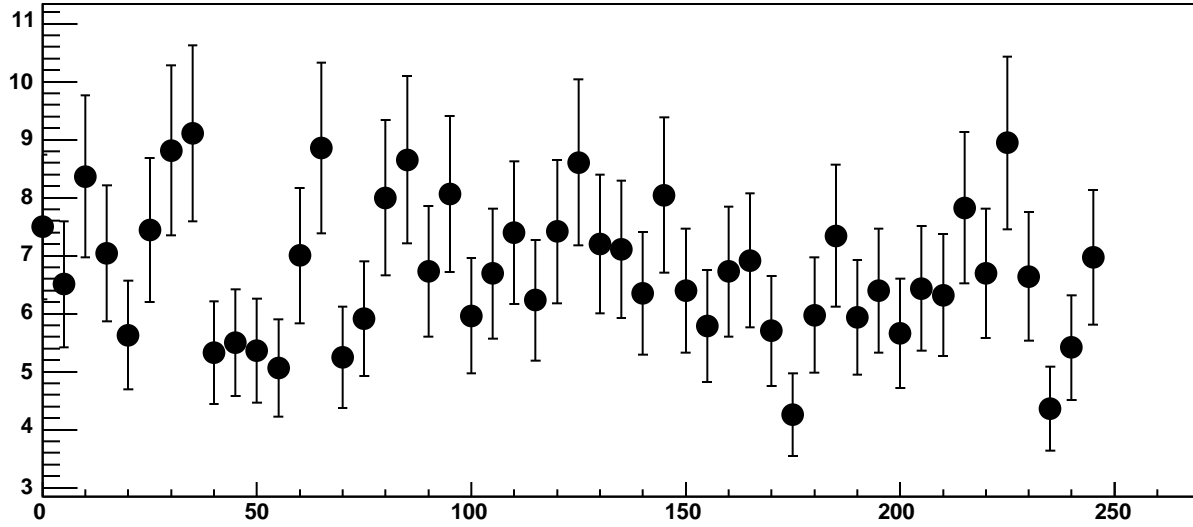


Chip 3, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold

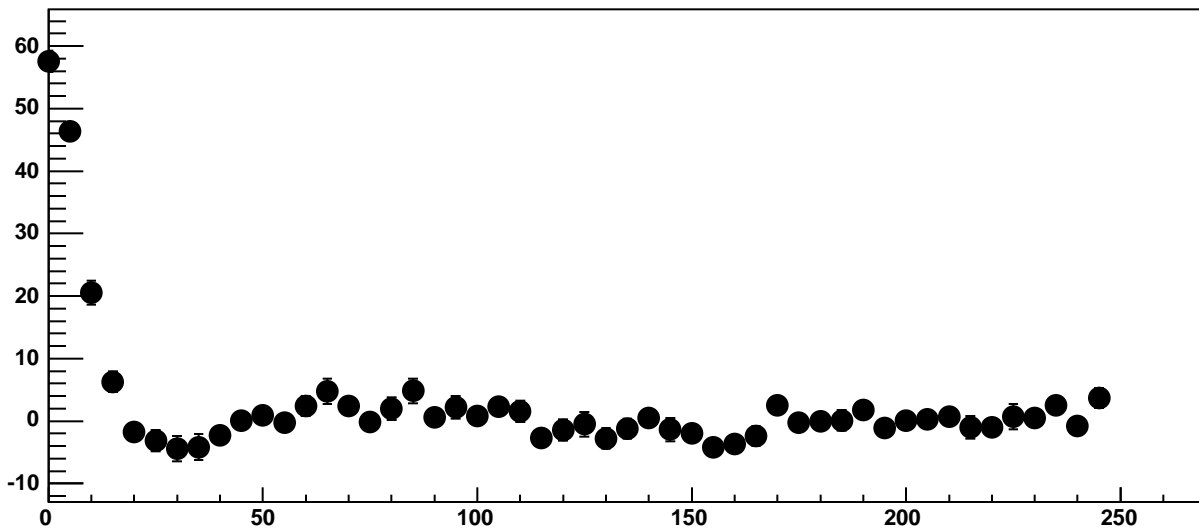


$\chi^2 / \text{ndf}$	98.54 / 41
p0	$379.8 \pm 2.751$
p1	$5.719 \pm 0.5593$
p2	$176.5 \pm 2.844$
p3	$28.8 \pm 0.6497$
p4	$-0.1708 \pm 0.007369$

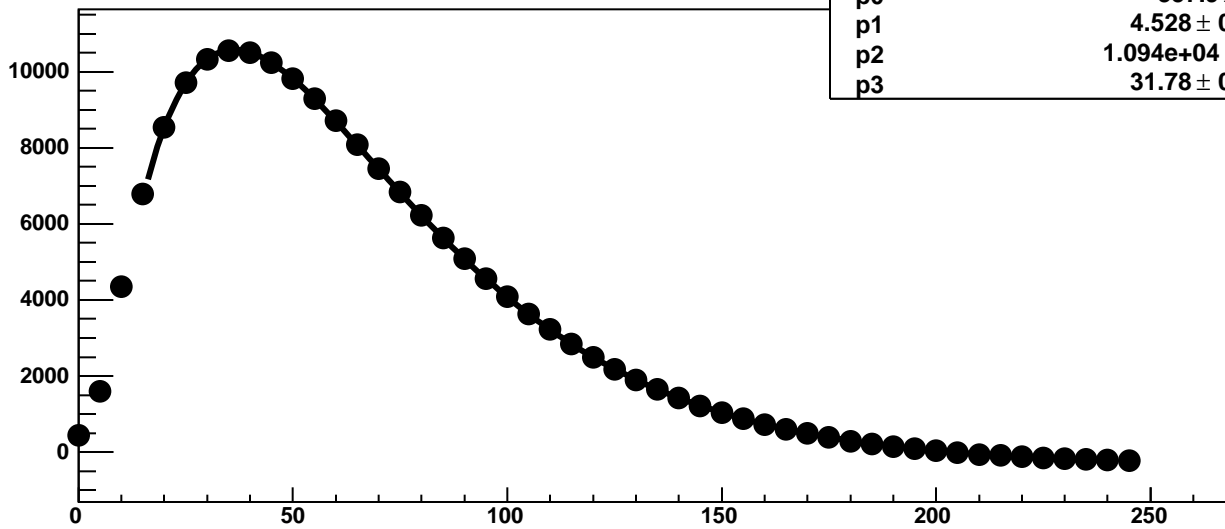
Chip 3, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold



Chip 3, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold



$\chi^2 / \text{ndf}$

406.9 / 42

p0

$-357.9 \pm 0.6084$

p1

$4.528 \pm 0.007936$

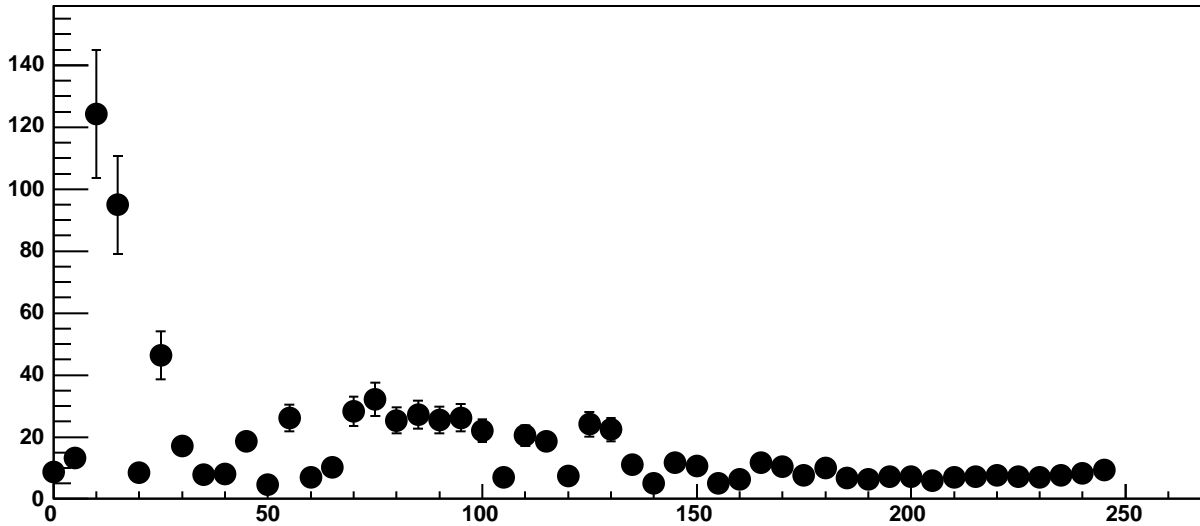
p2

$1.094\text{e}+04 \pm 0.8617$

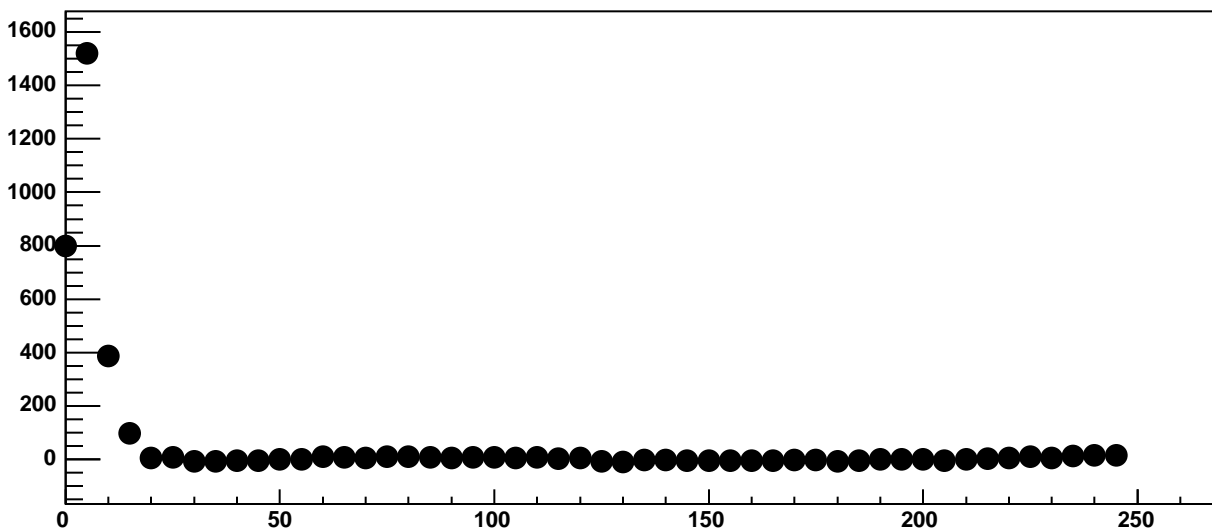
p3

$31.78 \pm 0.005836$

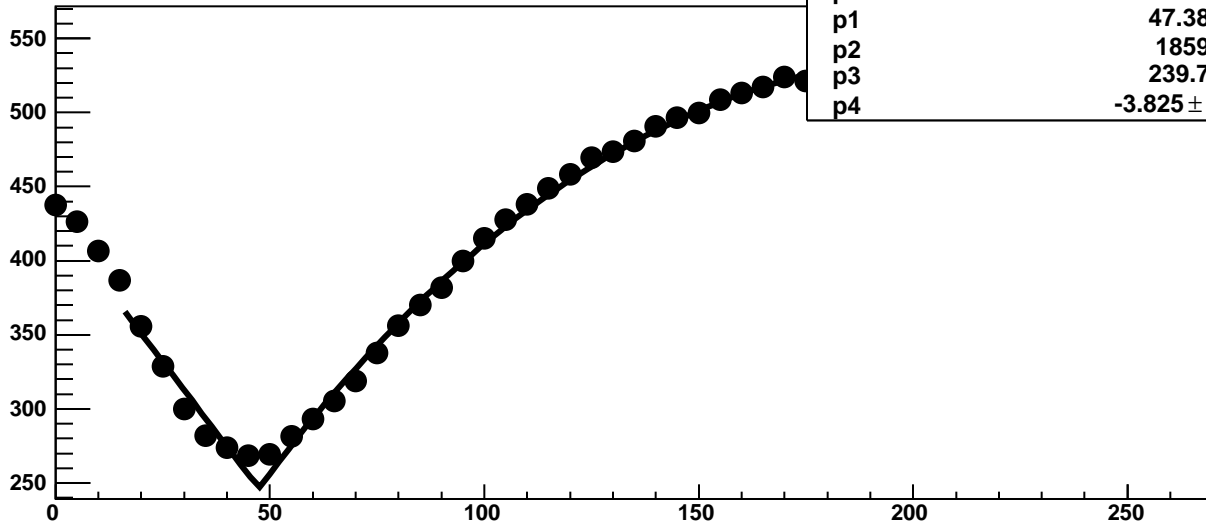
Chip 3, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold

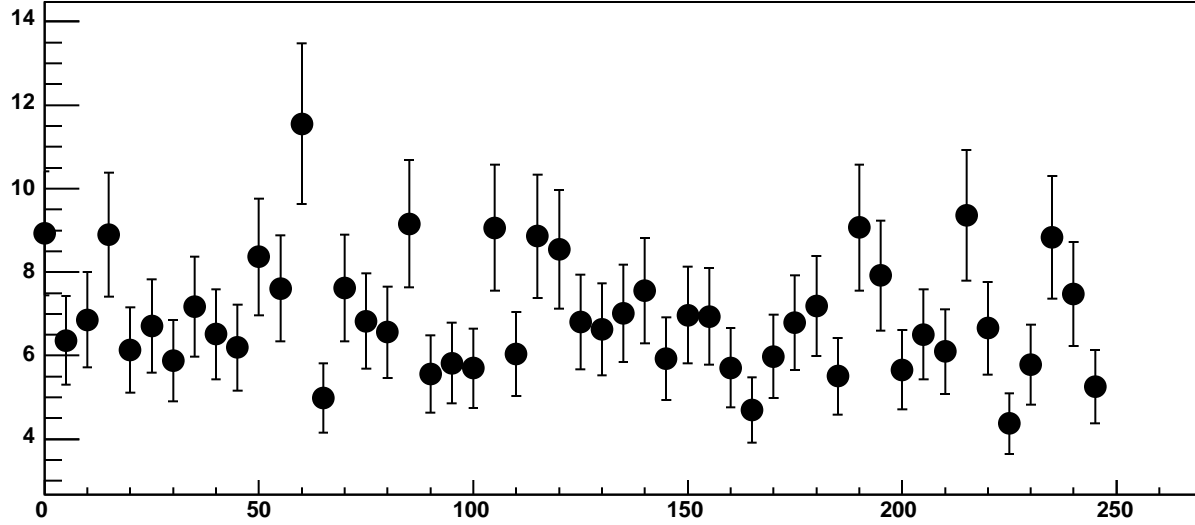


Chip 3, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

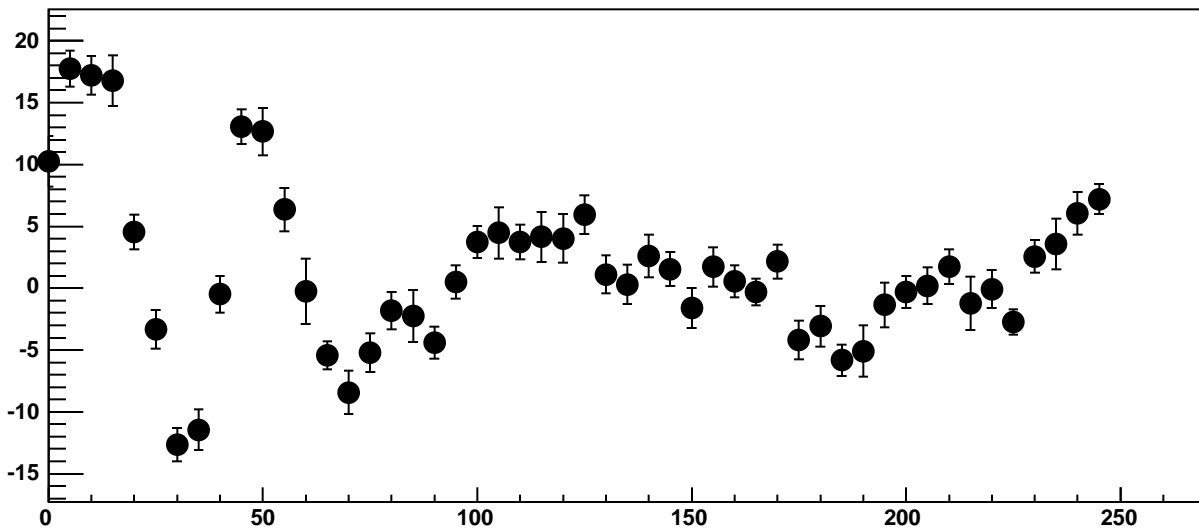


$\chi^2 / \text{ndf}$	551 / 41
p0	$246.3 \pm 0.6955$
p1	$47.38 \pm 0.192$
p2	$1859 \pm 37.19$
p3	$239.7 \pm 3.406$
p4	$-3.825 \pm 0.05995$

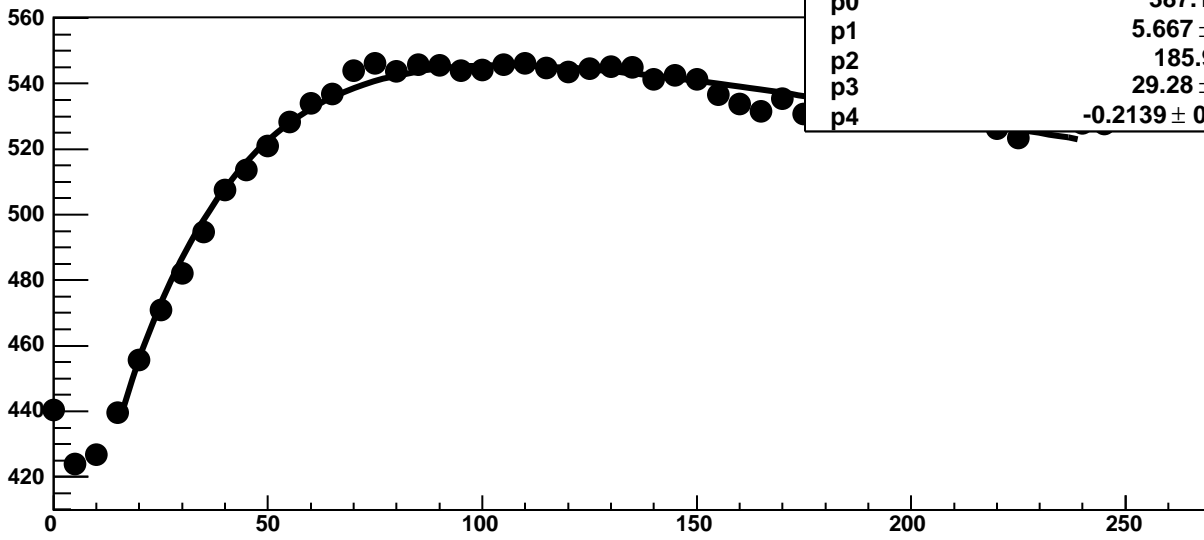
Chip 3, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

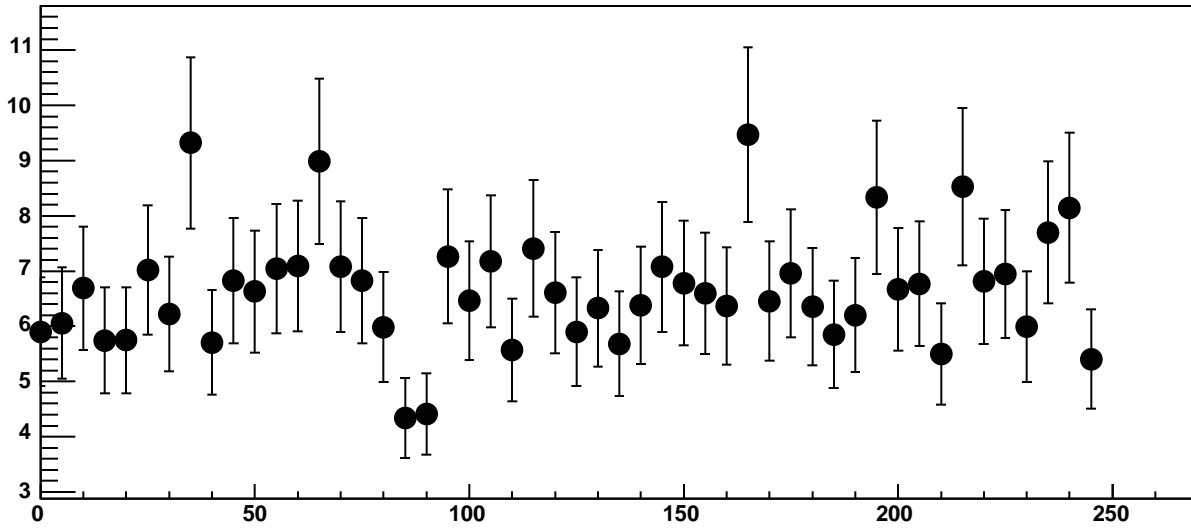


Chip 3, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

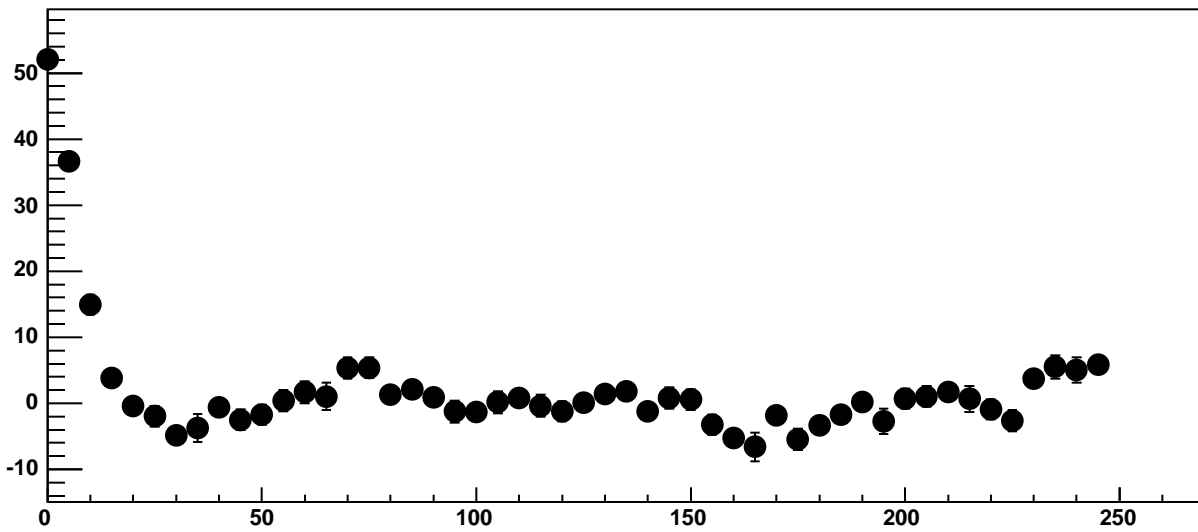


$\chi^2 / \text{ndf}$	144 / 41
p0	$387.1 \pm 2.778$
p1	$5.667 \pm 0.5276$
p2	$185.9 \pm 2.89$
p3	$29.28 \pm 0.6394$
p4	$-0.2139 \pm 0.007728$

Chip 3, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold

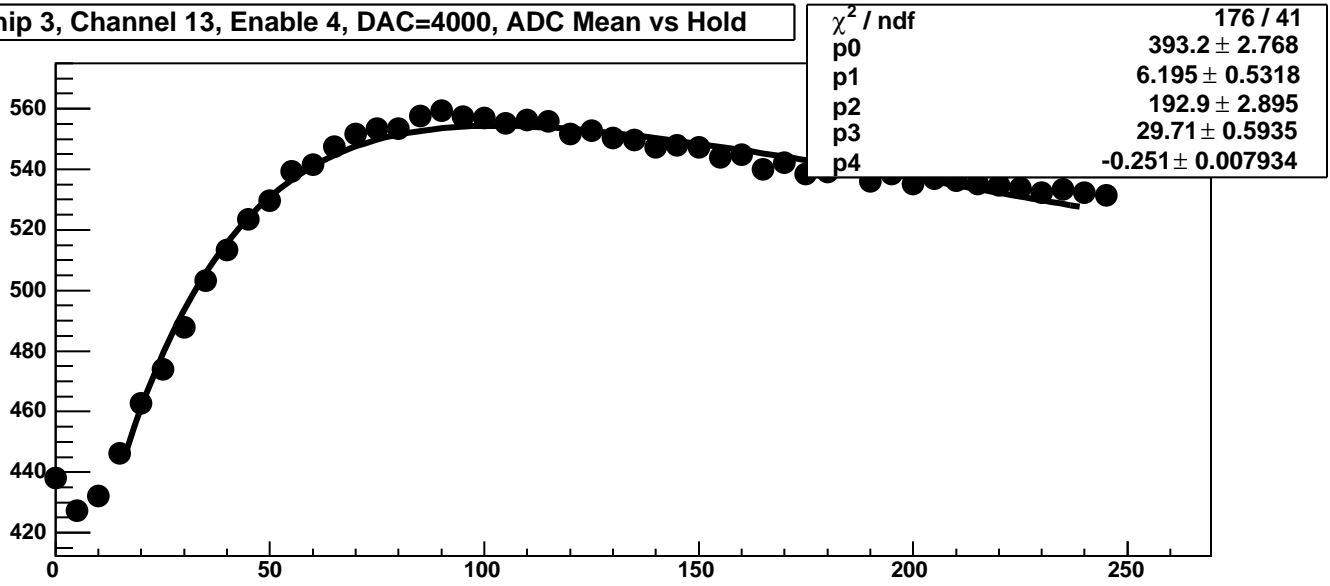


Chip 3, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

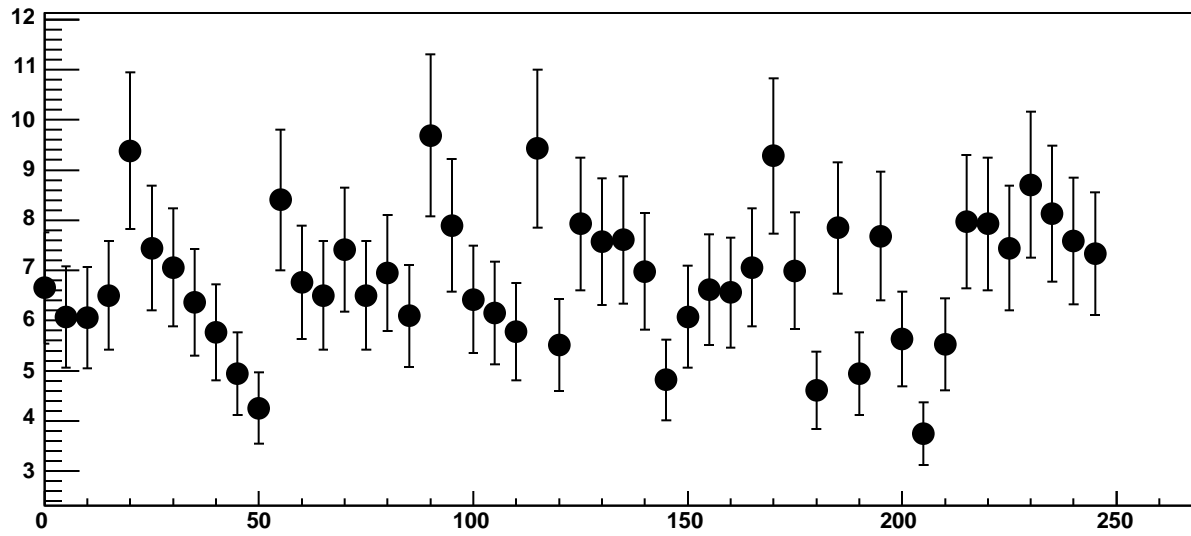




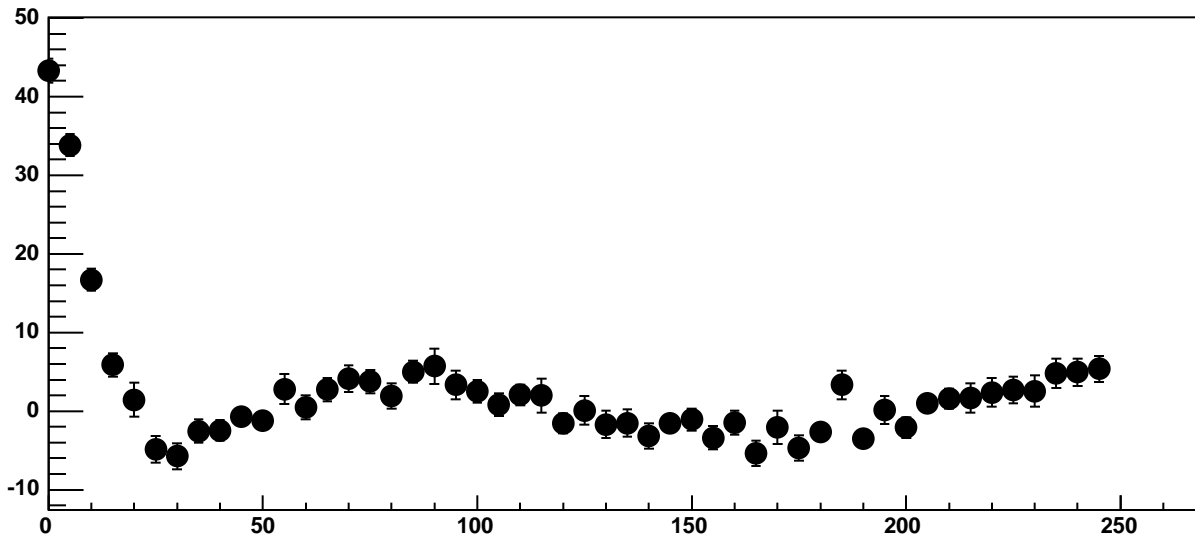
Chip 3, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold



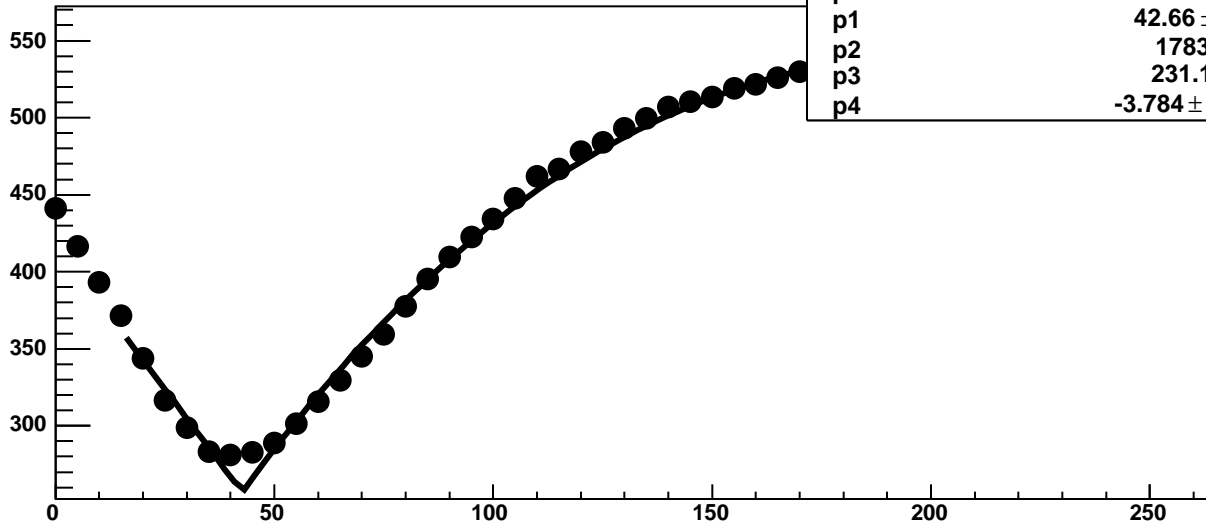
Chip 3, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold

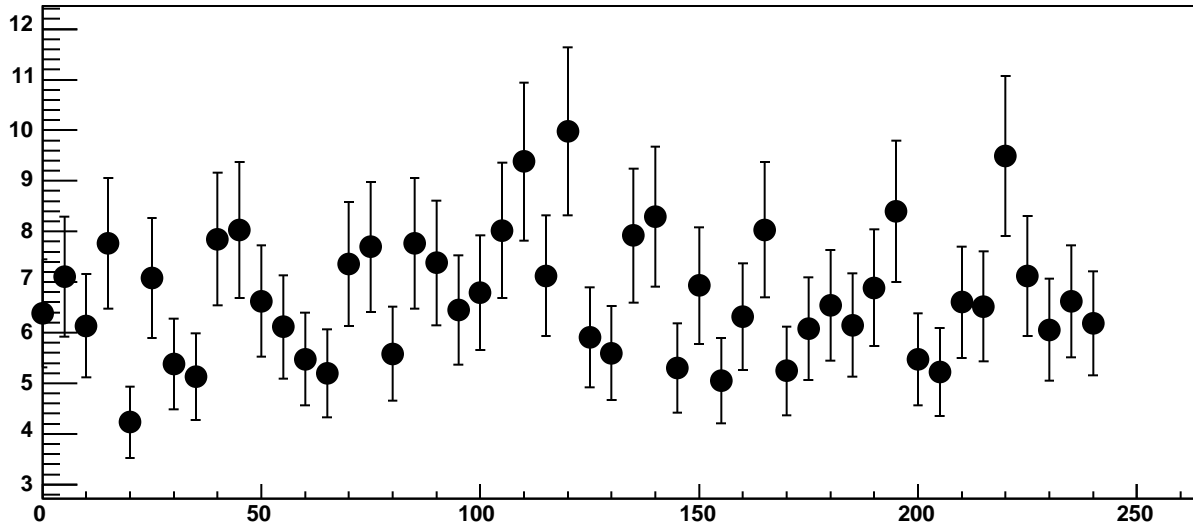


Chip 3, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

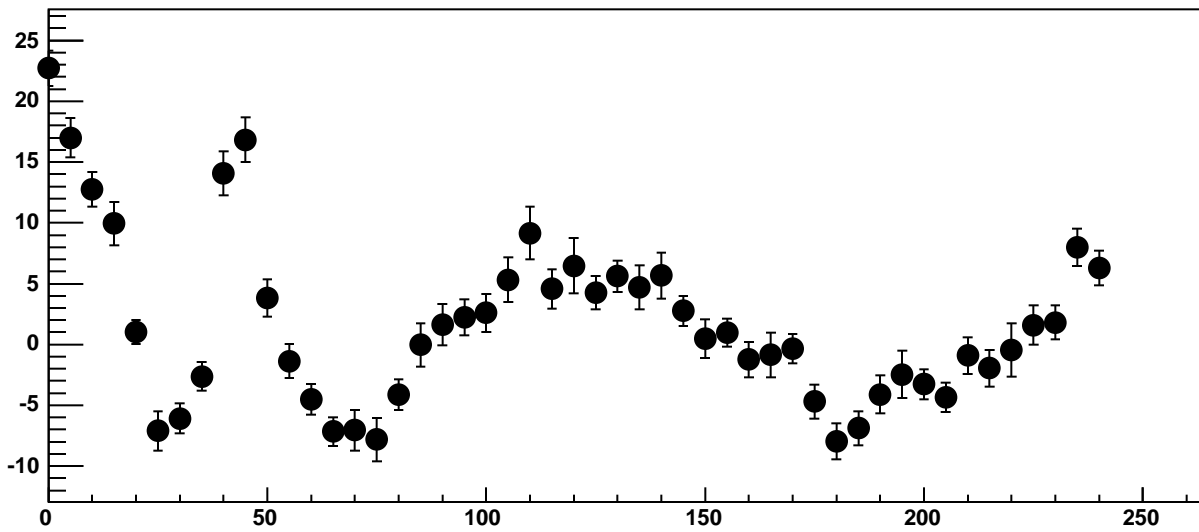


$\chi^2 / \text{ndf}$	572.7 / 41
p0	$256.9 \pm 0.6952$
p1	$42.66 \pm 0.1974$
p2	$1783 \pm 41.48$
p3	$231.1 \pm 3.628$
p4	$-3.784 \pm 0.06988$

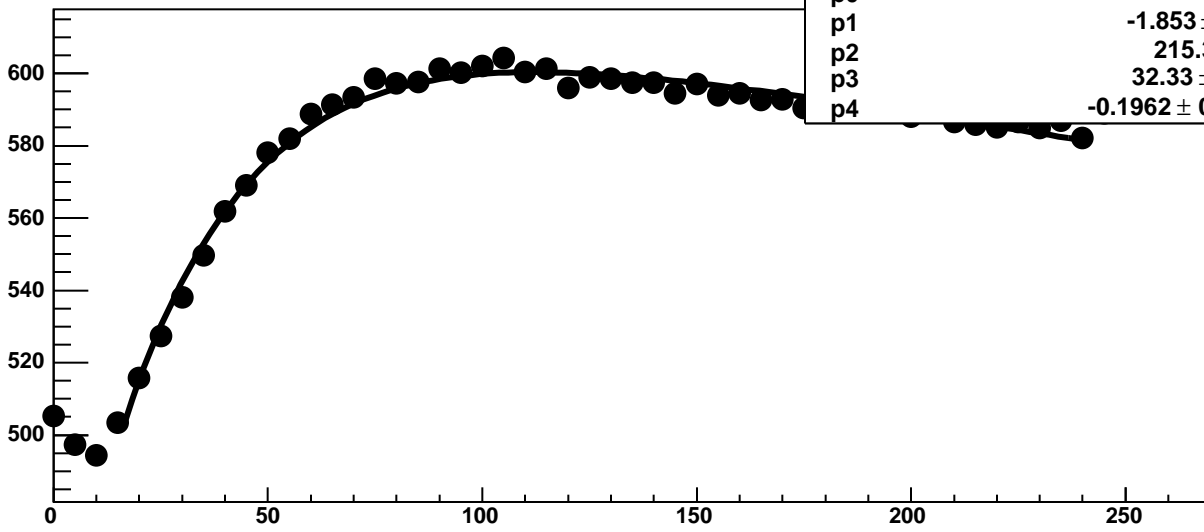
Chip 3, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold

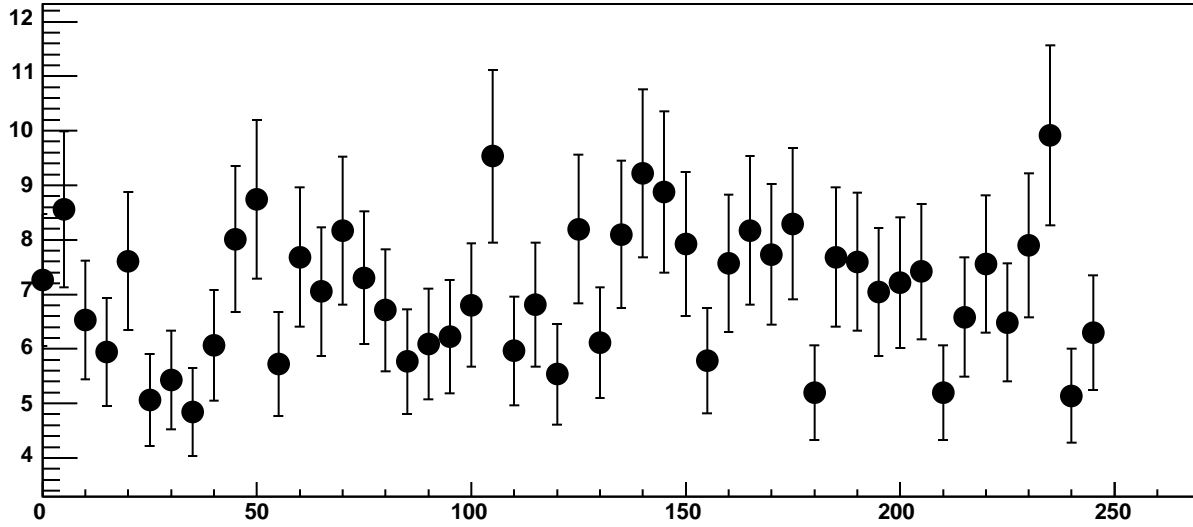


Chip 3, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

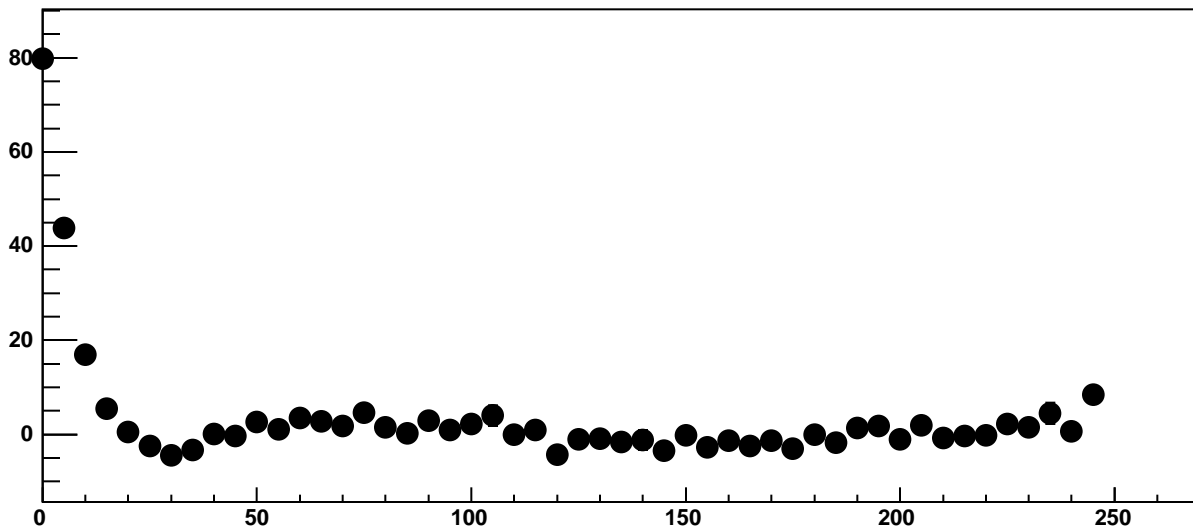


$\chi^2 / \text{ndf}$	109.2 / 41
p0	$413.7 \pm 2.882$
p1	$-1.853 \pm 0.6183$
p2	$215.3 \pm 2.99$
p3	$32.33 \pm 0.7674$
p4	$-0.1962 \pm 0.008331$

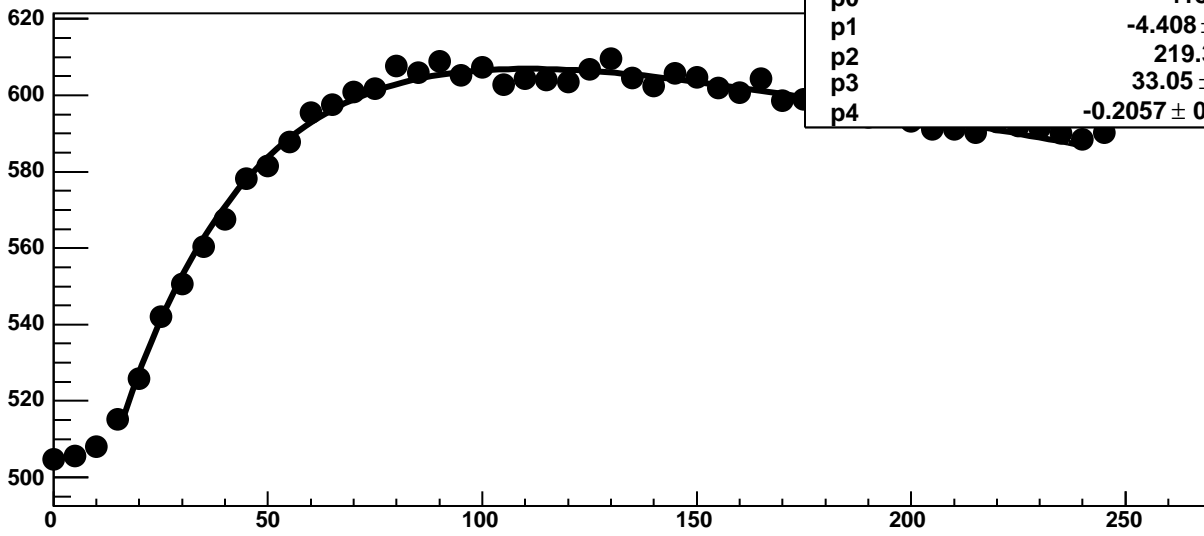
Chip 3, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

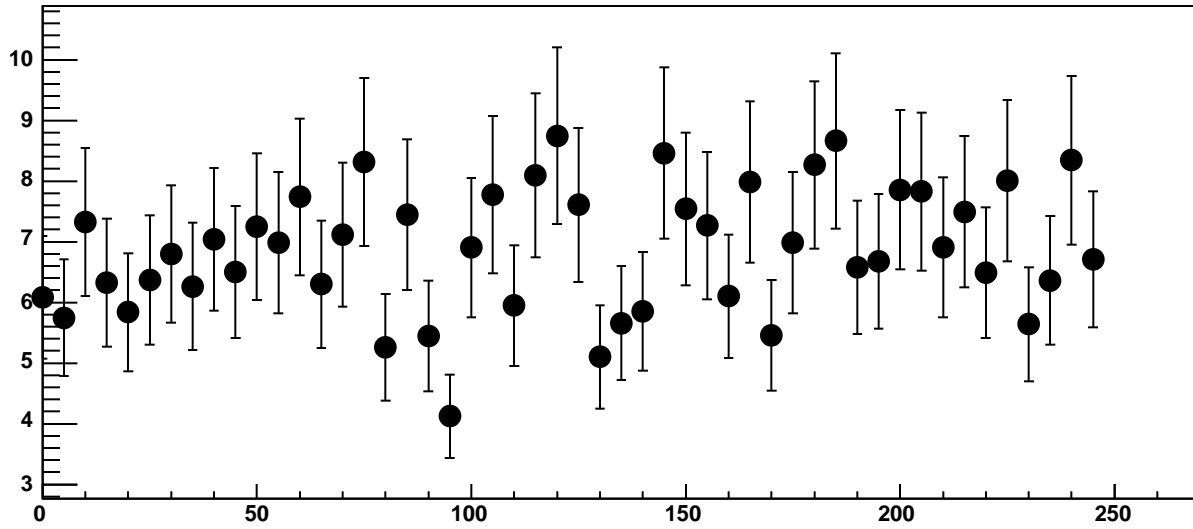


Chip 3, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold

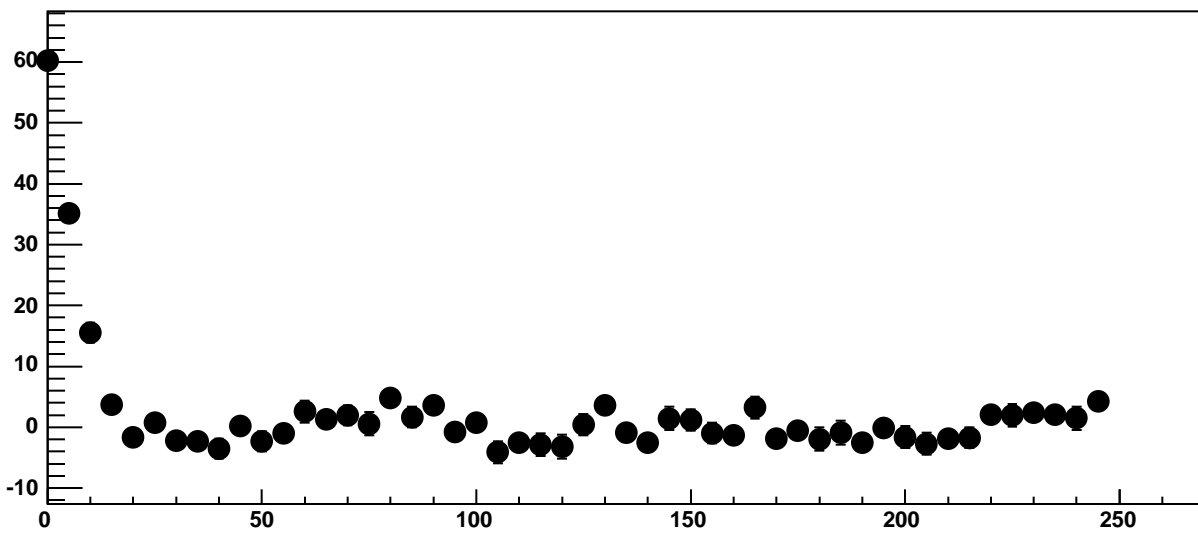


$\chi^2 / \text{ndf}$	103.7 / 41
p0	$418 \pm 2.854$
p1	$-4.408 \pm 0.6688$
p2	$219.3 \pm 2.96$
p3	$33.05 \pm 0.8464$
p4	$-0.2057 \pm 0.008578$

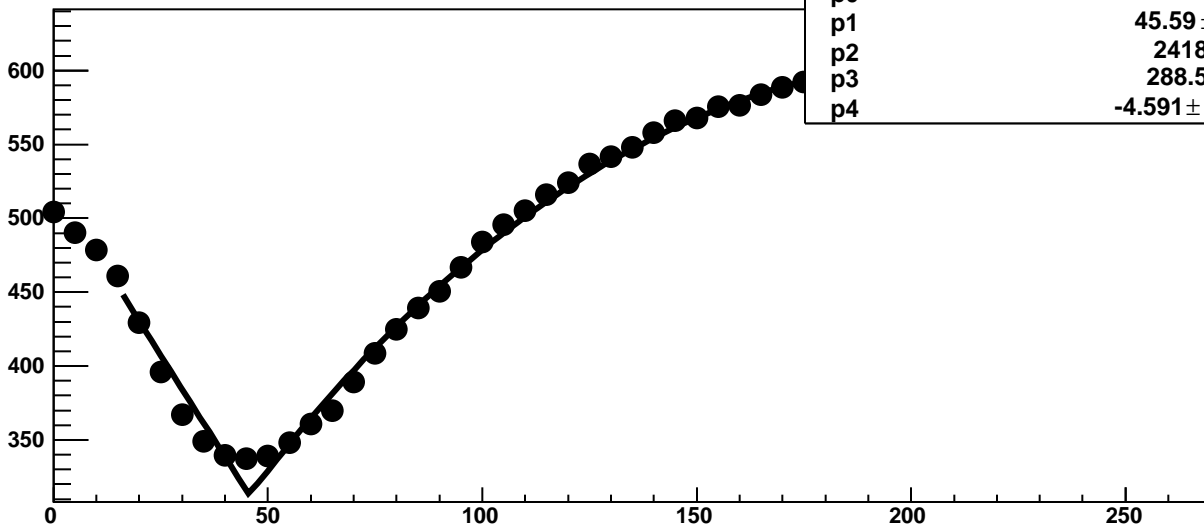
Chip 3, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold

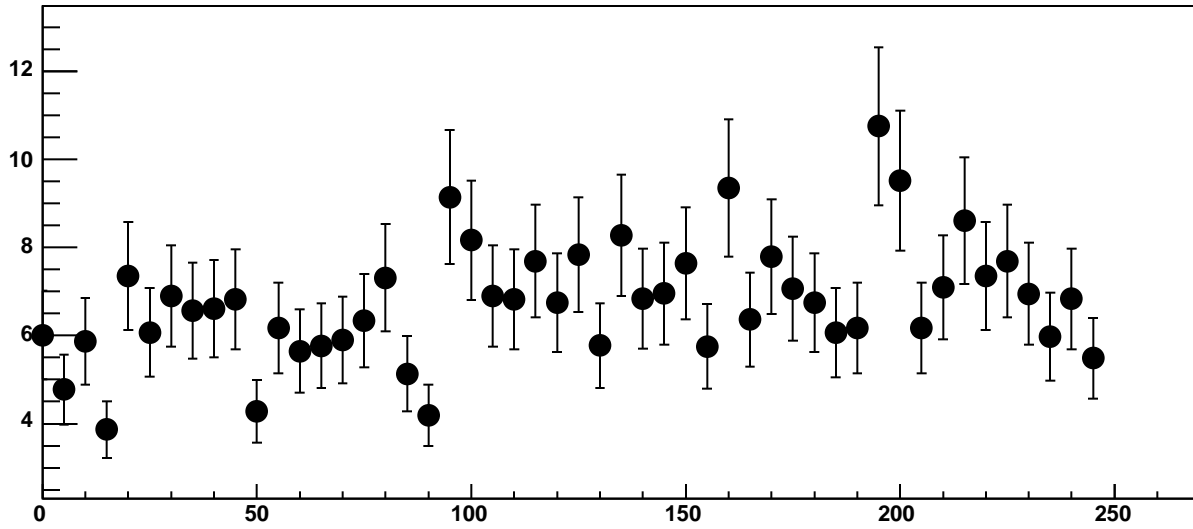


Chip 3, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

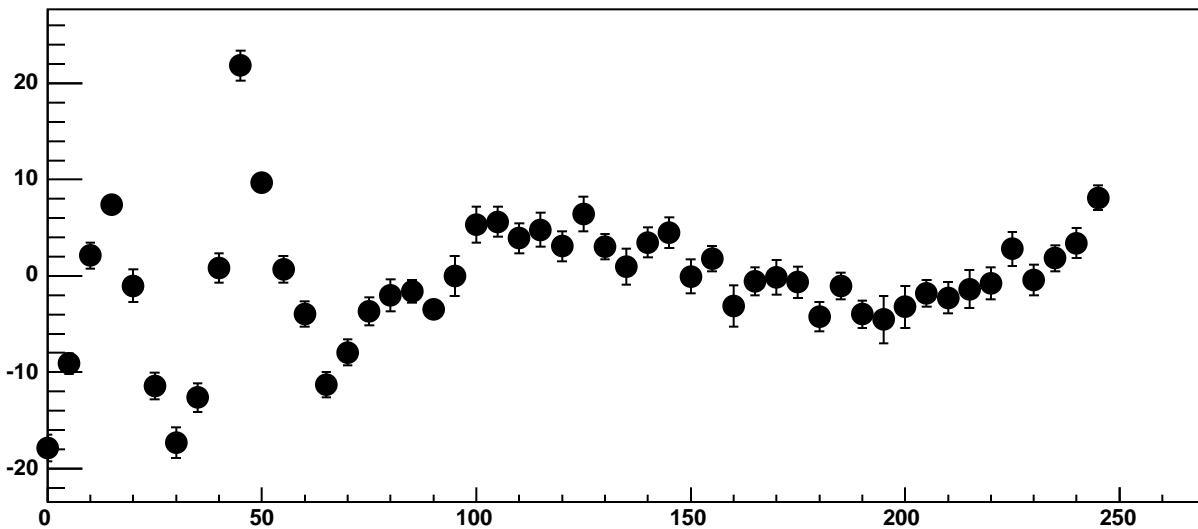


$\chi^2 / \text{ndf}$	868.5 / 41
p0	$312.9 \pm 0.6025$
p1	$45.59 \pm 0.1517$
p2	$2418 \pm 36.13$
p3	$288.5 \pm 3.225$
p4	$-4.591 \pm 0.04832$

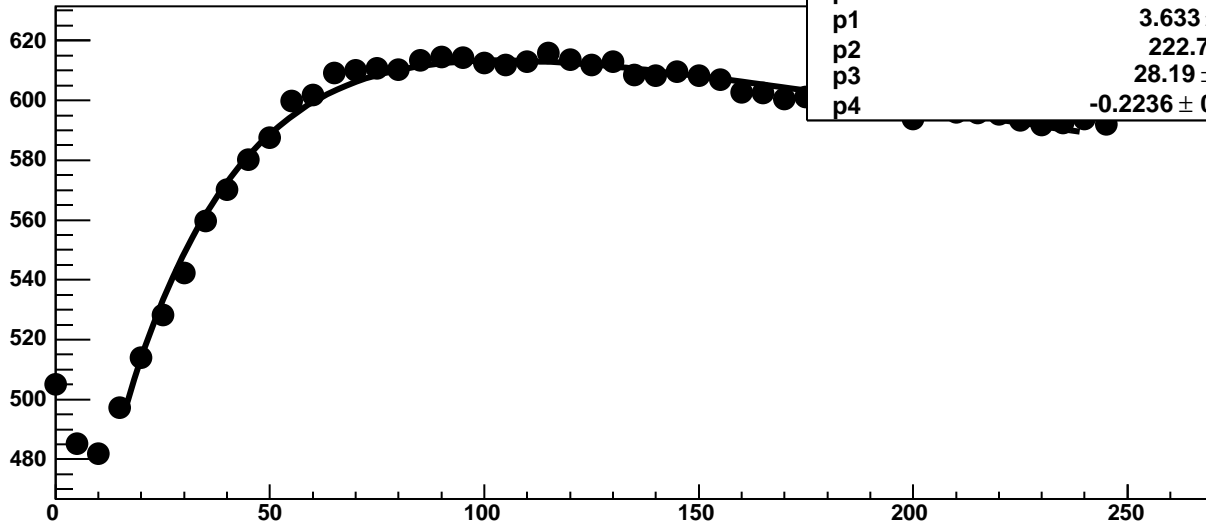
Chip 3, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

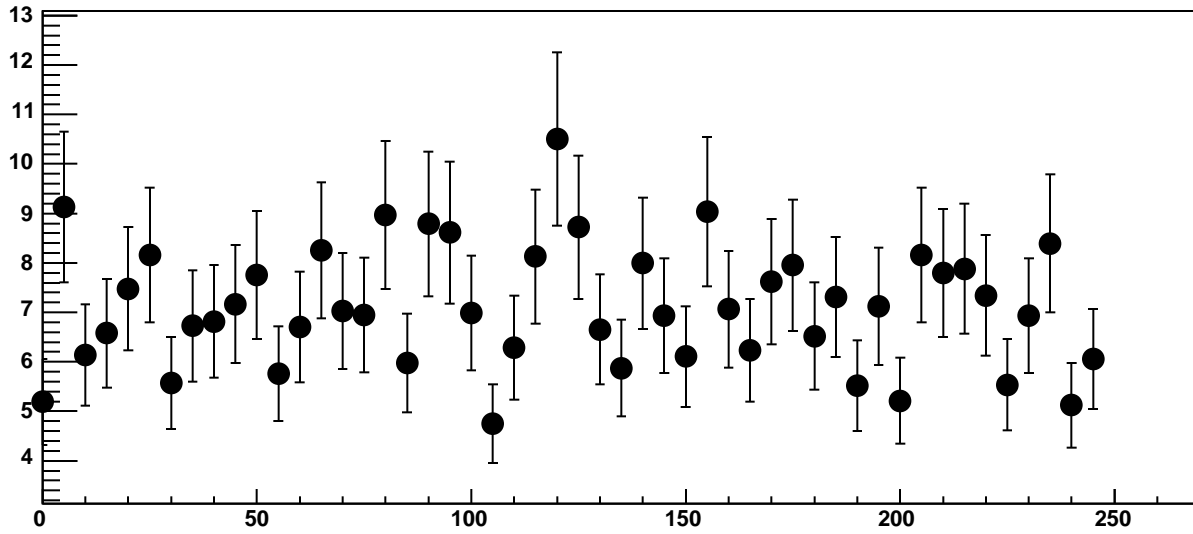


Chip 3, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

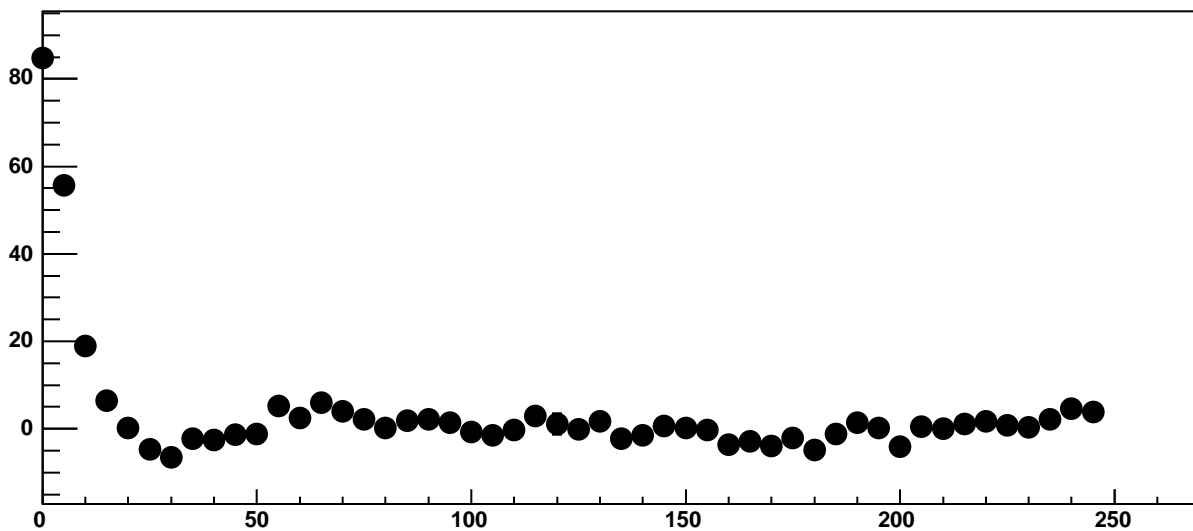


$\chi^2 / \text{ndf}$	164.6 / 41
p0	419.4 ± 2.949
p1	3.633 ± 0.4951
p2	222.7 ± 3.046
p3	28.19 ± 0.5675
p4	-0.2236 ± 0.007531

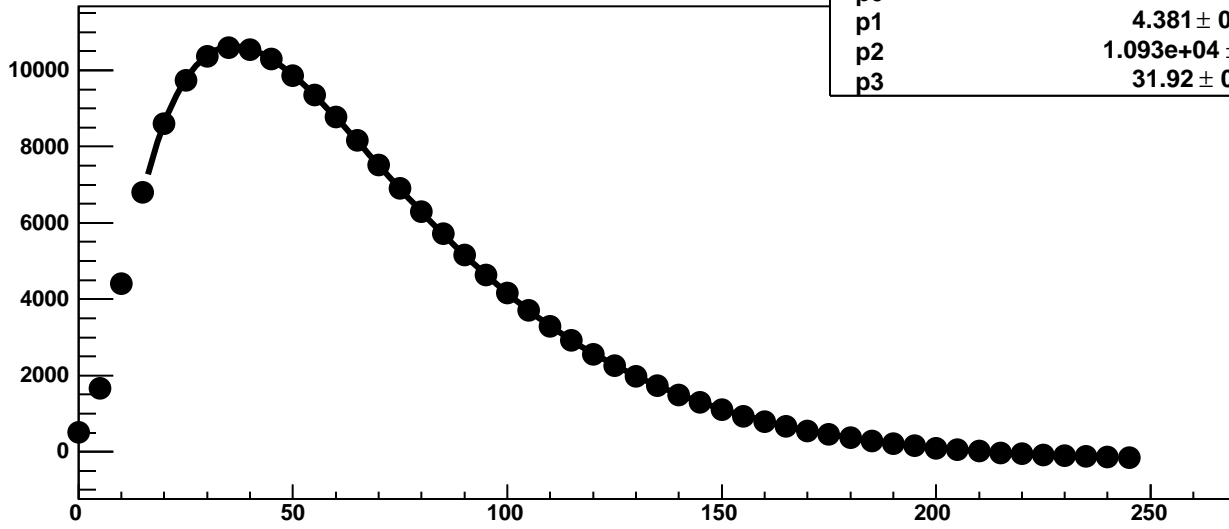
Chip 3, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

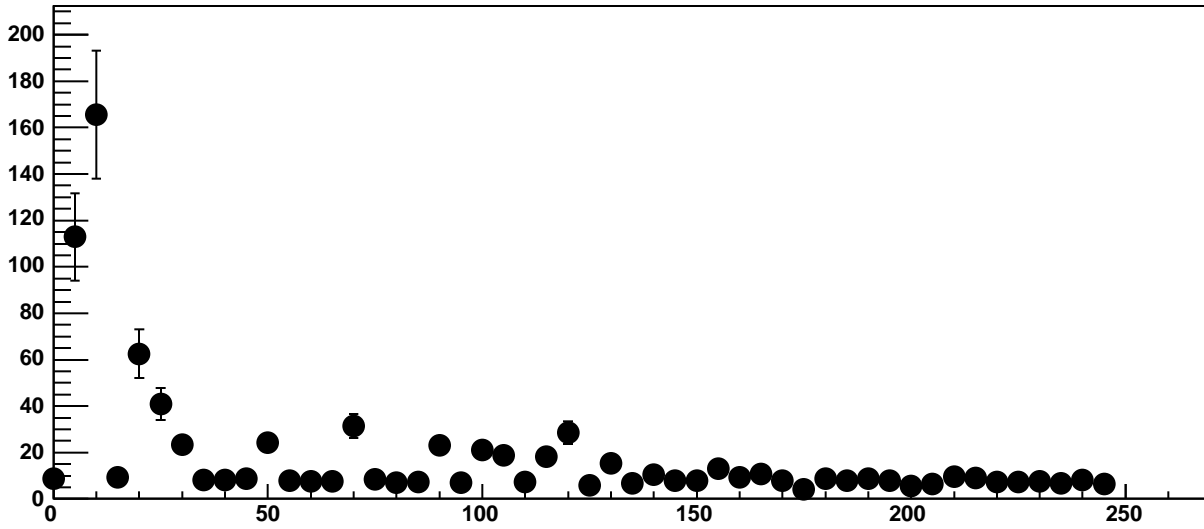


Chip 3, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold

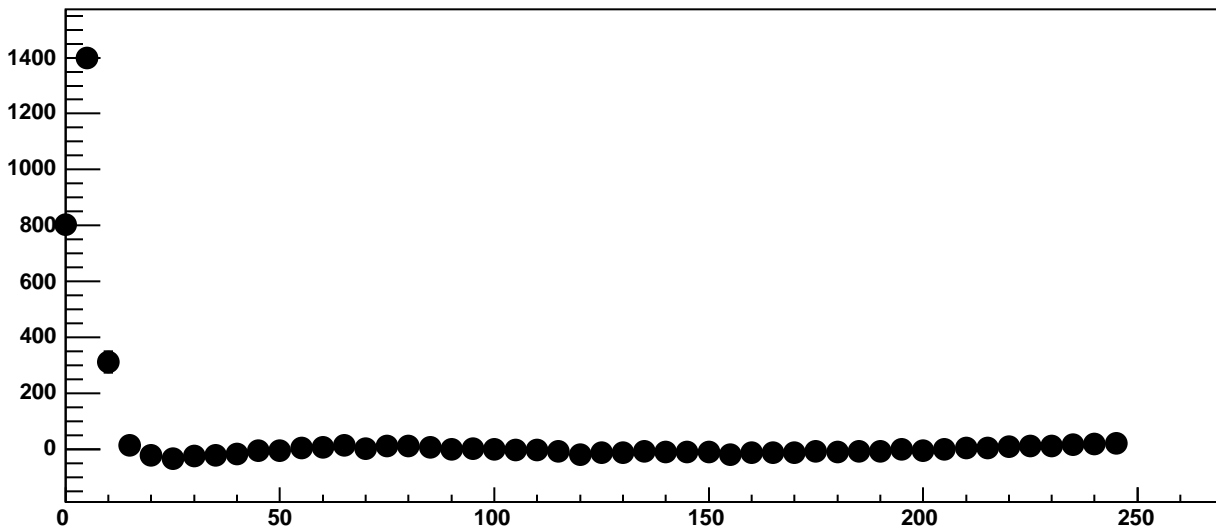


$\chi^2 / \text{ndf}$	1253 / 42
p0	-296.8 ± 0.5628
p1	4.381 ± 0.005786
p2	1.093e+04 ± 0.9269
p3	31.92 ± 0.004742

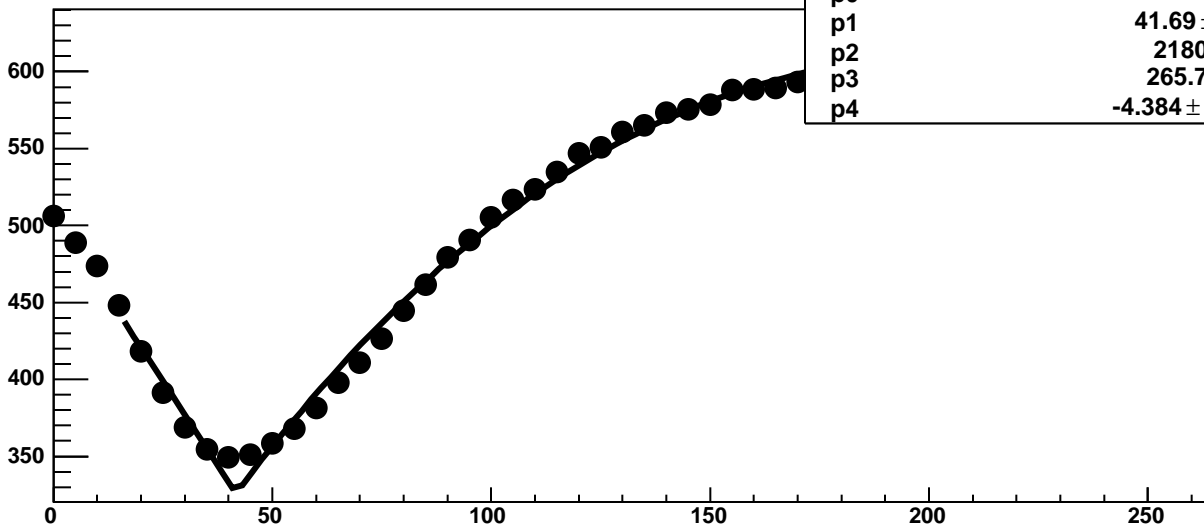
Chip 3, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold

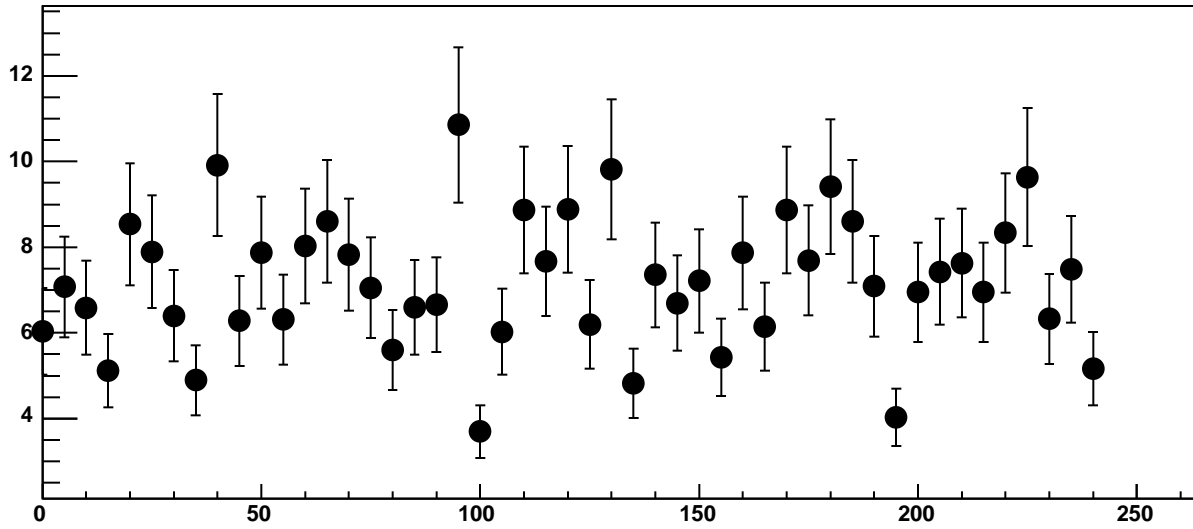


Chip 3, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

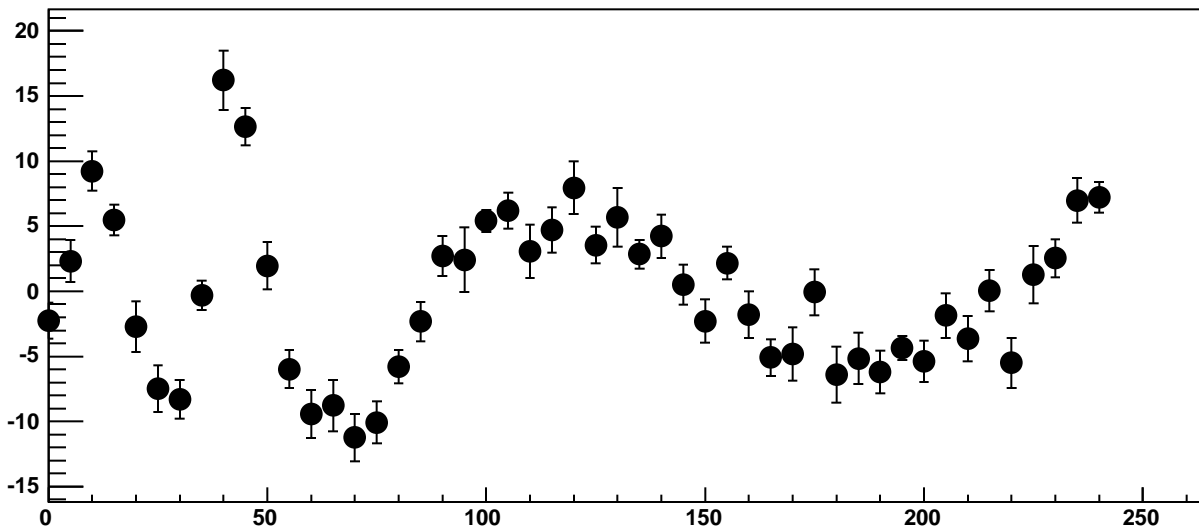


$\chi^2 / \text{ndf}$	640.4 / 41
p0	$325.7 \pm 0.689$
p1	$41.69 \pm 0.1863$
p2	$2180 \pm 46.84$
p3	$265.7 \pm 3.865$
p4	$-4.384 \pm 0.06996$

Chip 3, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold

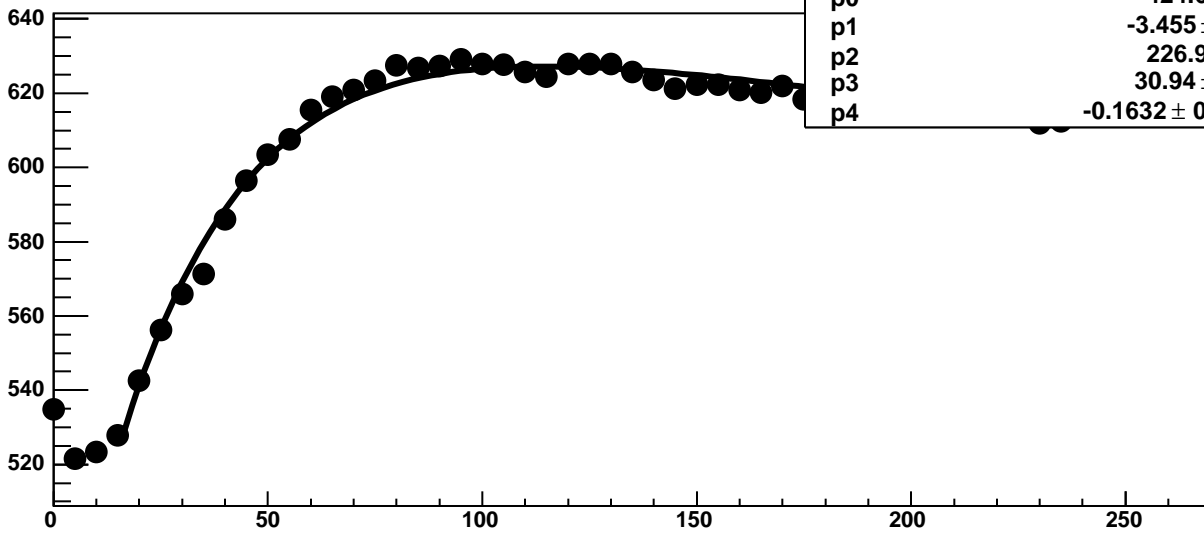


Chip 3, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold



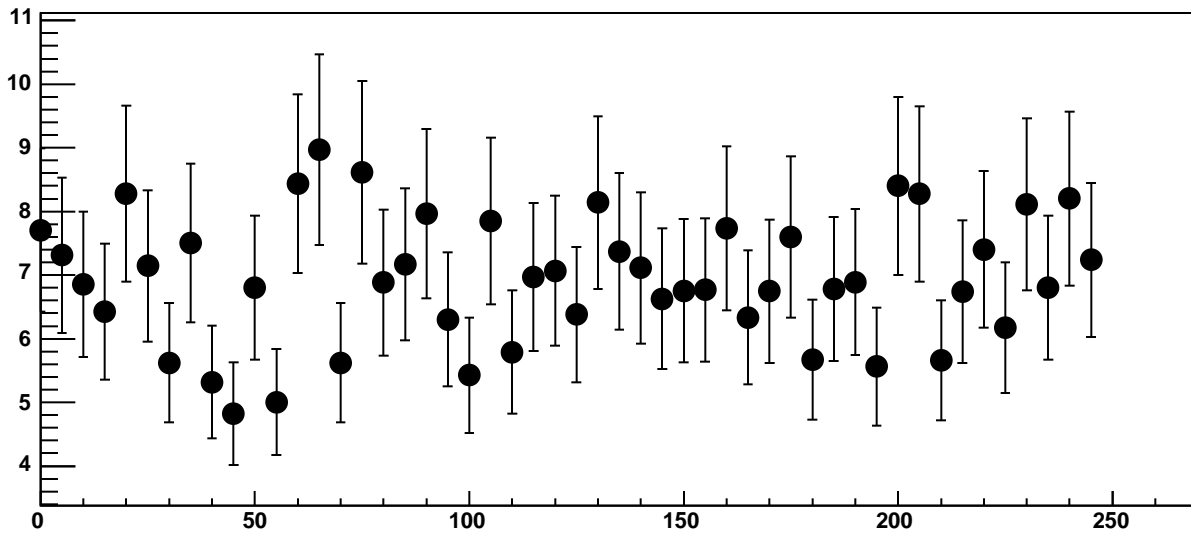


Chip 3, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

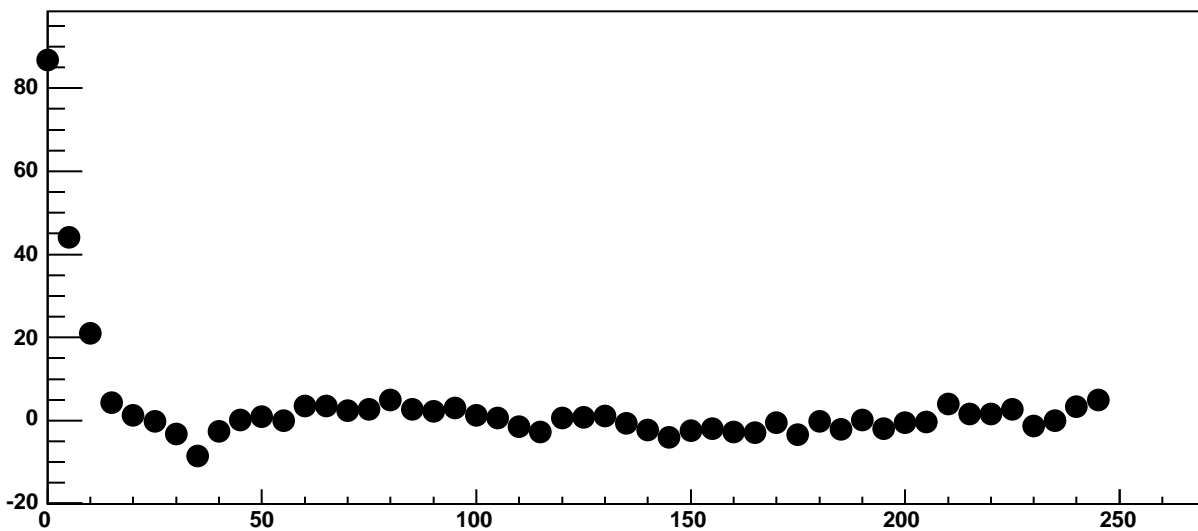


$\chi^2 / \text{ndf}$	128.3 / 41
p0	$424.6 \pm 2.857$
p1	$-3.455 \pm 0.6277$
p2	$226.9 \pm 2.938$
p3	$30.94 \pm 0.7208$
p4	$-0.1632 \pm 0.008173$

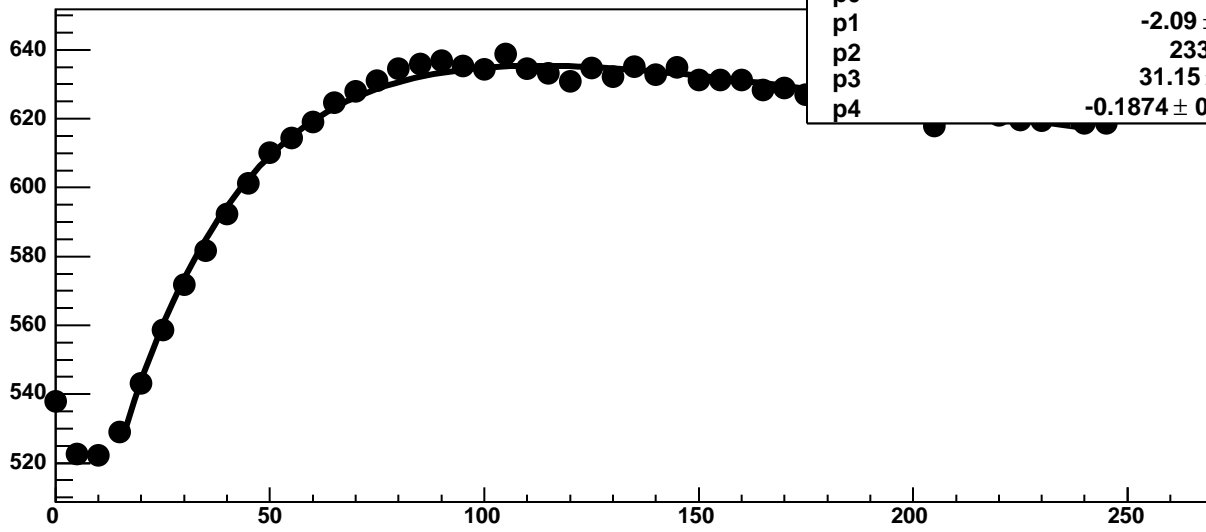
Chip 3, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



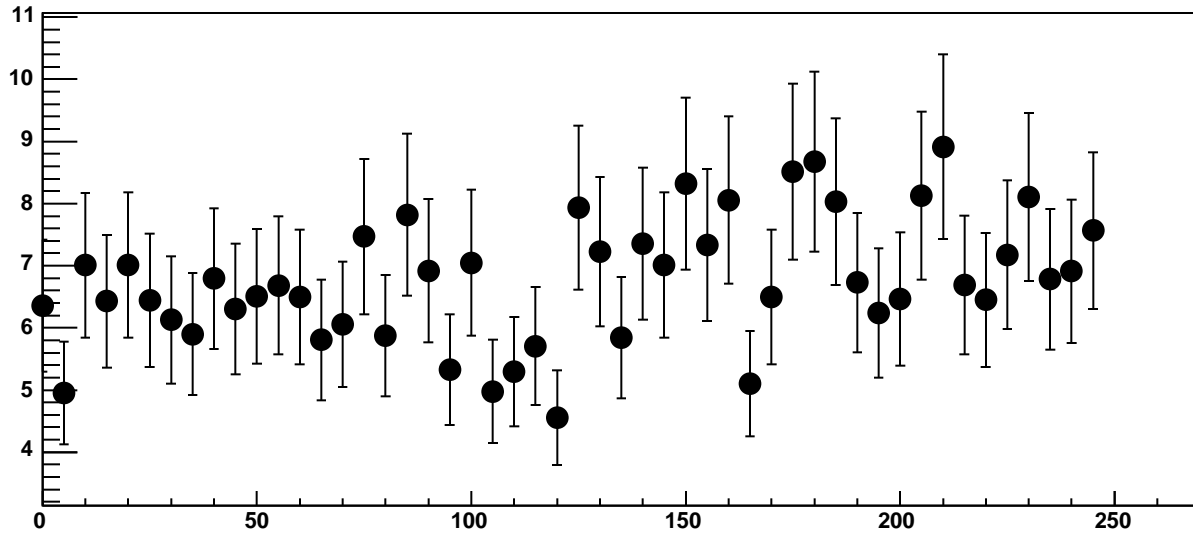
Chip 3, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold



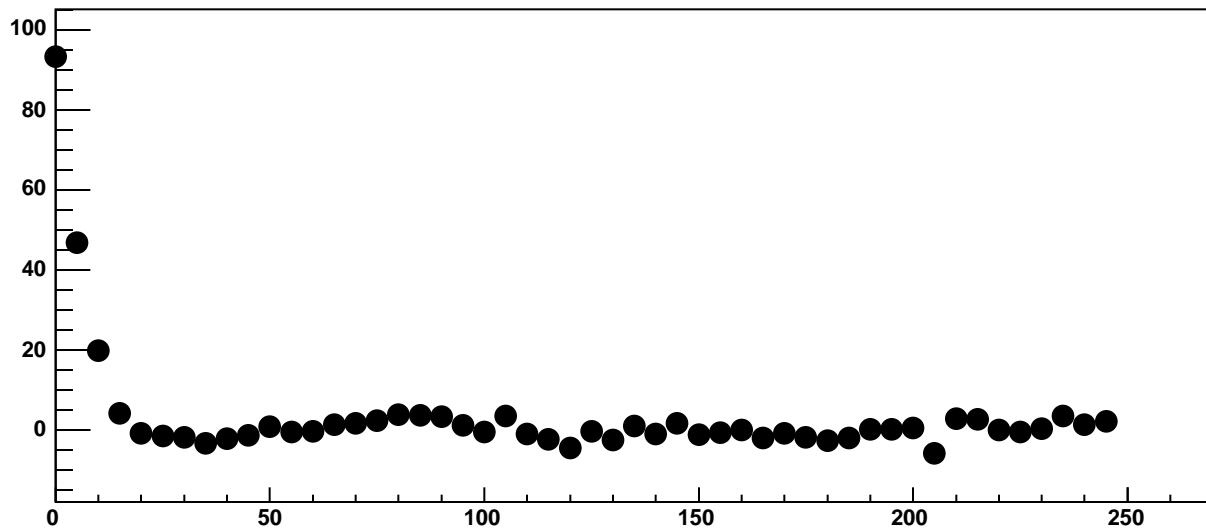
Chip 3, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold



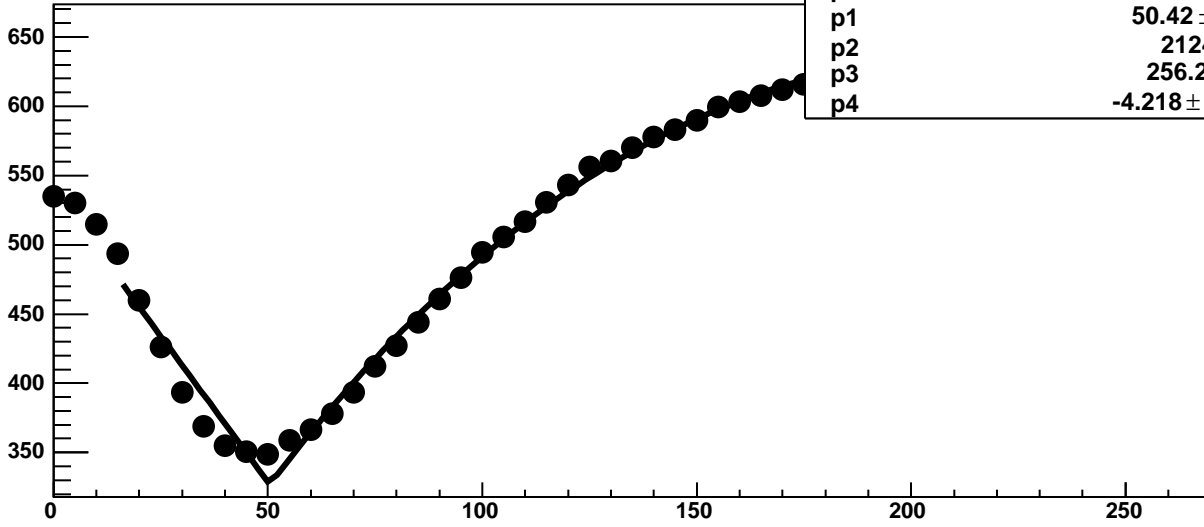
Chip 3, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

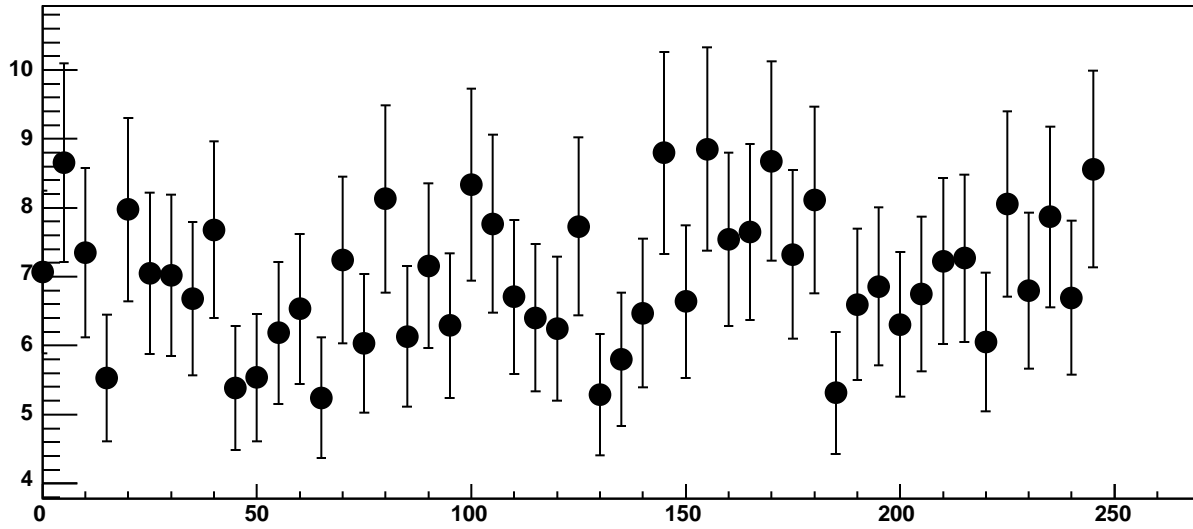


Chip 3, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold

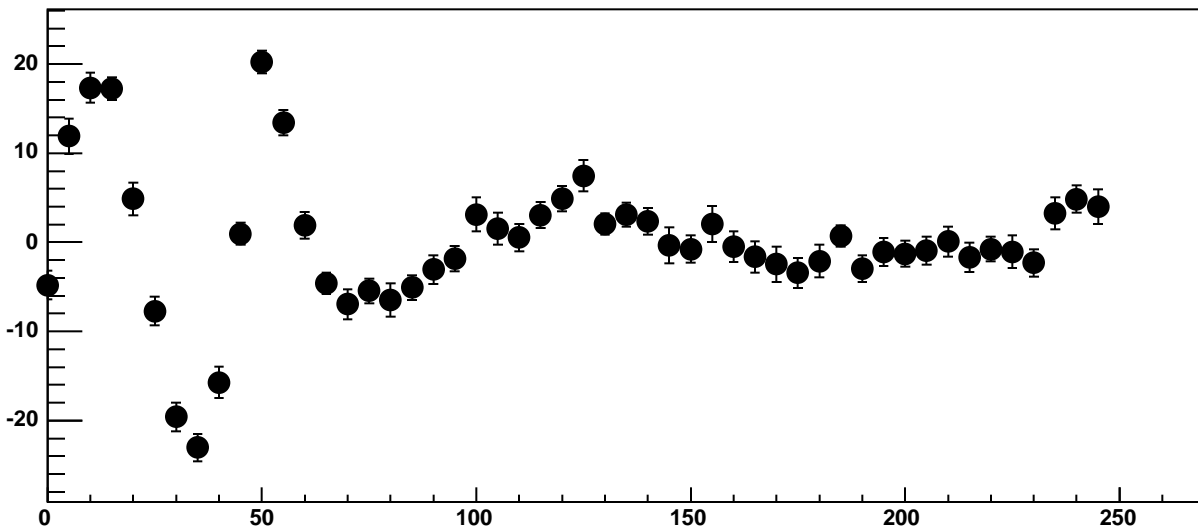


$\chi^2 / \text{ndf}$	1172 / 41
p0	$326.9 \pm 0.5849$
p1	$50.42 \pm 0.1466$
p2	$2124 \pm 28.9$
p3	$256.2 \pm 2.822$
p4	$-4.218 \pm 0.04179$

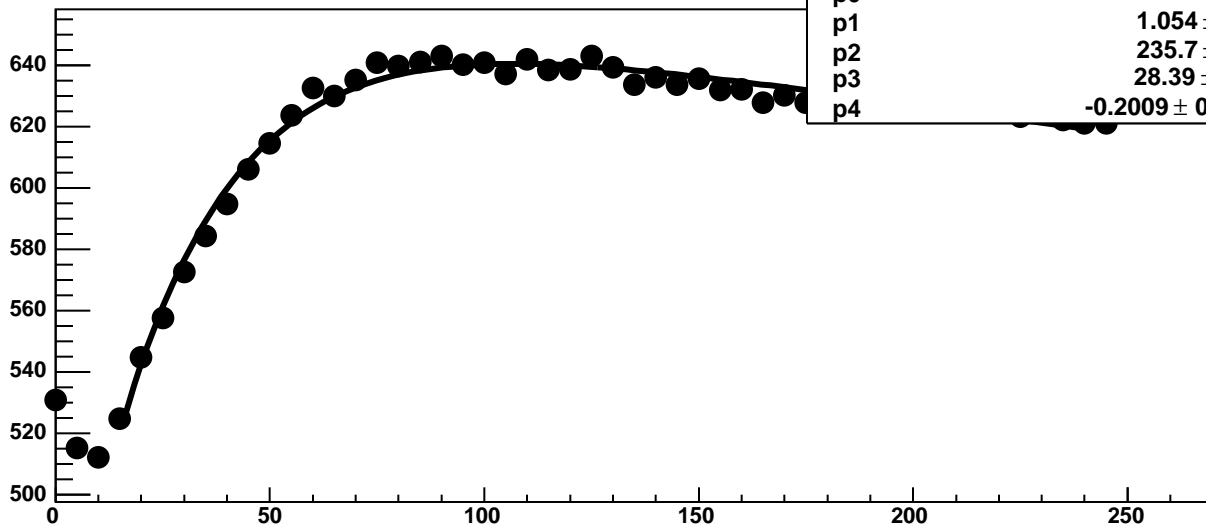
Chip 3, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold

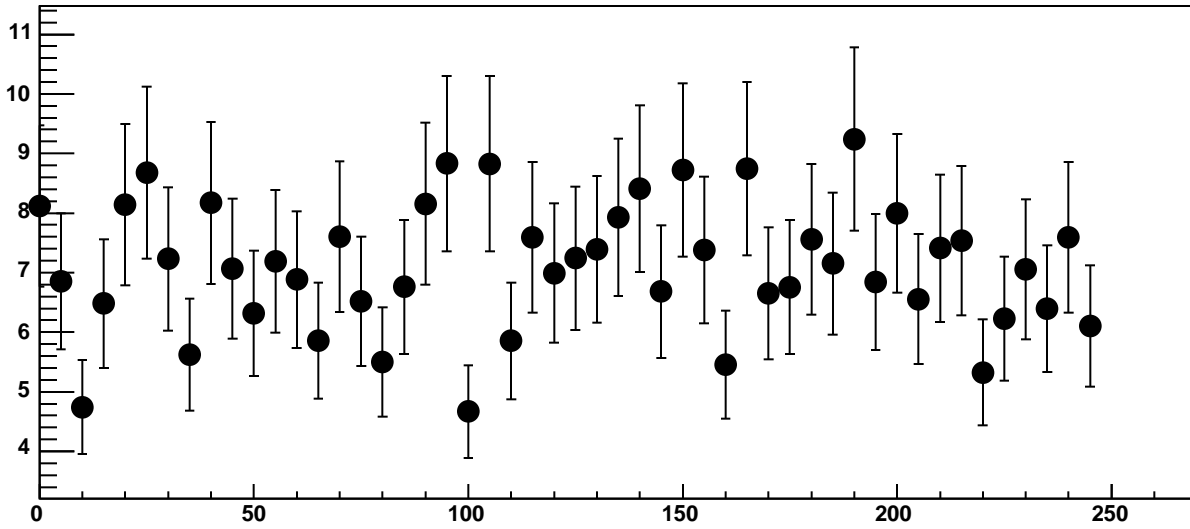


Chip 3, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

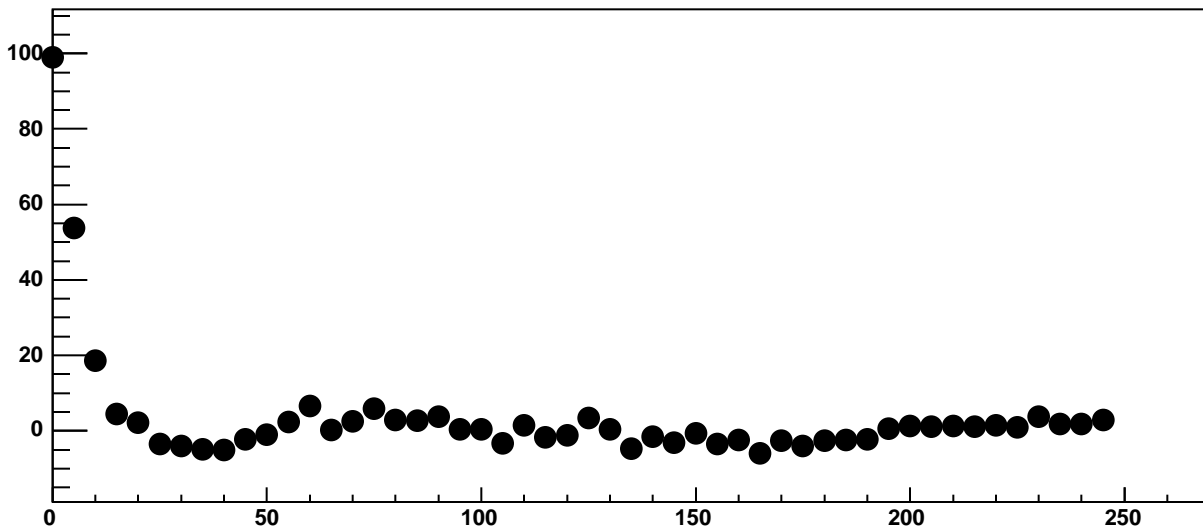


$\chi^2 / \text{ndf}$	158.9 / 41
p0	$431.6 \pm 0.7597$
p1	$1.054 \pm 0.3752$
p2	$235.7 \pm 0.8342$
p3	$28.39 \pm 0.6345$
p4	$-0.2009 \pm 0.008149$

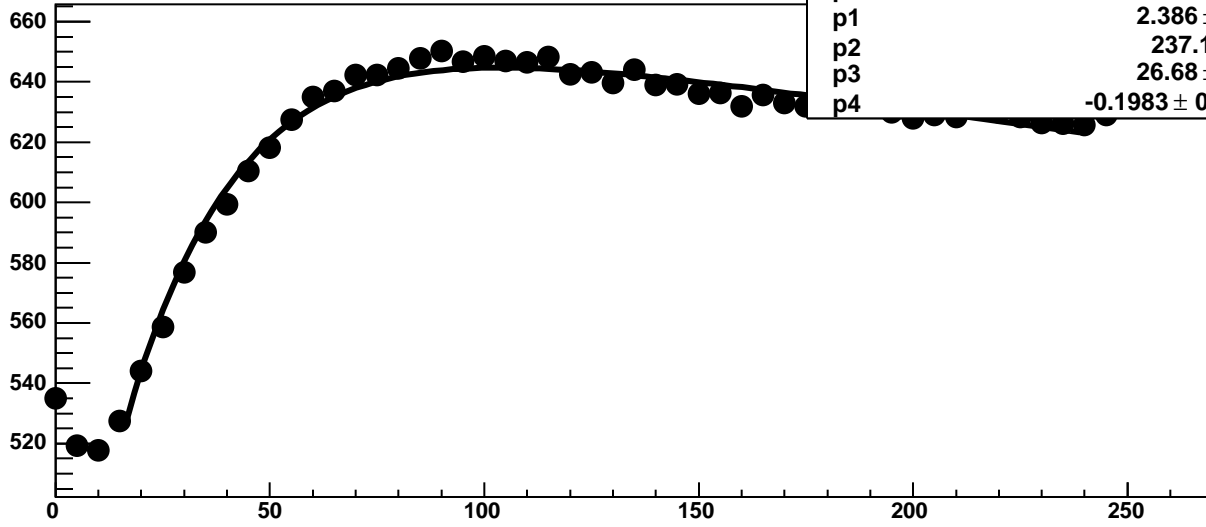
Chip 3, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

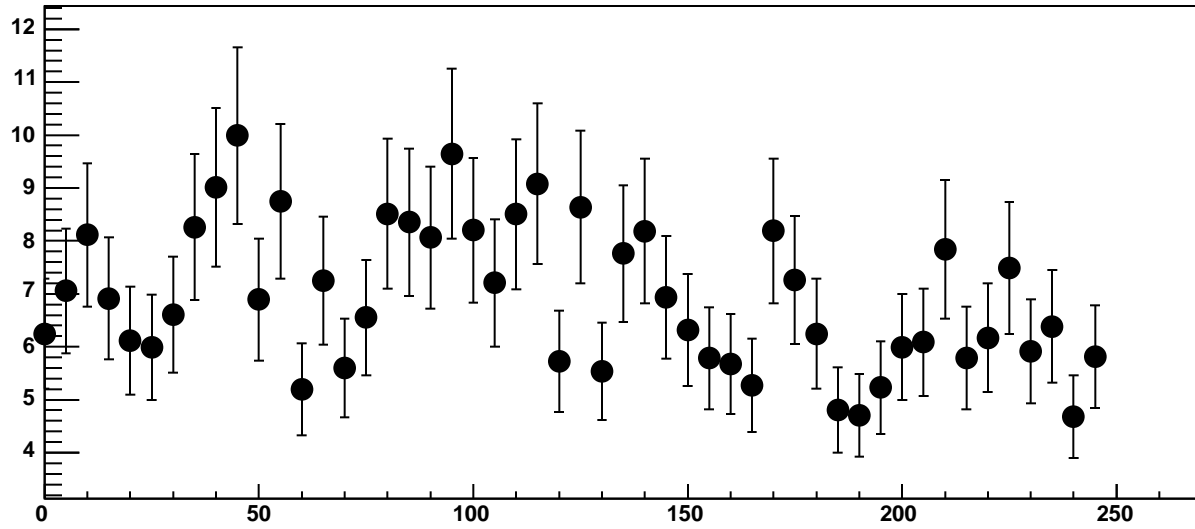


Chip 3, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

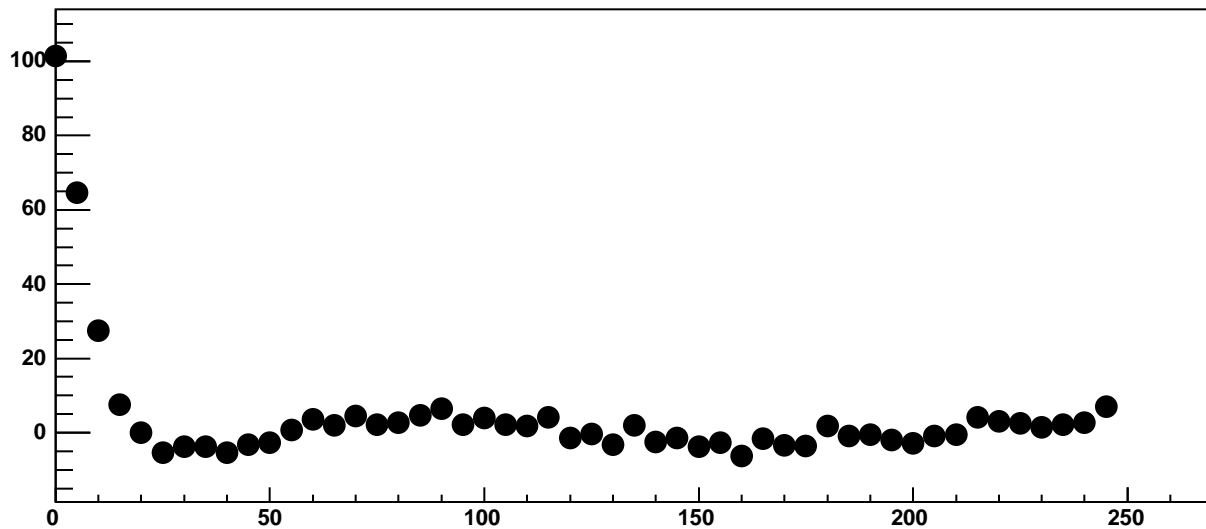


$\chi^2 / \text{ndf}$	206.7 / 41
p0	$433 \pm 2.834$
p1	$2.386 \pm 0.4579$
p2	$237.1 \pm 2.905$
p3	$26.68 \pm 0.5323$
p4	$-0.1983 \pm 0.006955$

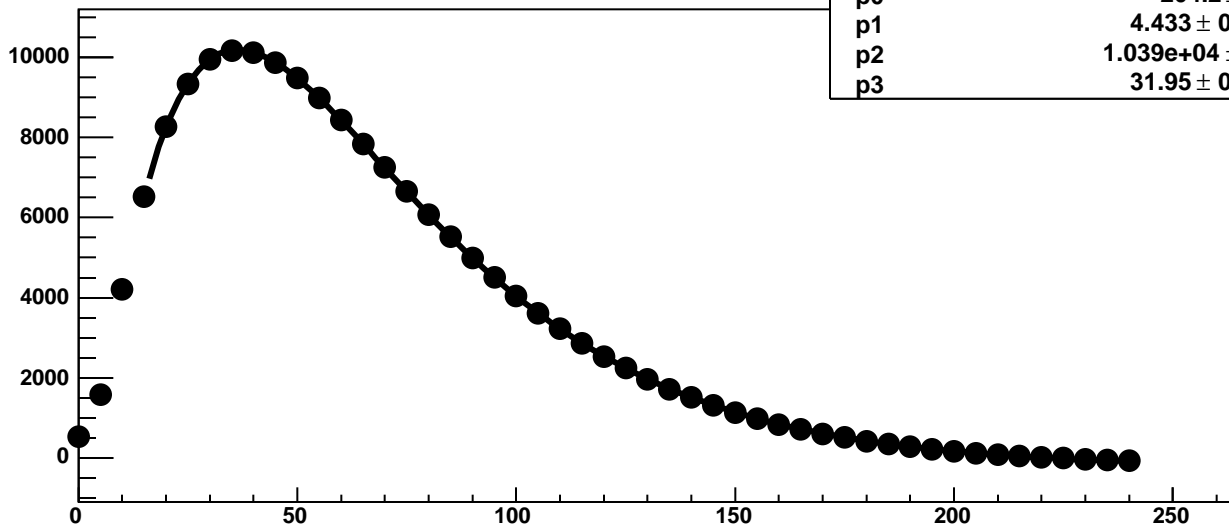
Chip 3, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

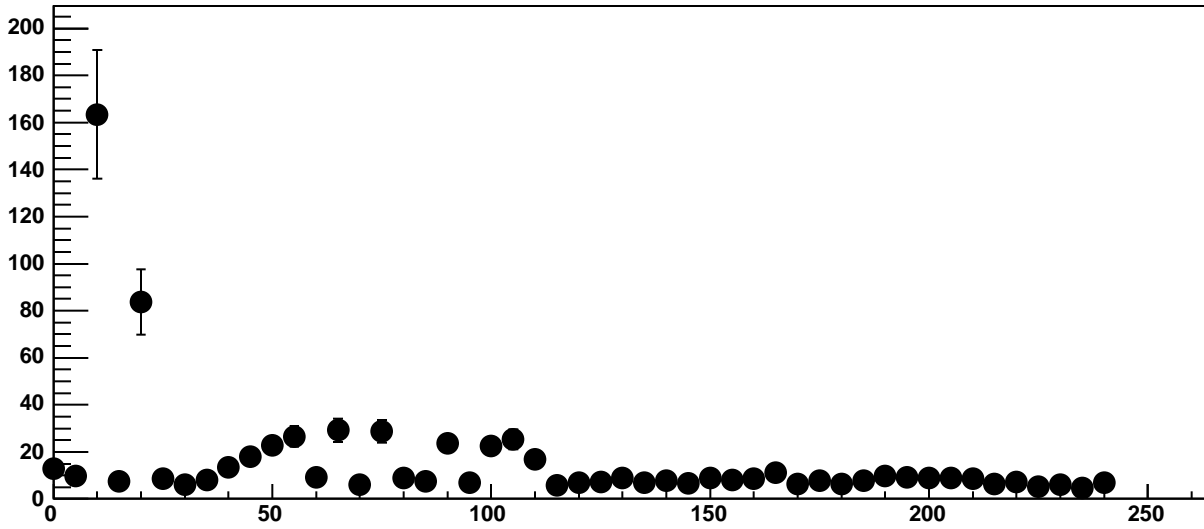


Chip 3, Channel 15, Enable 5!, DAC=4000, ADC Mean vs Hold

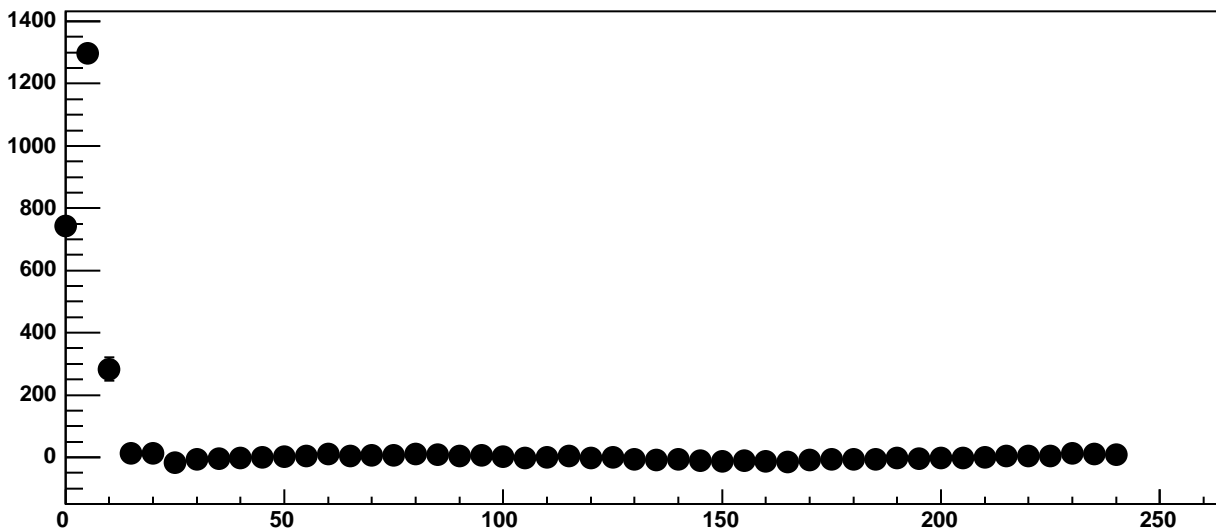


$\chi^2 / \text{ndf}$	858.3 / 42
p0	-204.2 ± 0.5147
p1	4.433 ± 0.005364
p2	1.039e+04 ± 0.9027
p3	31.95 ± 0.004454

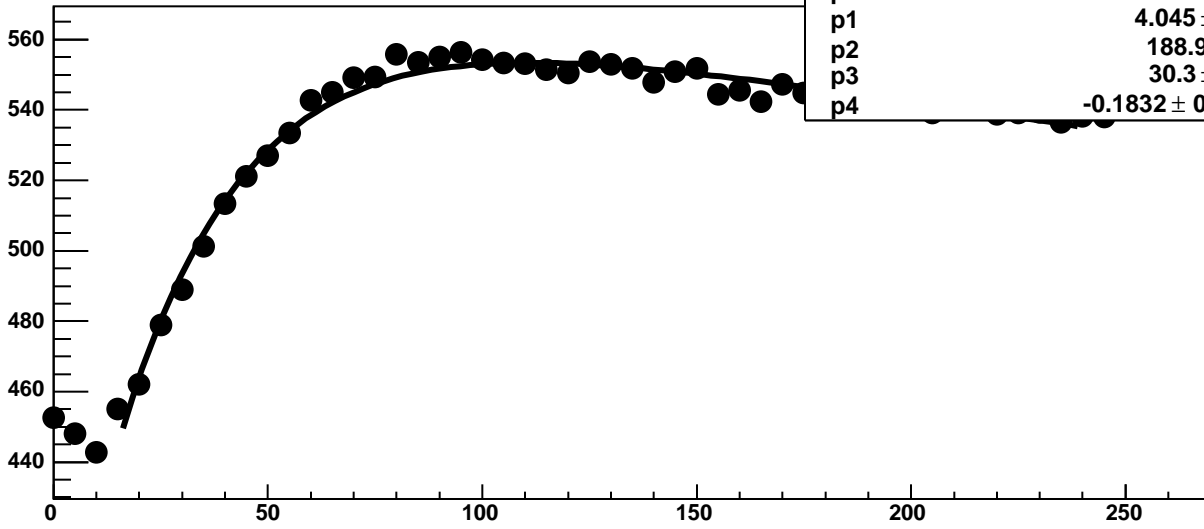
Chip 3, Channel 15, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 15, Enable 5!, DAC=4000, ADC Residuals vs Hold

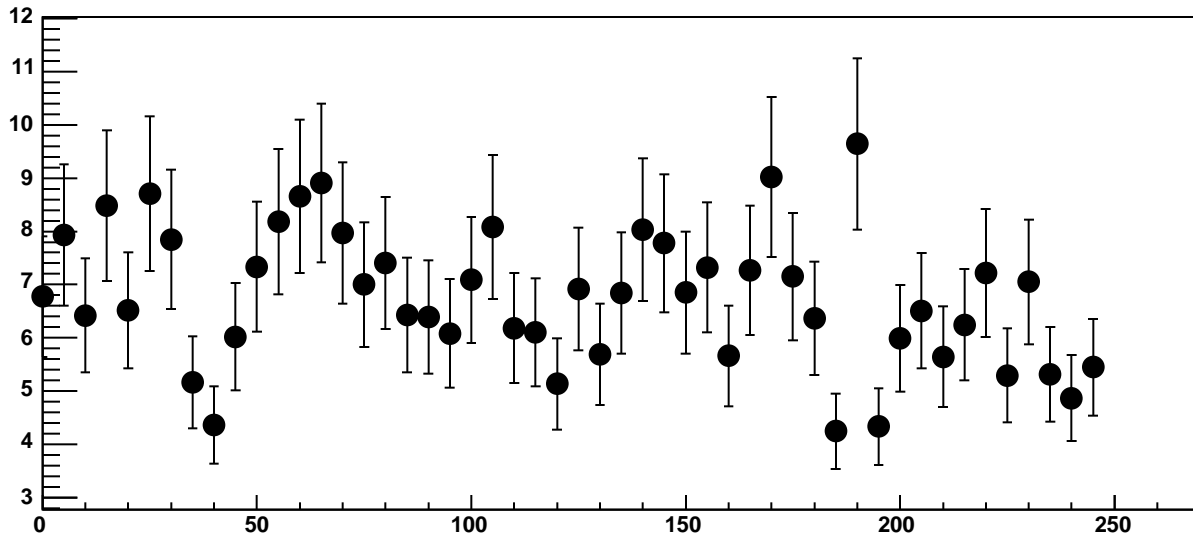


Chip 3, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

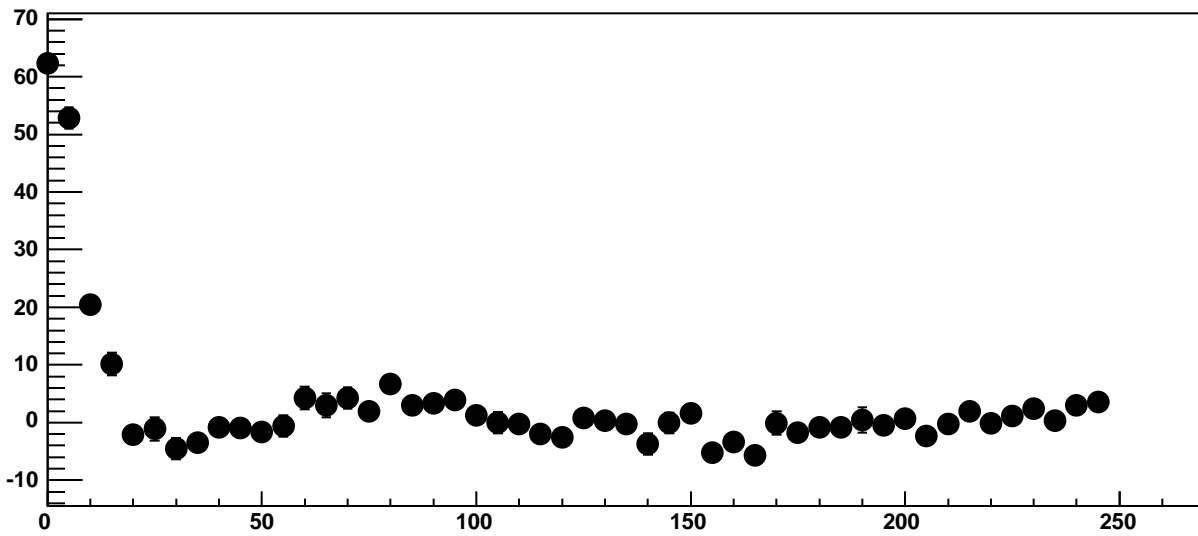


$\chi^2 / \text{ndf}$	151.5 / 41
p0	$389.6 \pm 2.747$
p1	$4.045 \pm 0.5993$
p2	$188.9 \pm 2.842$
p3	$30.3 \pm 0.6932$
p4	$-0.1832 \pm 0.007315$

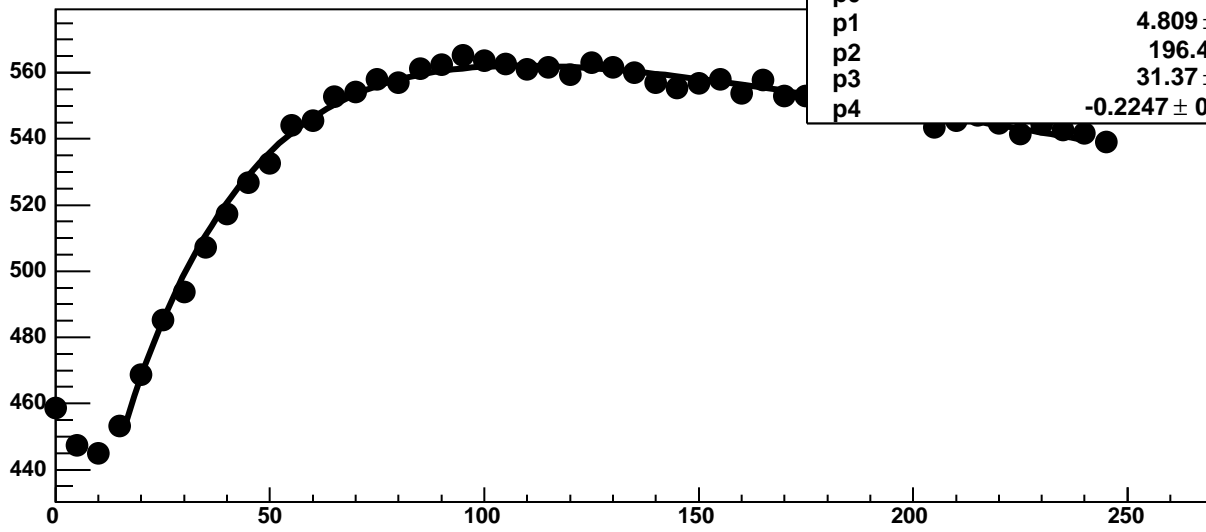
Chip 3, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



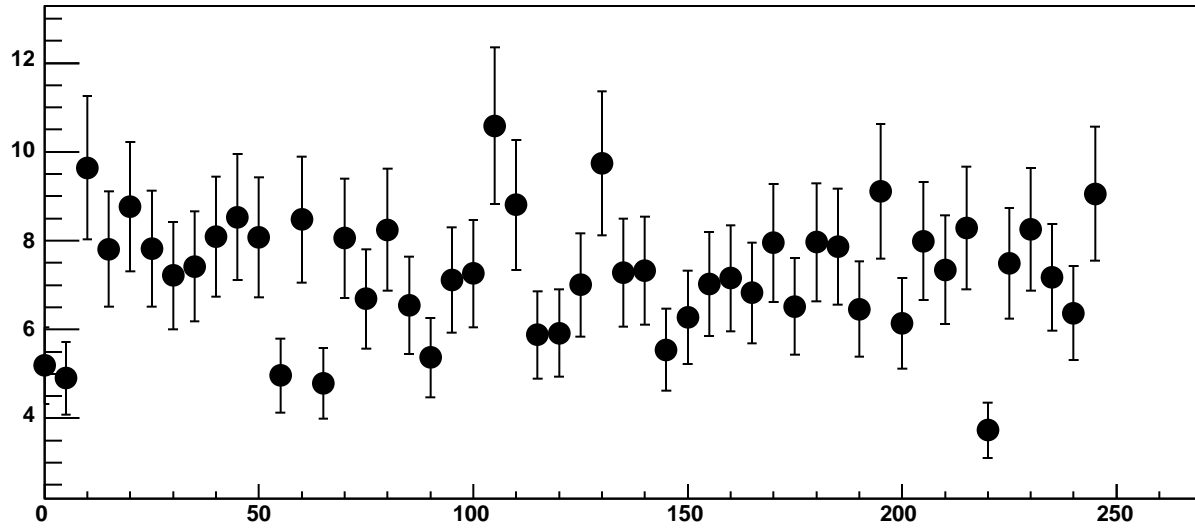
Chip 3, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold



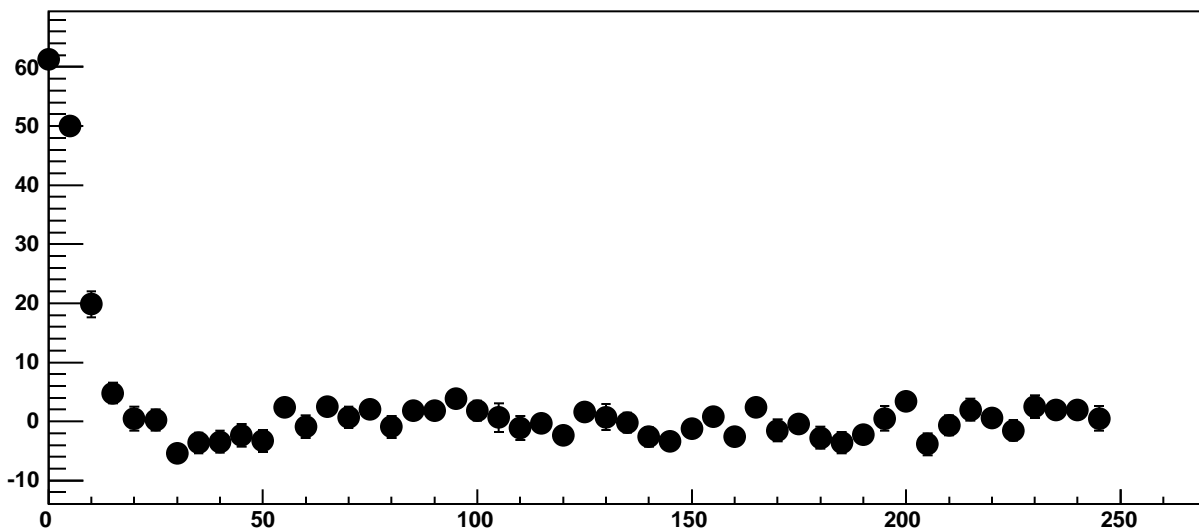
Chip 3, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 3, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold

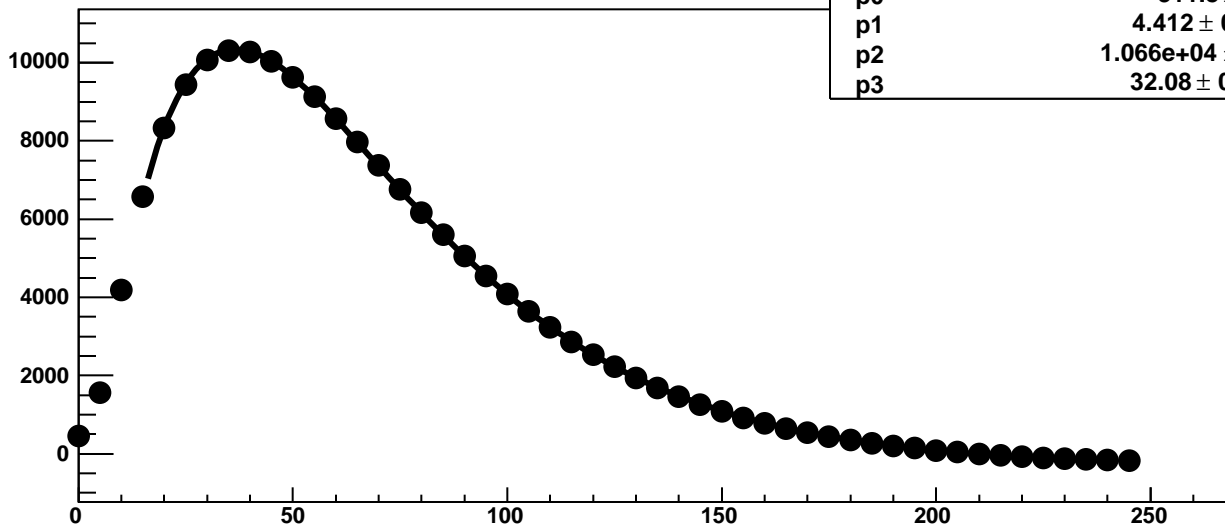


Chip 3, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold



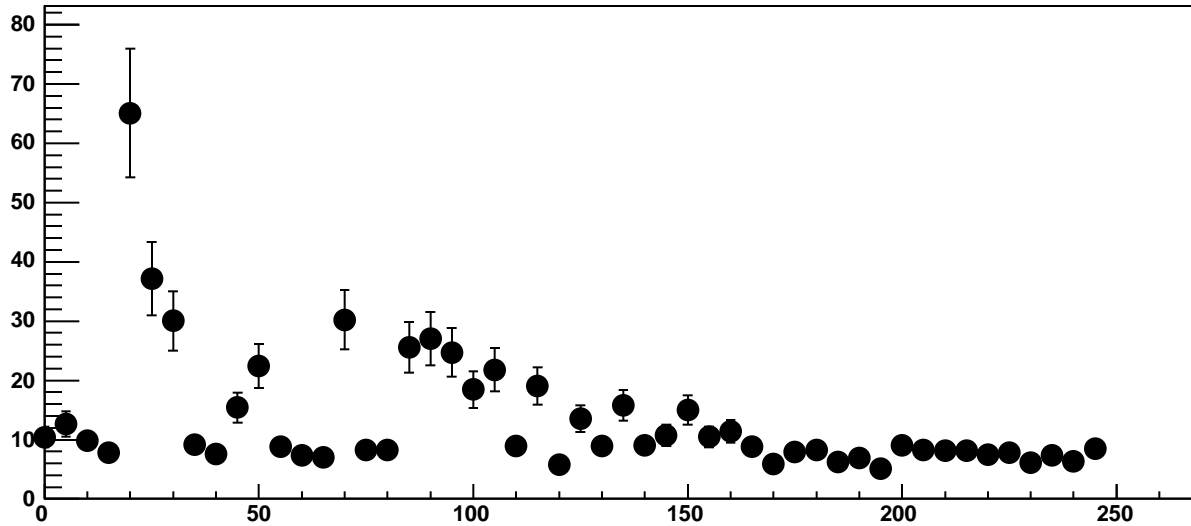


Chip 3, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold

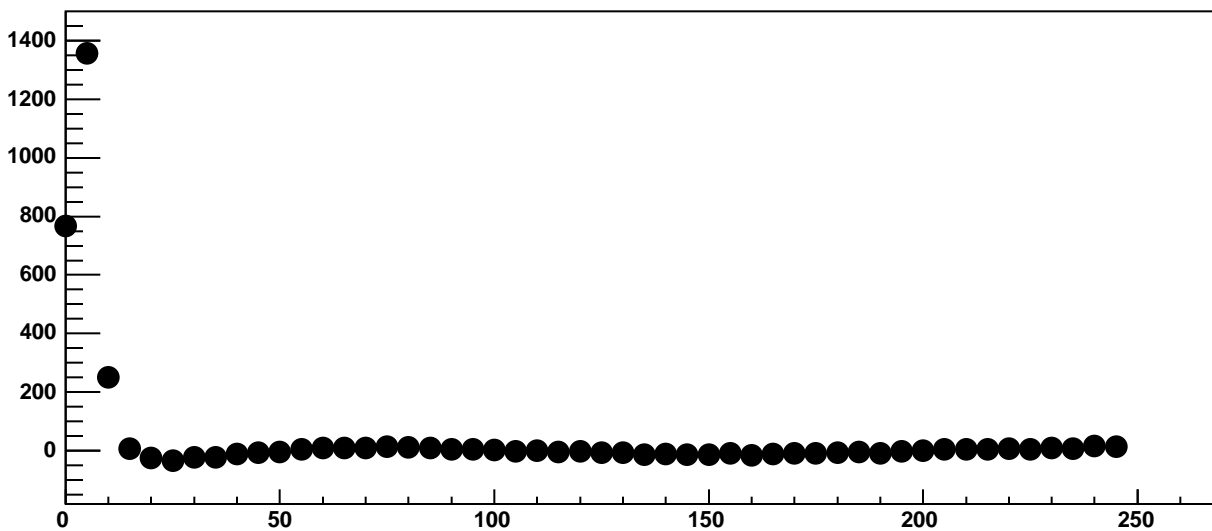


$\chi^2 / \text{ndf}$	929.4 / 42
p0	-311.8 ± 0.5725
p1	4.412 ± 0.005442
p2	1.066e+04 ± 0.9726
p3	32.08 ± 0.005335

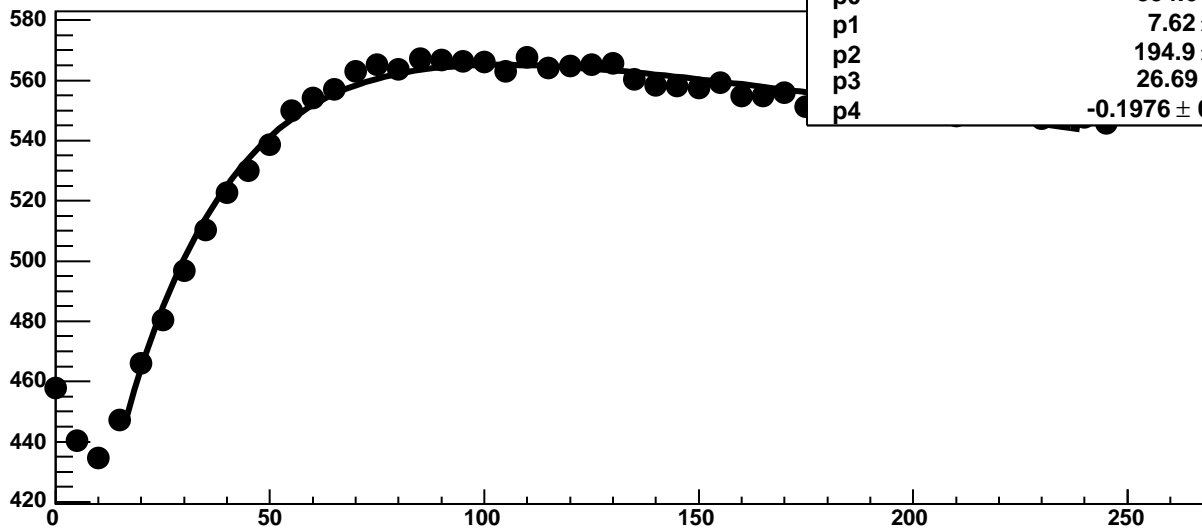
Chip 3, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold

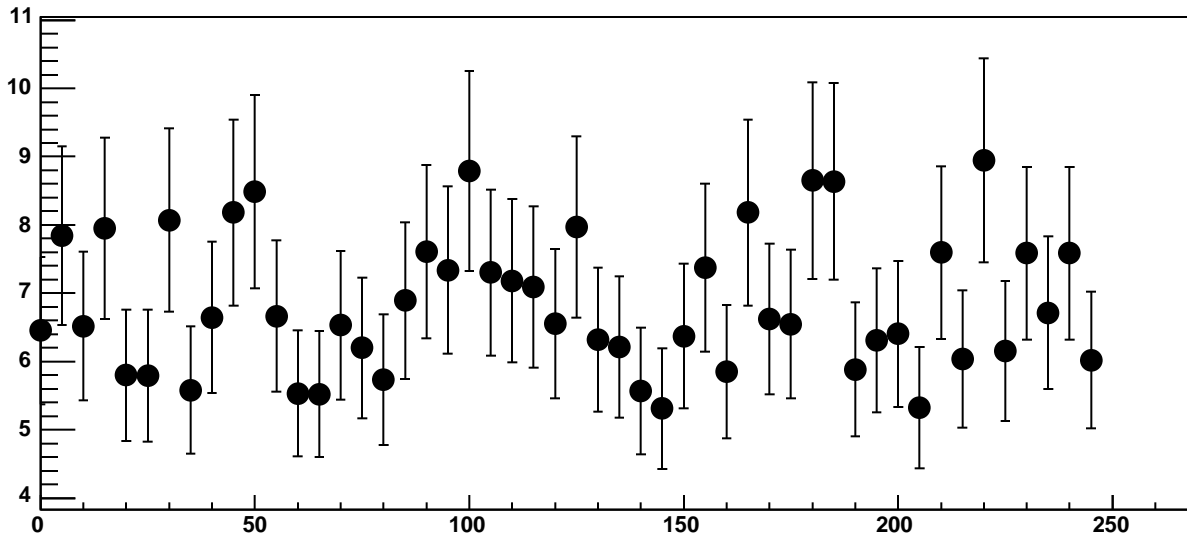


Chip 3, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

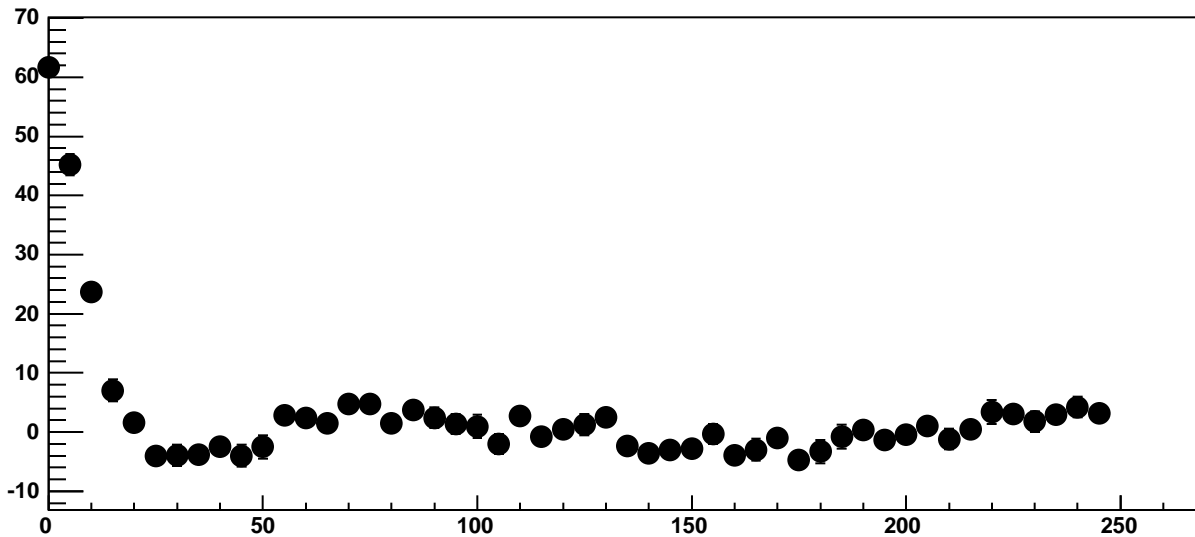


$\chi^2 / \text{ndf}$	160.8 / 41
p0	$394.6 \pm 0.6839$
p1	$7.62 \pm 0.2796$
p2	$194.9 \pm 0.7594$
p3	$26.69 \pm 0.5733$
p4	$-0.1976 \pm 0.007952$

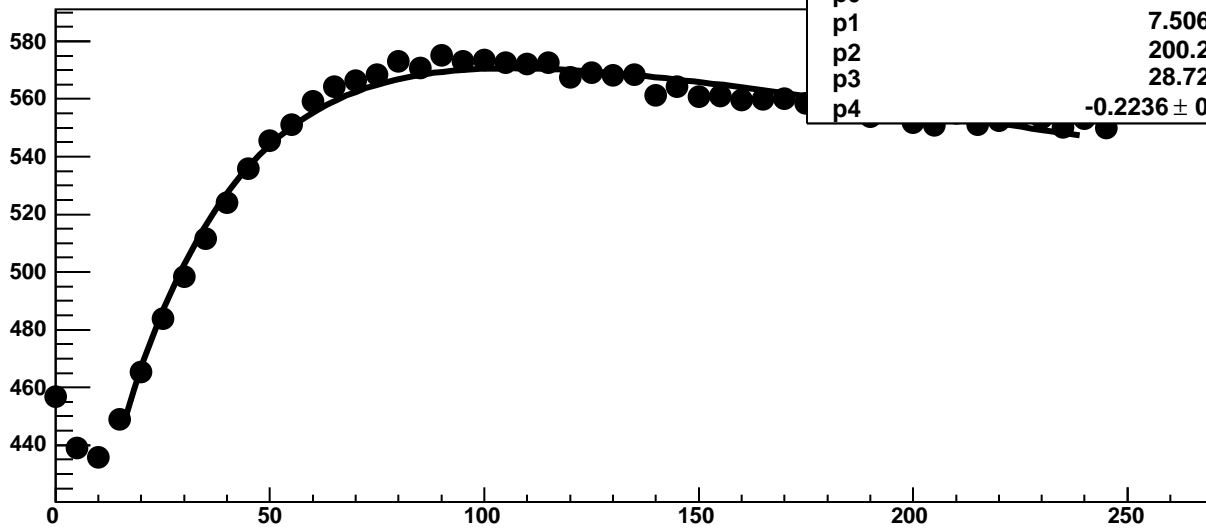
Chip 3, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

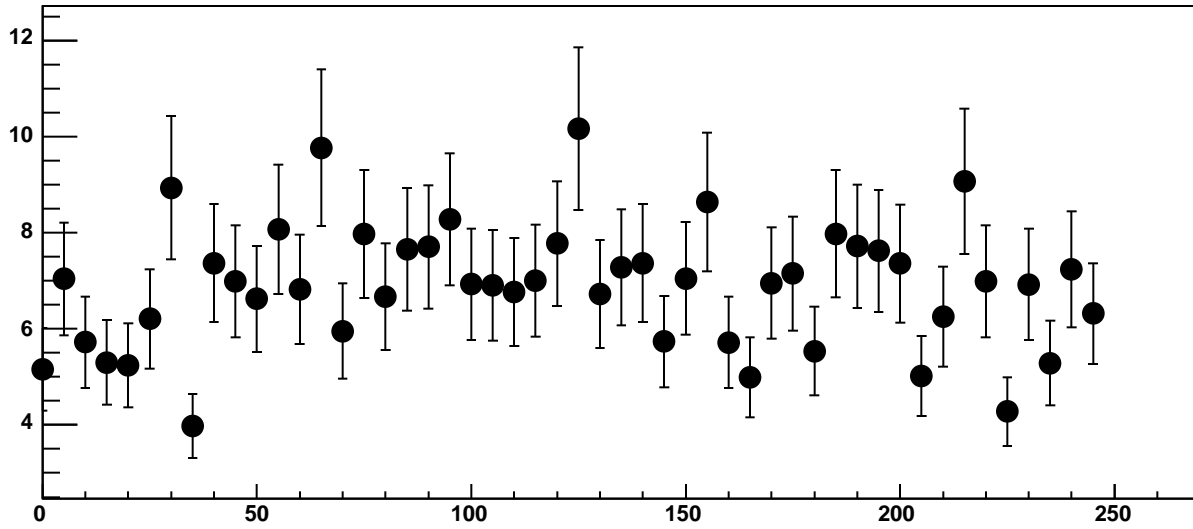


Chip 3, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

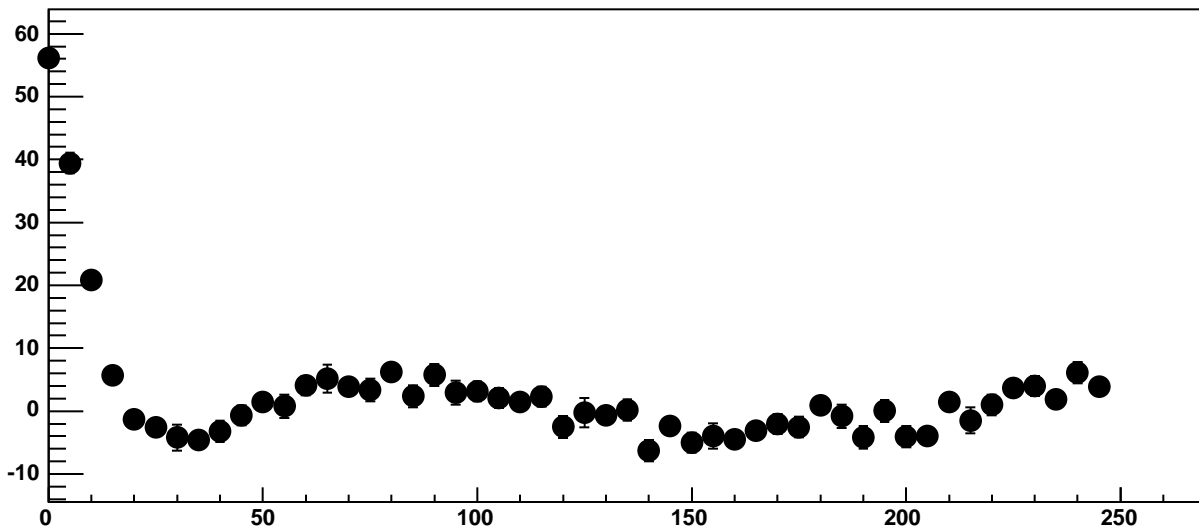


$\chi^2 / \text{ndf}$	242.5 / 41
p0	$399 \pm 2.834$
p1	$7.506 \pm 0.466$
p2	$200.2 \pm 2.949$
p3	$28.72 \pm 0.513$
p4	$-0.2236 \pm 0.007346$

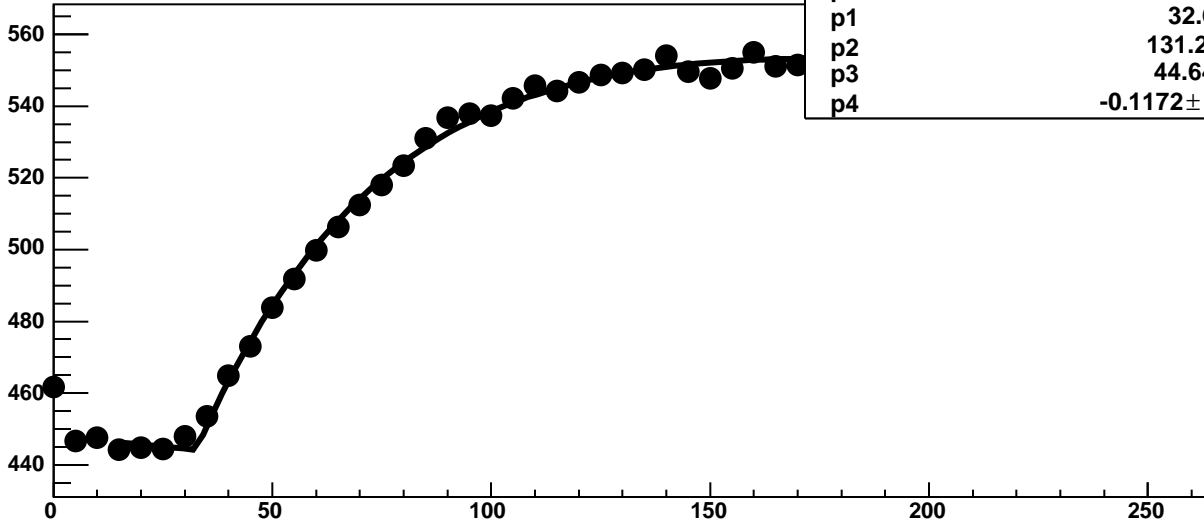
Chip 3, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

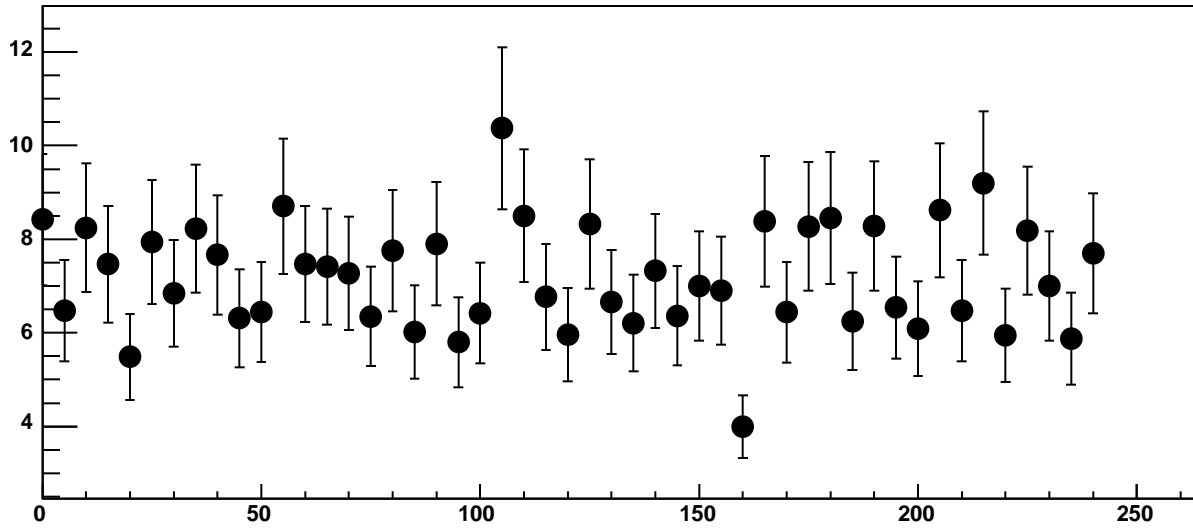


Chip 3, Channel 16, Enable 5, DAC=4000, ADC Mean vs Hold

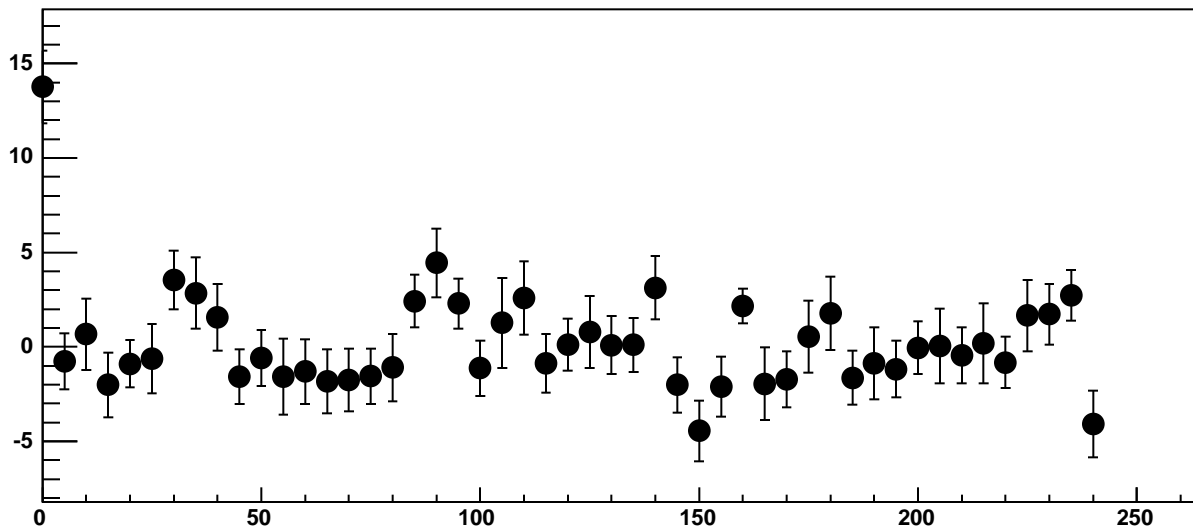


$\chi^2 / \text{ndf}$	69.31 / 41
p0	$444.2 \pm 0.7628$
p1	$32.6 \pm 0.57$
p2	$131.2 \pm 4.196$
p3	$44.64 \pm 2.04$
p4	$-0.1172 \pm 0.02009$

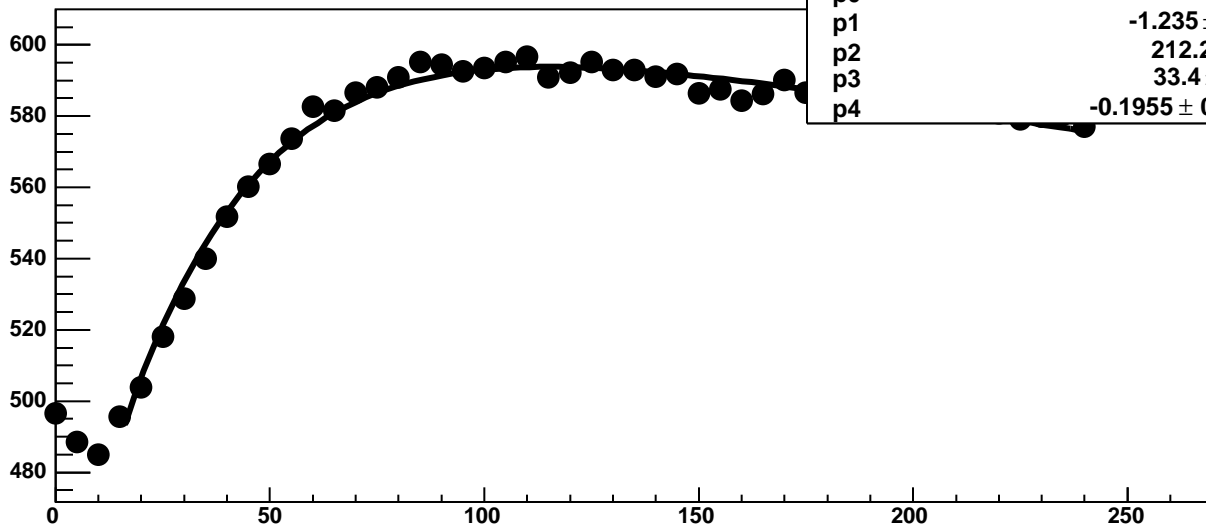
Chip 3, Channel 16, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 16, Enable 5, DAC=4000, ADC Residuals vs Hold

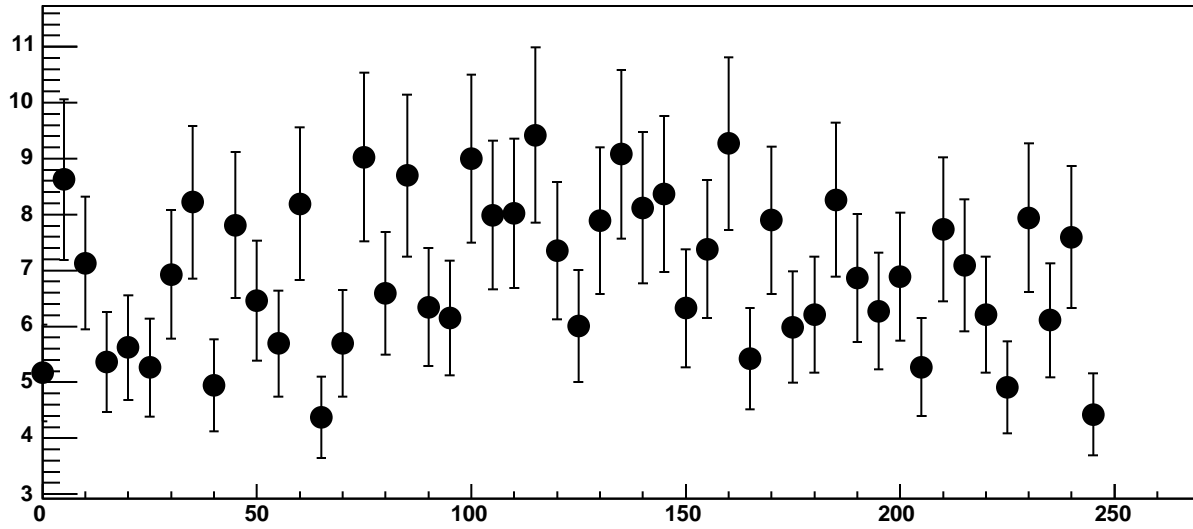


Chip 3, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

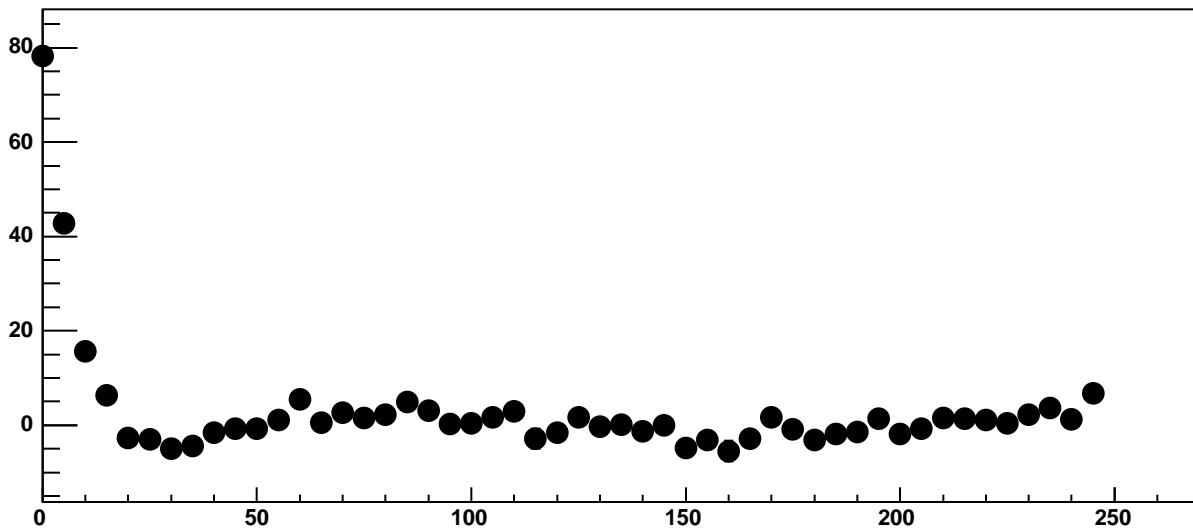


$\chi^2 / \text{ndf}$	136.7 / 41
p0	$410.8 \pm 2.861$
p1	$-1.235 \pm 0.5872$
p2	$212.2 \pm 2.981$
p3	$33.4 \pm 0.7581$
p4	$-0.1955 \pm 0.008681$

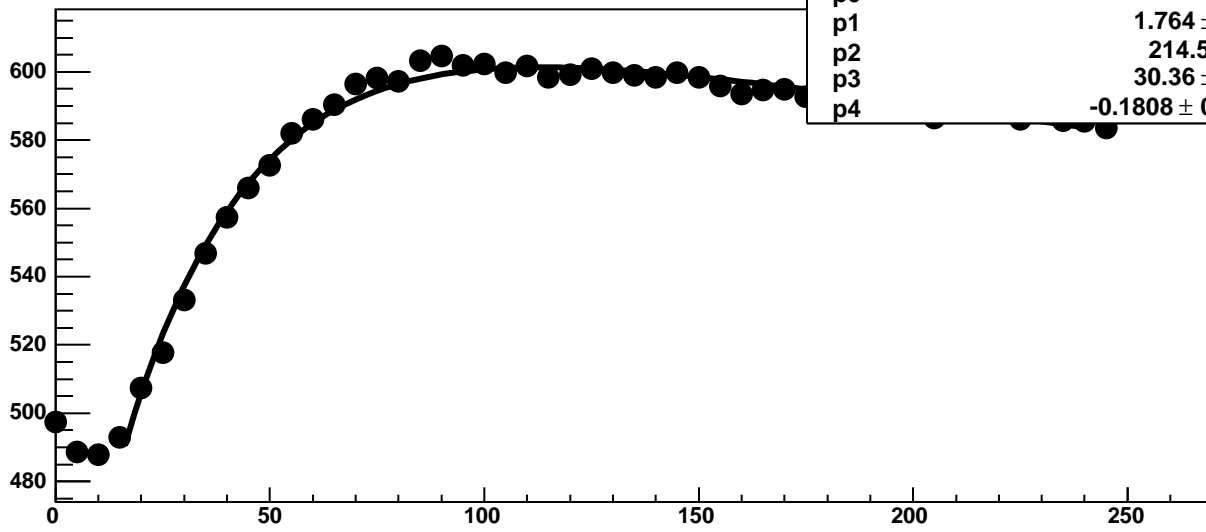
Chip 3, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

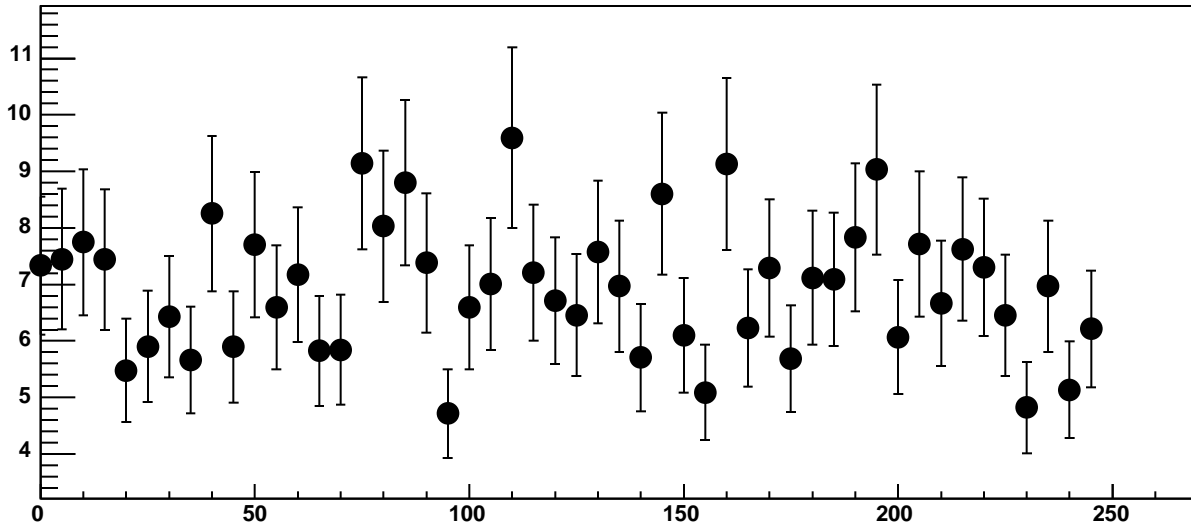


Chip 3, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

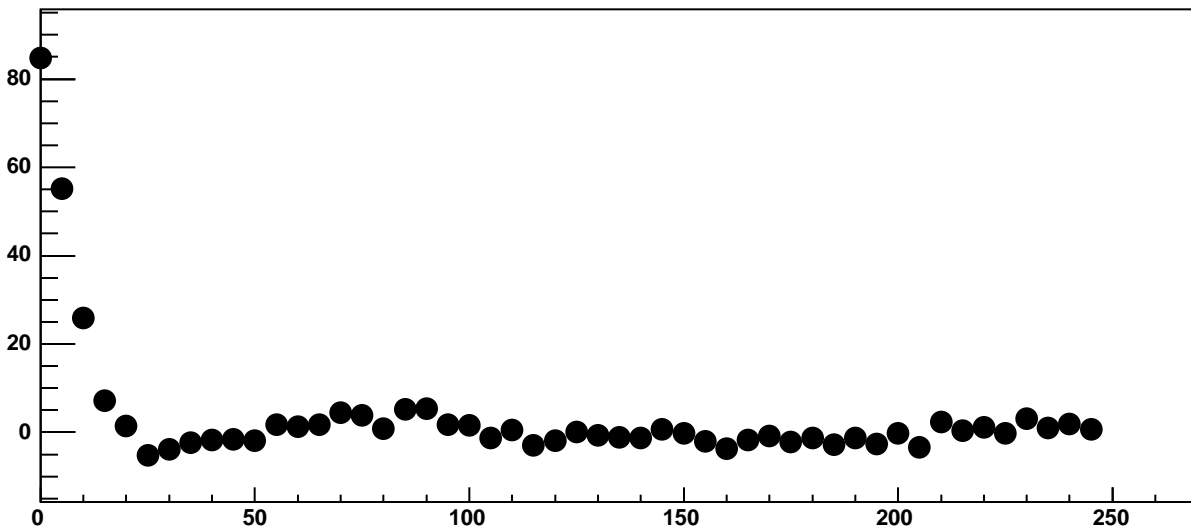


$\chi^2 / \text{ndf}$	126.8 / 41
p0	$412.4 \pm 2.842$
p1	$1.764 \pm 0.5444$
p2	$214.5 \pm 2.932$
p3	$30.36 \pm 0.6574$
p4	$-0.1808 \pm 0.007661$

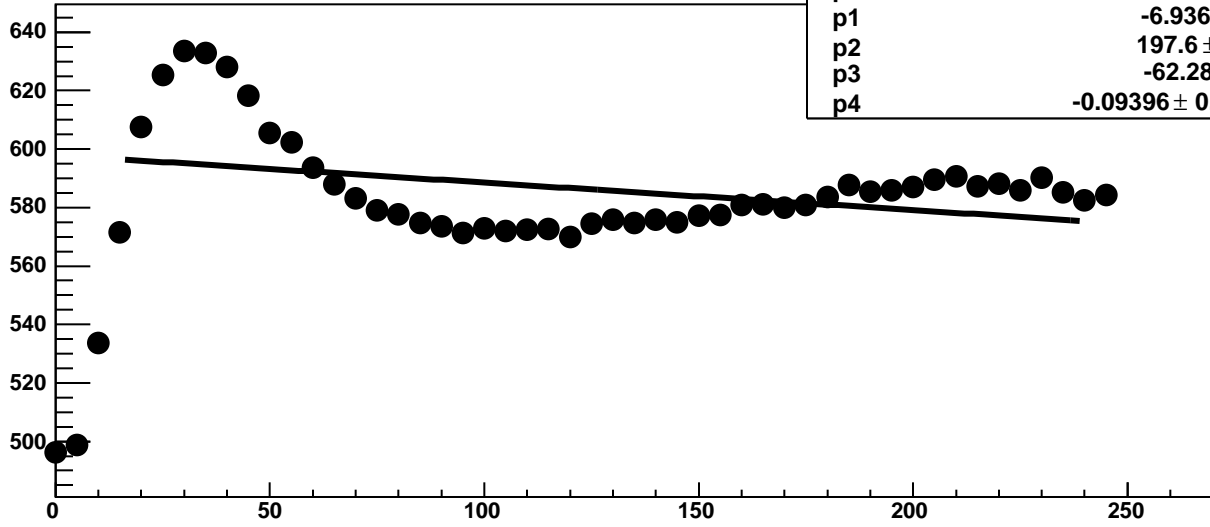
Chip 3, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

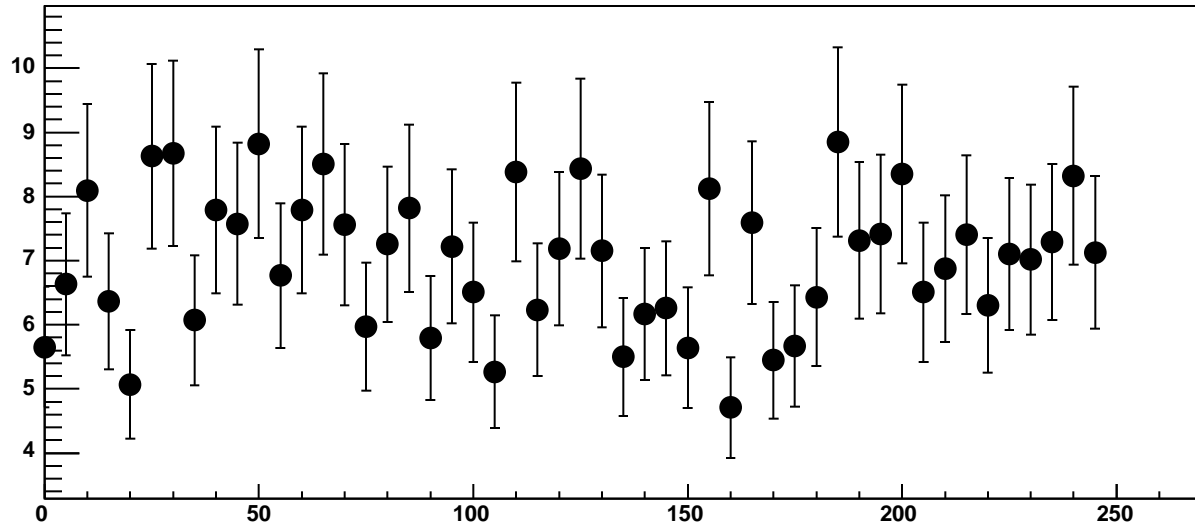


Chip 3, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold

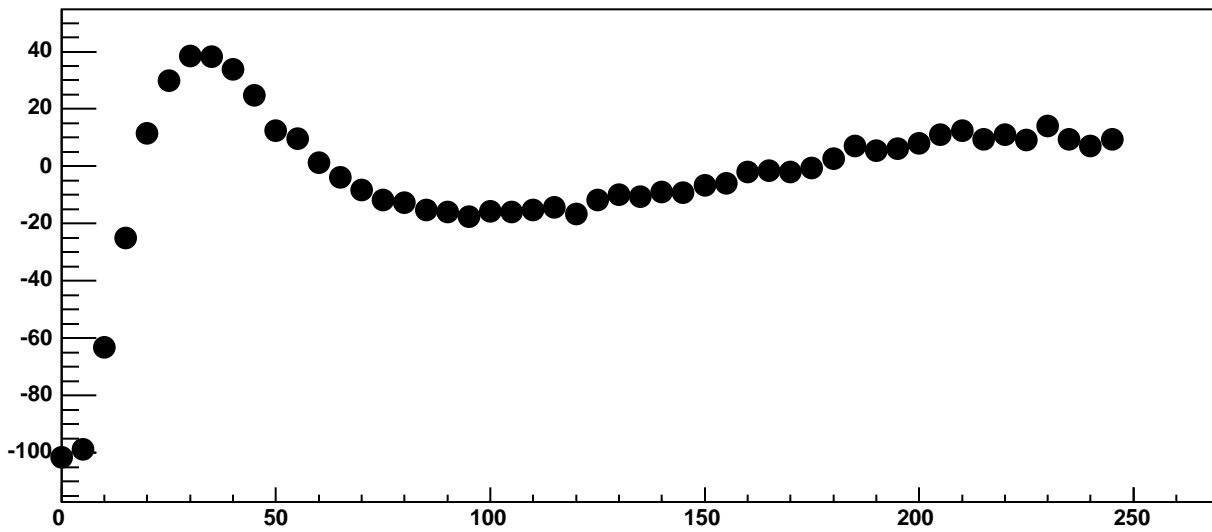


$\chi^2 / \text{ndf}$	4102 / 41
p0	$400.9 \pm 0.3191$
p1	$-6.936 \pm 16.57$
p2	$197.6 \pm 0.2179$
p3	$-62.28 \pm 4.701$
p4	$-0.09396 \pm 0.001594$

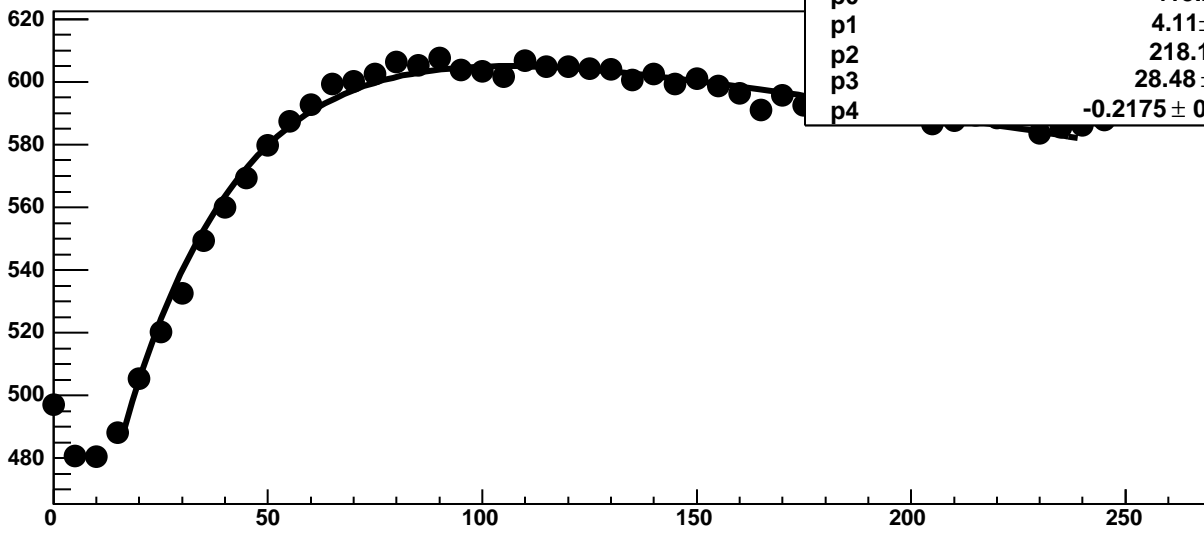
Chip 3, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold

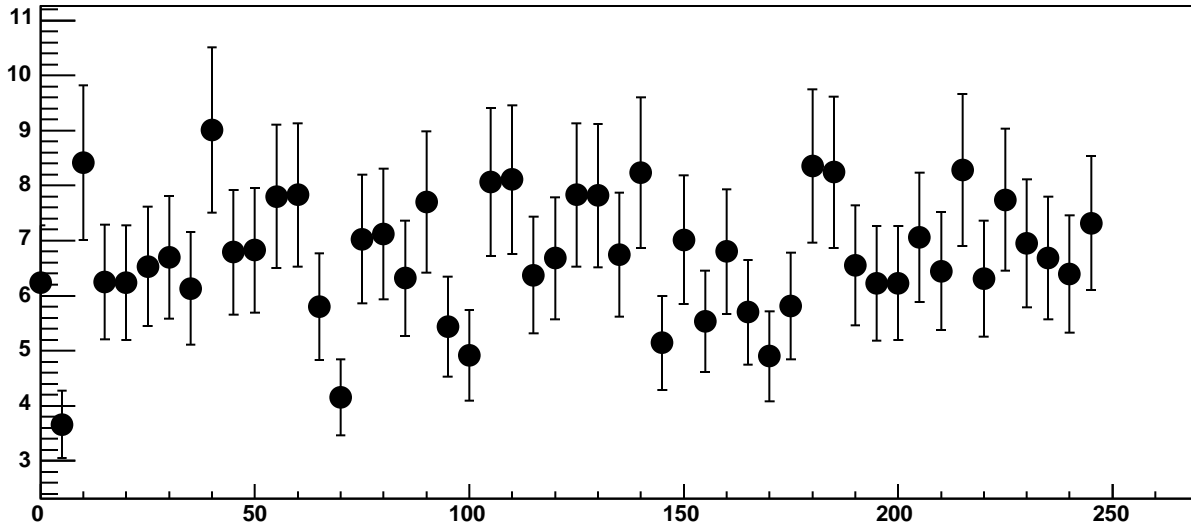


Chip 3, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold

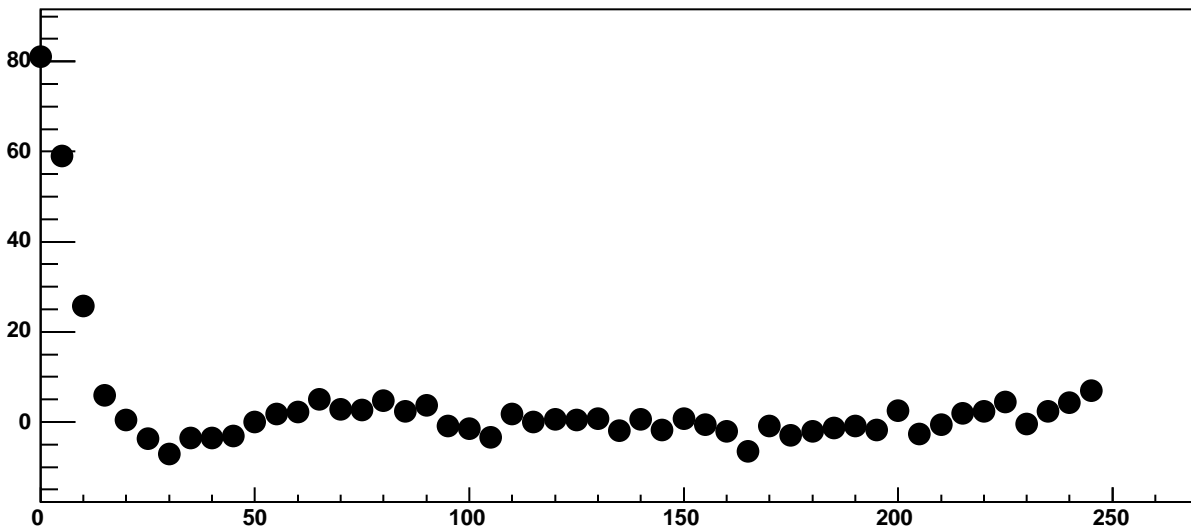


$\chi^2 / \text{ndf}$	175.6 / 41
p0	$415.2 \pm 2.79$
p1	$4.11 \pm 0.4675$
p2	$218.1 \pm 2.884$
p3	$28.48 \pm 0.5607$
p4	$-0.2175 \pm 0.007566$

Chip 3, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold

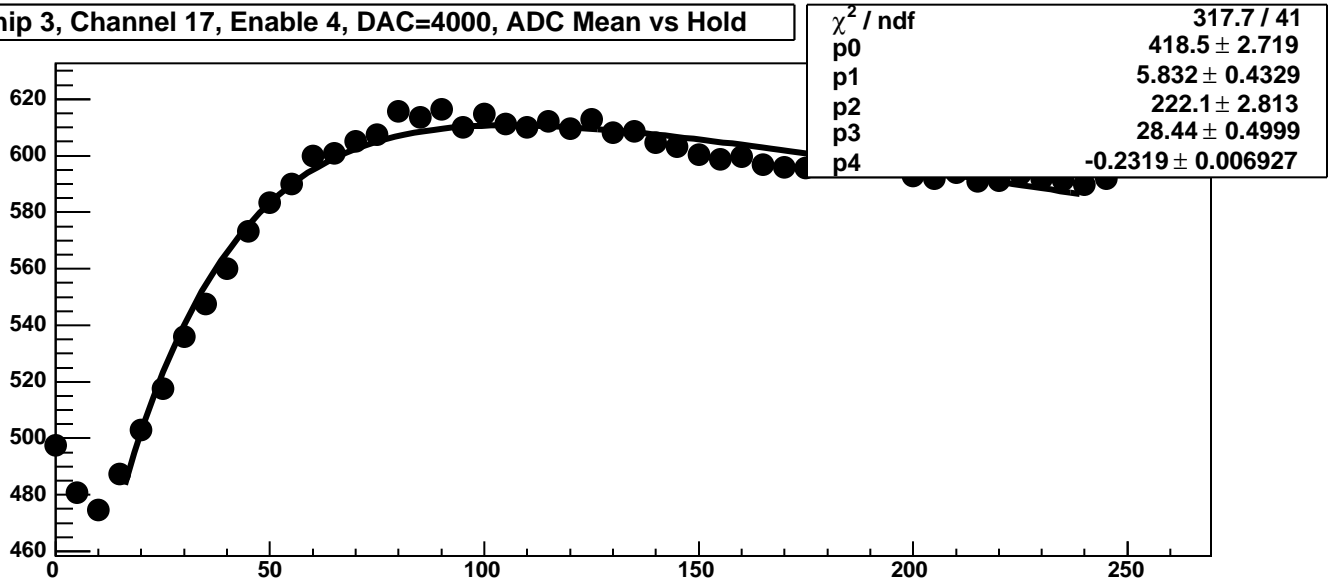


Chip 3, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold

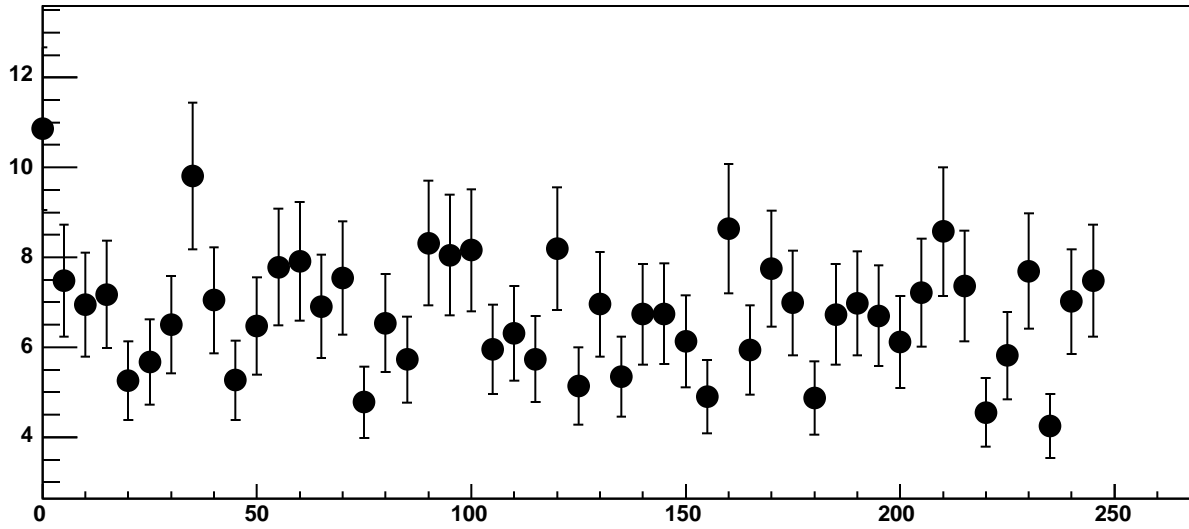




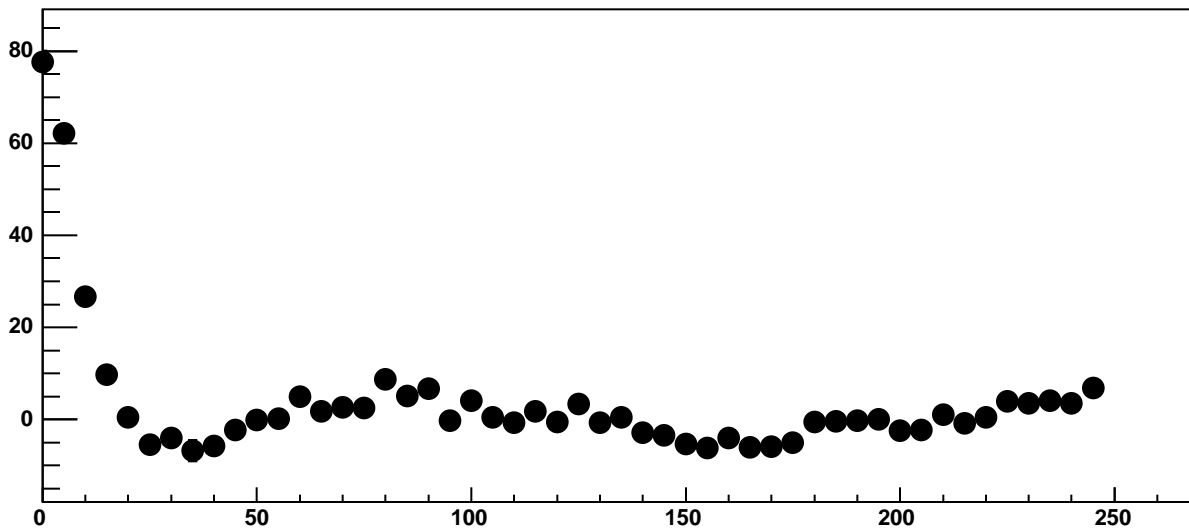
Chip 3, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



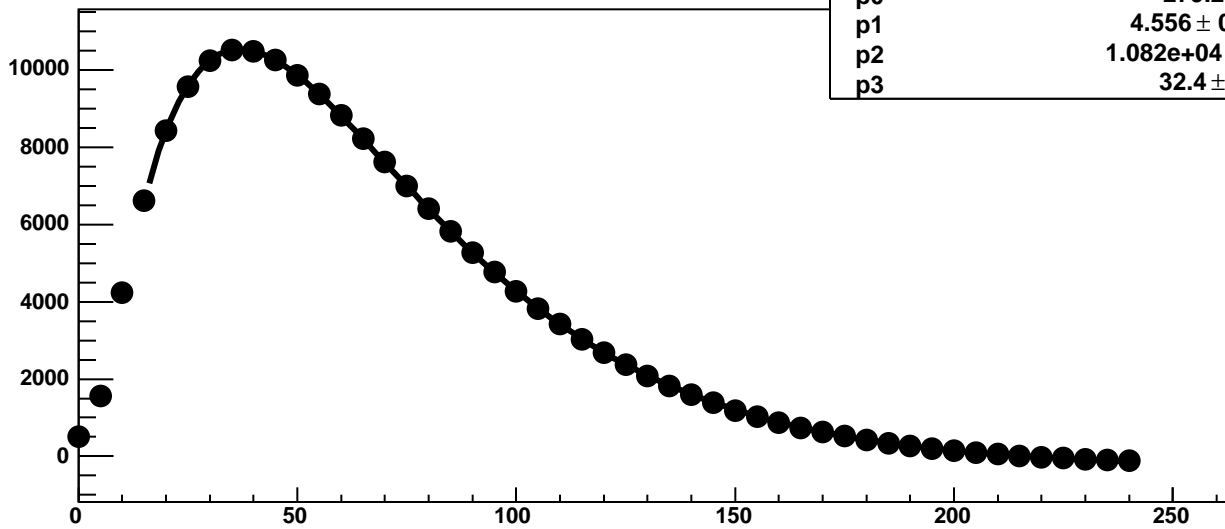
Chip 3, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

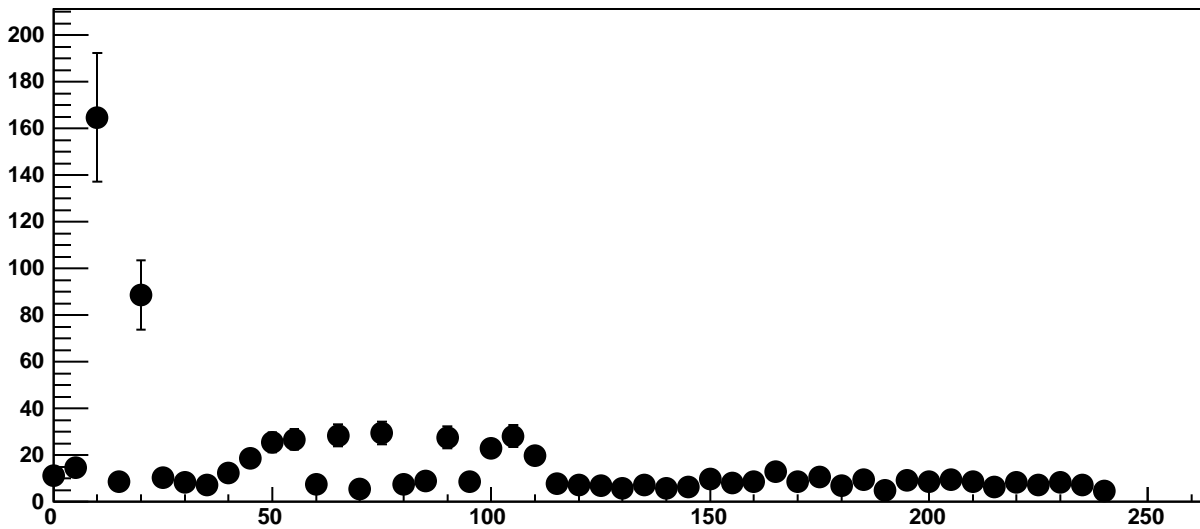


Chip 3, Channel 17, Enable 5!, DAC=4000, ADC Mean vs Hold

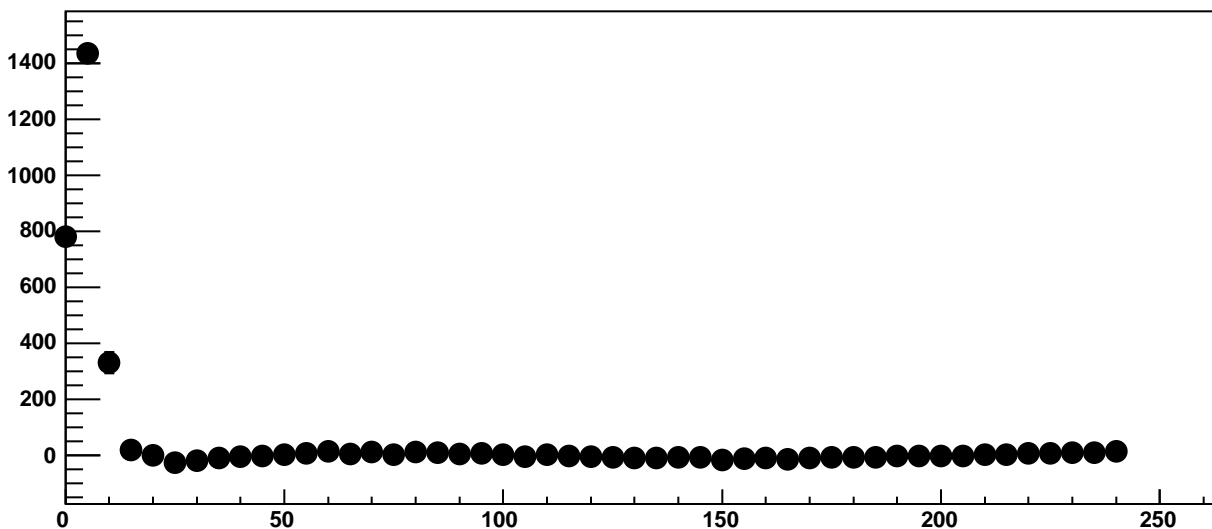


$\chi^2 / \text{ndf}$	1212 / 42
p0	-279.2 ± 0.5621
p1	4.556 ± 0.005598
p2	1.082e+04 ± 0.9251
p3	32.4 ± 0.00464

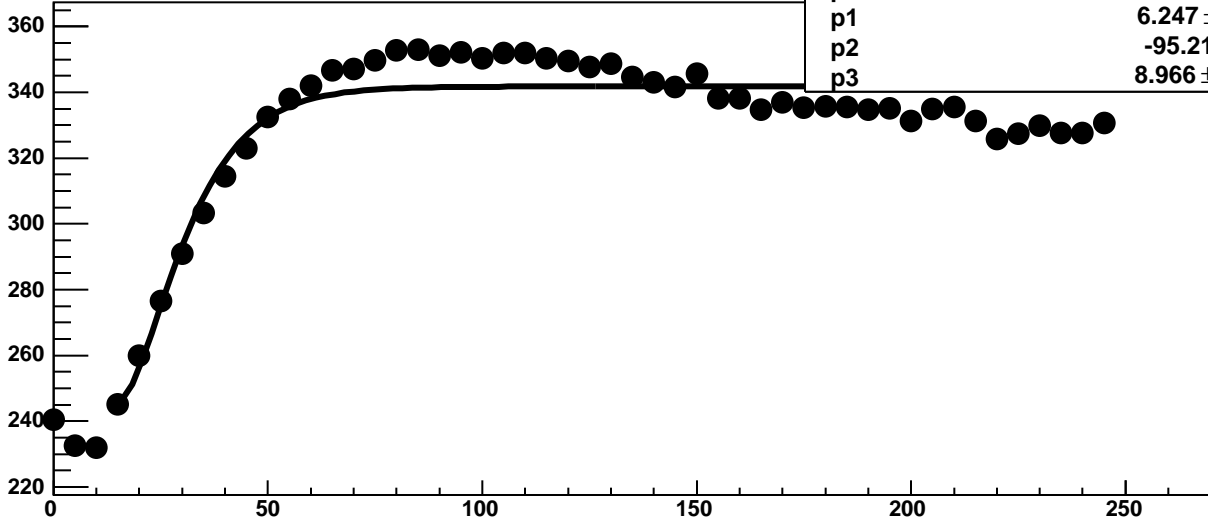
Chip 3, Channel 17, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 17, Enable 5!, DAC=4000, ADC Residuals vs Hold

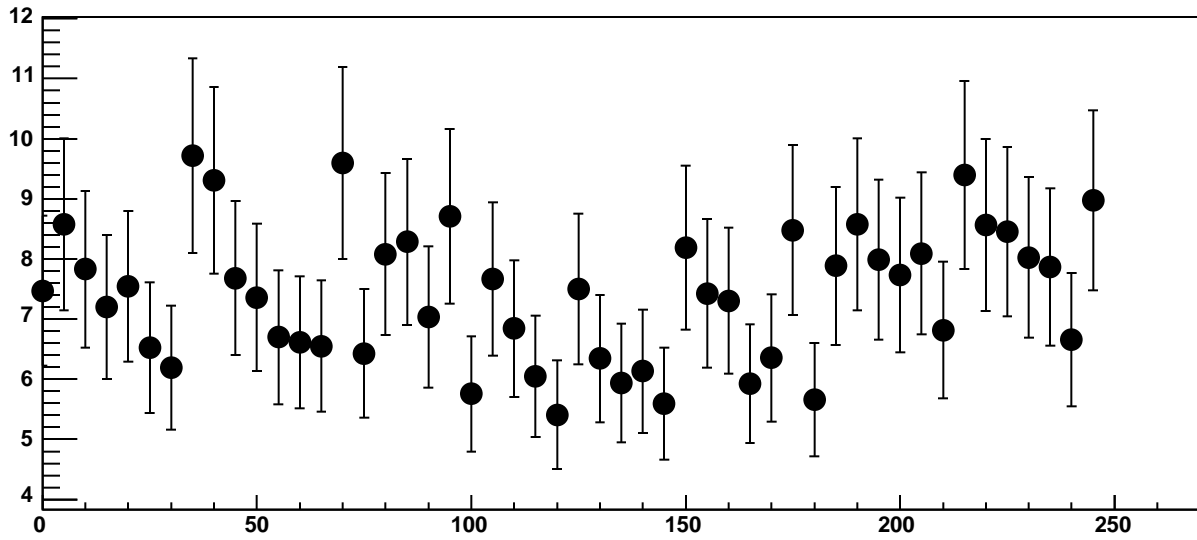


Chip 4, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold

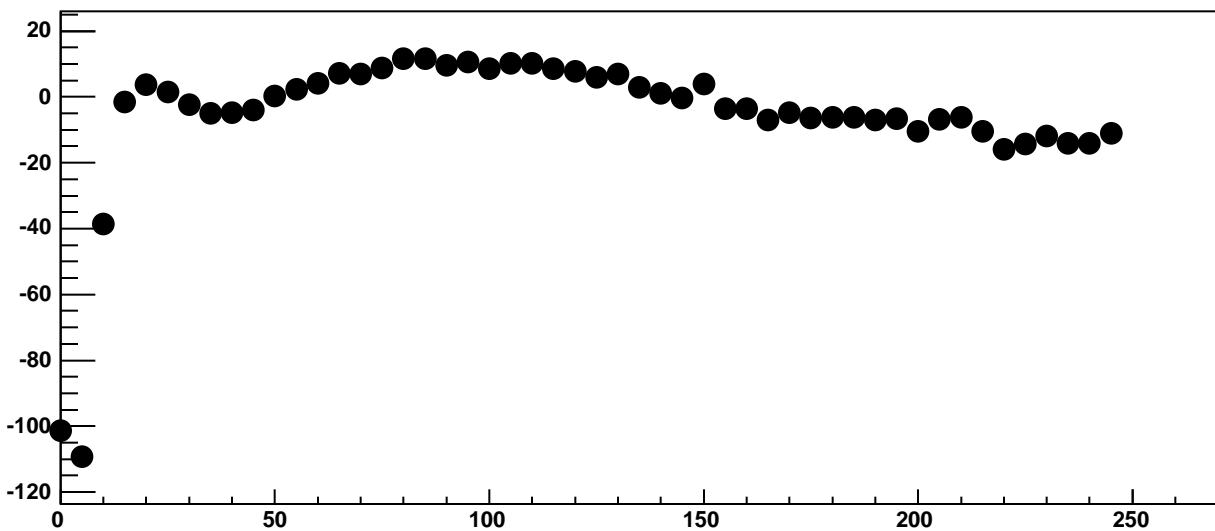


$\chi^2 / \text{ndf}$	993.8 / 42
p0	$341.8 \pm 0.2687$
p1	$6.247 \pm 0.7121$
p2	$-95.21 \pm 1.541$
p3	$8.966 \pm 0.2433$

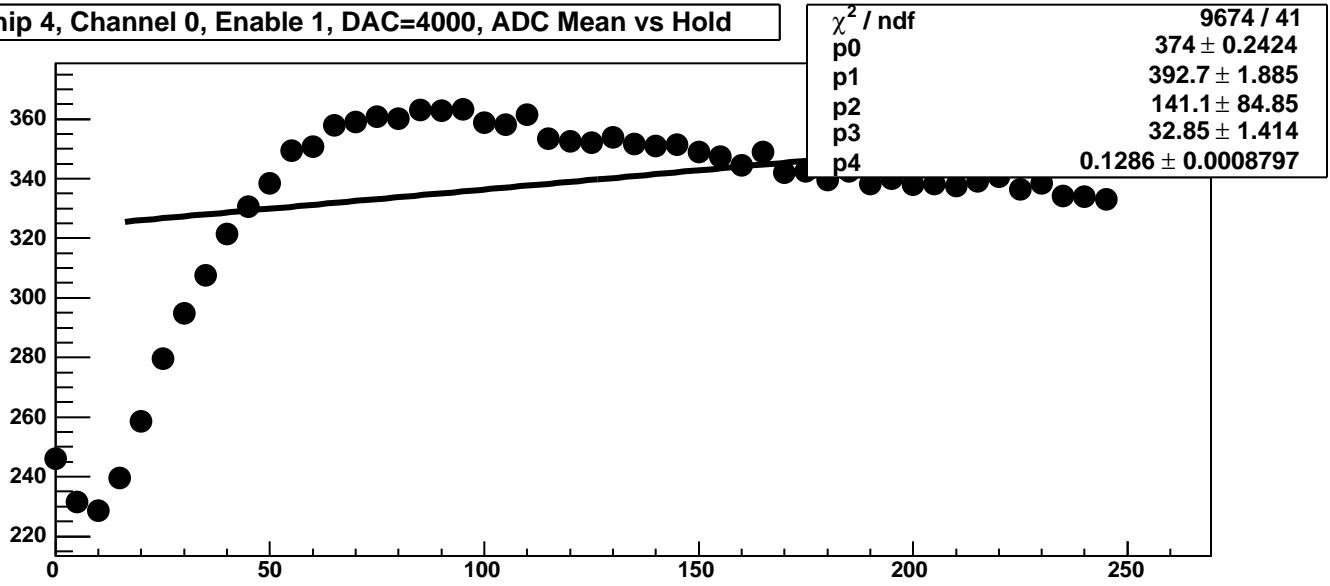
Chip 4, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold



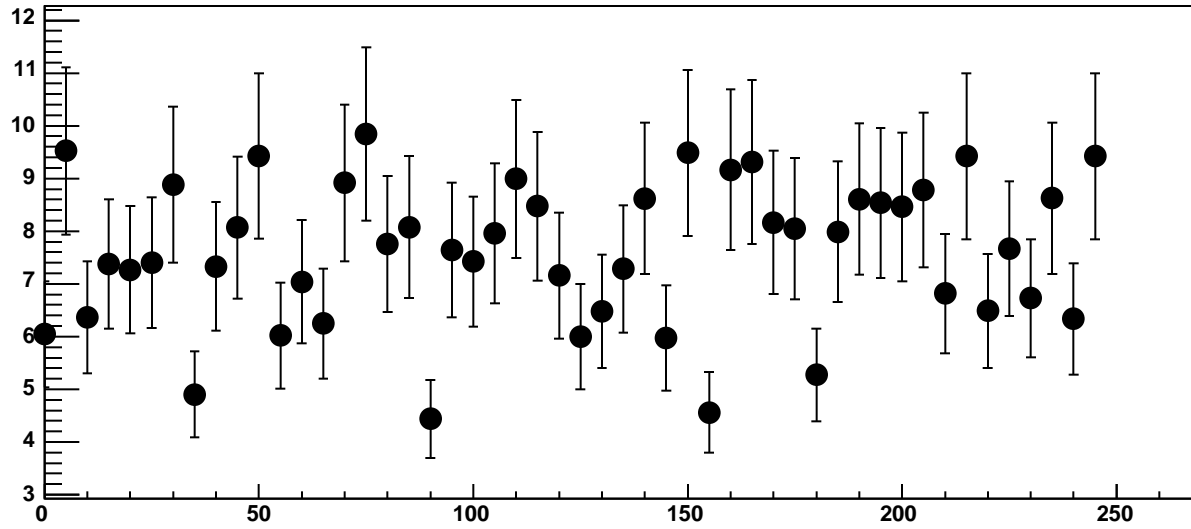
Chip 4, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold



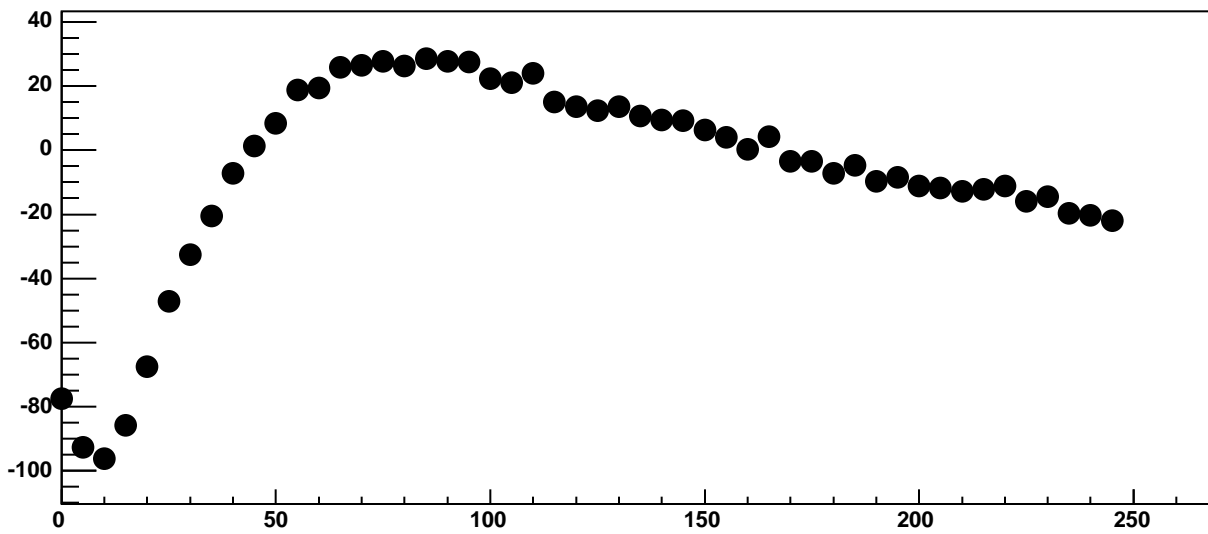
Chip 4, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold



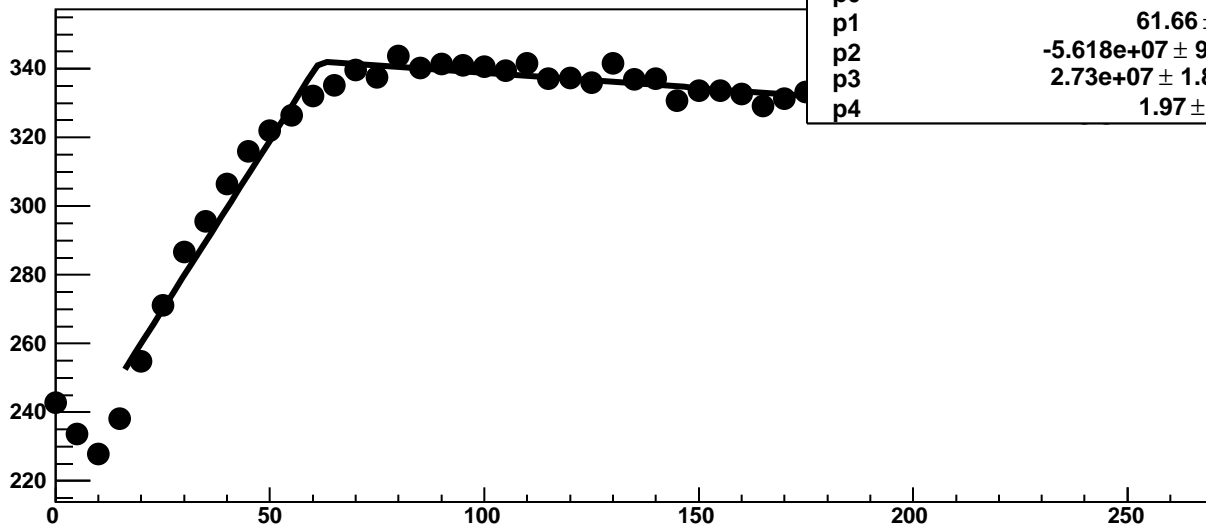
Chip 4, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold

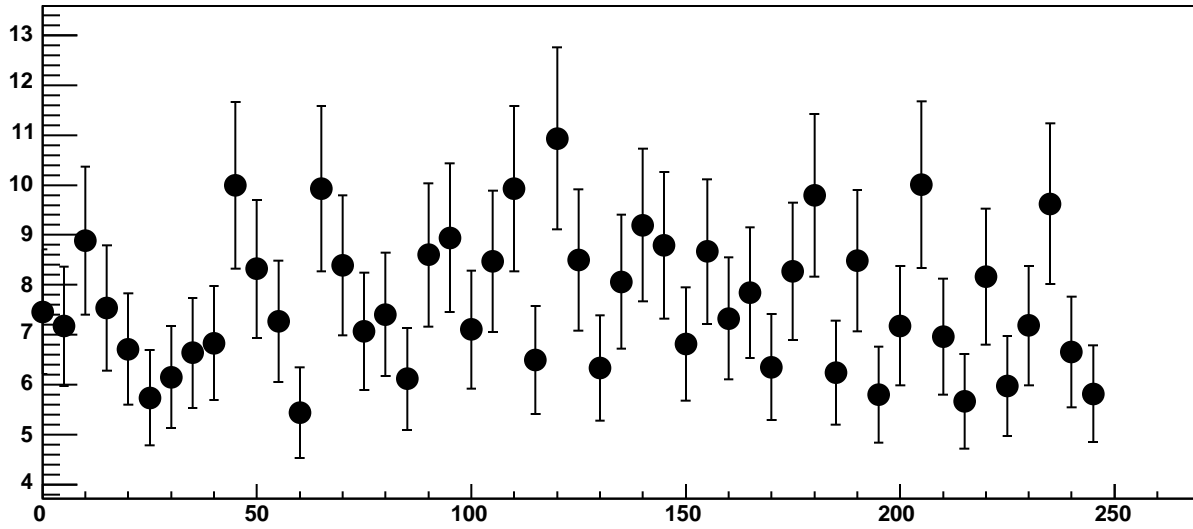


Chip 4, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

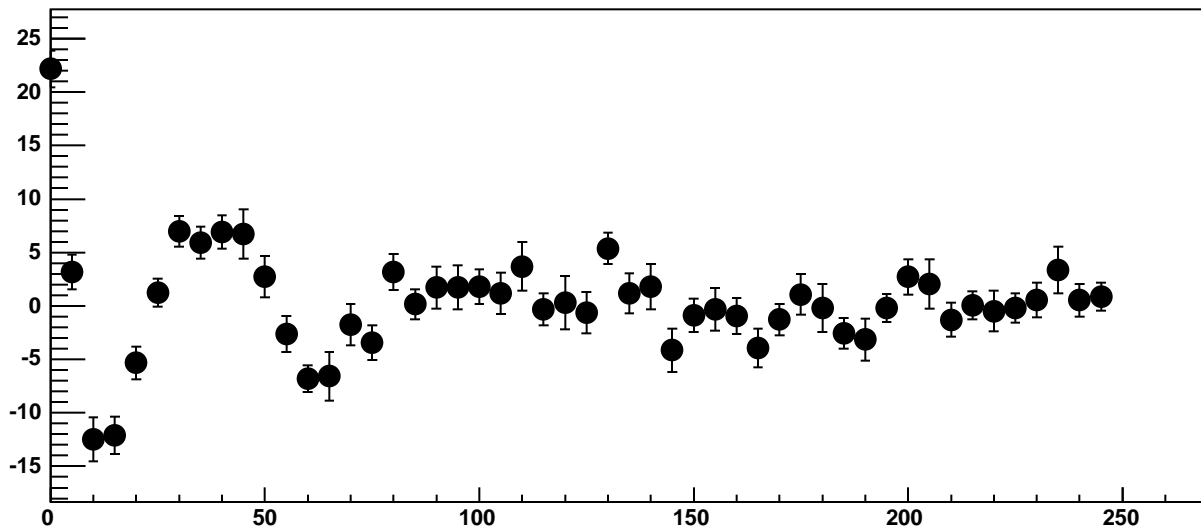


$\chi^2 / \text{ndf}$	224.9 / 41
p0	$342.1 \pm 0.577$
p1	$61.66 \pm 0.5467$
p2	$-5.618\text{e}+07 \pm 9.87\text{e}+05$
p3	$2.73\text{e}+07 \pm 1.846\text{e}+05$
p4	$1.97 \pm 0.03331$

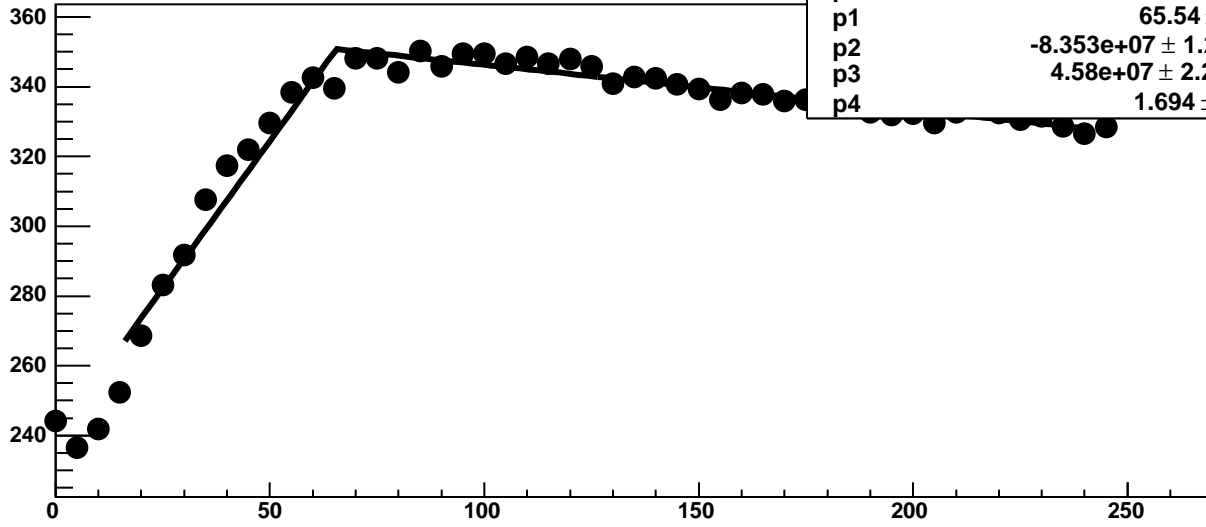
Chip 4, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

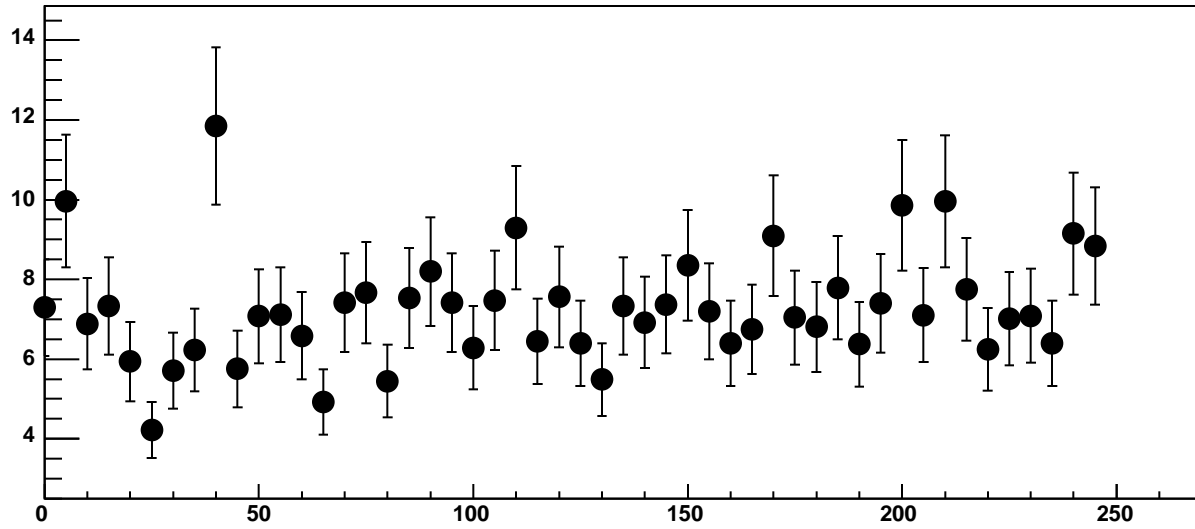


Chip 4, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold

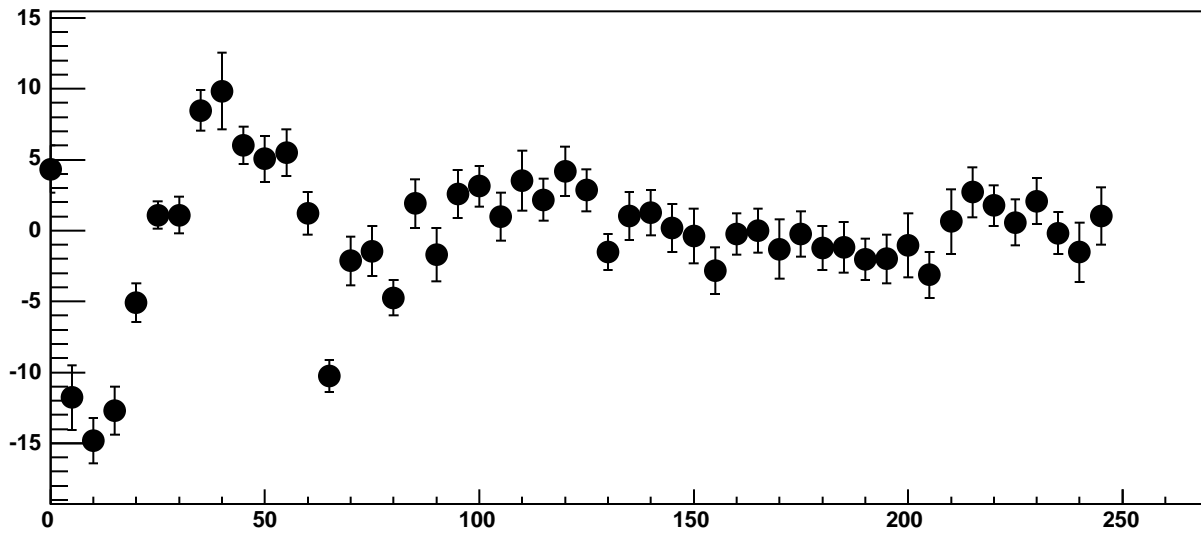


$\chi^2 / \text{ndf}$	306.6 / 41
p0	$350.8 \pm 0.5226$
p1	$65.54 \pm 0.5251$
p2	$-8.353e+07 \pm 1.237e+06$
p3	$4.58e+07 \pm 2.277e+05$
p4	$1.694 \pm 0.0254$

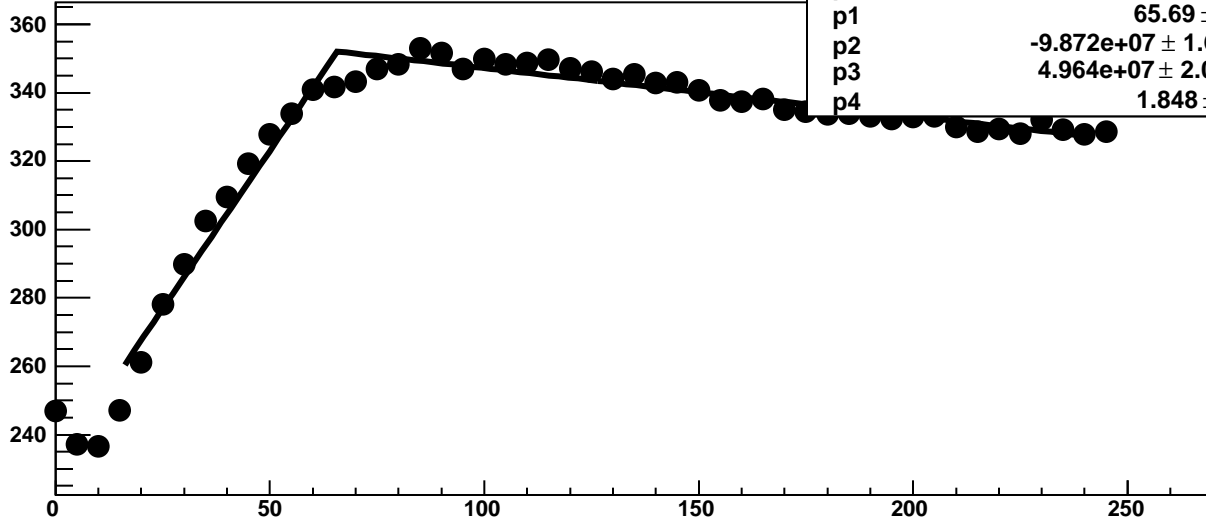
Chip 4, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold

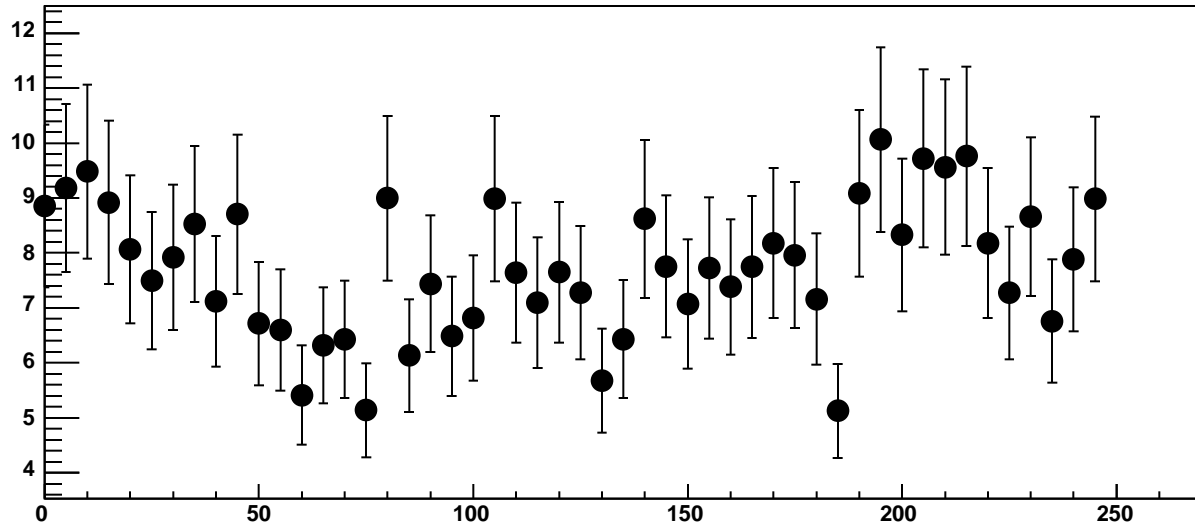


Chip 4, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

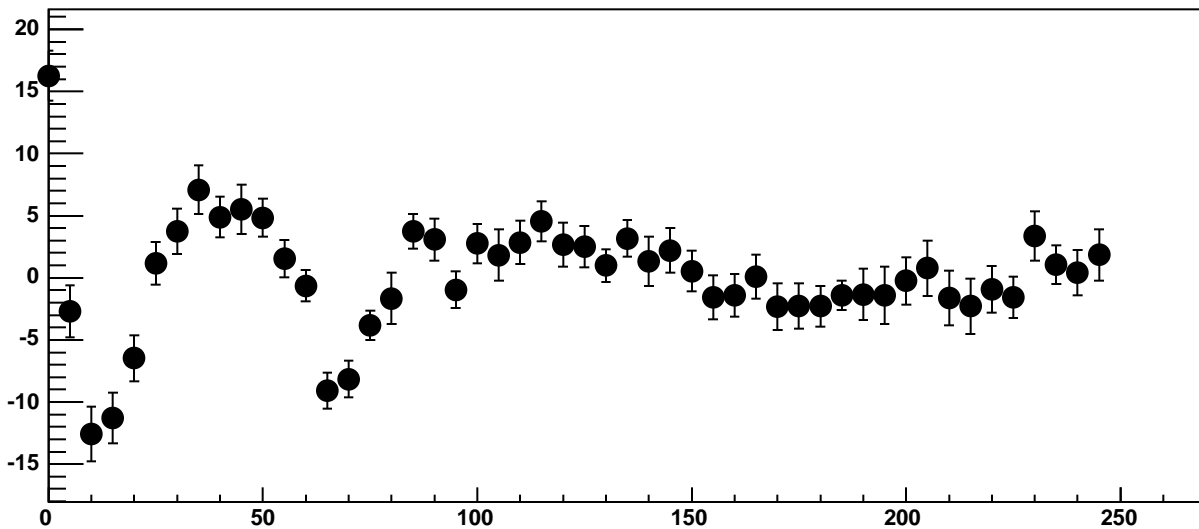


$\chi^2 / \text{ndf}$	221 / 41
p0	$352 \pm 0.5057$
p1	$65.69 \pm 0.5044$
p2	$-9.872\text{e}+07 \pm 1.628\text{e}+06$
p3	$4.964\text{e}+07 \pm 2.045\text{e}+05$
p4	$1.848 \pm 0.0313$

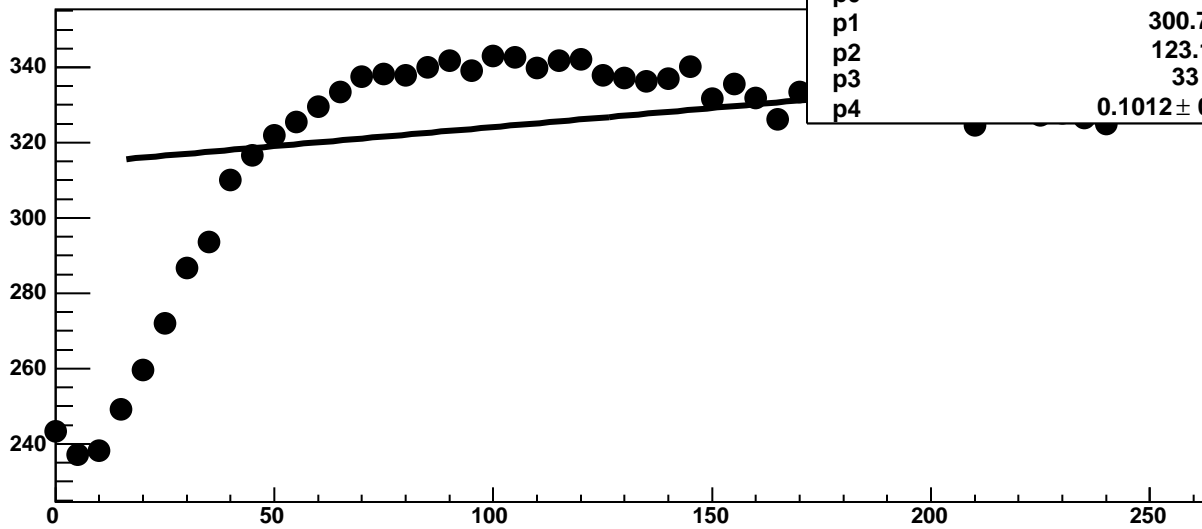
Chip 4, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

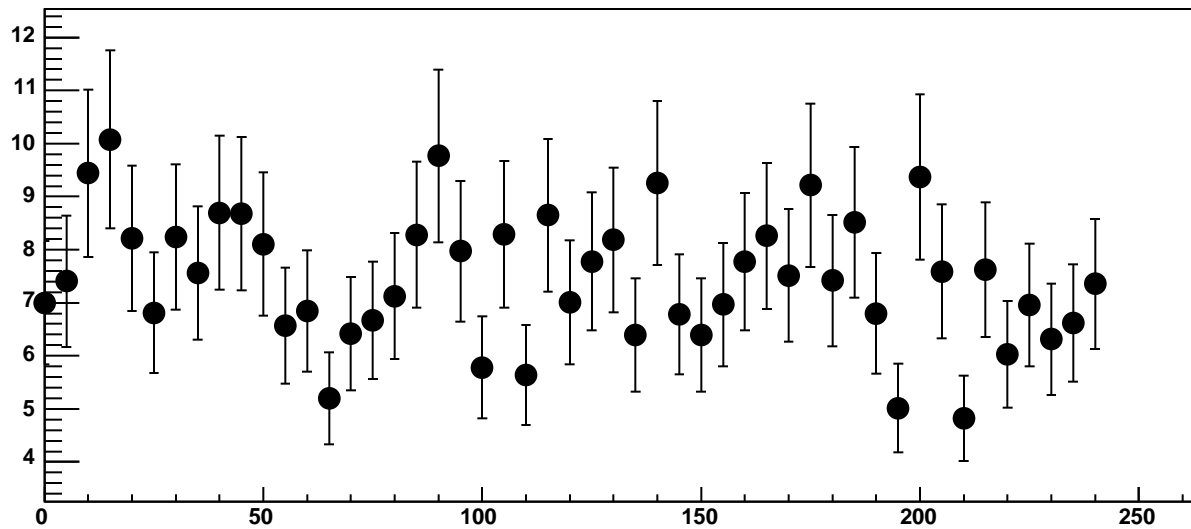


Chip 4, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold

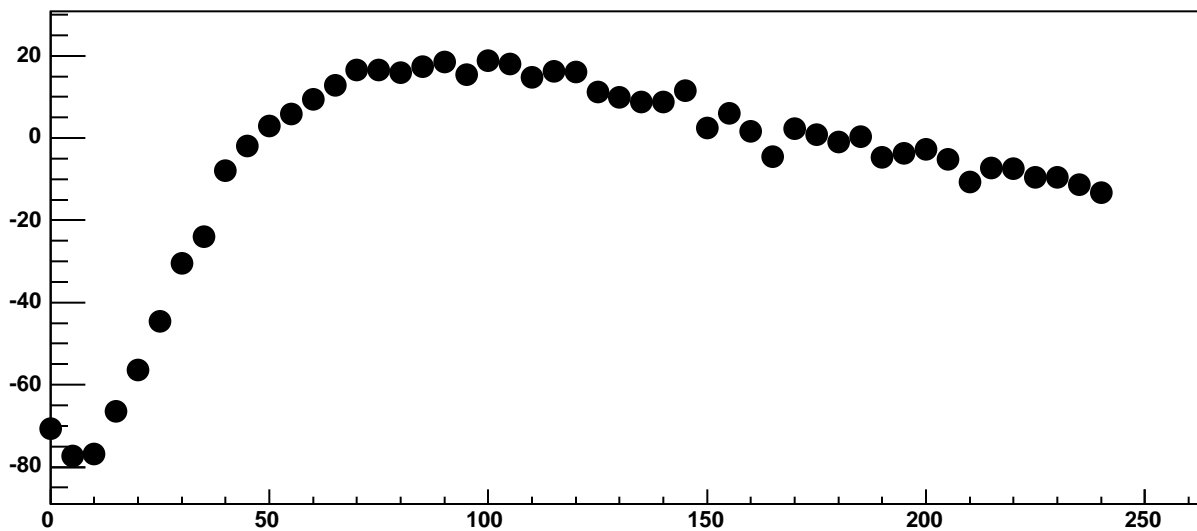


$\chi^2 / \text{ndf}$	4881 / 41
p0	$344.4 \pm 0.6662$
p1	$300.7 \pm 0.502$
p2	$123.1 \pm 2.747$
p3	$33 \pm 0.2128$
p4	$0.1012 \pm 0.003632$

Chip 4, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold

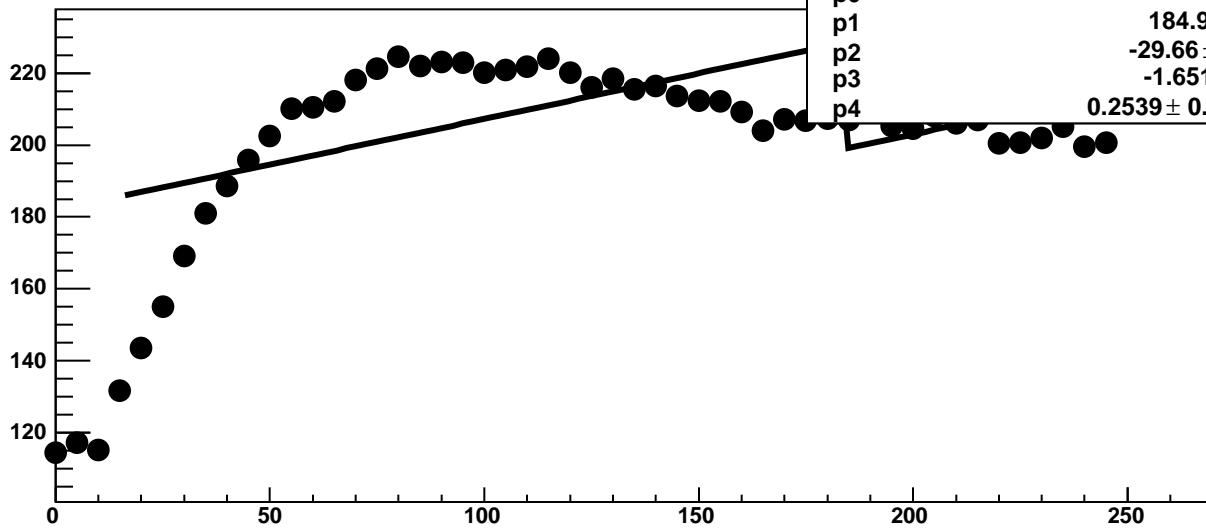


Chip 4, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold



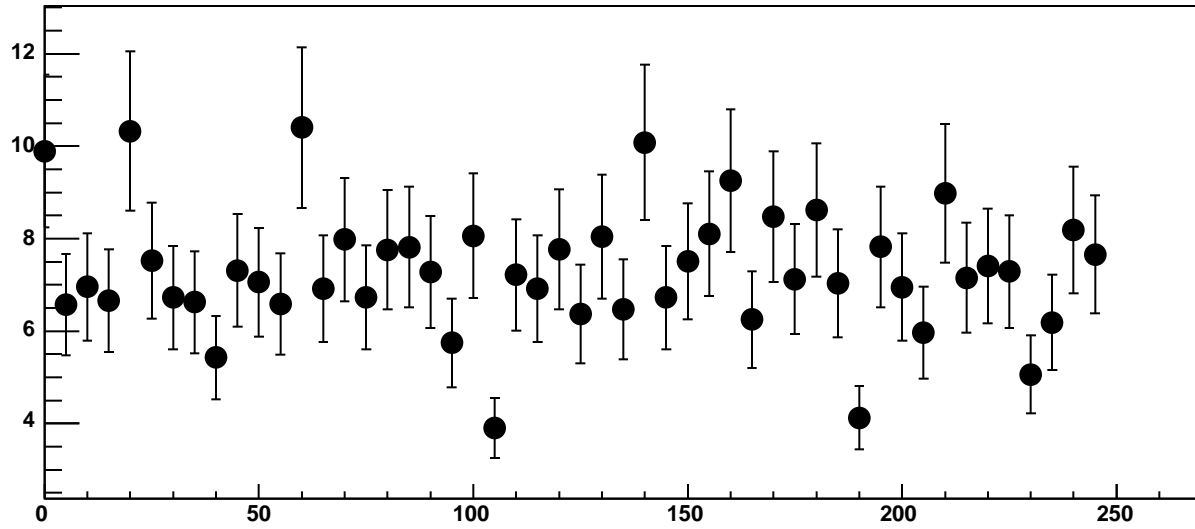


Chip 4, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold

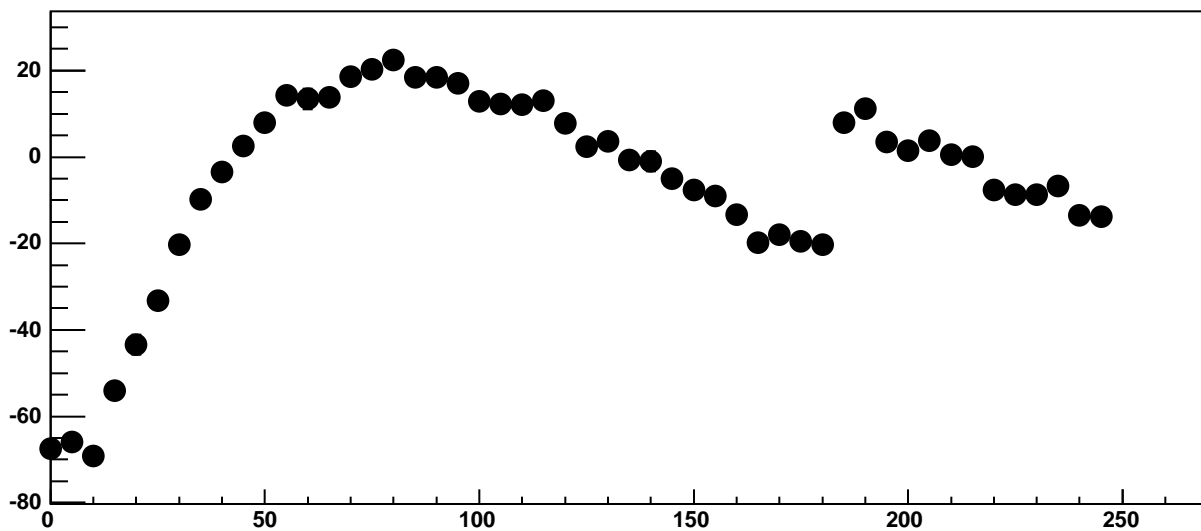


$\chi^2 / \text{ndf}$	4584 / 41
p0	$228.8 \pm 0.2046$
p1	$184.9 \pm 5.164$
p2	$-29.66 \pm 0.3458$
p3	$-1.651 \pm 8.187$
p4	$0.2539 \pm 0.0009011$

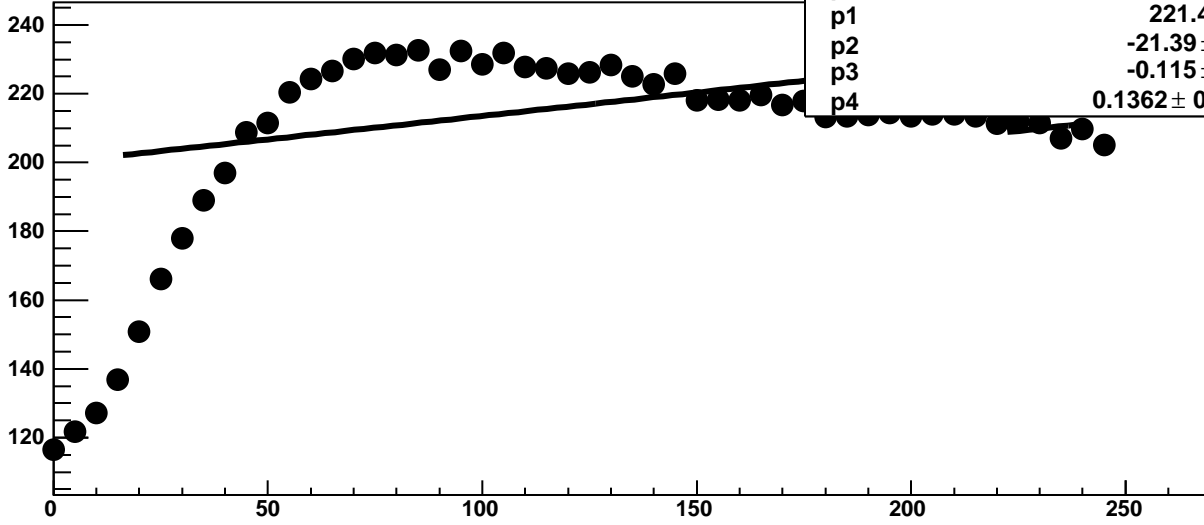
Chip 4, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold

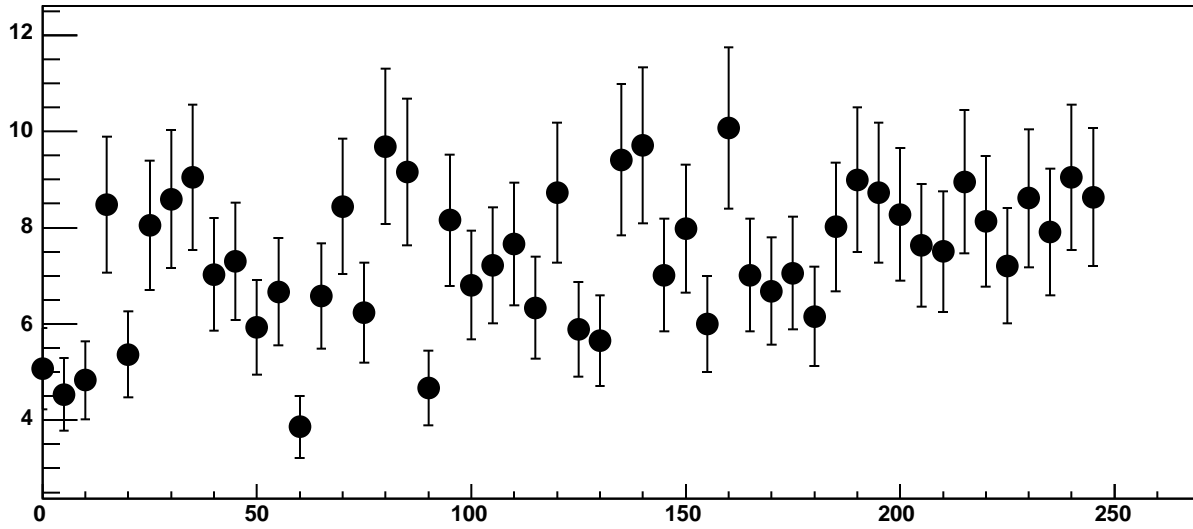


Chip 4, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

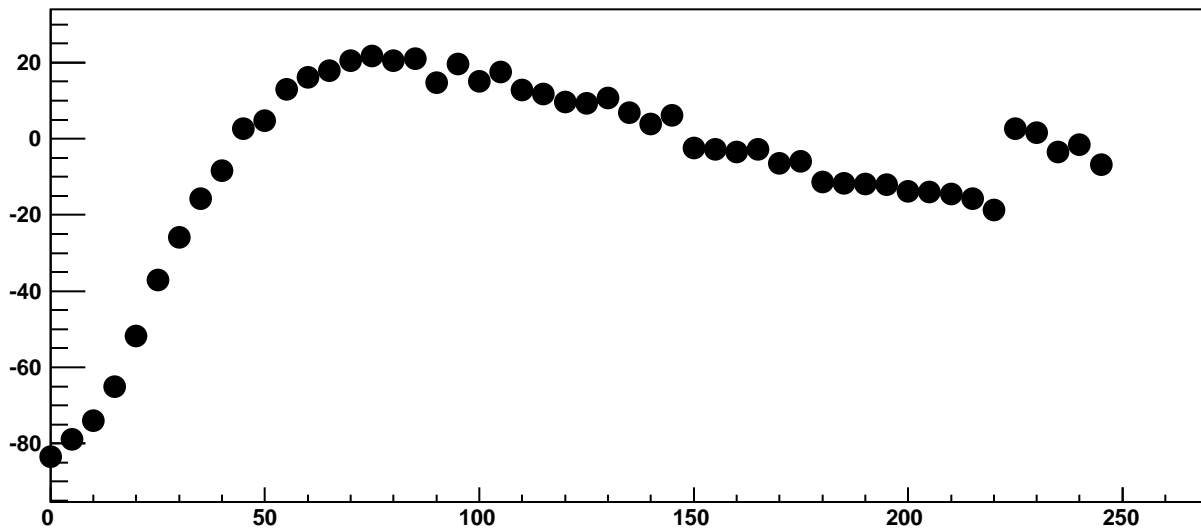


$\chi^2 / \text{ndf}$	6003 / 41
p0	$230.1 \pm 0.4728$
p1	$221.4 \pm 1.118$
p2	$-21.39 \pm 0.7739$
p3	$-0.115 \pm 0.0517$
p4	$0.1362 \pm 0.003773$

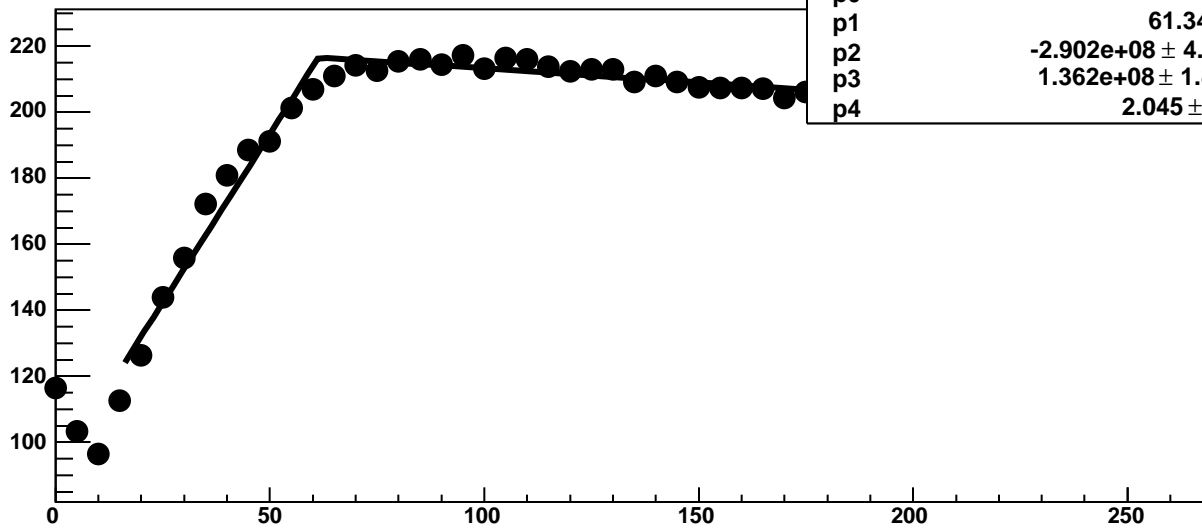
Chip 4, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

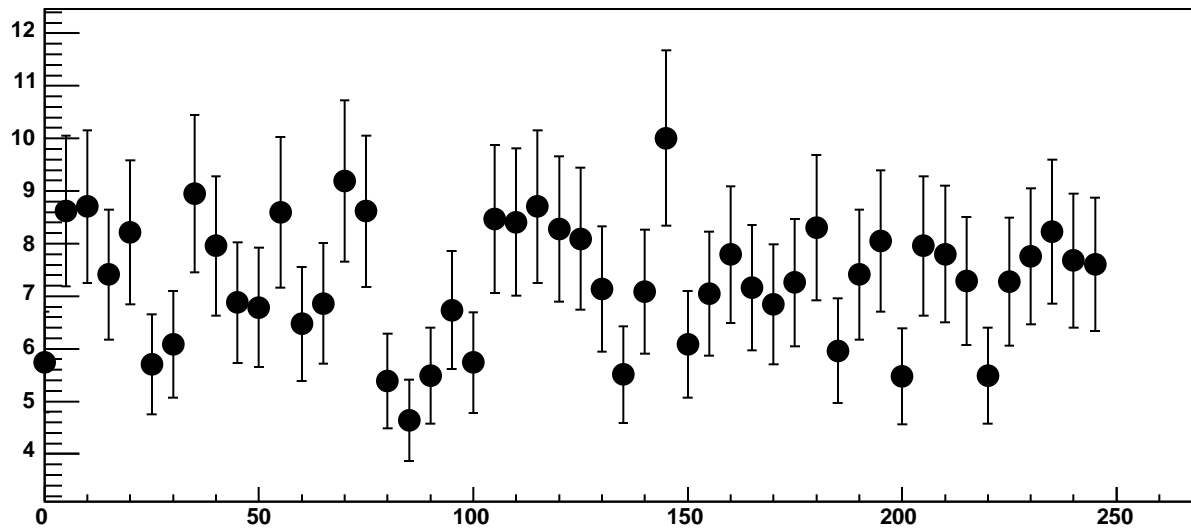


Chip 4, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

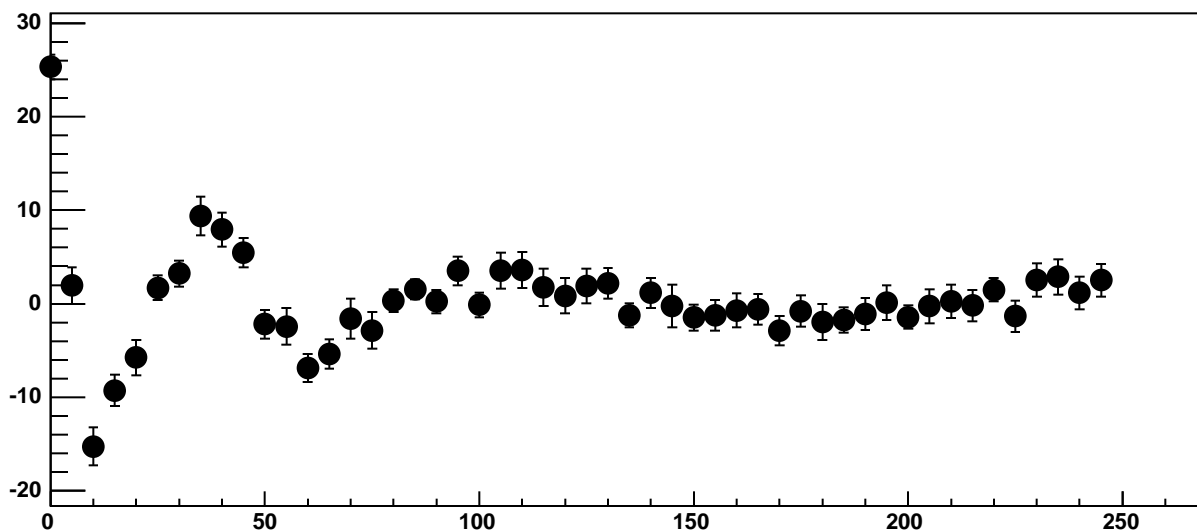


$\chi^2 / \text{ndf}$	173 / 41
p0	$216.6 \pm 0.497$
p1	$61.34 \pm 0.526$
p2	$-2.902\text{e}+08 \pm 4.912\text{e}+06$
p3	$1.362\text{e}+08 \pm 1.844\text{e}+05$
p4	$2.045 \pm 0.03567$

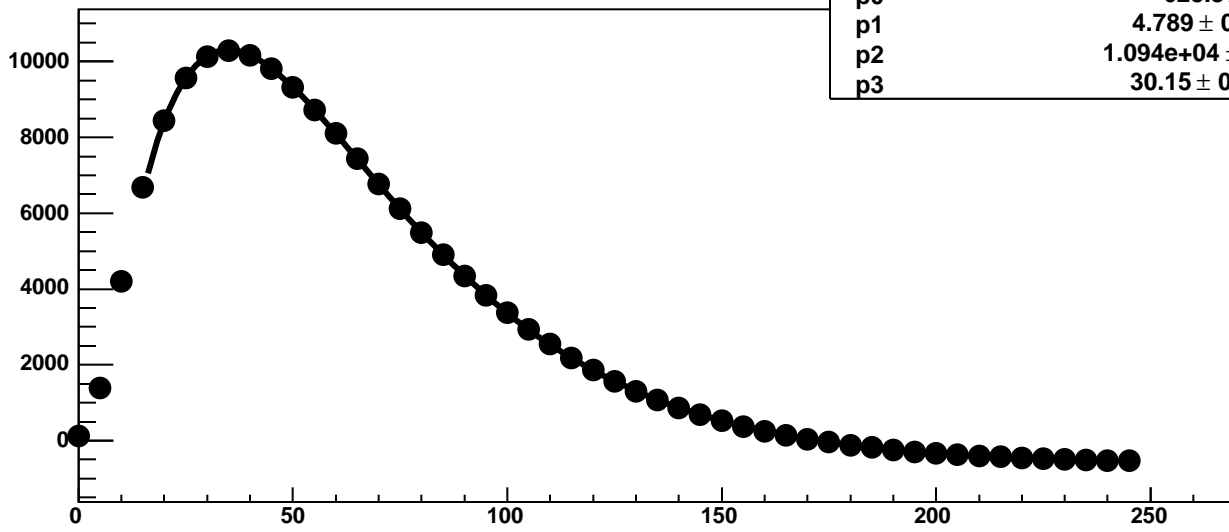
Chip 4, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

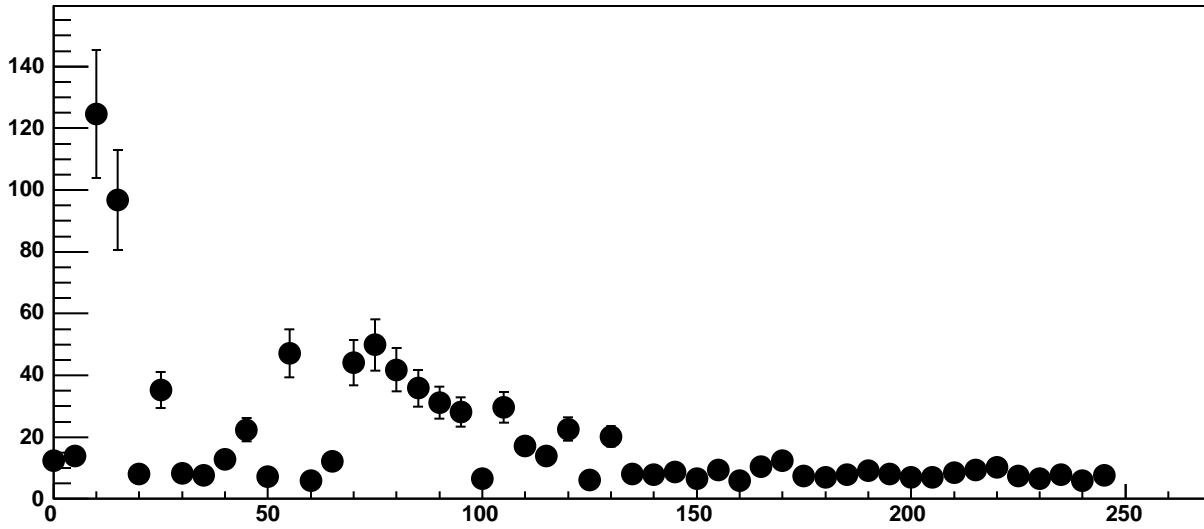


Chip 4, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold

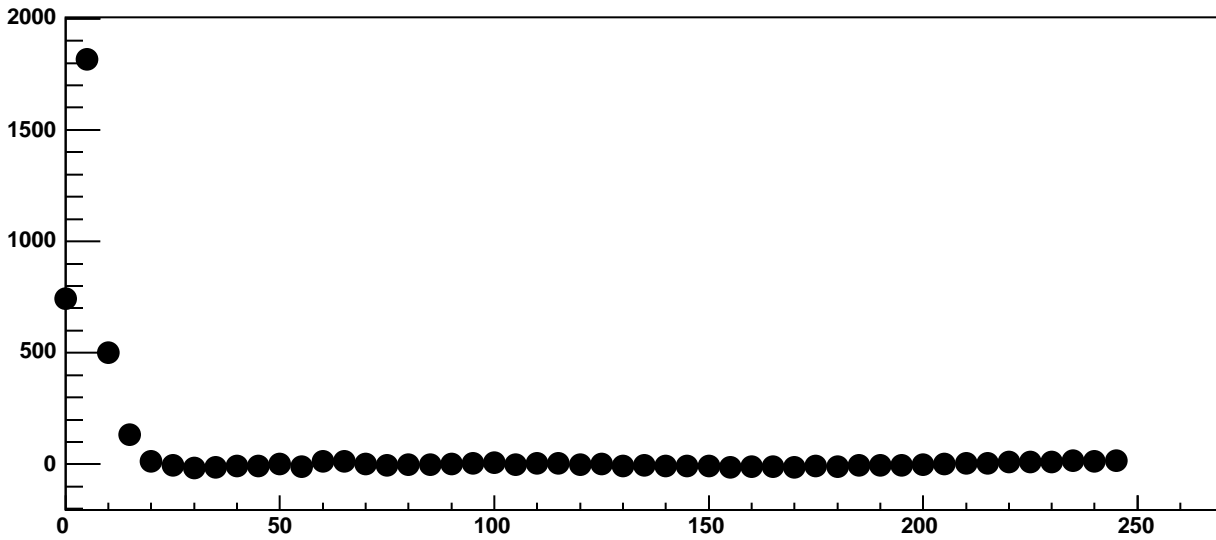


$\chi^2 / \text{ndf}$	921.1 / 42
p0	$-628.3 \pm 0.5871$
p1	$4.789 \pm 0.008112$
p2	$1.094\text{e}+04 \pm 0.9267$
p3	$30.15 \pm 0.005768$

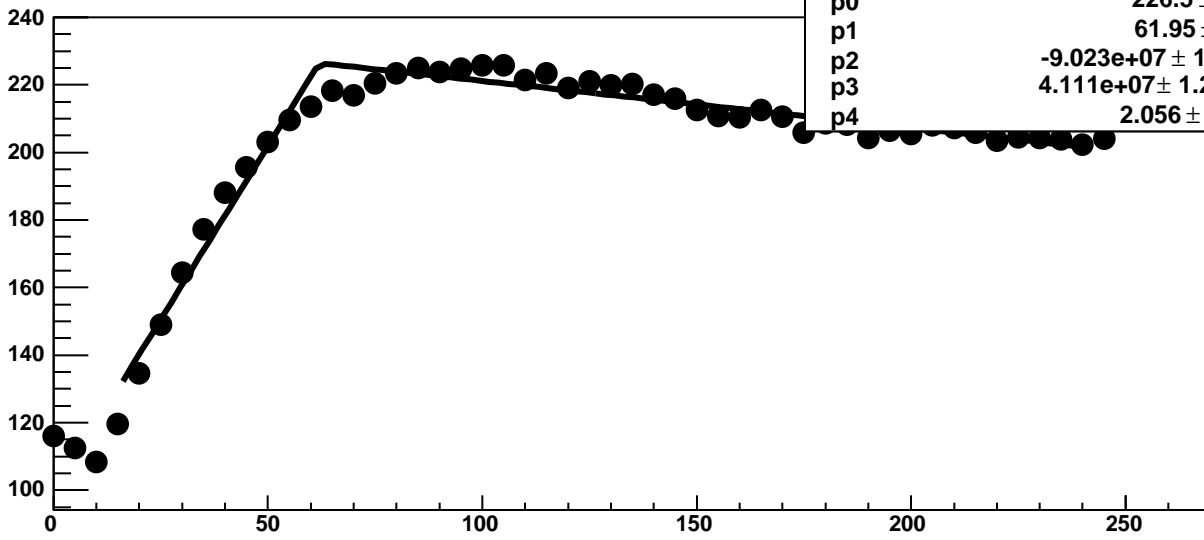
Chip 4, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold

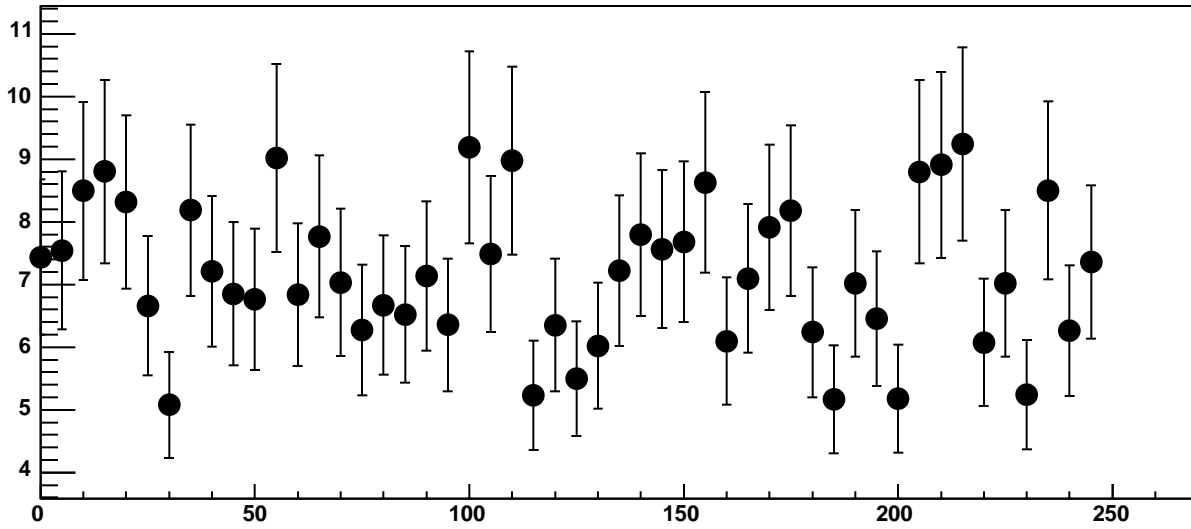


Chip 4, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

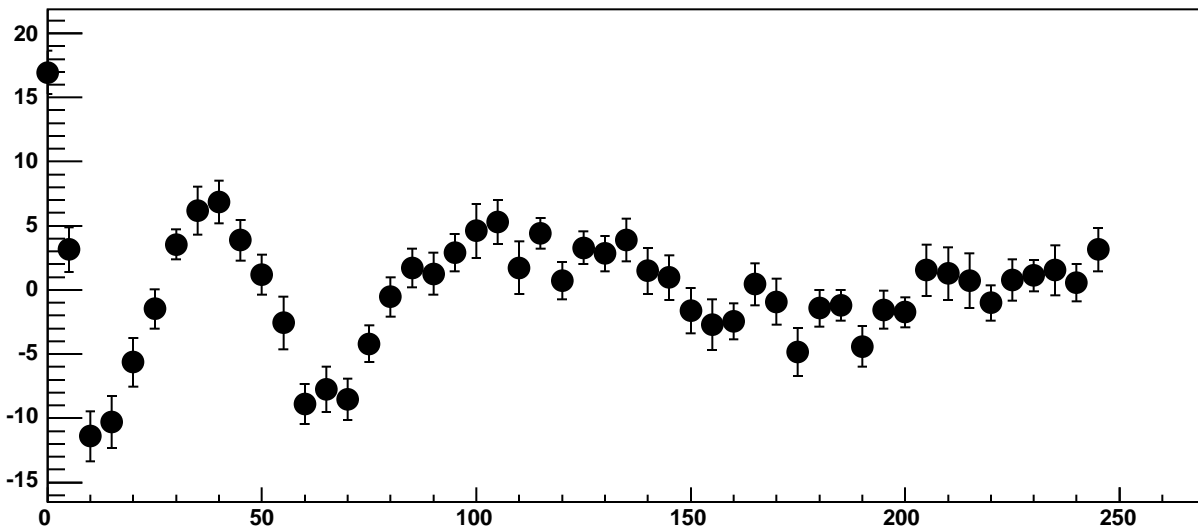


$\chi^2 / \text{ndf}$	250 / 41
p0	$226.5 \pm 0.4966$
p1	$61.95 \pm 0.5382$
p2	$-9.023\text{e}+07 \pm 1.59\text{e}+06$
p3	$4.111\text{e}+07 \pm 1.234\text{e}+05$
p4	$2.056 \pm 0.03813$

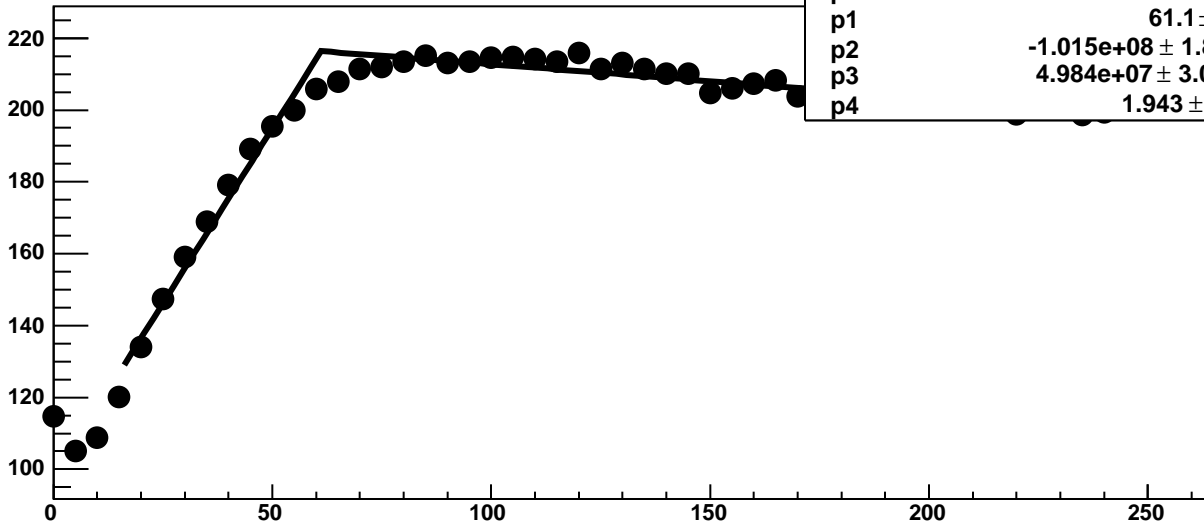
Chip 4, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

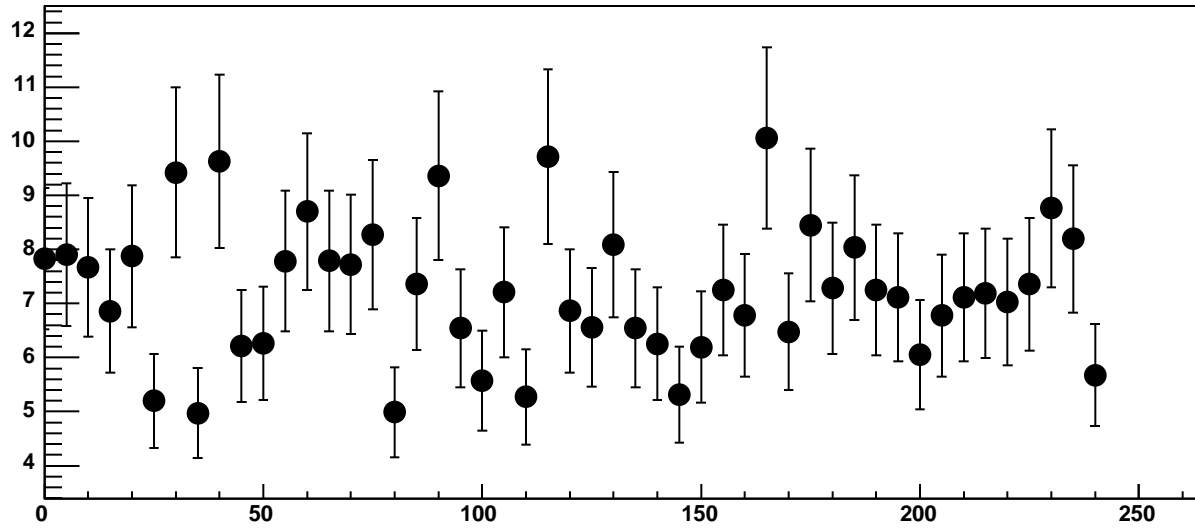


Chip 4, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

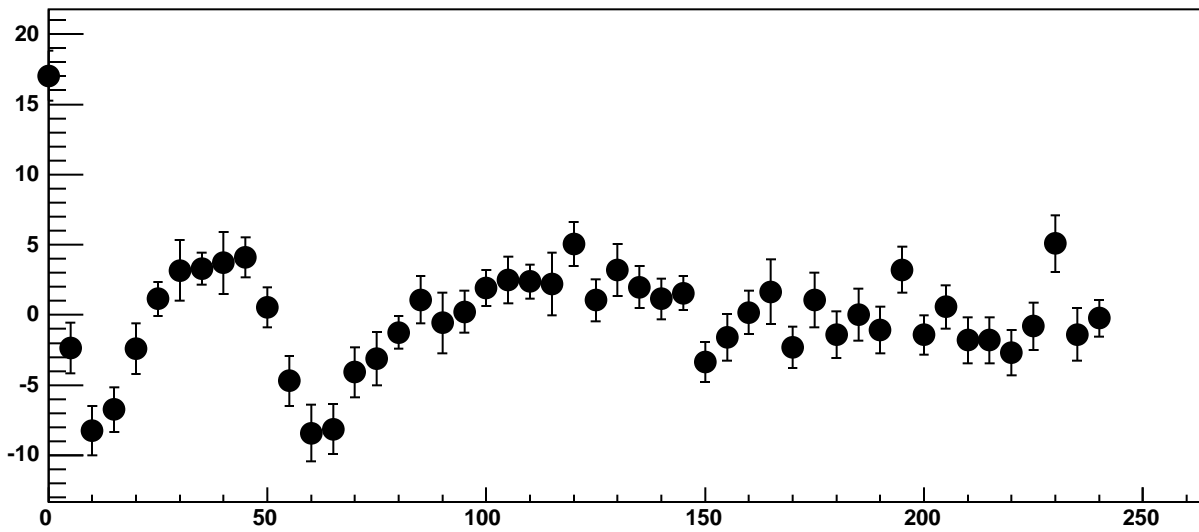


$\chi^2 / \text{ndf}$	153.1 / 41
p0	$216.4 \pm 0.5132$
p1	$61.1 \pm 0.5736$
p2	$-1.015\text{e}+08 \pm 1.884\text{e}+06$
p3	$4.984\text{e}+07 \pm 3.053\text{e}+05$
p4	$1.943 \pm 0.03681$

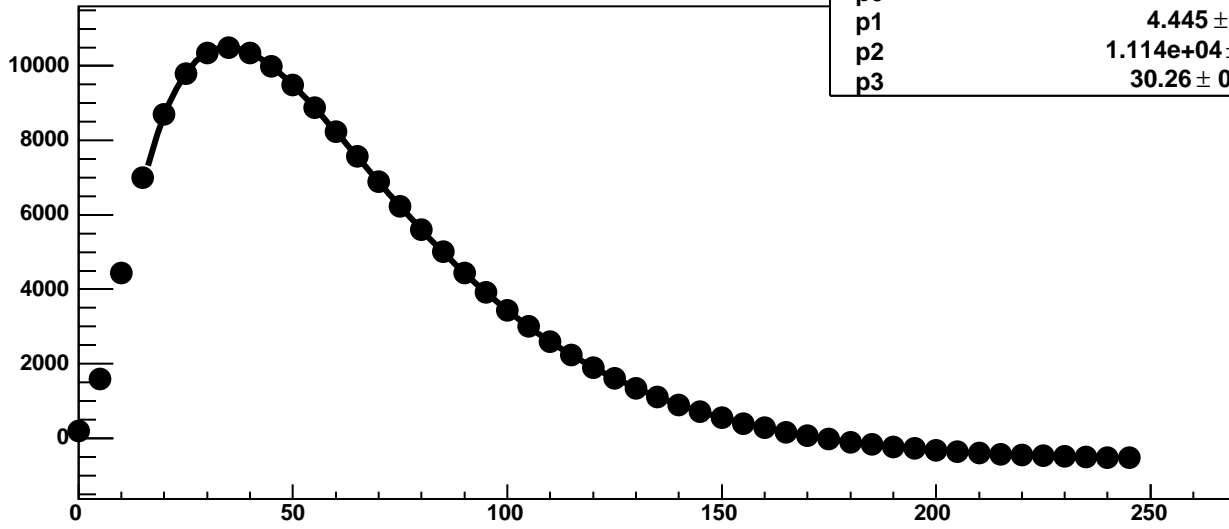
Chip 4, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold

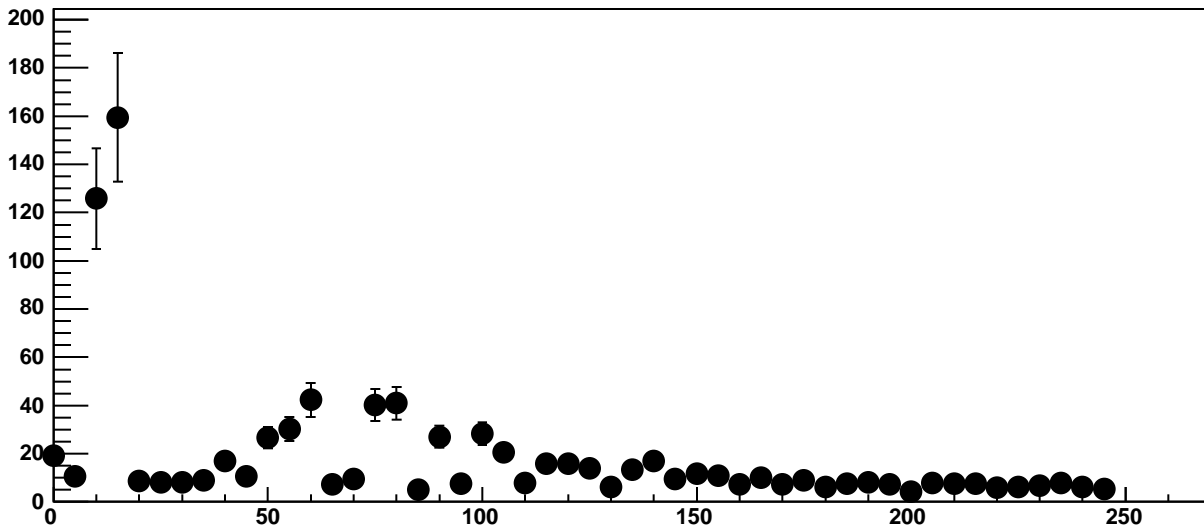


Chip 4, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold

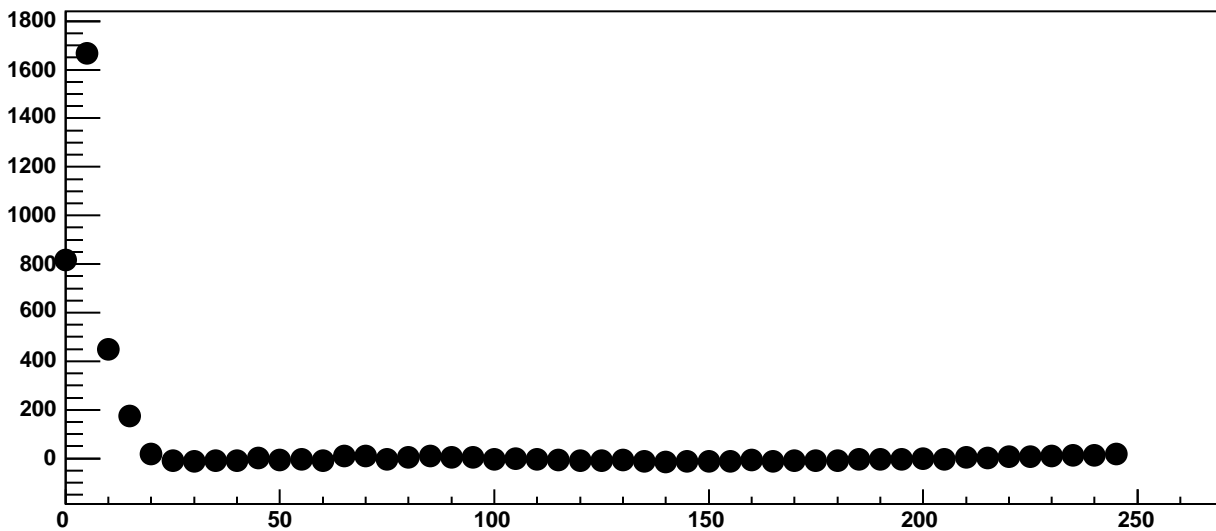


$\chi^2 / \text{ndf}$	844 / 42
p0	$-629.7 \pm 0.5132$
p1	$4.445 \pm 0.00881$
p2	$1.114\text{e}+04 \pm 0.9698$
p3	$30.26 \pm 0.005594$

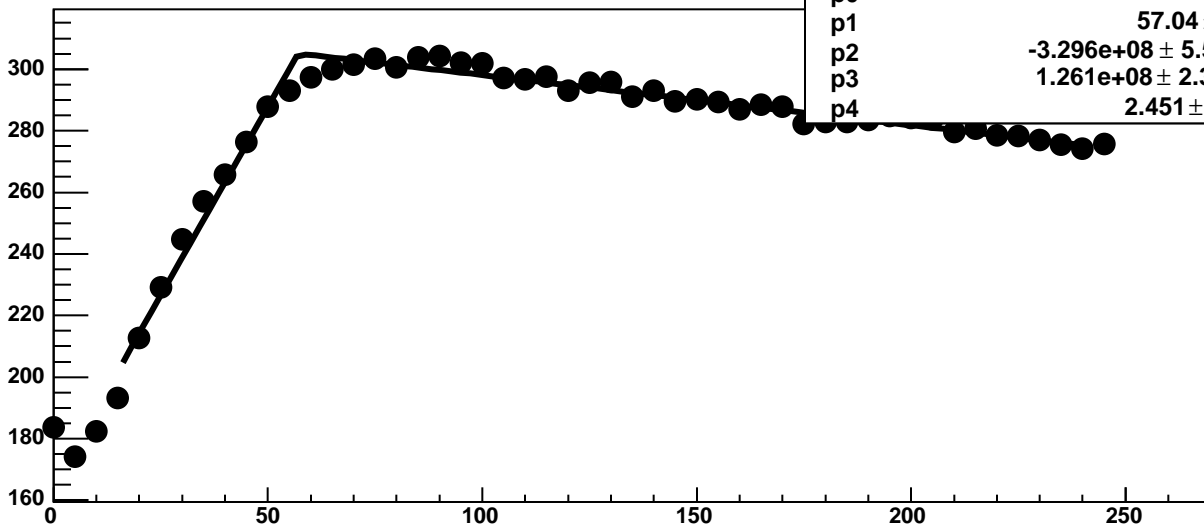
Chip 4, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold

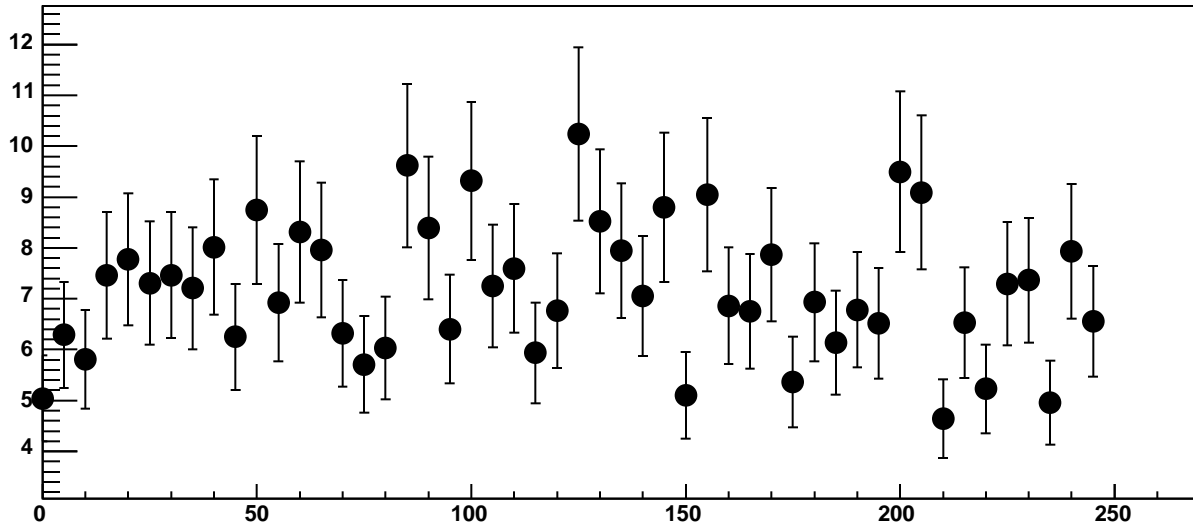


Chip 4, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

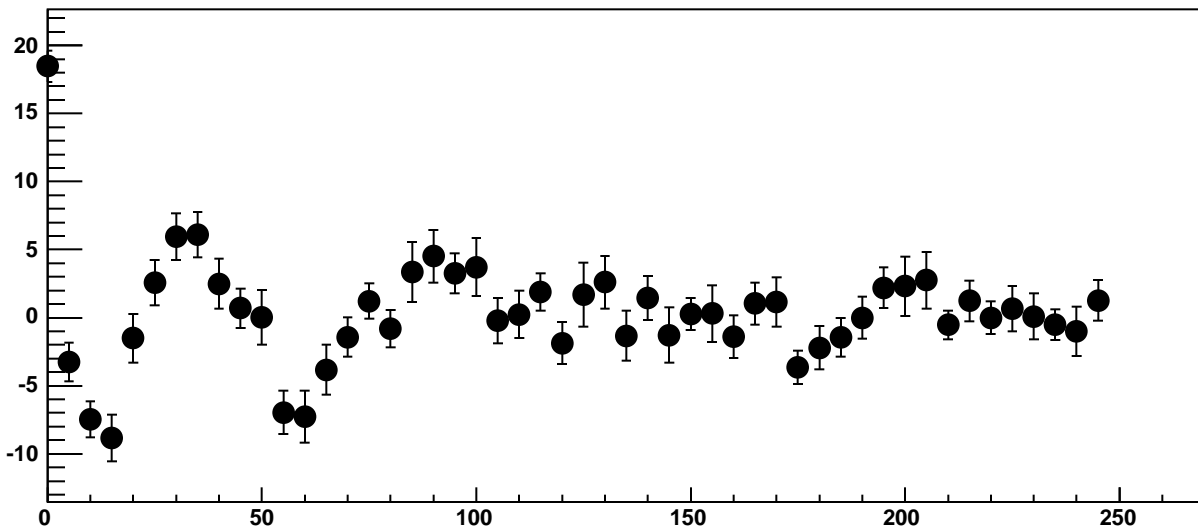


$\chi^2 / \text{ndf}$	141 / 41
p0	$305.1 \pm 0.497$
p1	$57.04 \pm 0.4521$
p2	$-3.296e+08 \pm 5.552e+06$
p3	$1.261e+08 \pm 2.376e+05$
p4	$2.451 \pm 0.04291$

Chip 4, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold

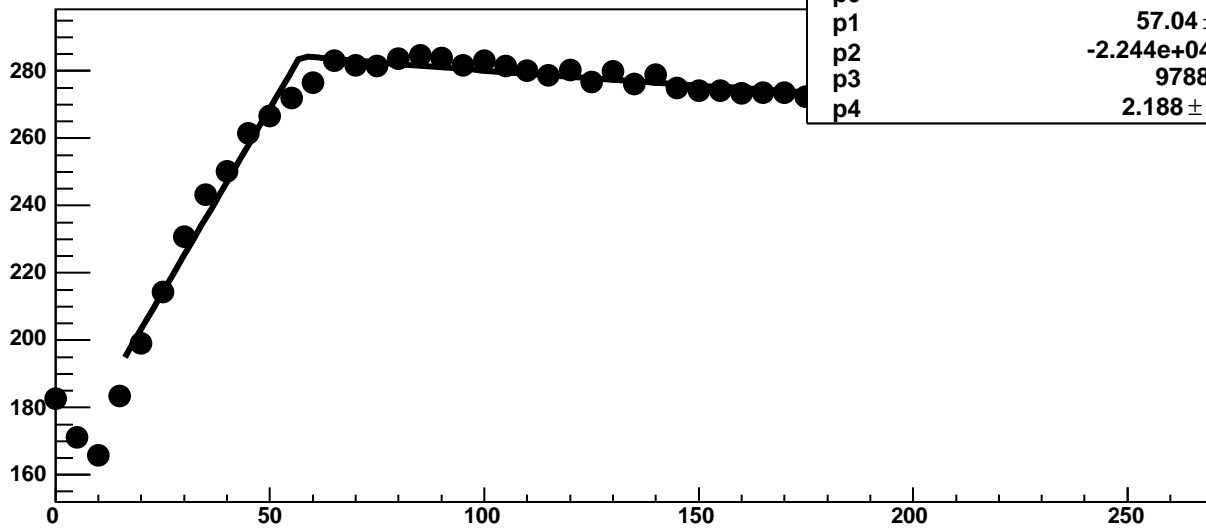


Chip 4, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold



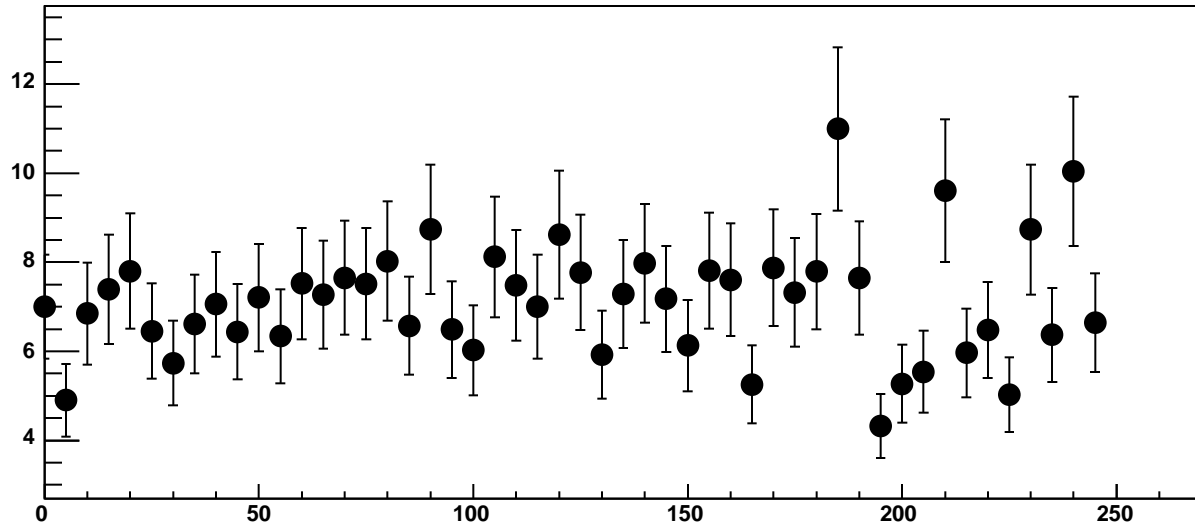


Chip 4, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

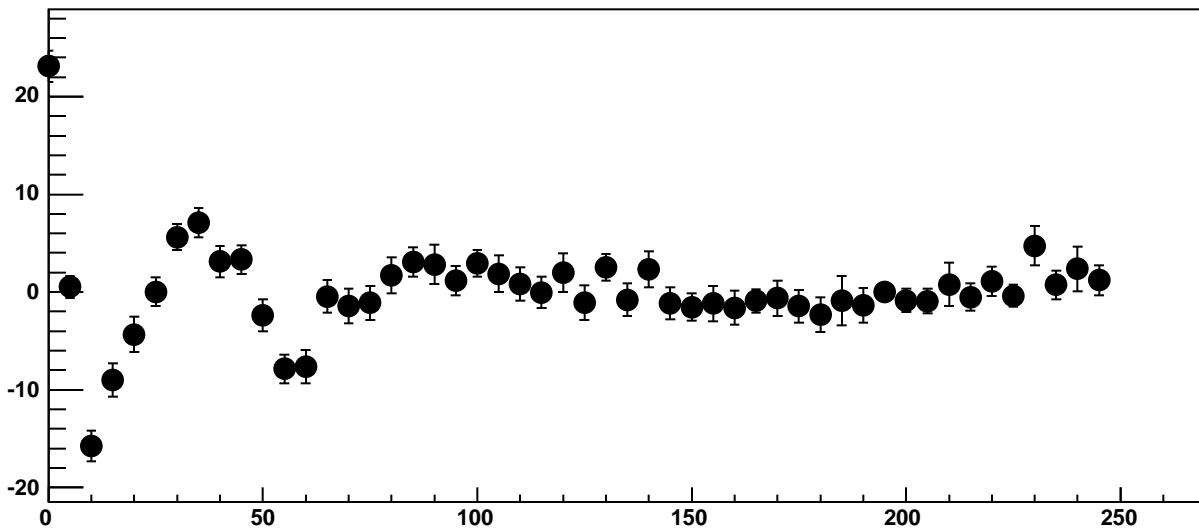


$\chi^2 / \text{ndf}$	170.8 / 41
p0	$284.4 \pm 0.54$
p1	$57.04 \pm 0.5017$
p2	$-2.244\text{e}+04 \pm 3601$
p3	$9788 \pm 1582$
p4	$2.188 \pm 0.04102$

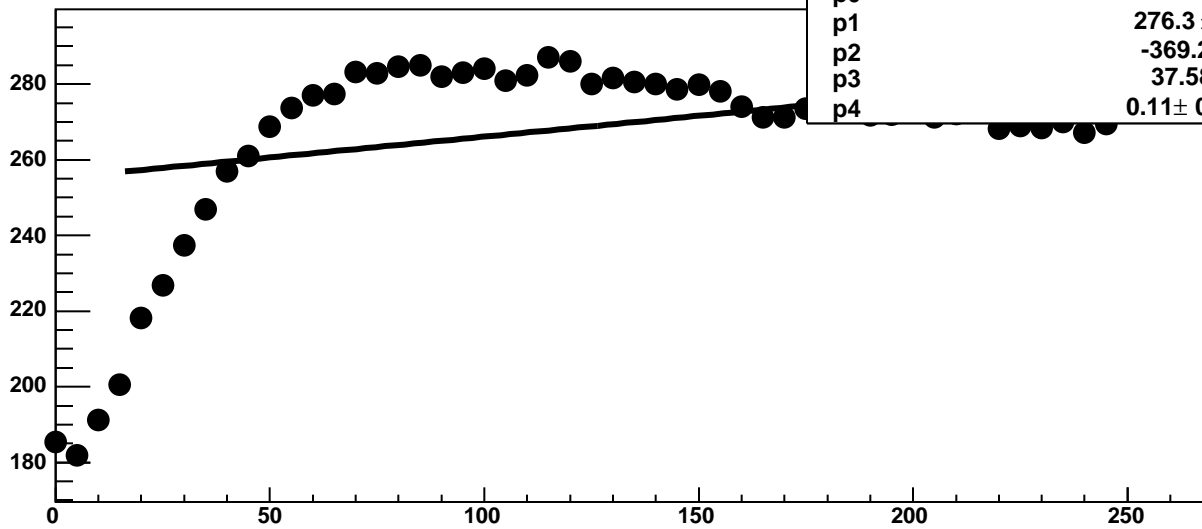
Chip 4, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

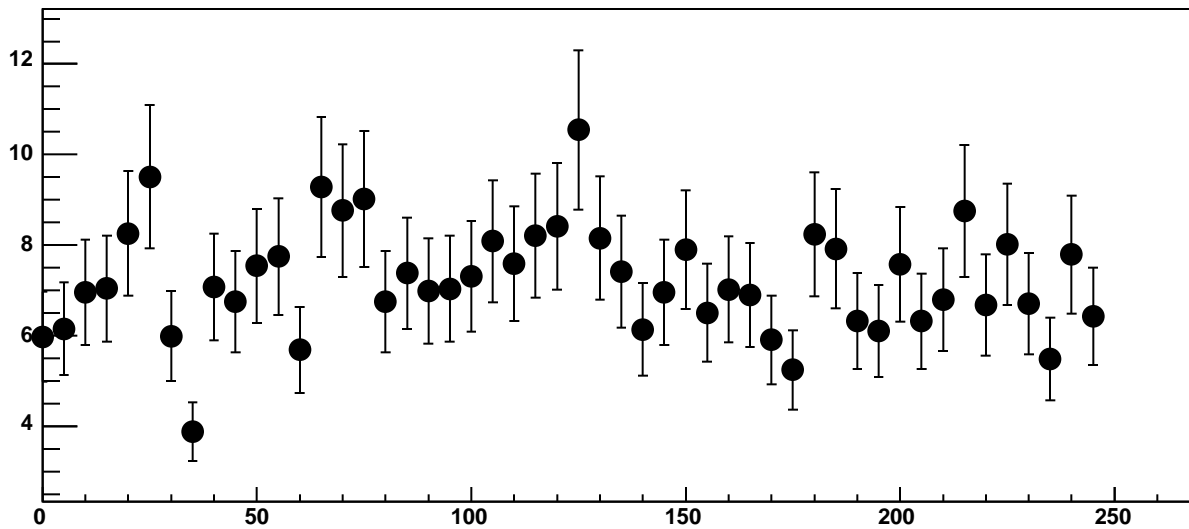


Chip 4, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold

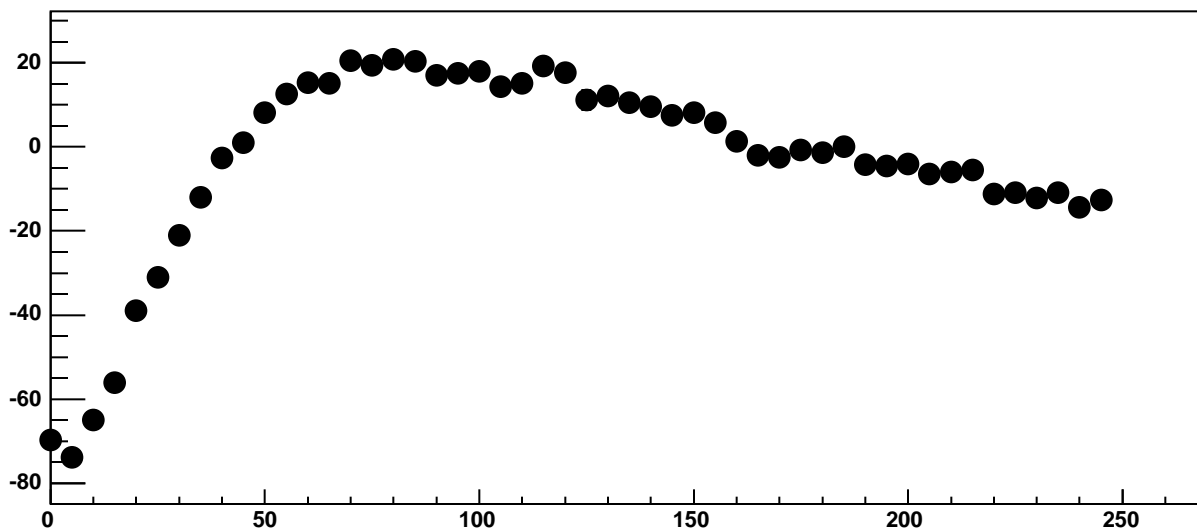


$\chi^2 / \text{ndf}$	4273 / 41
p0	$285.5 \pm 0.5232$
p1	$276.3 \pm 0.5456$
p2	$-369.2 \pm 9.646$
p3	$37.58 \pm 0.451$
p4	$0.11 \pm 0.003425$

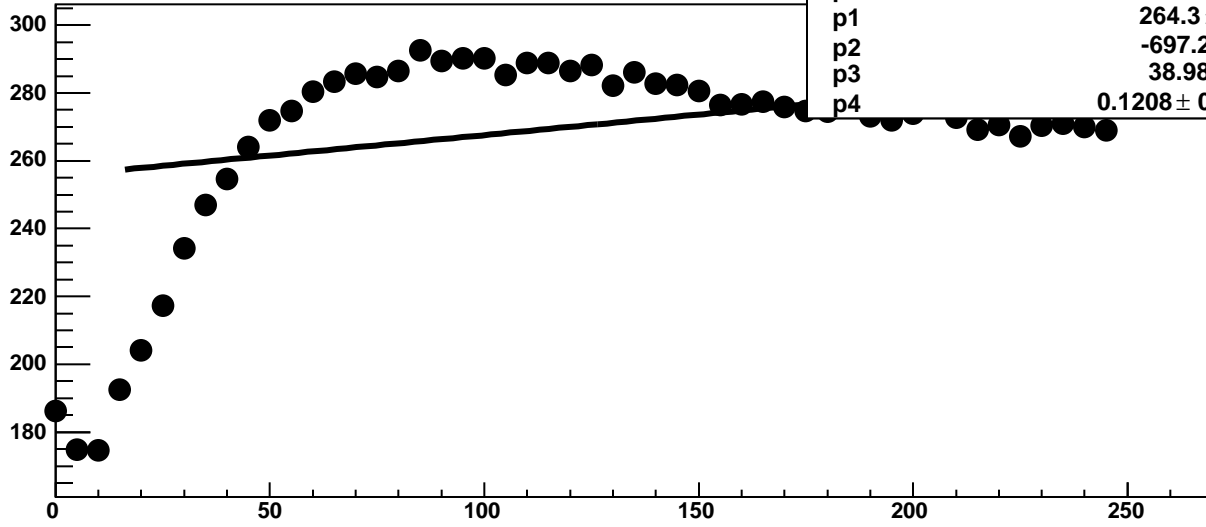
Chip 4, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold



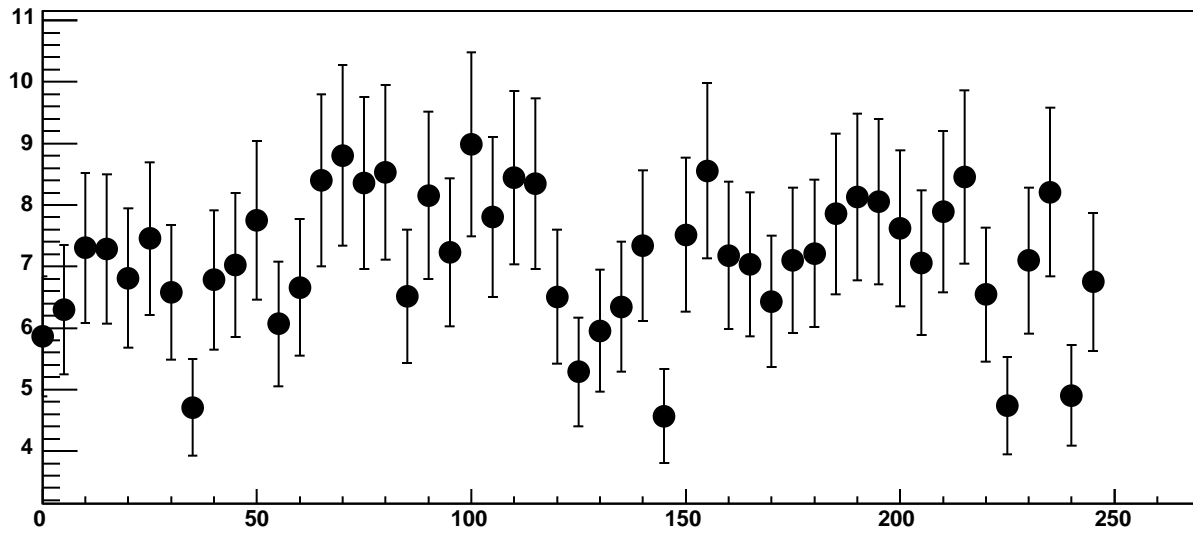
Chip 4, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold



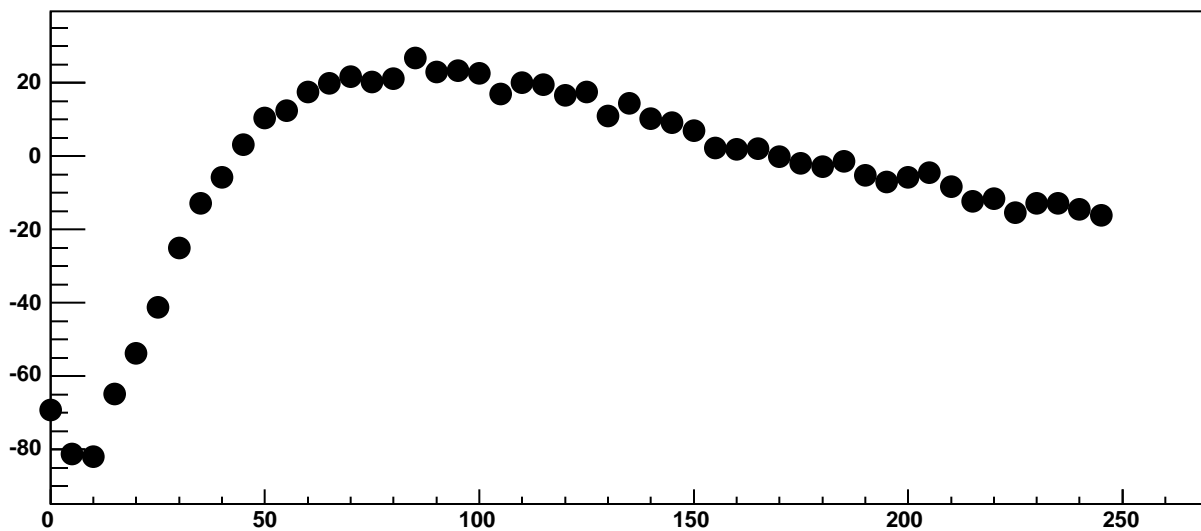
Chip 4, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold



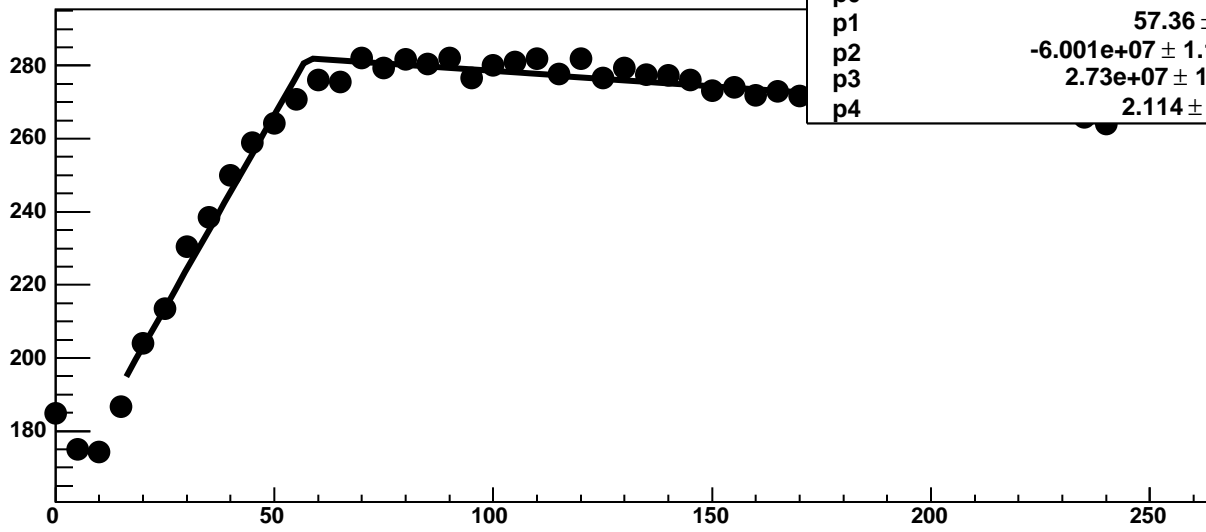
Chip 4, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

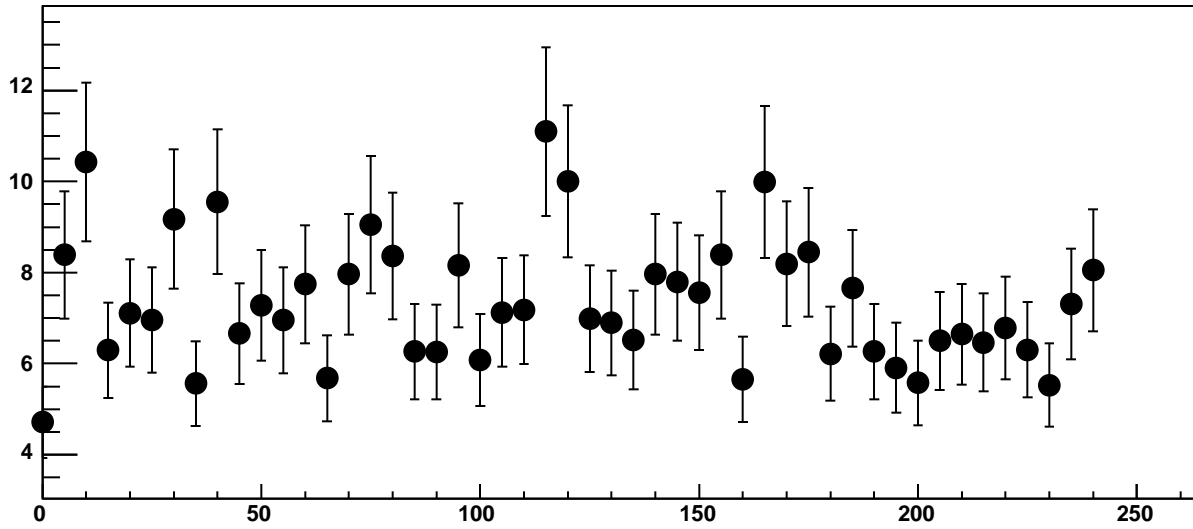


Chip 4, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

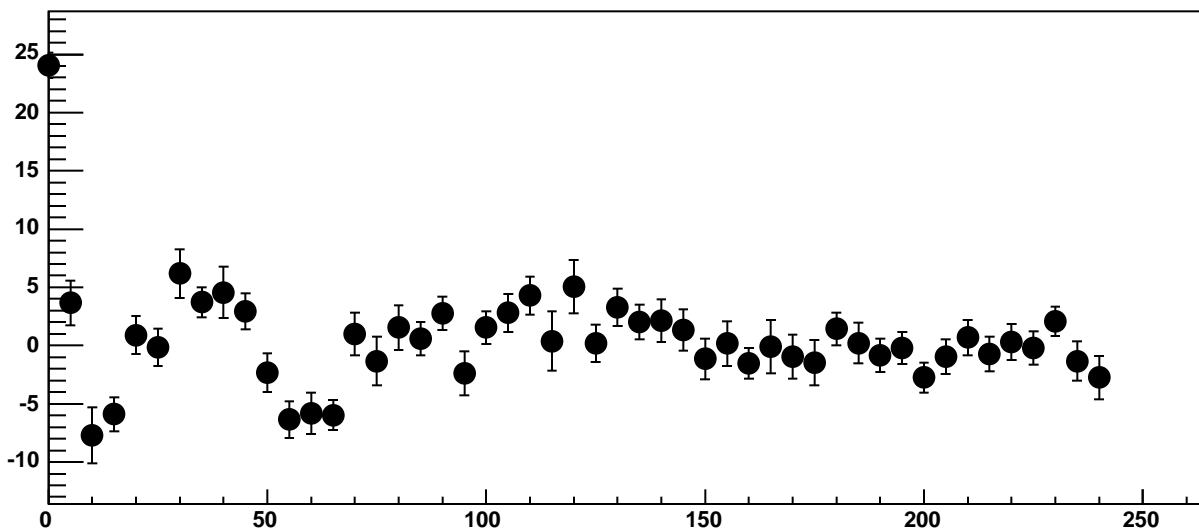


$\chi^2 / \text{ndf}$	137.5 / 41
p0	282.1 ± 0.5211
p1	57.36 ± 0.5466
p2	-6.001e+07 ± 1.137e+06
p3	2.73e+07 ± 1.39e+05
p4	2.114 ± 0.04063

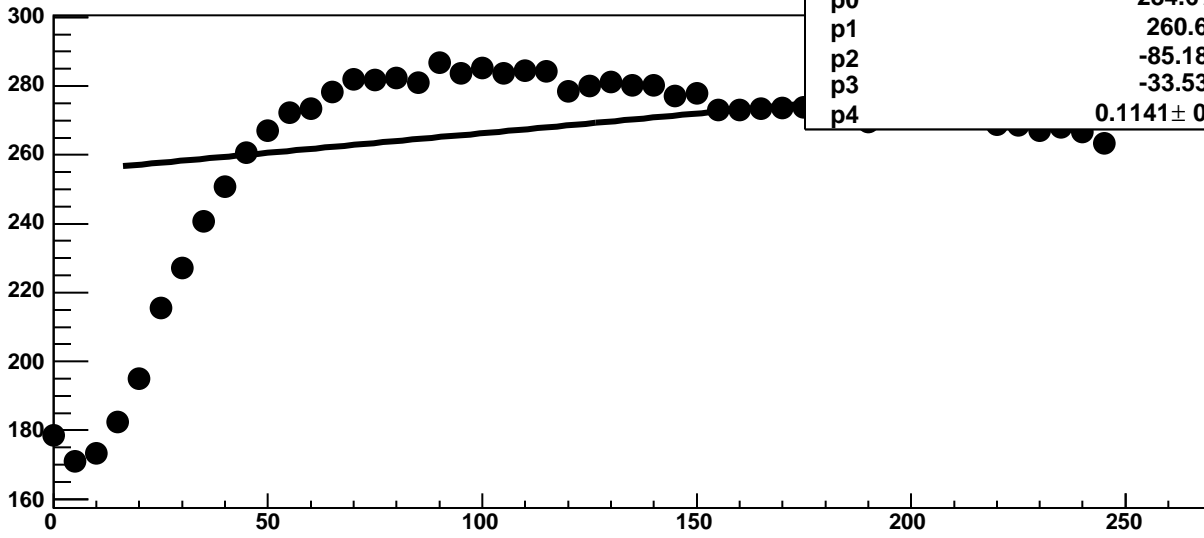
Chip 4, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

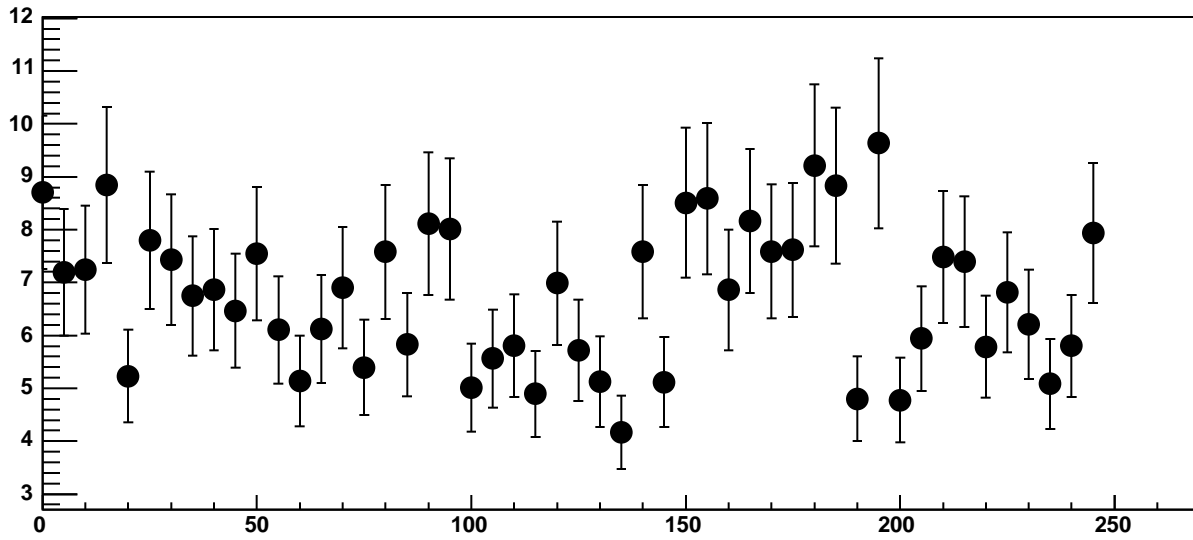


Chip 4, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold

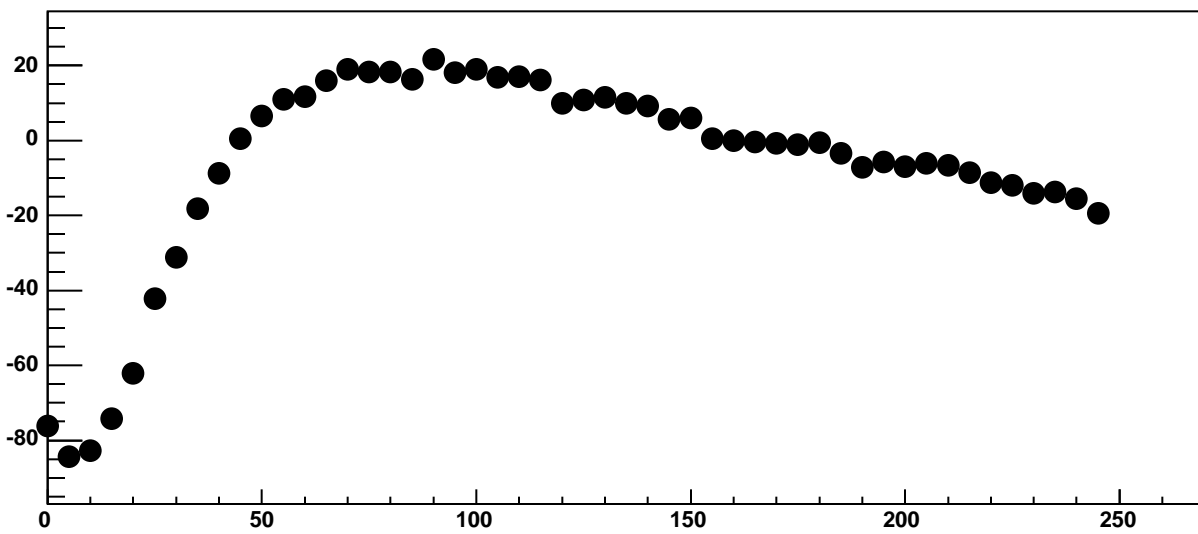


$\chi^2 / \text{ndf}$	8073 / 41
p0	$284.6 \pm 0.2121$
p1	$260.6 \pm 1.859$
p2	$-85.18 \pm 84.85$
p3	$-33.53 \pm 1.414$
p4	$0.1141 \pm 0.001444$

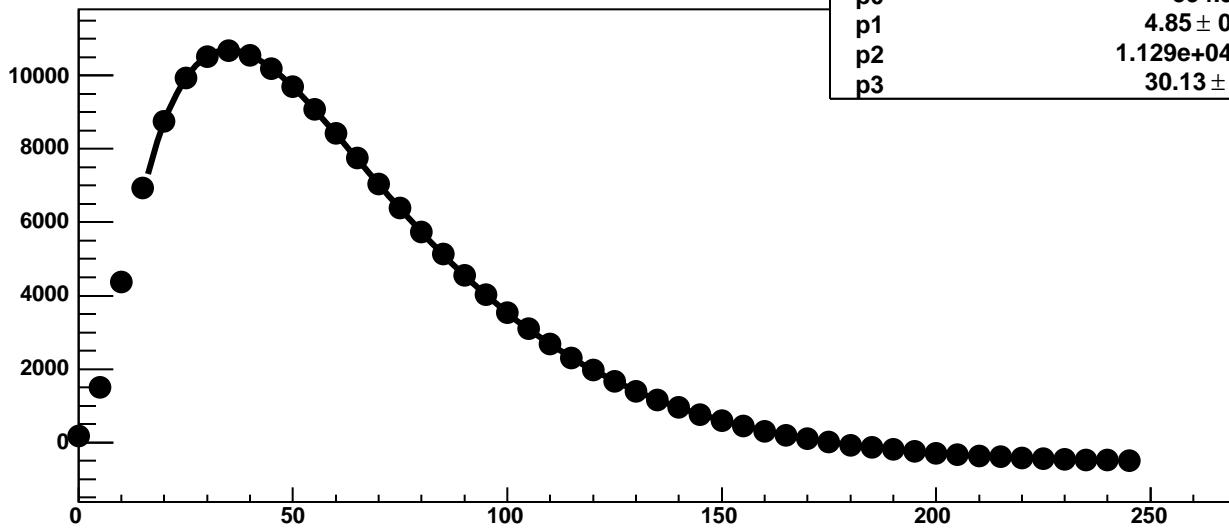
Chip 4, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

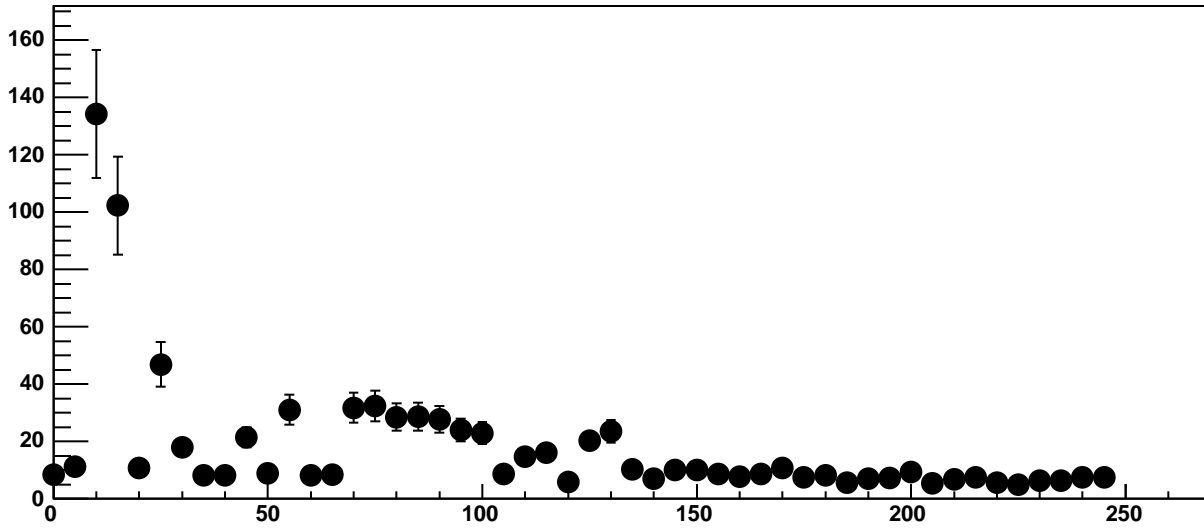


Chip 4, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold



$\chi^2 / \text{ndf}$	815.1 / 42
p0	$-594.9 \pm 0.528$
p1	$4.85 \pm 0.009209$
p2	$1.129\text{e}+04 \pm 1.017$
p3	$30.13 \pm 0.00575$

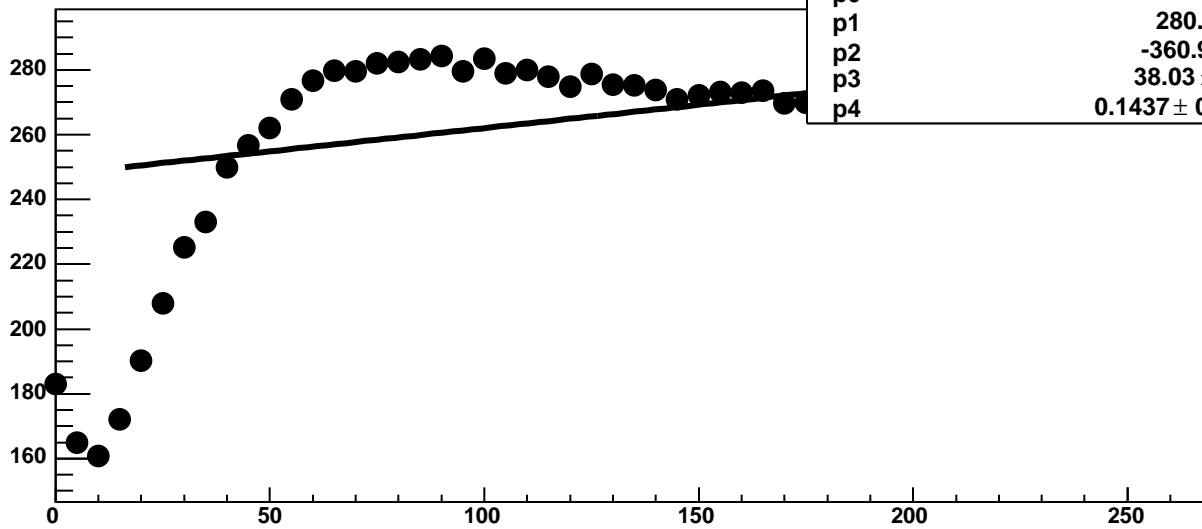
Chip 4, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold

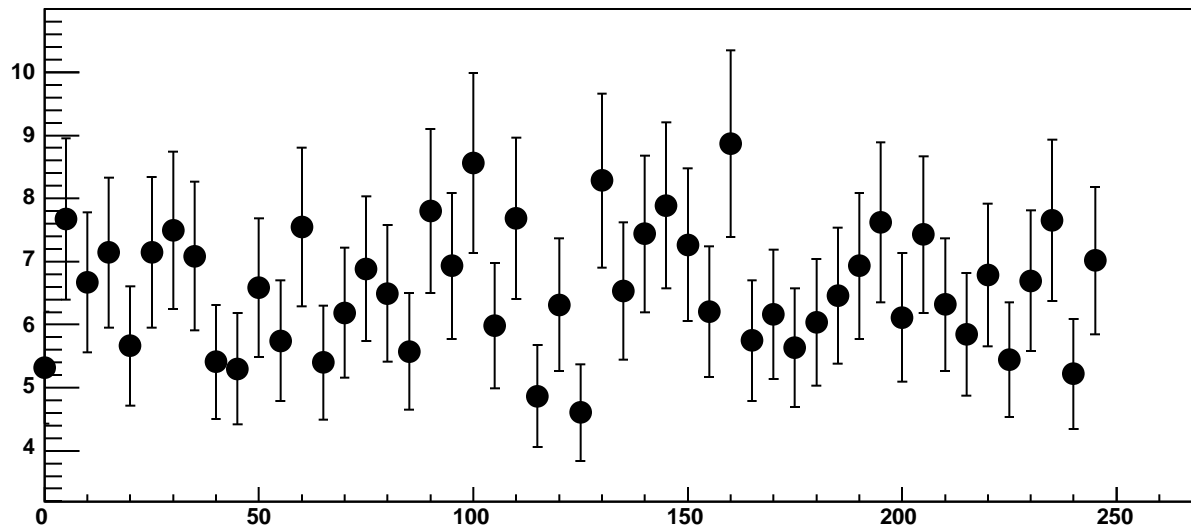


Chip 4, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

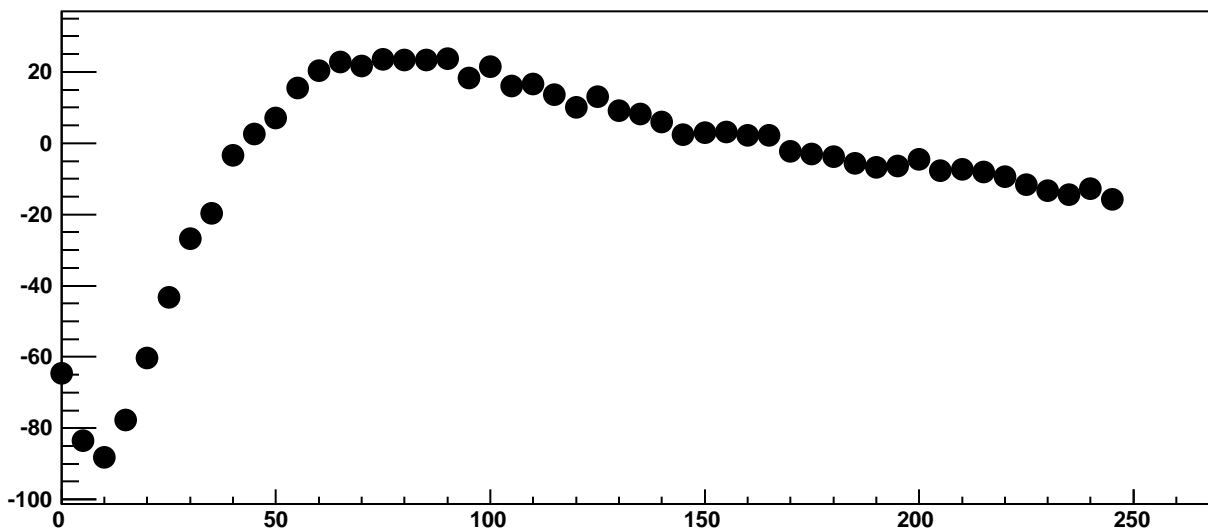


$\chi^2 / \text{ndf}$	8794 / 41
p0	$288 \pm 0.4673$
p1	$280.5 \pm 0.68$
p2	$-360.9 \pm 7.753$
p3	$38.03 \pm 0.3765$
p4	$0.1437 \pm 0.003217$

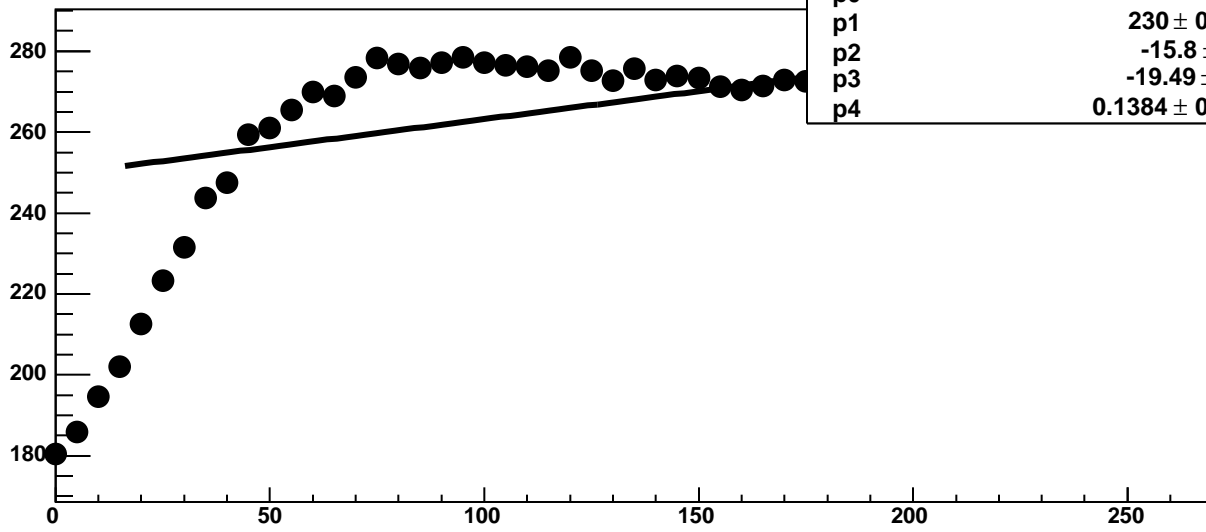
Chip 4, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

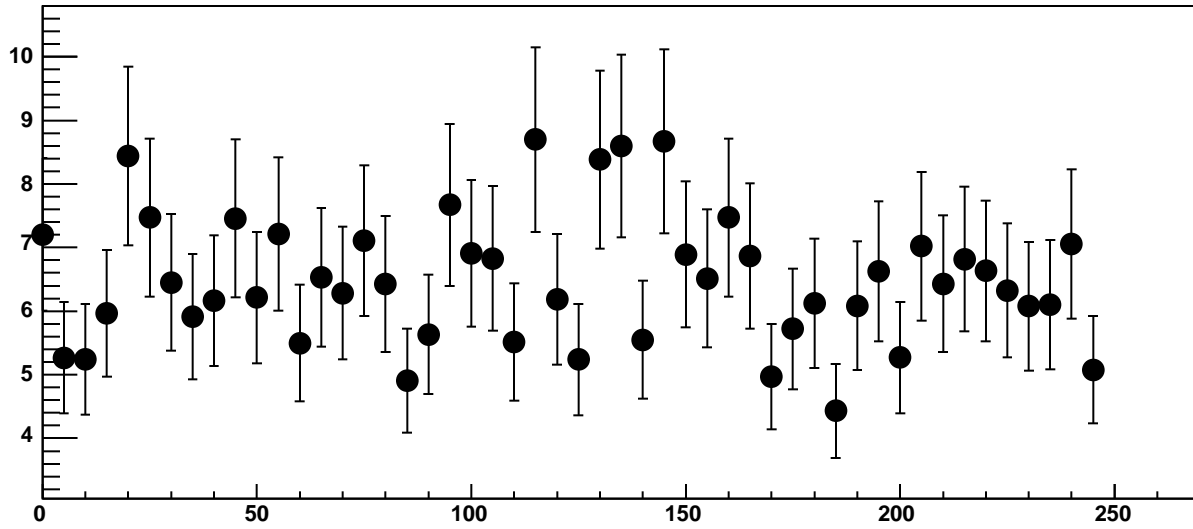


Chip 4, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

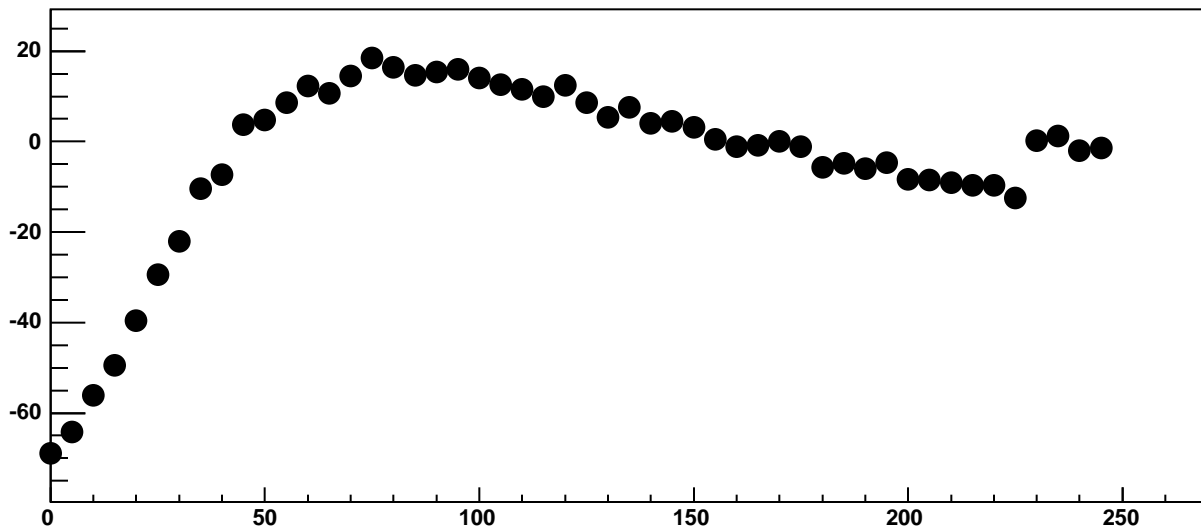


$\chi^2 / \text{ndf}$	4020 / 41
p0	$281.2 \pm 0.4087$
p1	$230 \pm 0.000287$
p2	$-15.8 \pm 0.9639$
p3	$-19.49 \pm 0.2736$
p4	$0.1384 \pm 0.003295$

Chip 4, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold

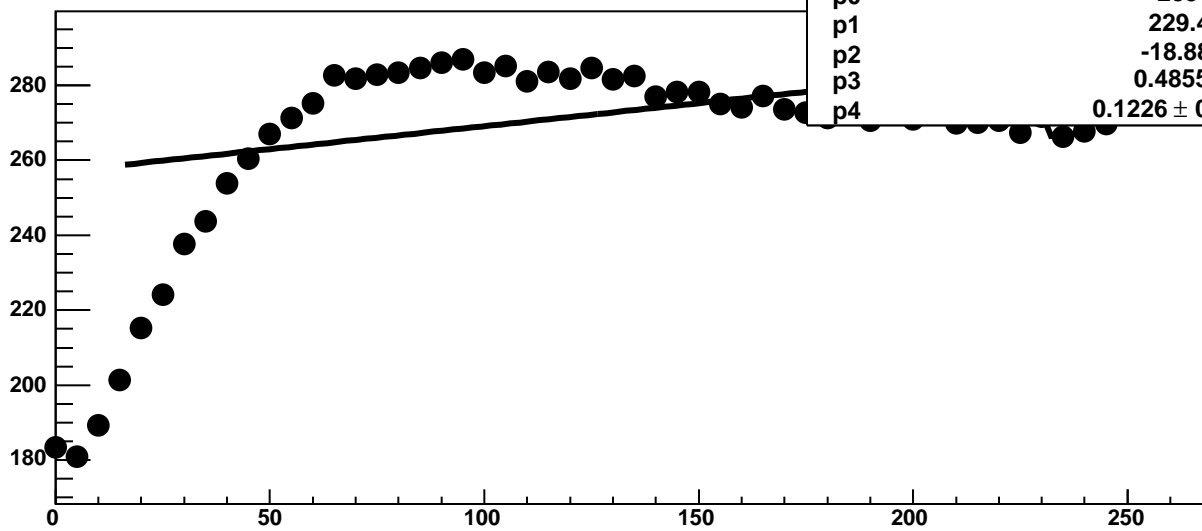


Chip 4, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold



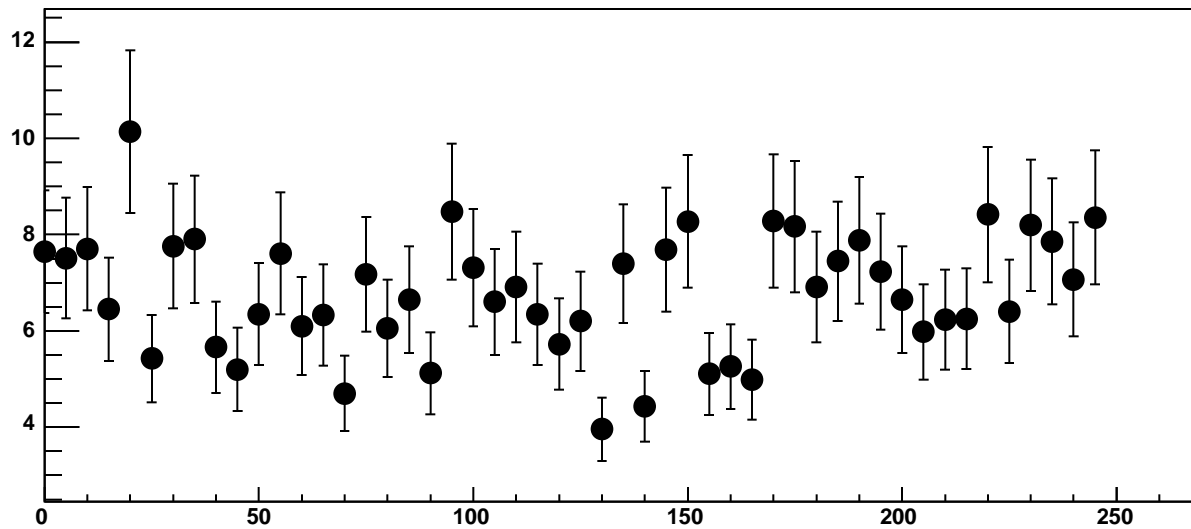


Chip 4, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold

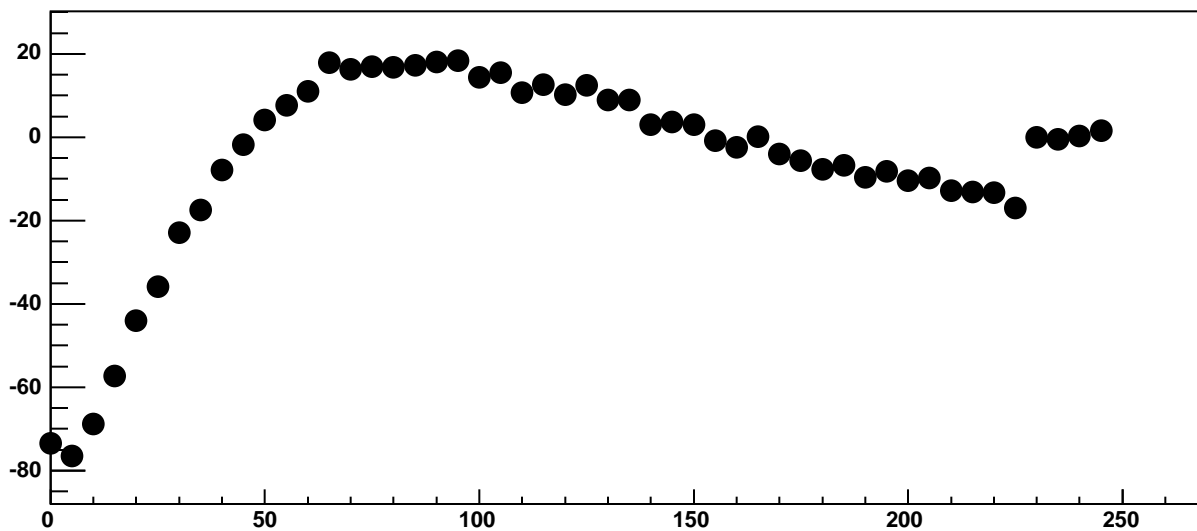


$\chi^2 / \text{ndf}$	5291 / 41
p0	$285 \pm 0.5333$
p1	$229.4 \pm 2.502$
p2	$-18.88 \pm 1.286$
p3	$0.4855 \pm 1.997$
p4	$0.1226 \pm 0.003576$

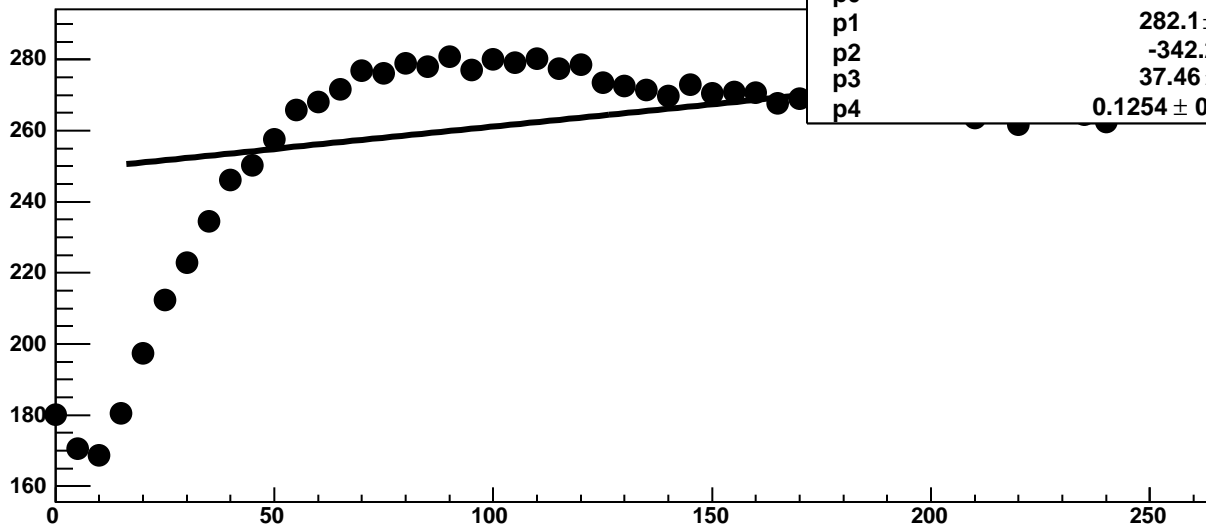
Chip 4, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold

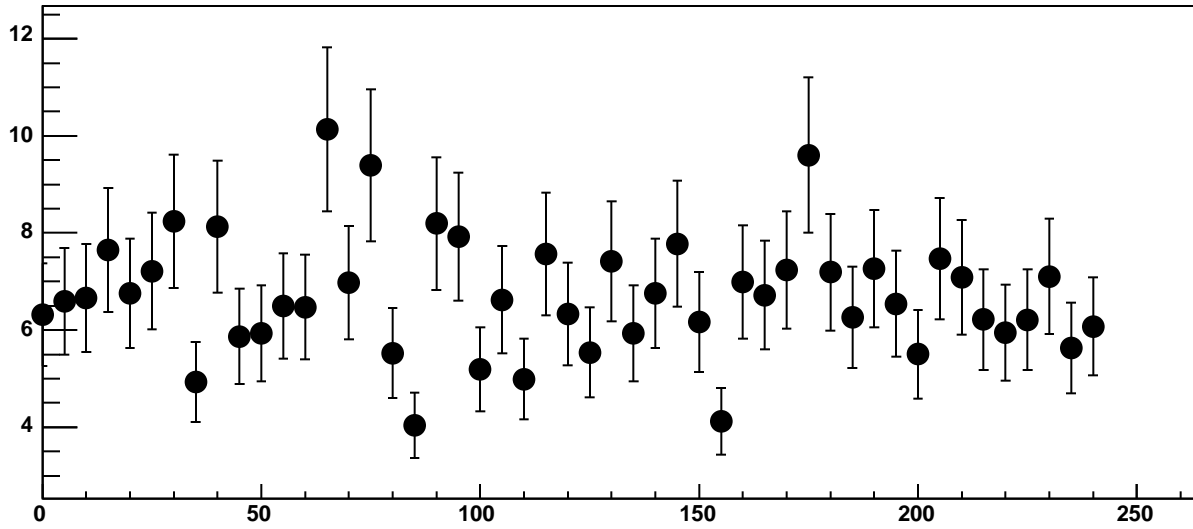


Chip 4, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold

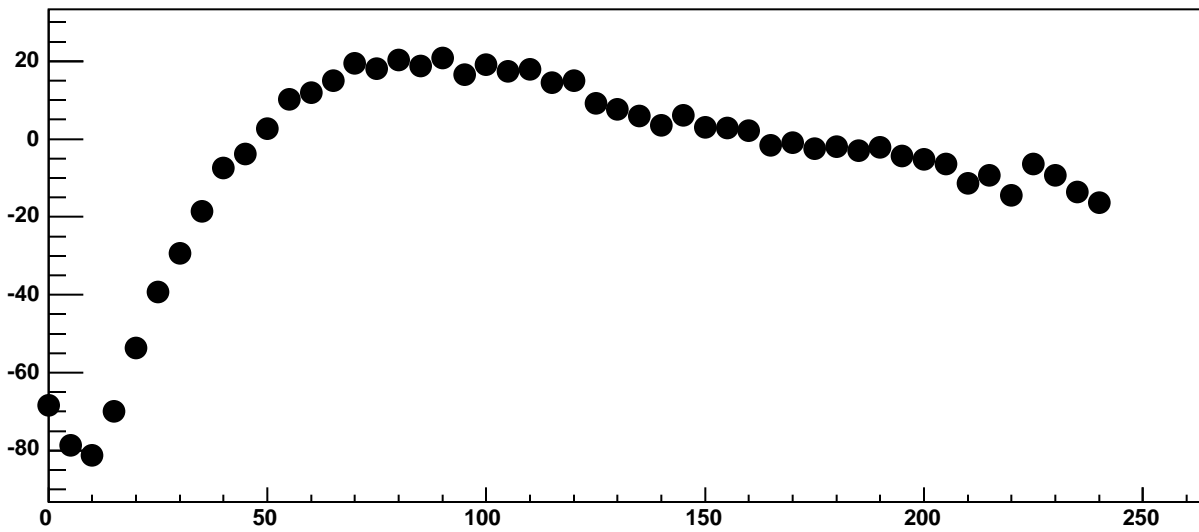


$\chi^2 / \text{ndf}$	6626 / 41
p0	284 ± 0.5024
p1	282.1 ± 0.5517
p2	-342.2 ± 9.02
p3	37.46 ± 0.4291
p4	0.1254 ± 0.003307

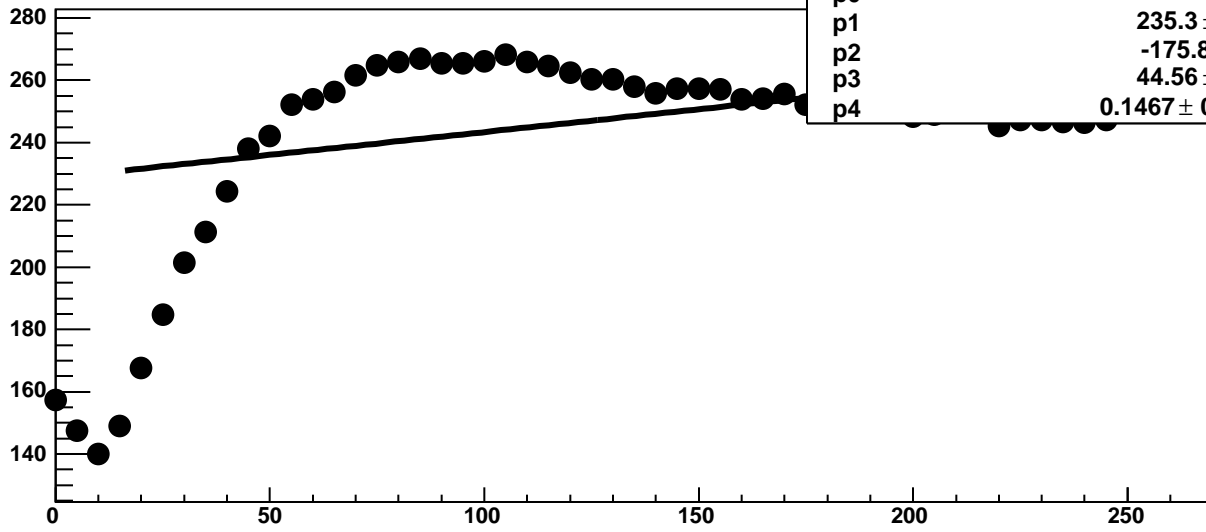
Chip 4, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold

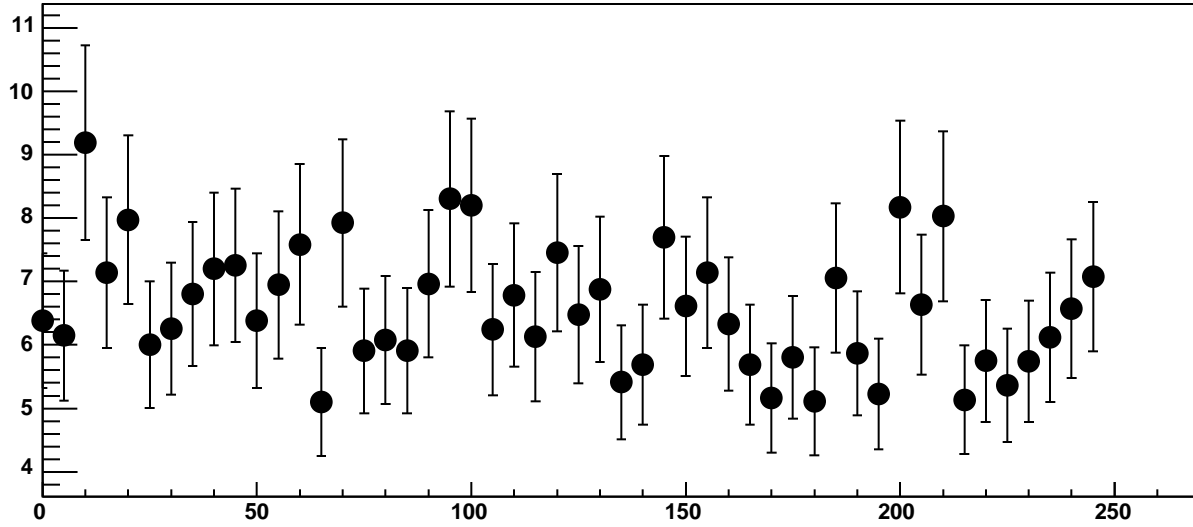


Chip 4, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

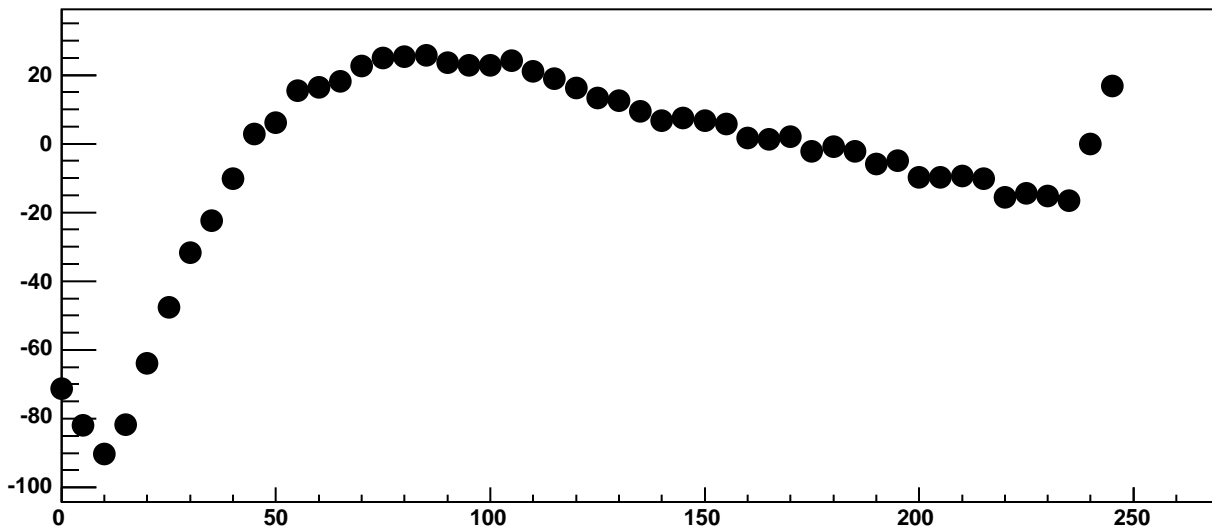


$\chi^2 / \text{ndf}$	9557 / 41
p0	$263.2 \pm 0.4033$
p1	$235.3 \pm 0.3722$
p2	$-175.8 \pm 9.069$
p3	$44.56 \pm 0.4029$
p4	$0.1467 \pm 0.00331$

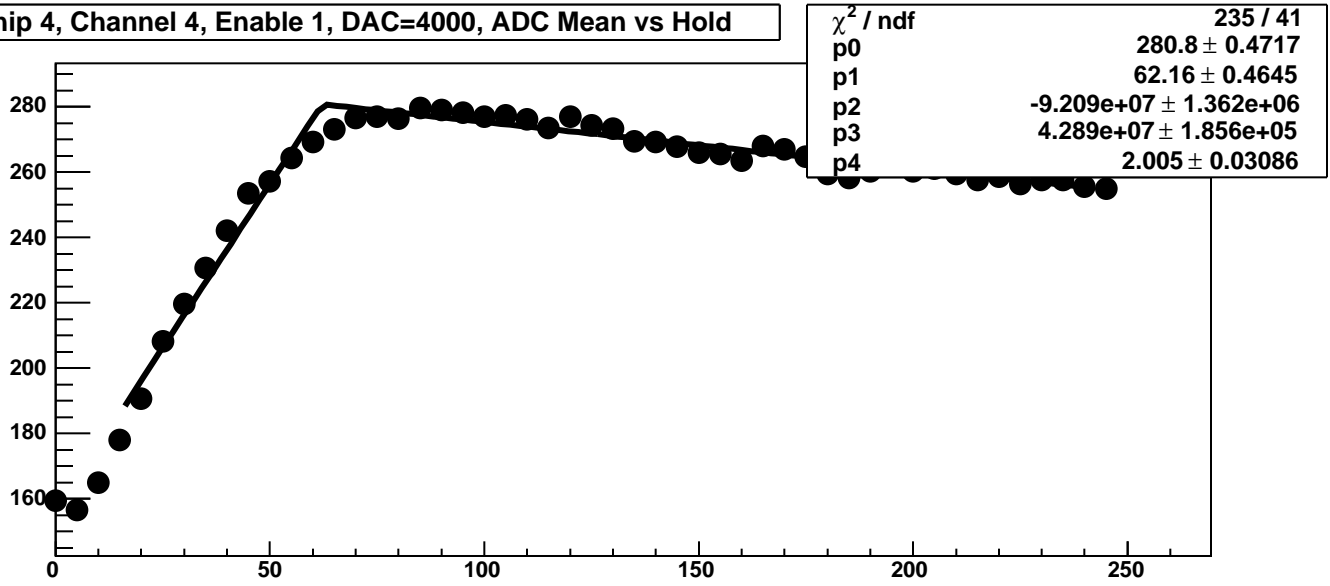
Chip 4, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



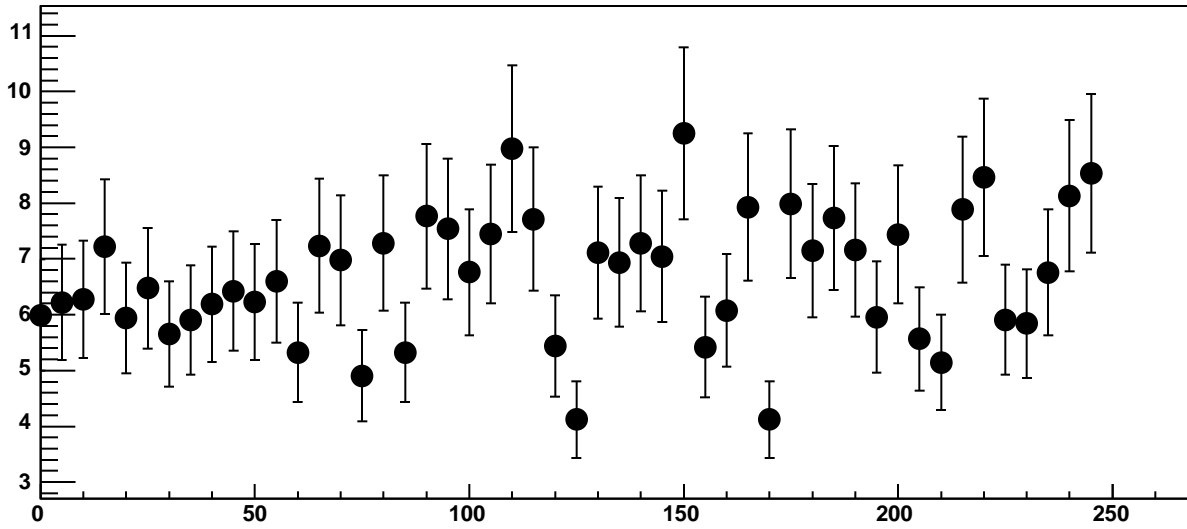
Chip 4, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold



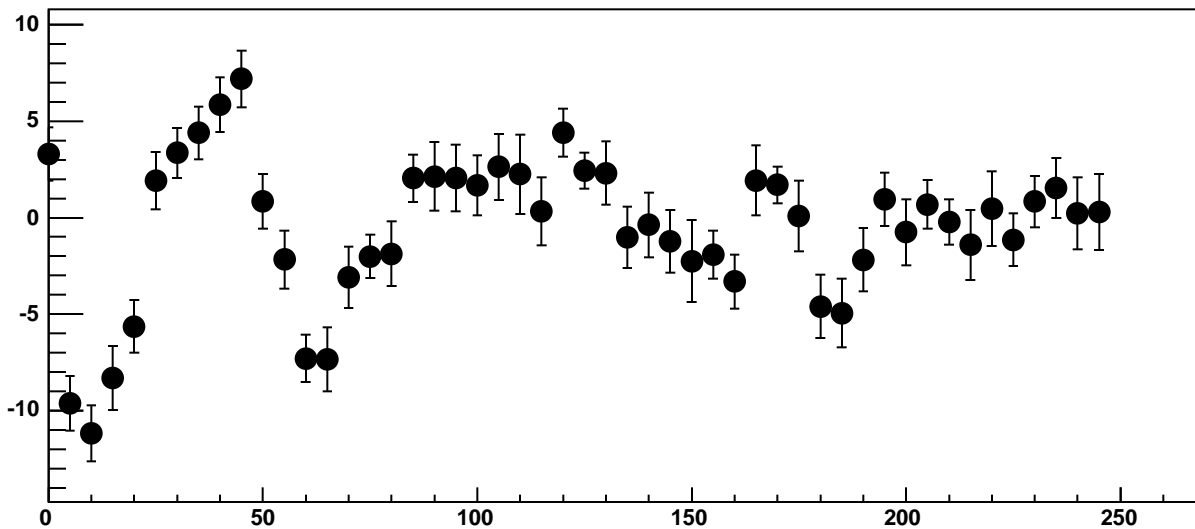
Chip 4, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold



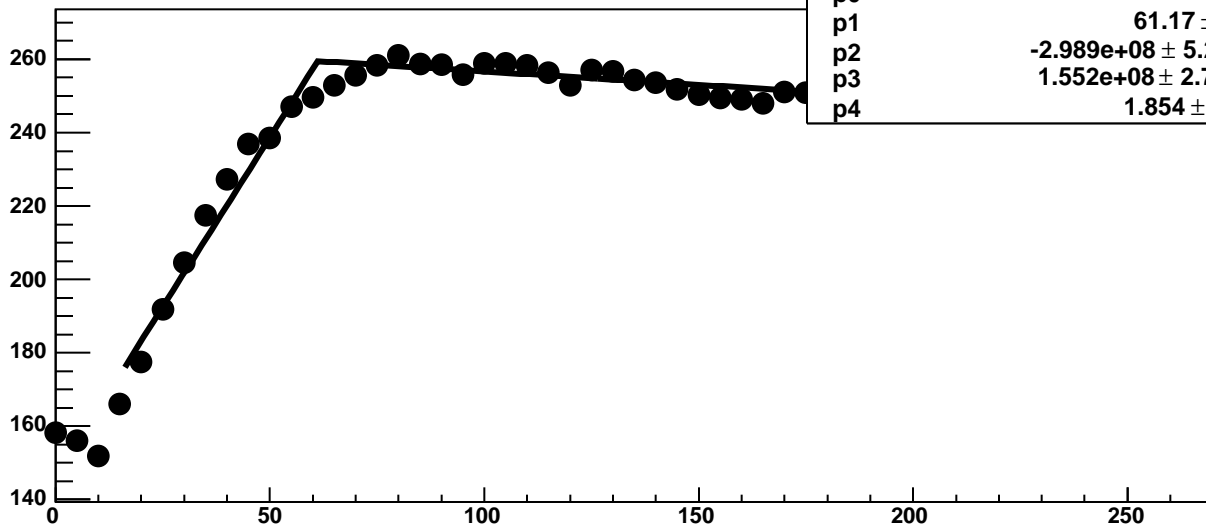
Chip 4, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold

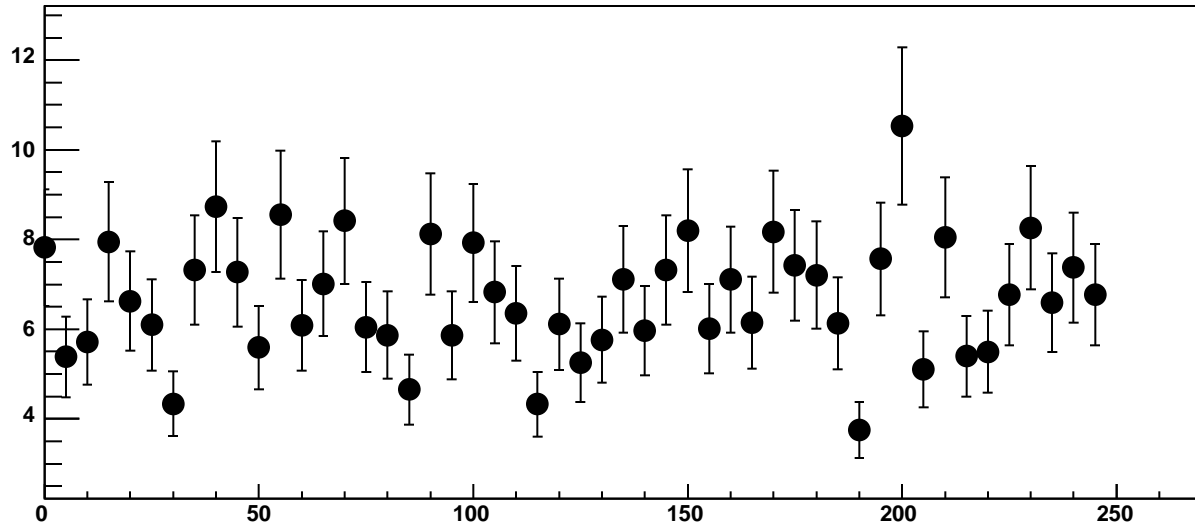


Chip 4, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

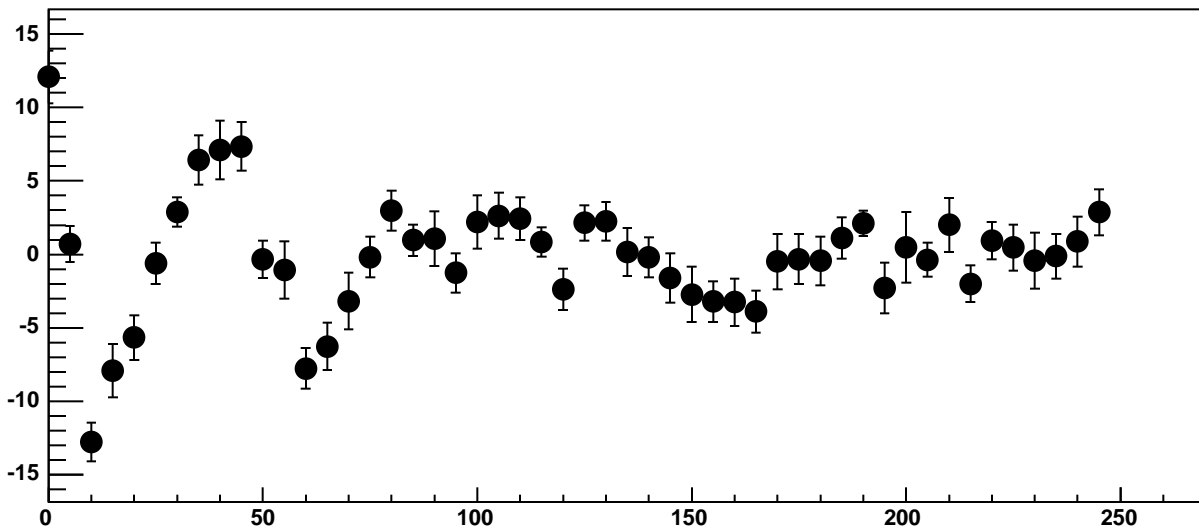


$\chi^2 / \text{ndf}$	194.6 / 41
p0	$259.5 \pm 0.4689$
p1	$61.17 \pm 0.5505$
p2	$-2.989\text{e}+08 \pm 5.278\text{e}+06$
p3	$1.552\text{e}+08 \pm 2.712\text{e}+05$
p4	$1.854 \pm 0.03381$

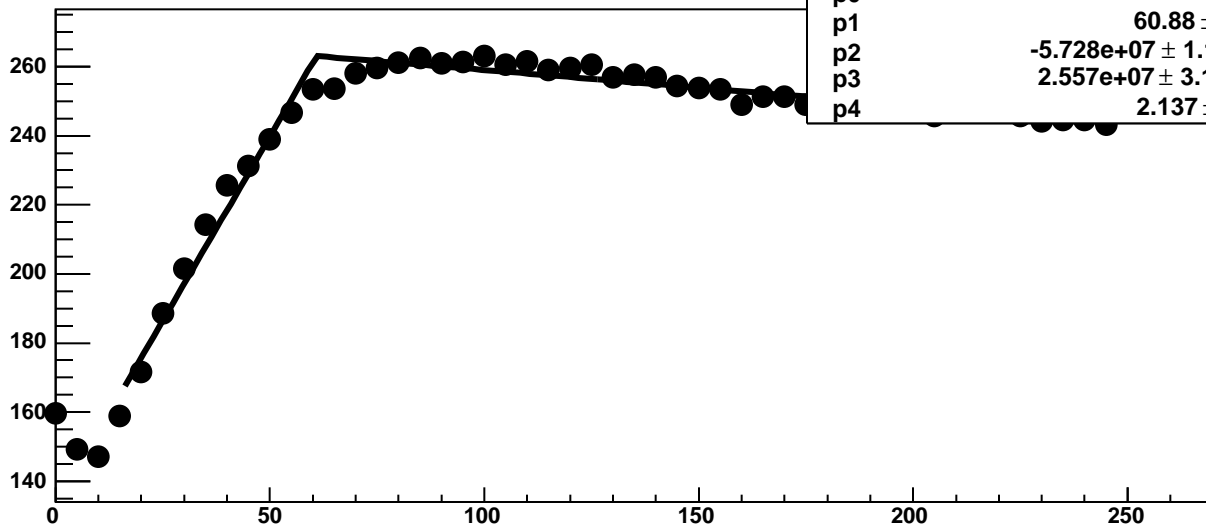
Chip 4, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

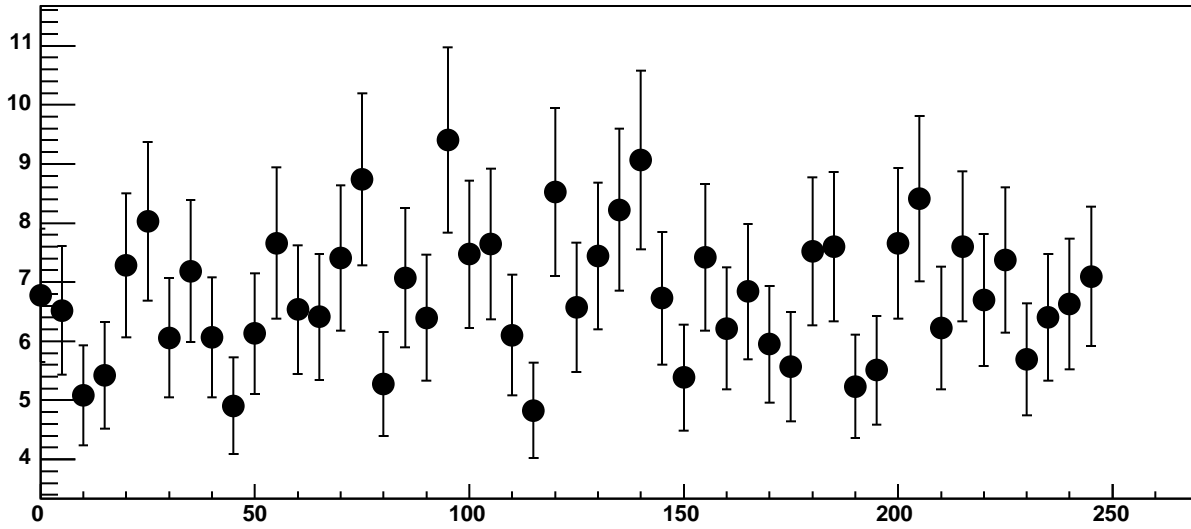


Chip 4, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

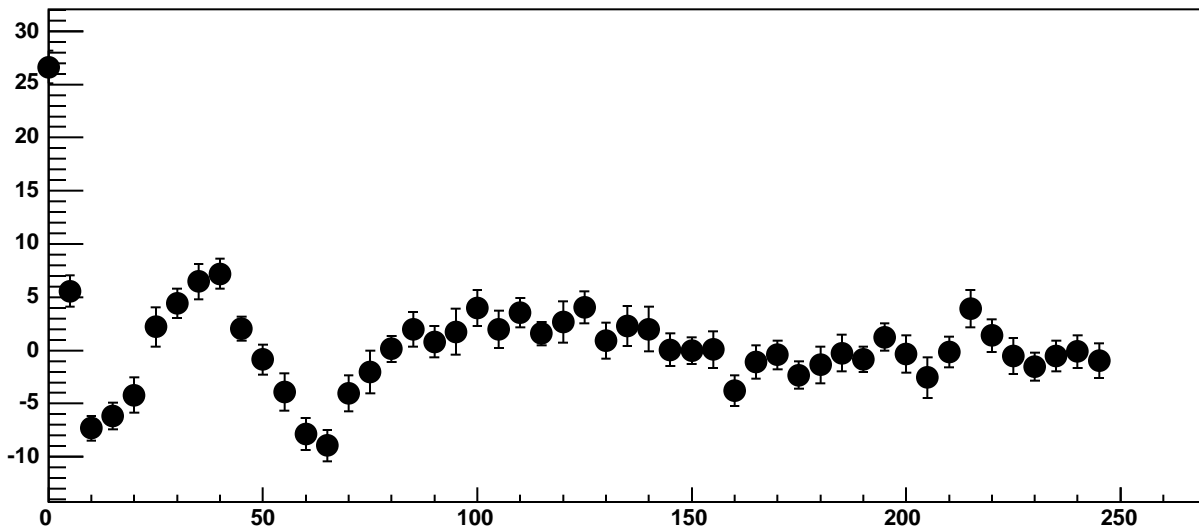


$\chi^2 / \text{ndf}$	215.9 / 41
p0	$263.1 \pm 0.5031$
p1	$60.88 \pm 0.4586$
p2	$-5.728\text{e}+07 \pm 1.104\text{e}+06$
p3	$2.557\text{e}+07 \pm 3.104\text{e}+05$
p4	$2.137 \pm 0.0322$

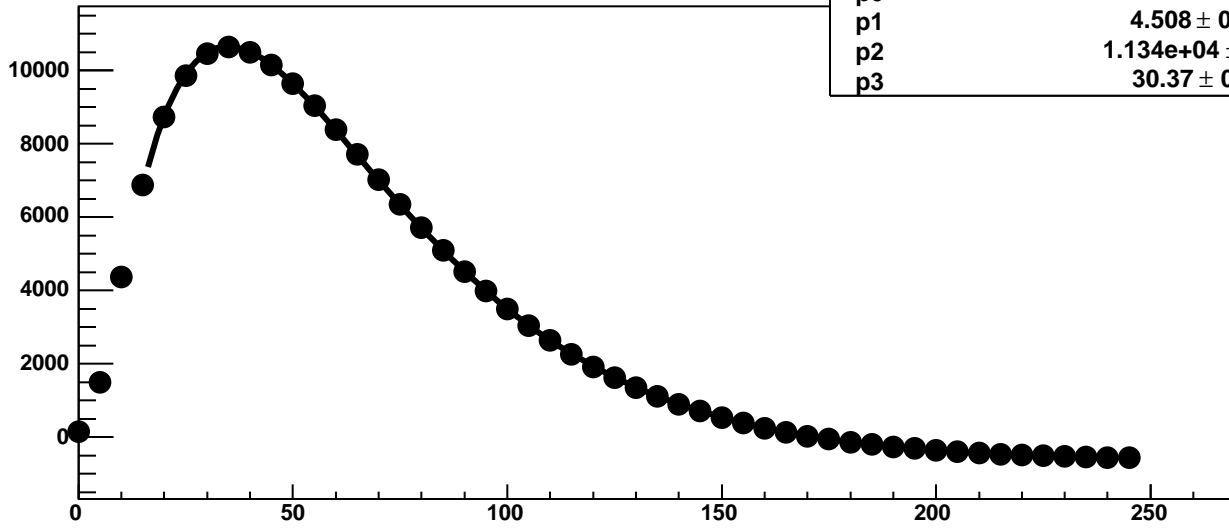
Chip 4, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

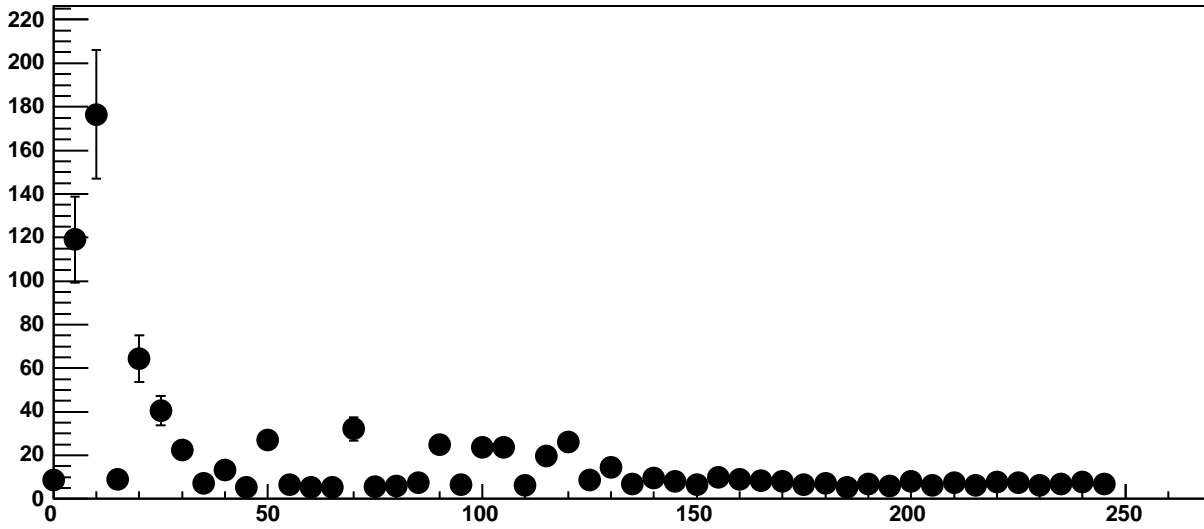


Chip 4, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold

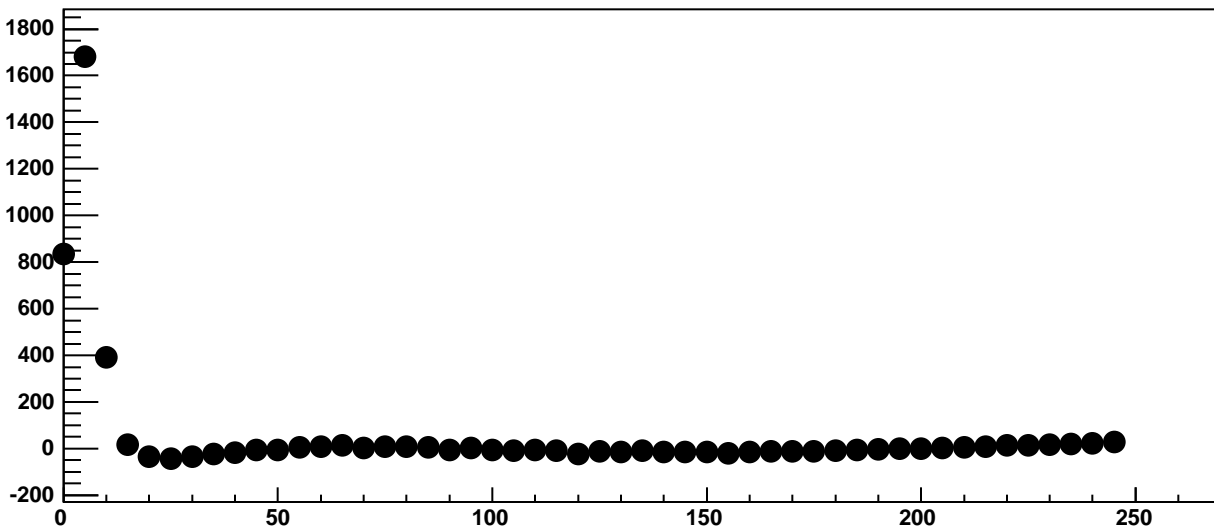


$\chi^2 / \text{ndf}$	1942 / 42
p0	-677.4 ± 0.4976
p1	4.508 ± 0.005206
p2	1.134e+04 ± 0.8528
p3	30.37 ± 0.004117

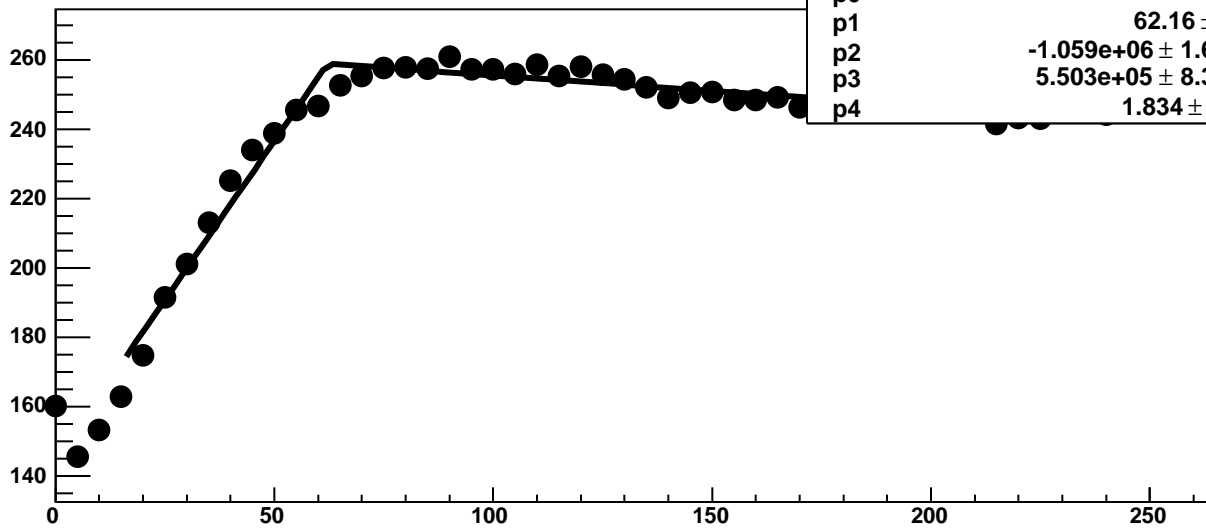
Chip 4, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold



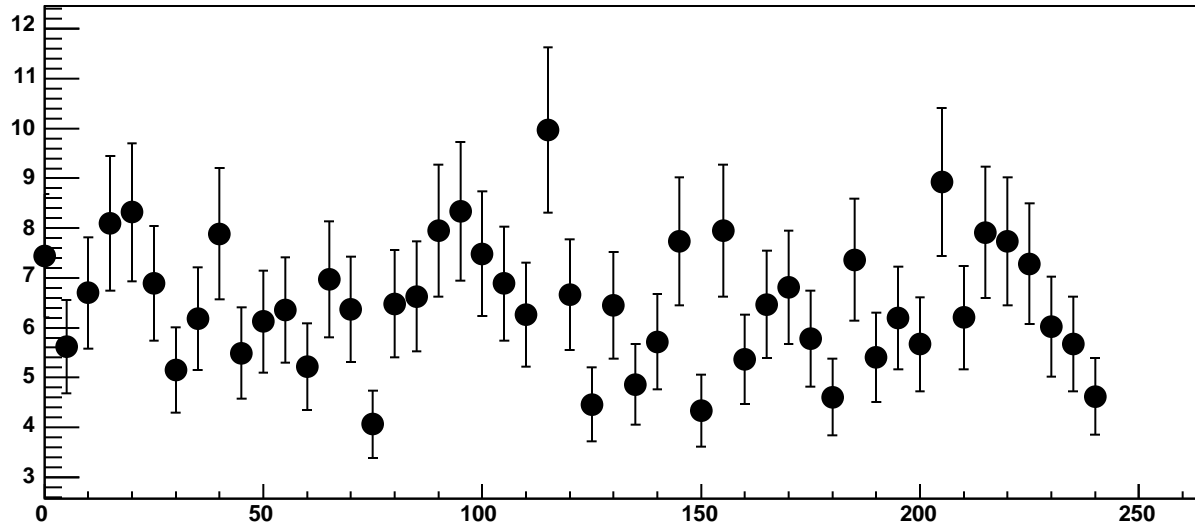
Chip 4, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold



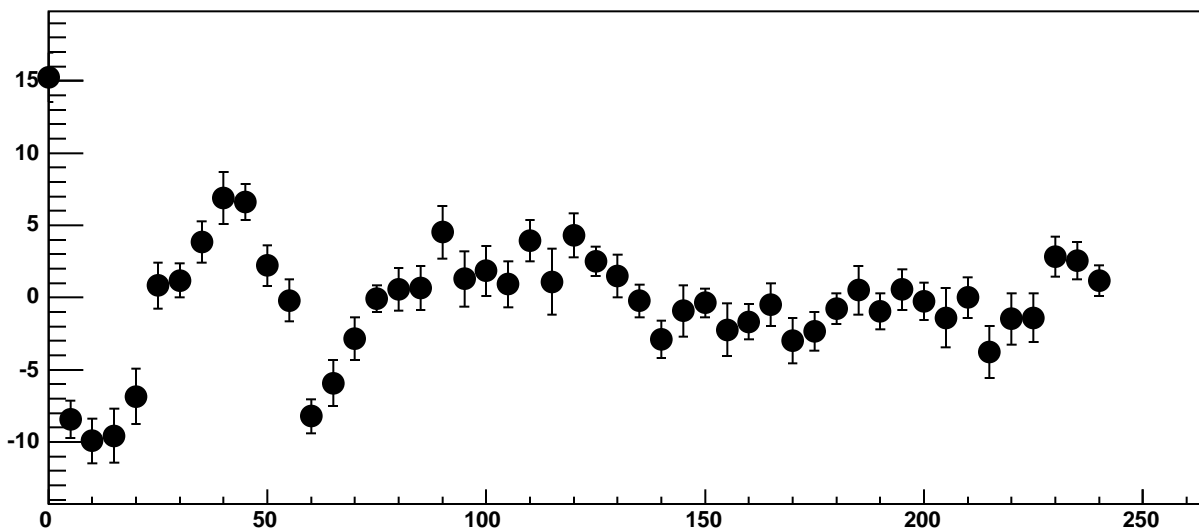
Chip 4, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold



Chip 4, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold

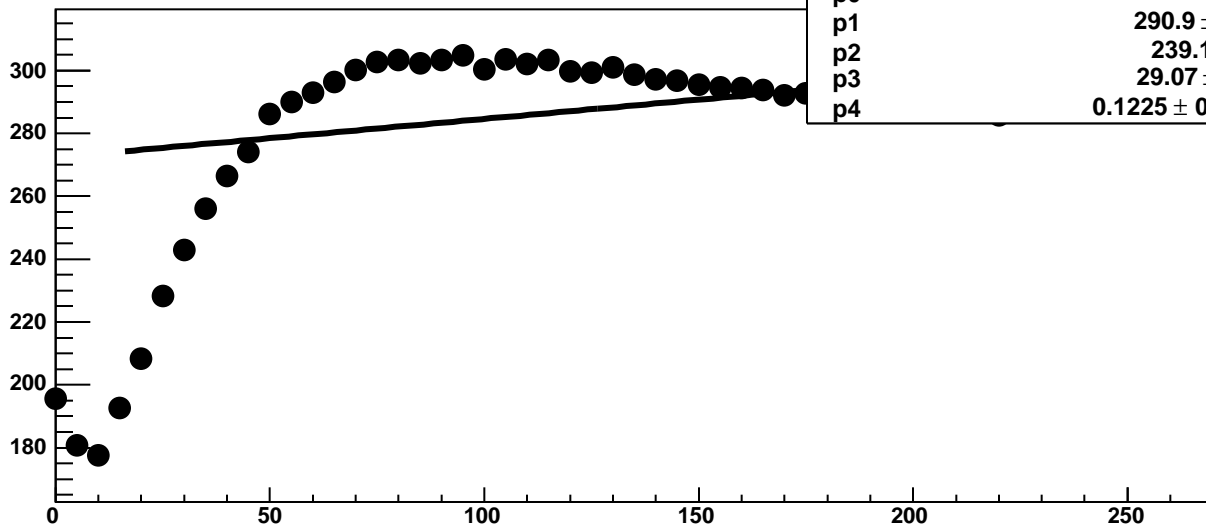


Chip 4, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold



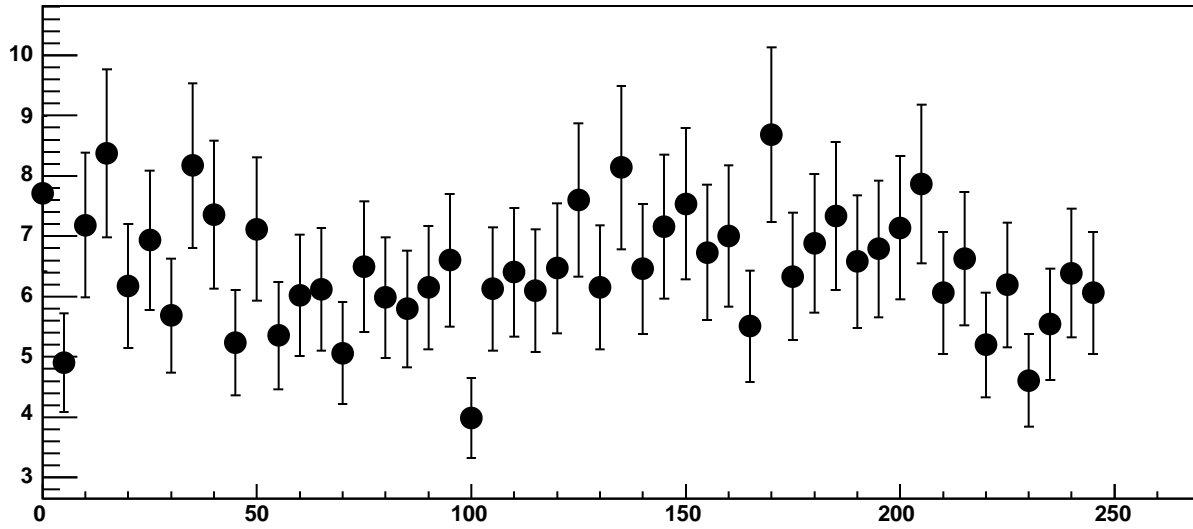


Chip 4, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold

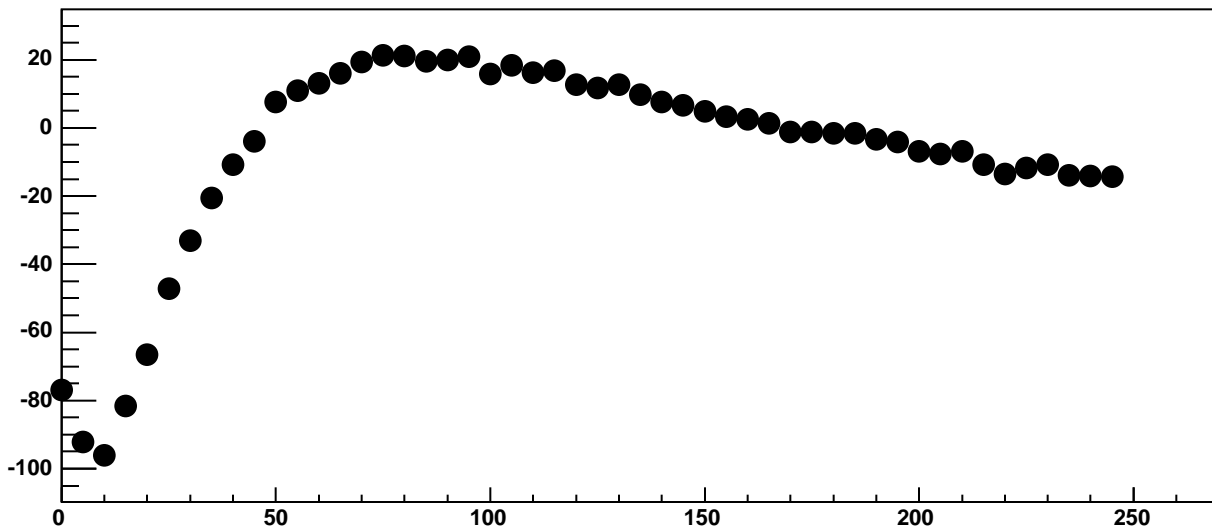


$\chi^2 / \text{ndf}$	9017 / 41
p0	$308 \pm 0.5248$
p1	$290.9 \pm 0.5375$
p2	$239.1 \pm 4.106$
p3	$29.07 \pm 0.2199$
p4	$0.1225 \pm 0.003154$

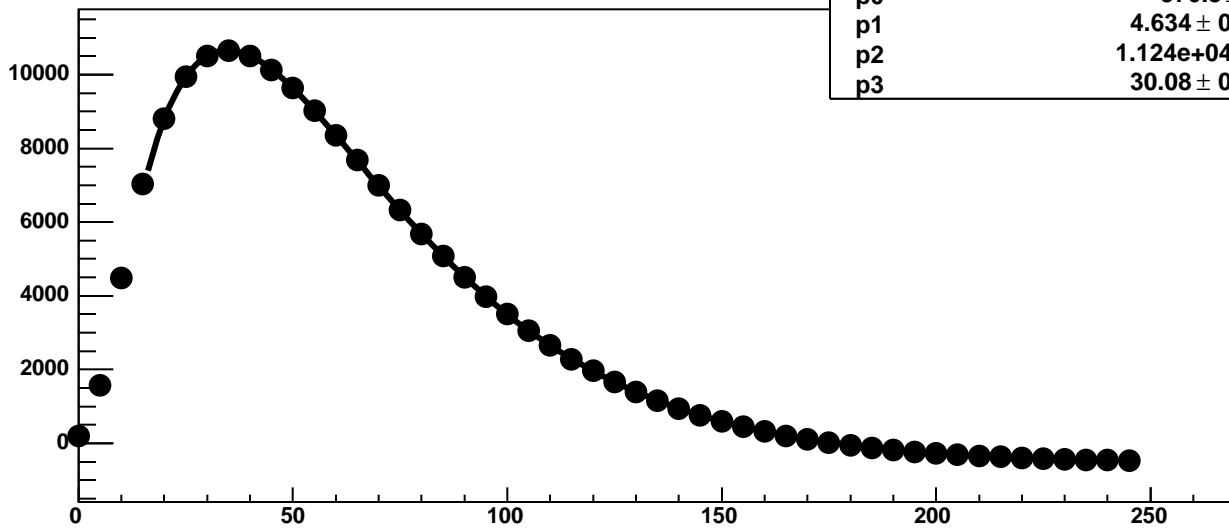
Chip 4, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

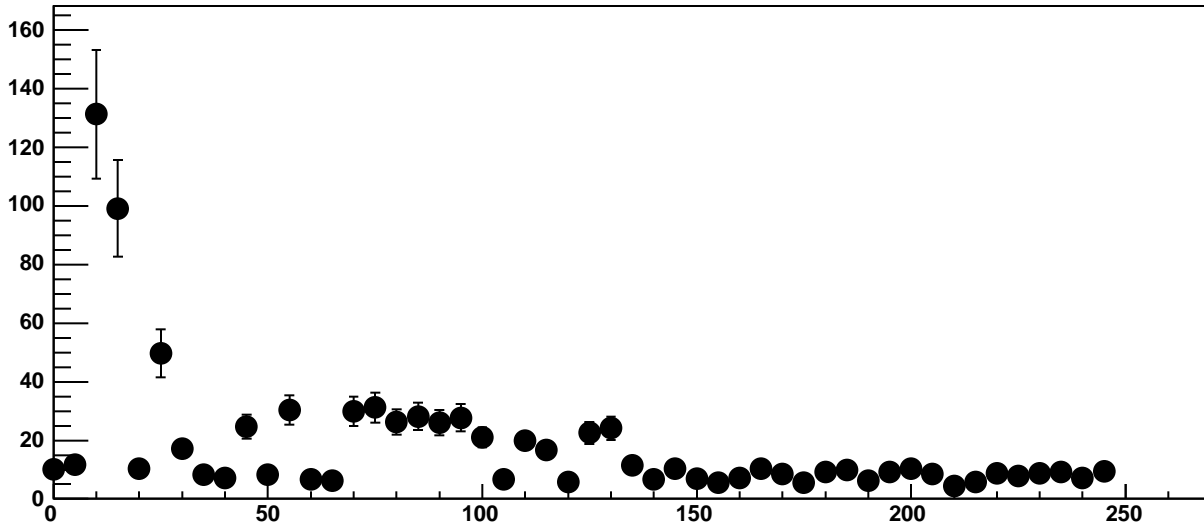


Chip 4, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold

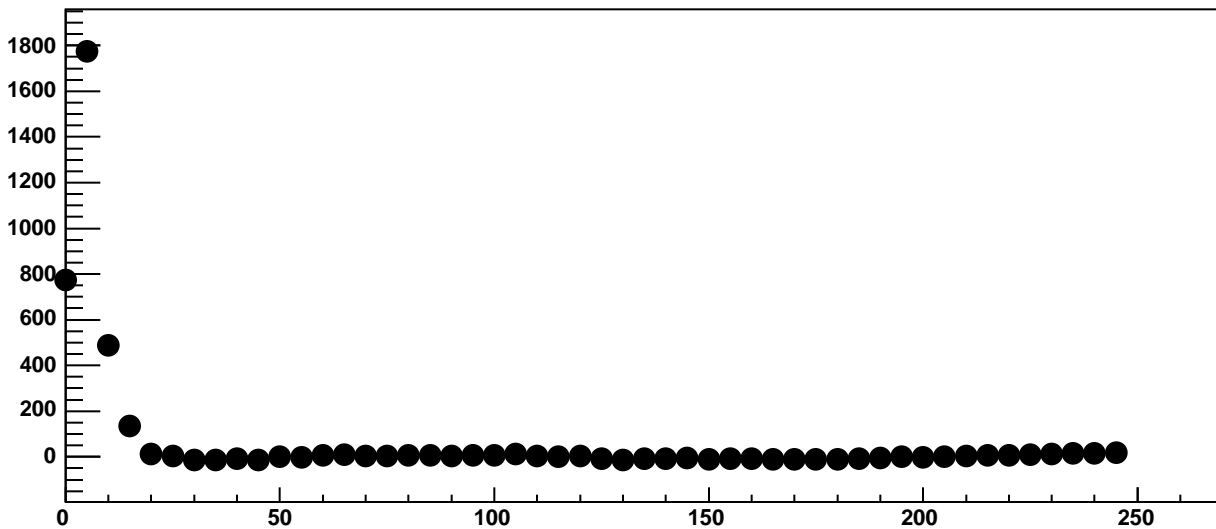


$\chi^2 / \text{ndf}$	844.7 / 42
p0	-570.8 ± 0.5637
p1	4.634 ± 0.008984
p2	1.124e+04 ± 0.976
p3	30.08 ± 0.005644

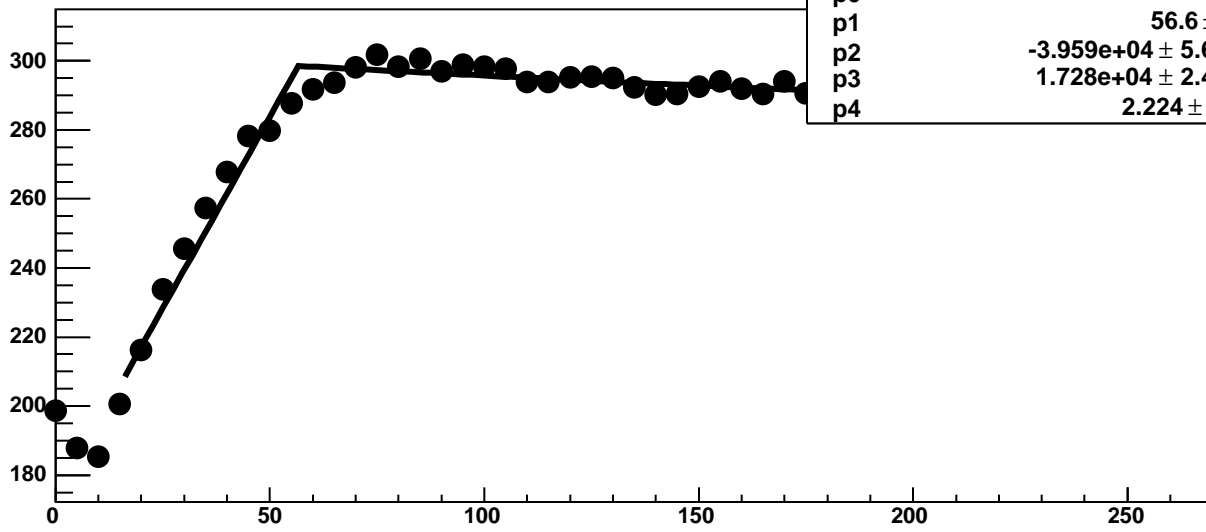
Chip 4, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold

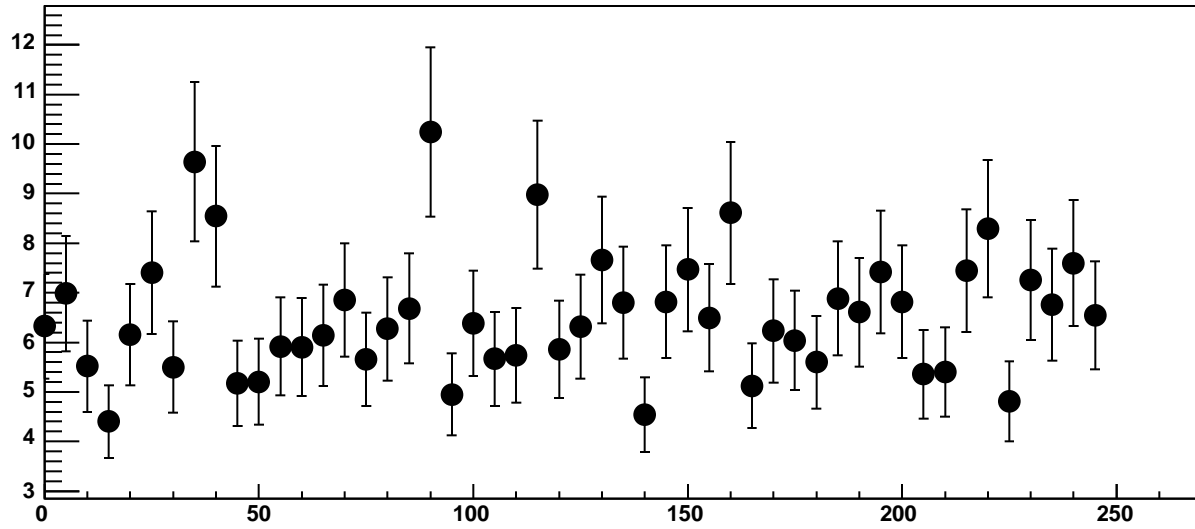


Chip 4, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

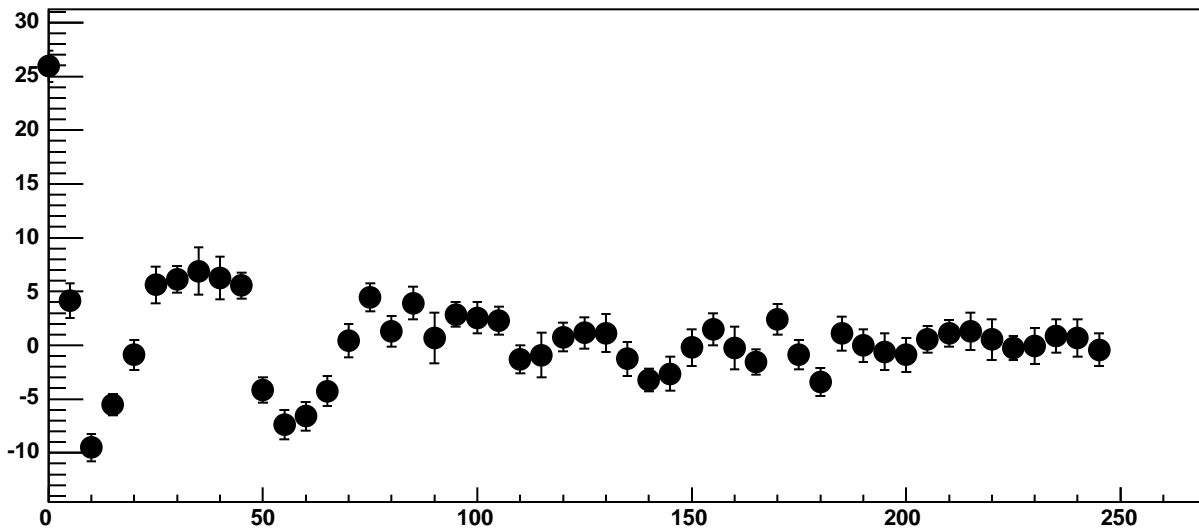


$\chi^2 / \text{ndf}$	244.7 / 41
p0	$298.6 \pm 0.6918$
p1	$56.6 \pm 0.4897$
p2	$-3.959\text{e}+04 \pm 5.646\text{e}+04$
p3	$1.728\text{e}+04 \pm 2.476\text{e}+04$
p4	$2.224 \pm 0.03155$

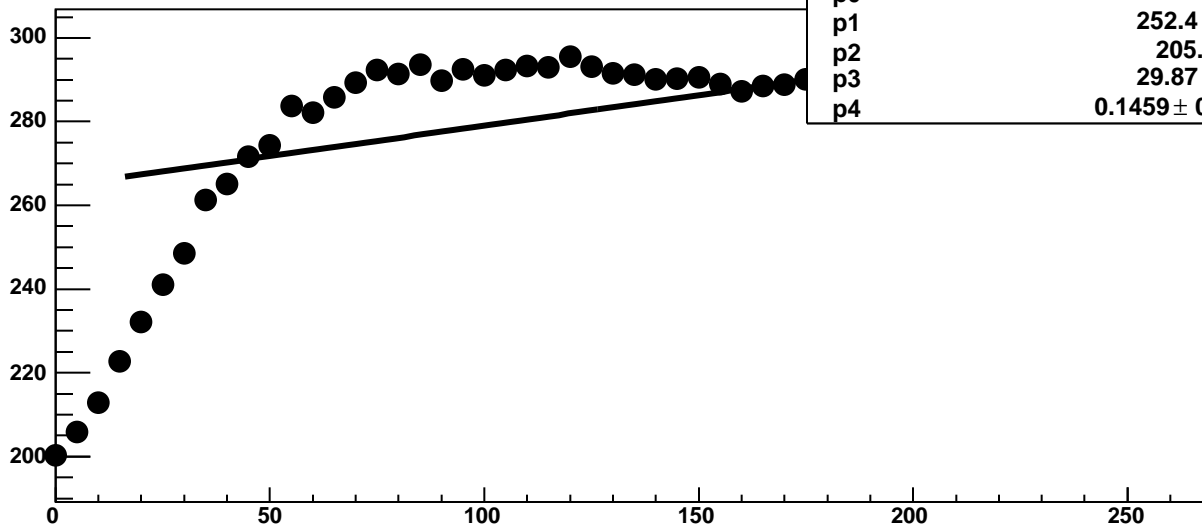
Chip 4, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



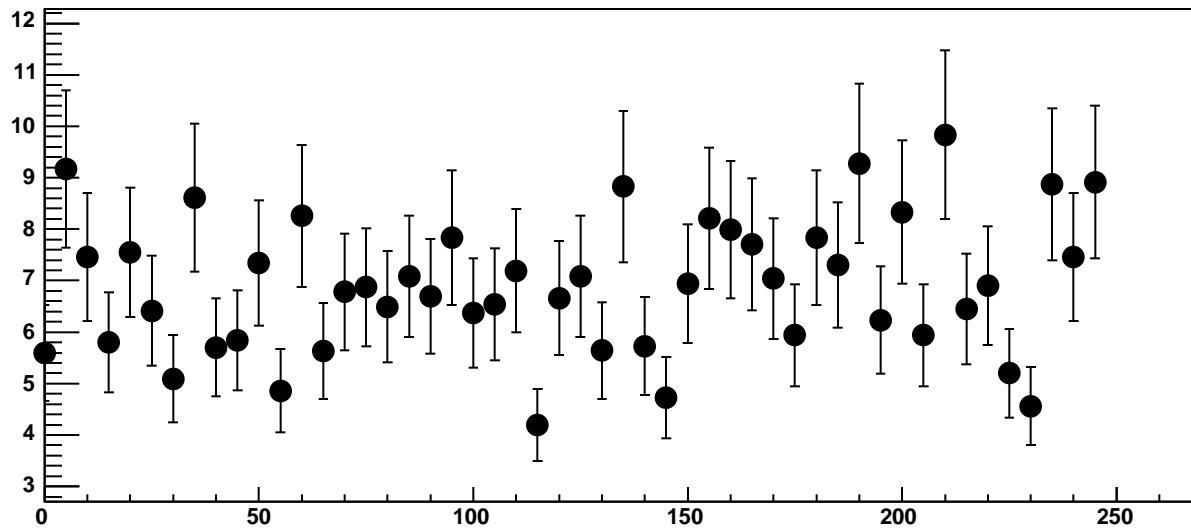
Chip 4, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold



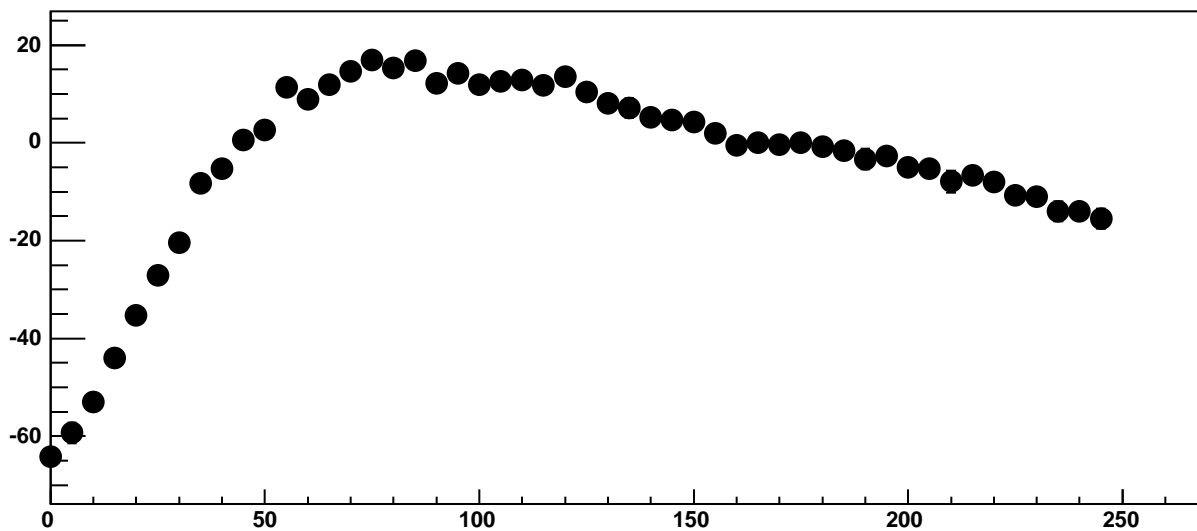
Chip 4, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold



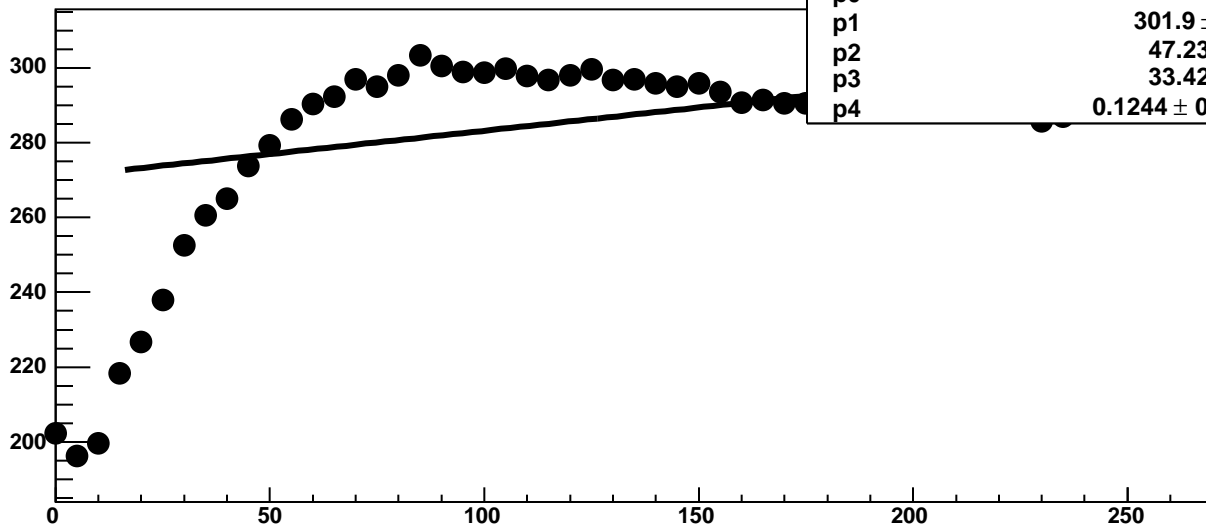
Chip 4, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



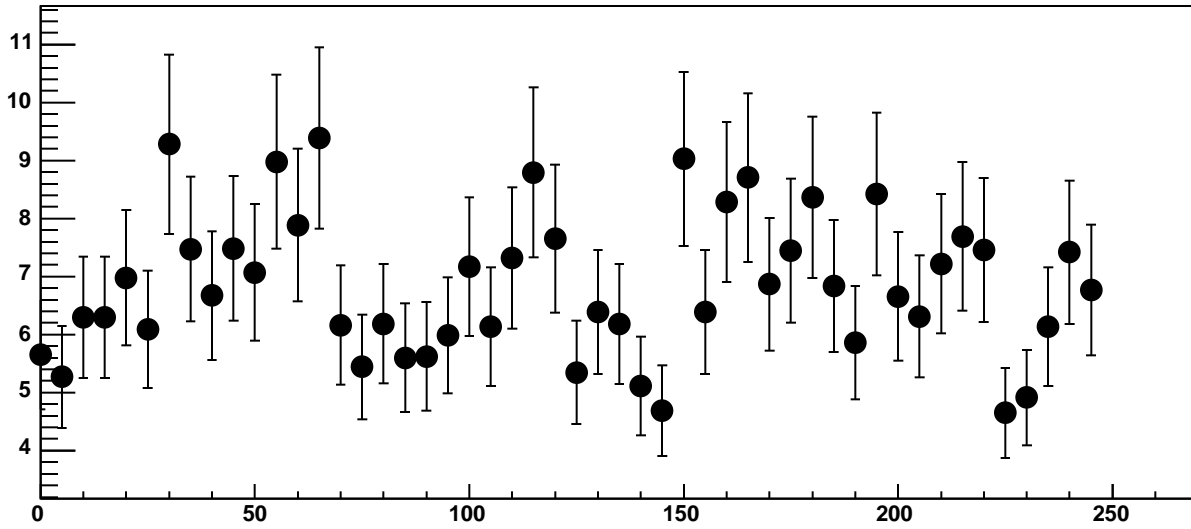
Chip 4, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold



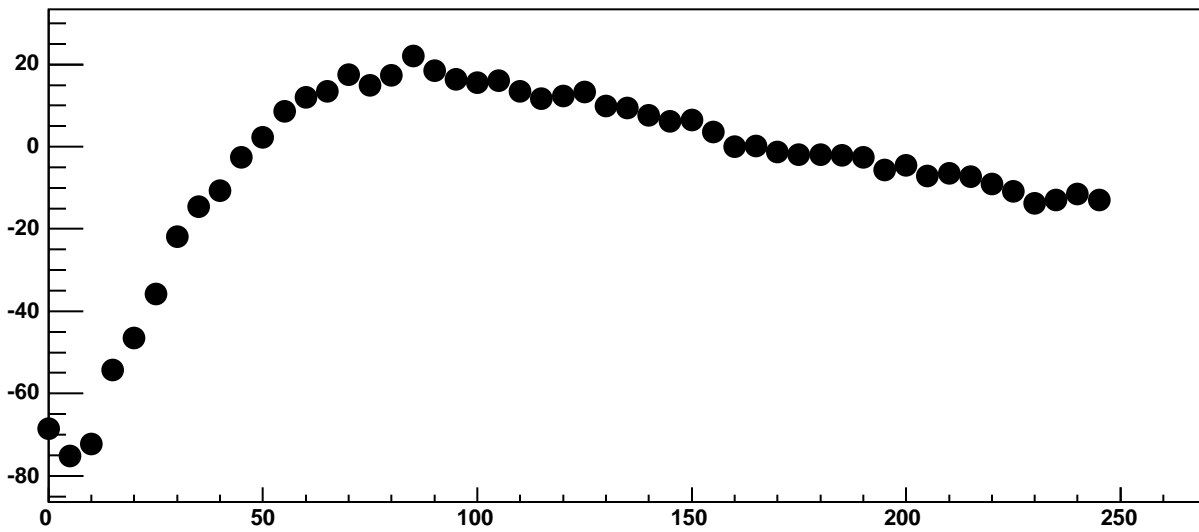
Chip 4, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold



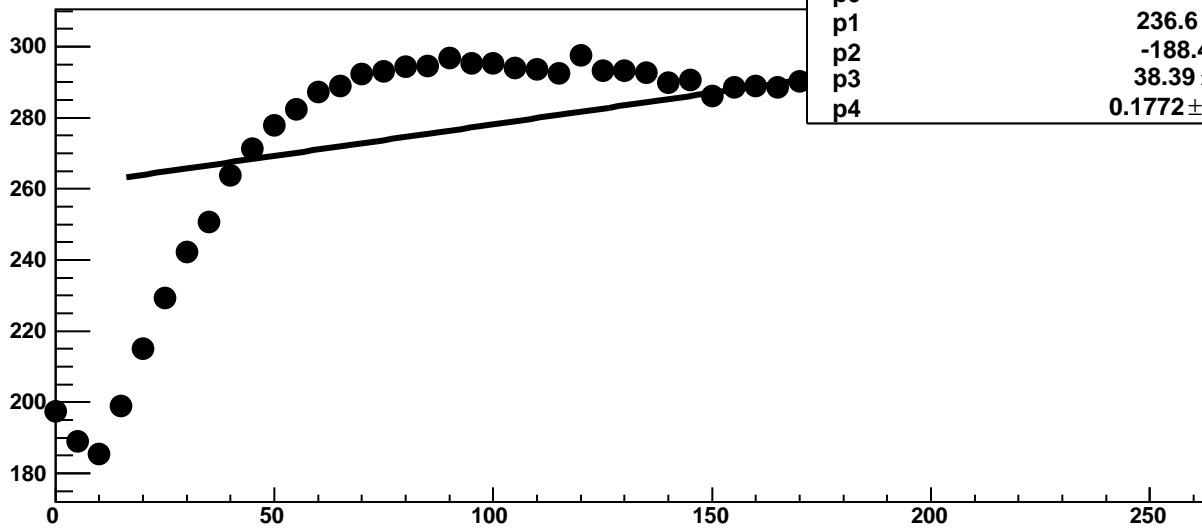
Chip 4, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold

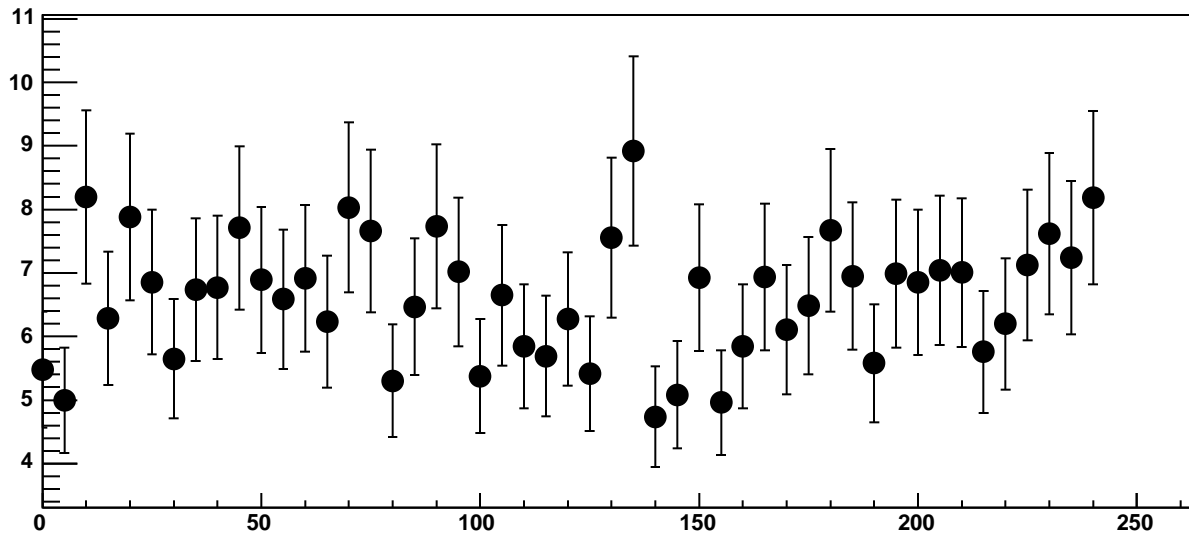


Chip 4, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

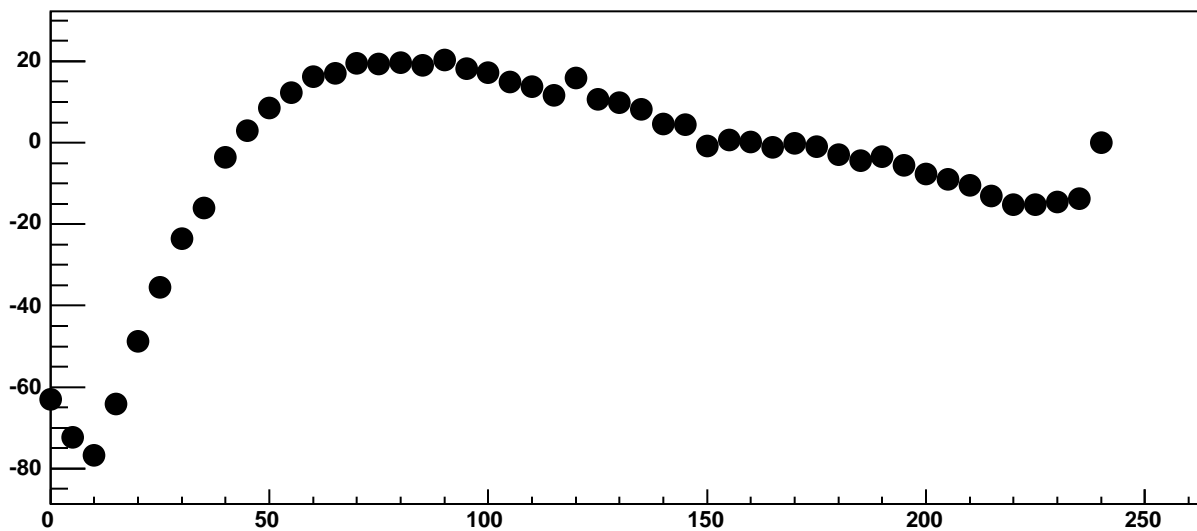


$\chi^2 / \text{ndf}$	6239 / 41
p0	$302.3 \pm 0.4377$
p1	$236.6 \pm 0.4537$
p2	$-188.4 \pm 4.736$
p3	$38.39 \pm 0.3306$
p4	$0.1772 \pm 0.00355$

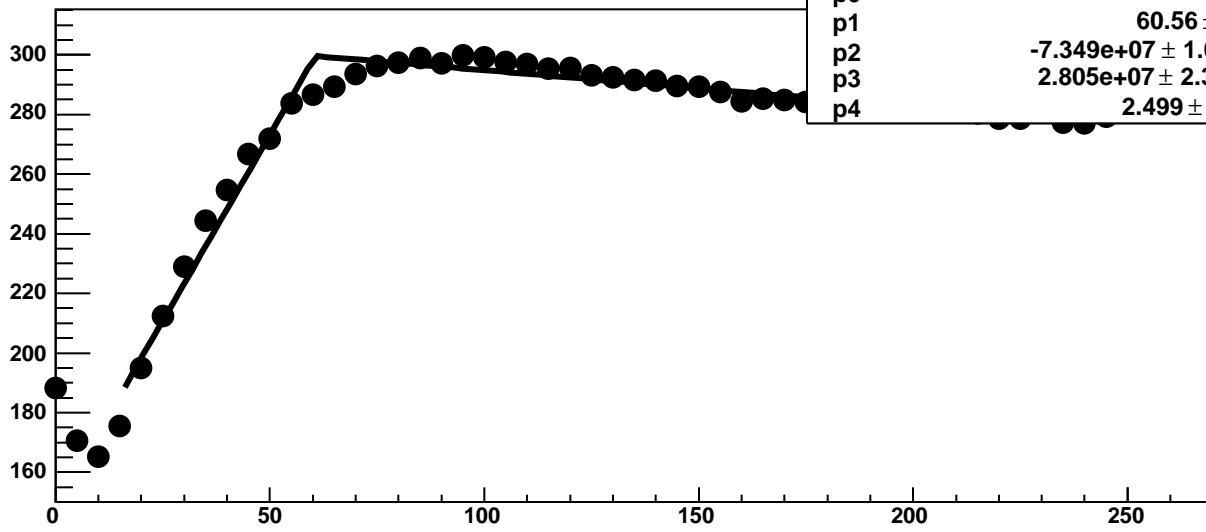
Chip 4, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

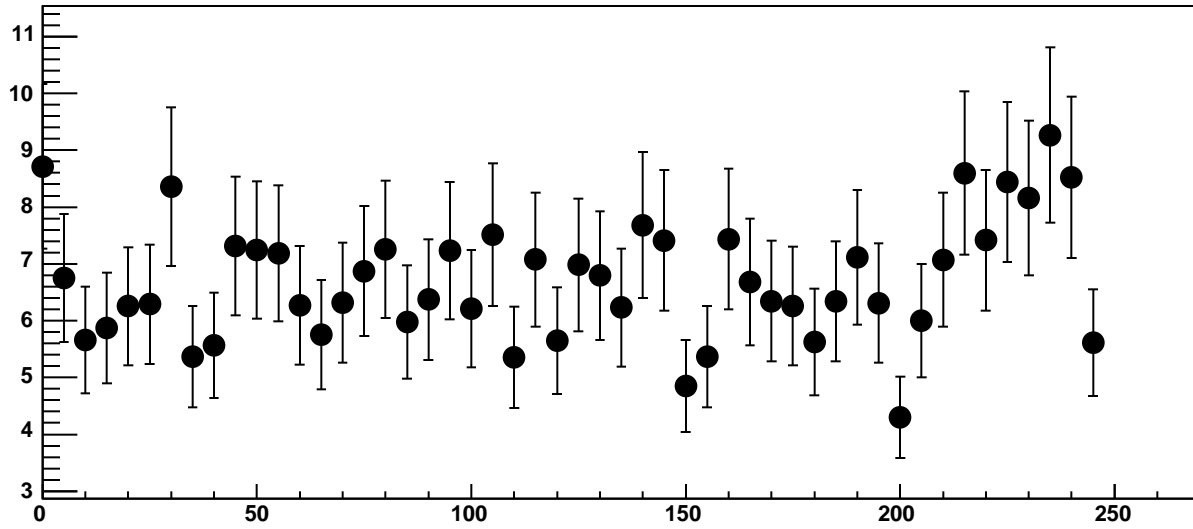


Chip 4, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

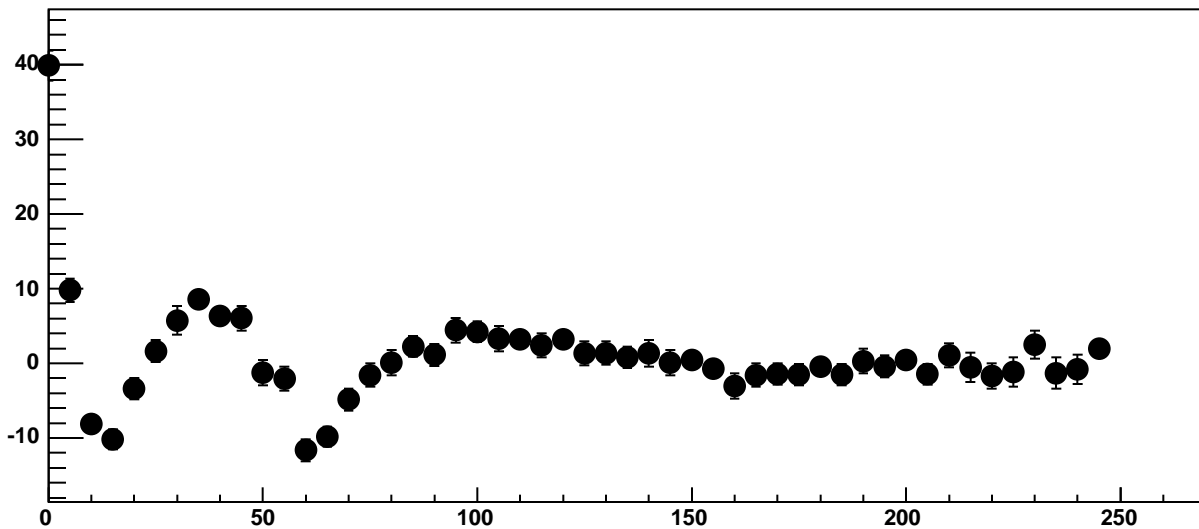


$\chi^2 / \text{ndf}$	350.1 / 41
p0	$299.6 \pm 0.4873$
p1	$60.56 \pm 0.3962$
p2	$-7.349\text{e}+07 \pm 1.086\text{e}+06$
p3	$2.805\text{e}+07 \pm 2.329\text{e}+05$
p4	$2.499 \pm 0.03233$

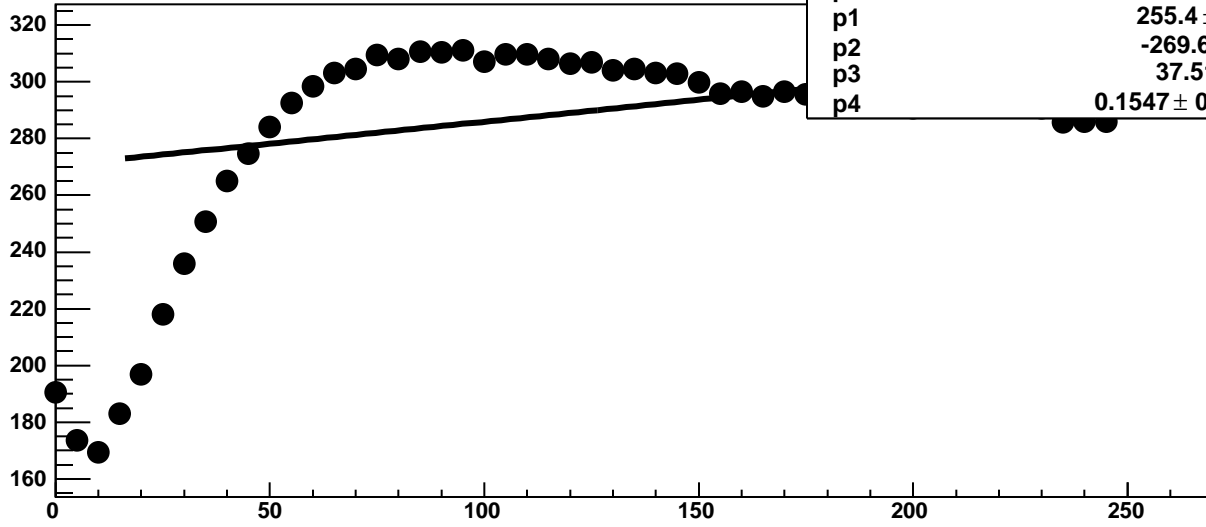
Chip 4, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold

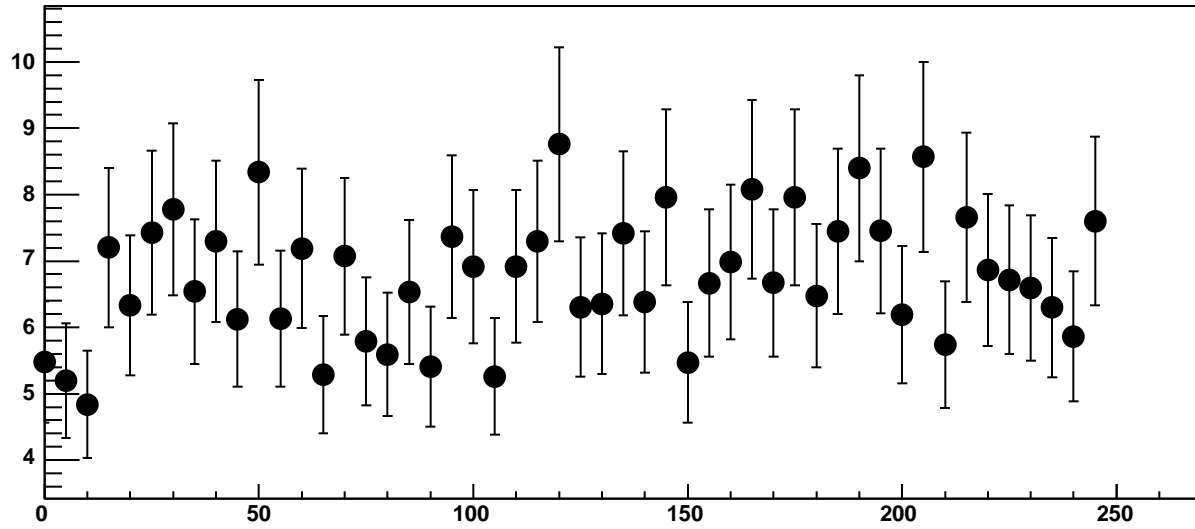


Chip 4, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold

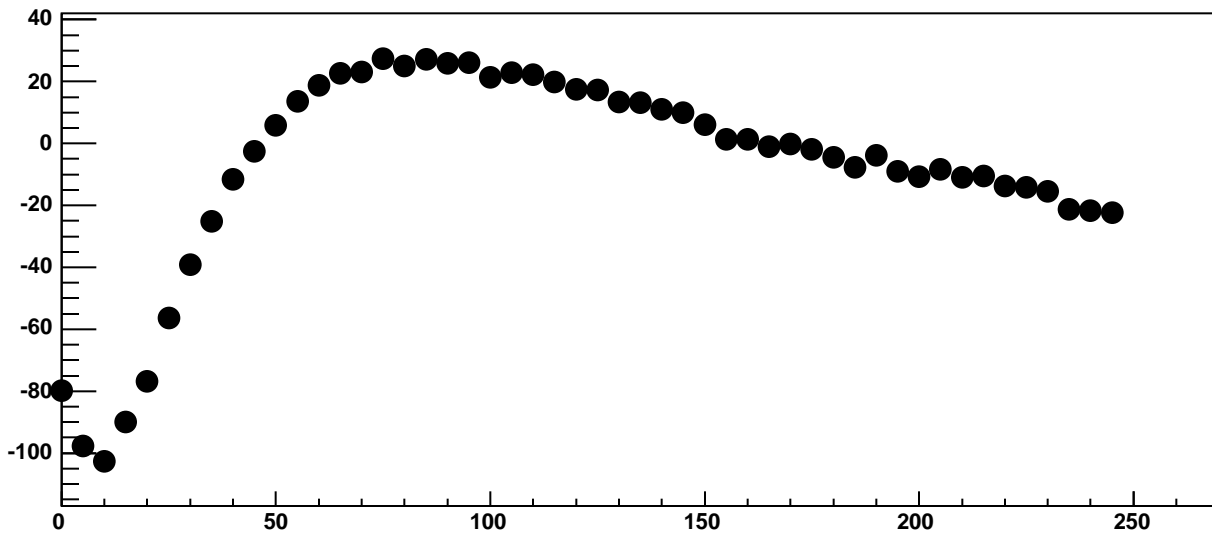


$\chi^2 / \text{ndf}$	1.258e+04 / 41
p0	310 ± 0.4586
p1	255.4 ± 0.4209
p2	-269.6 ± 7.992
p3	37.51 ± 0.411
p4	0.1547 ± 0.003418

Chip 4, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold

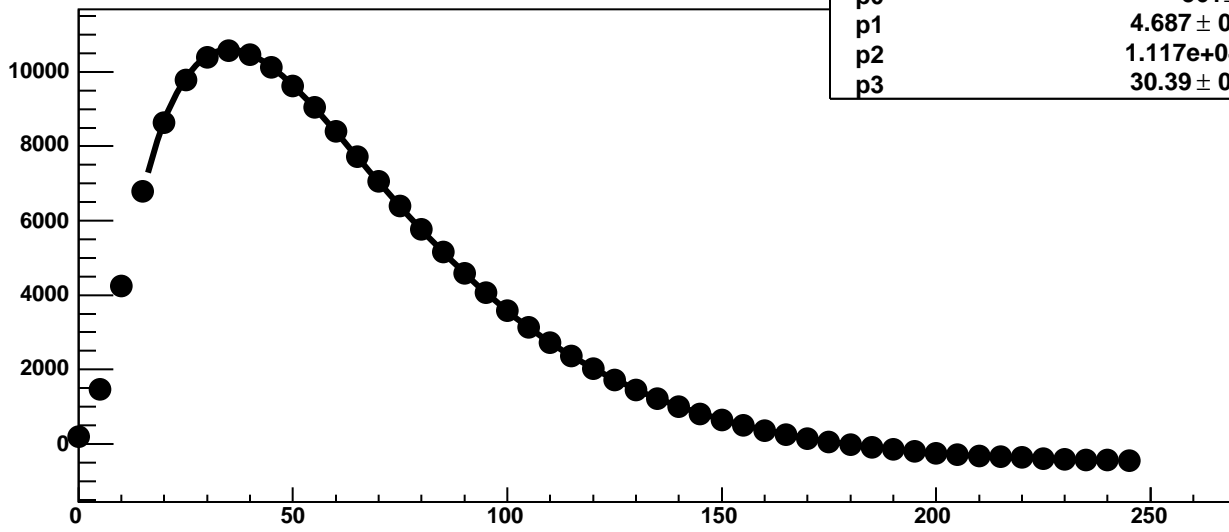


Chip 4, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold



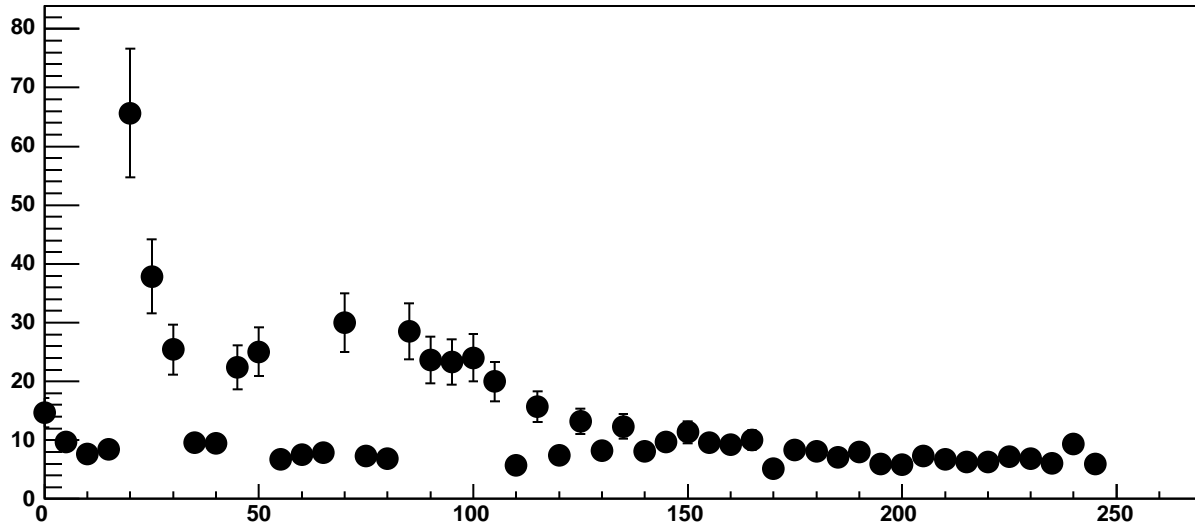


Chip 4, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold

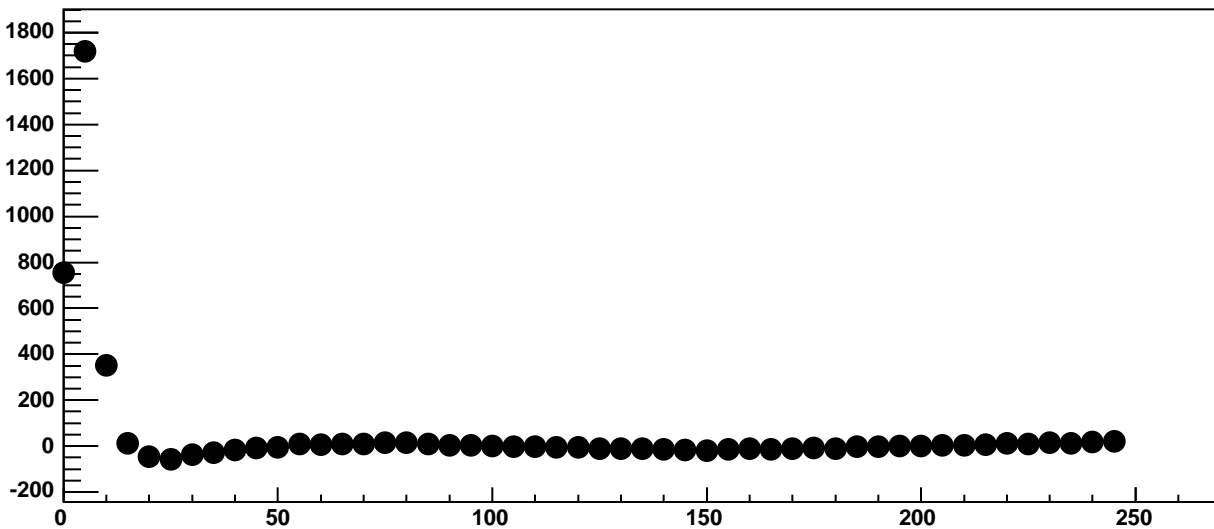


$\chi^2 / \text{ndf}$	1508 / 42
p0	-561 ± 0.5176
p1	4.687 ± 0.005264
p2	1.117e+04 ± 1.05
p3	30.39 ± 0.004783

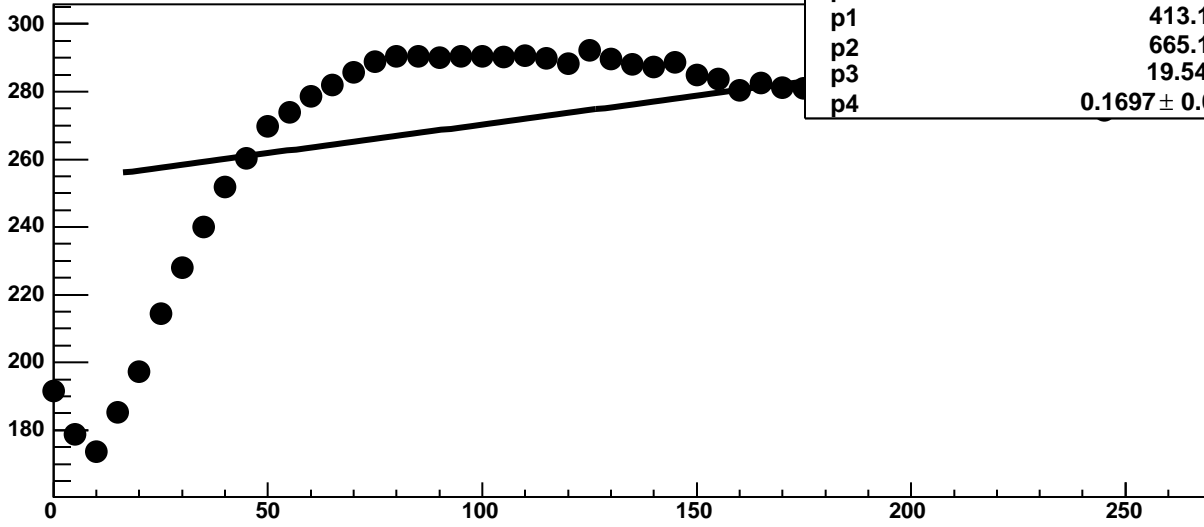
Chip 4, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold

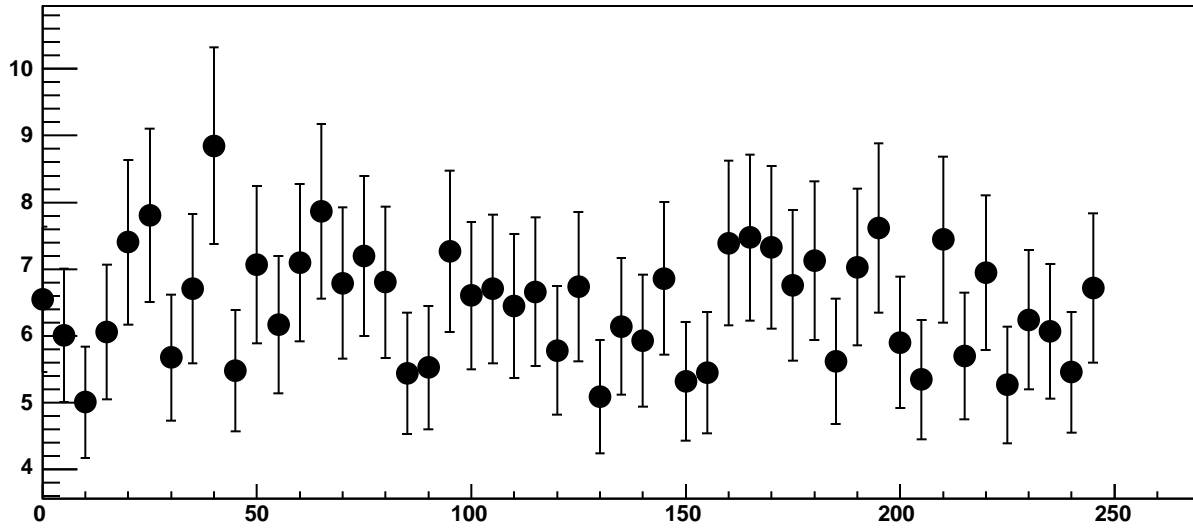


Chip 4, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

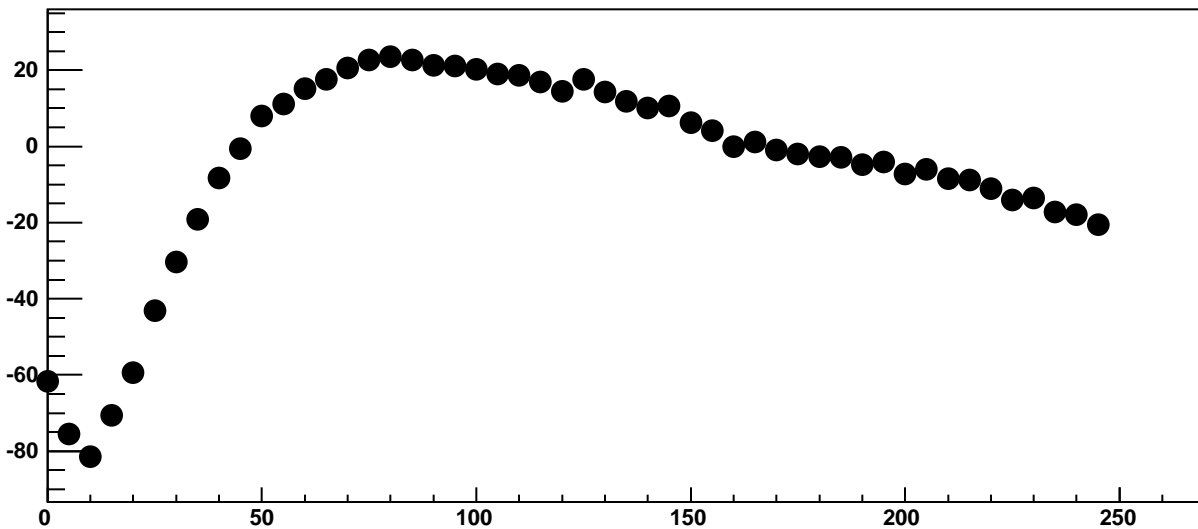


$\chi^2 / \text{ndf}$	8733 / 41
p0	$323.5 \pm 0.2148$
p1	$413.1 \pm 1.265$
p2	$665.1 \pm 84.85$
p3	$19.54 \pm 1.414$
p4	$0.1697 \pm 0.0007427$

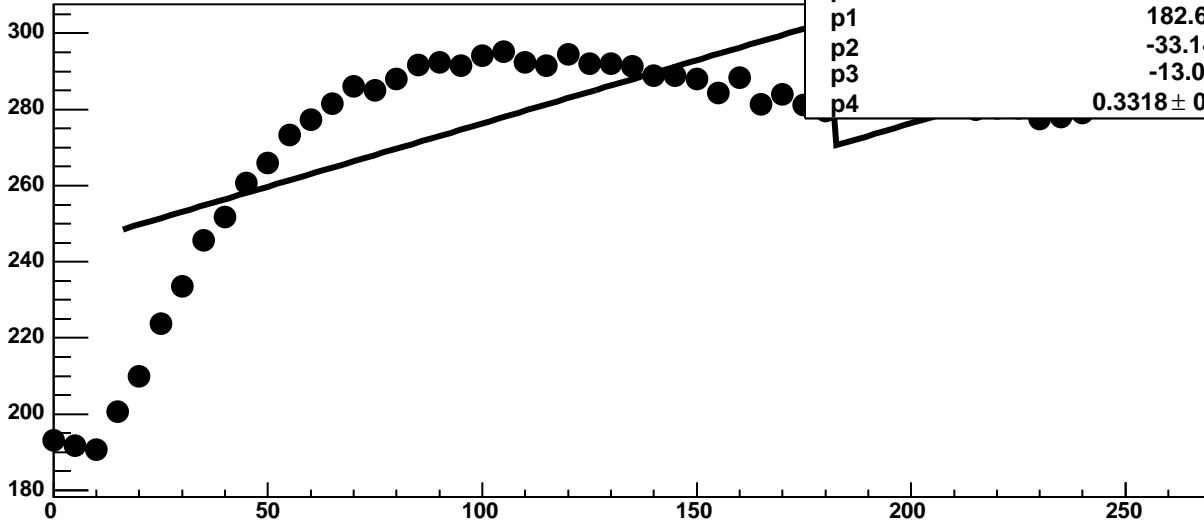
Chip 4, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

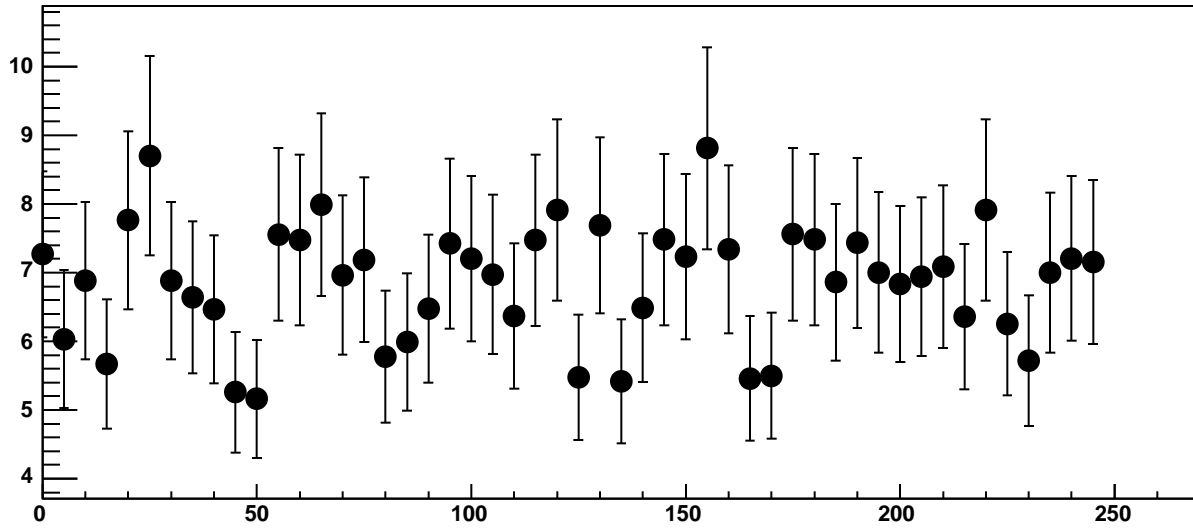


Chip 4, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

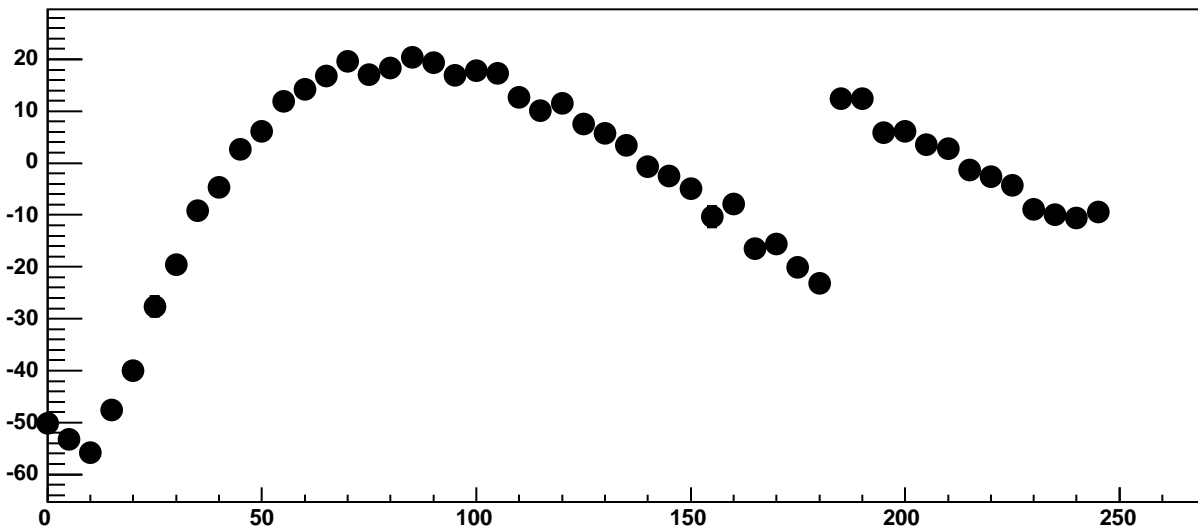


$\chi^2 / \text{ndf}$	4827 / 41
p0	$303.7 \pm 1.347$
p1	$182.6 \pm 4.854$
p2	$-33.14 \pm 3.27$
p3	$-13.01 \pm 54.3$
p4	$0.3318 \pm 0.001526$

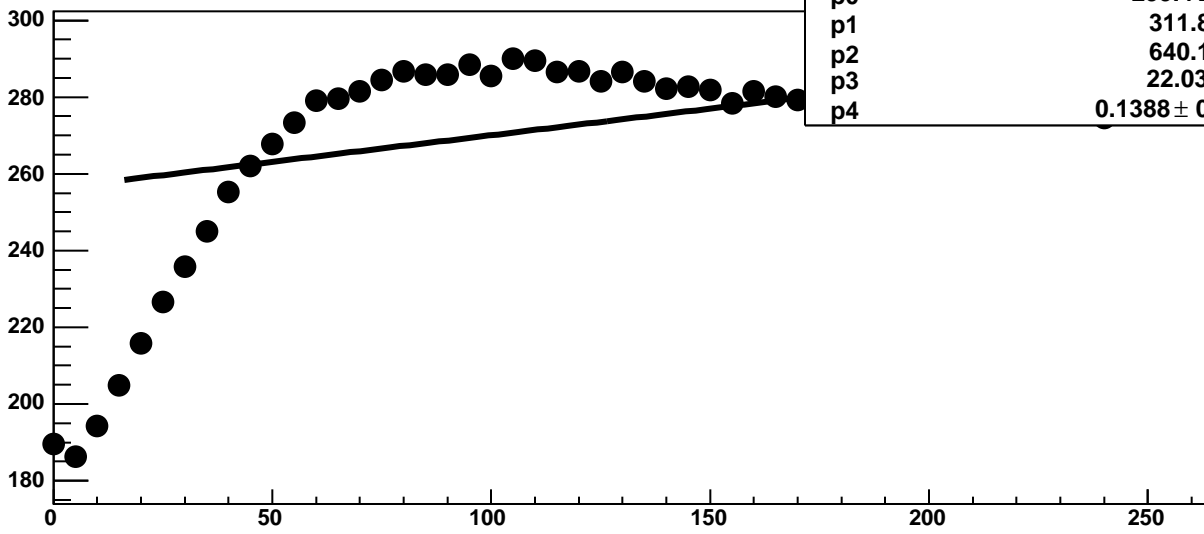
Chip 4, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

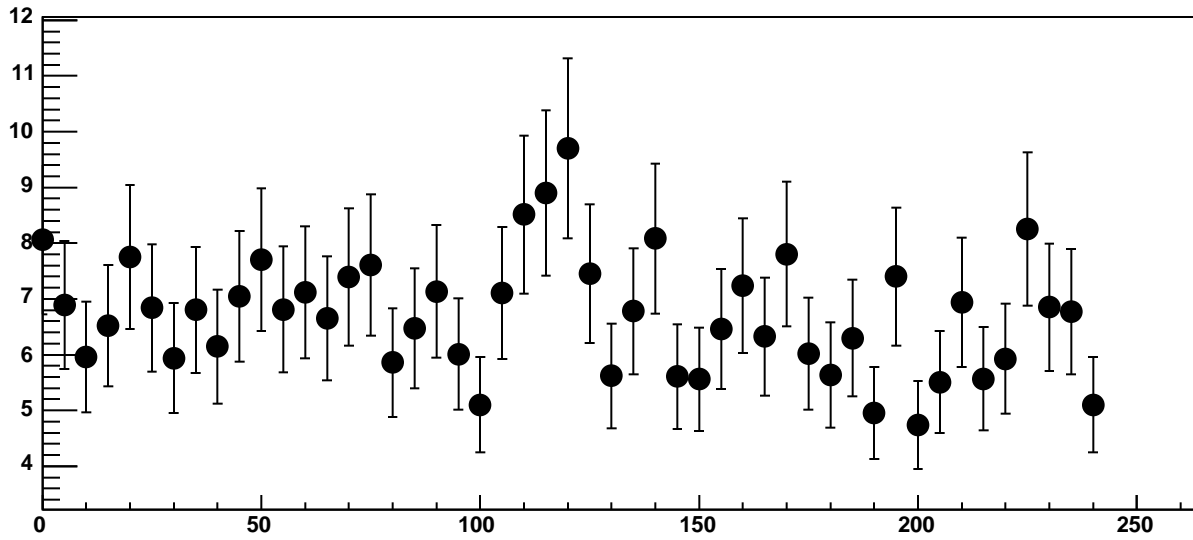


Chip 4, Channel 6, Enable 5, DAC=4000, ADC Mean vs Hold

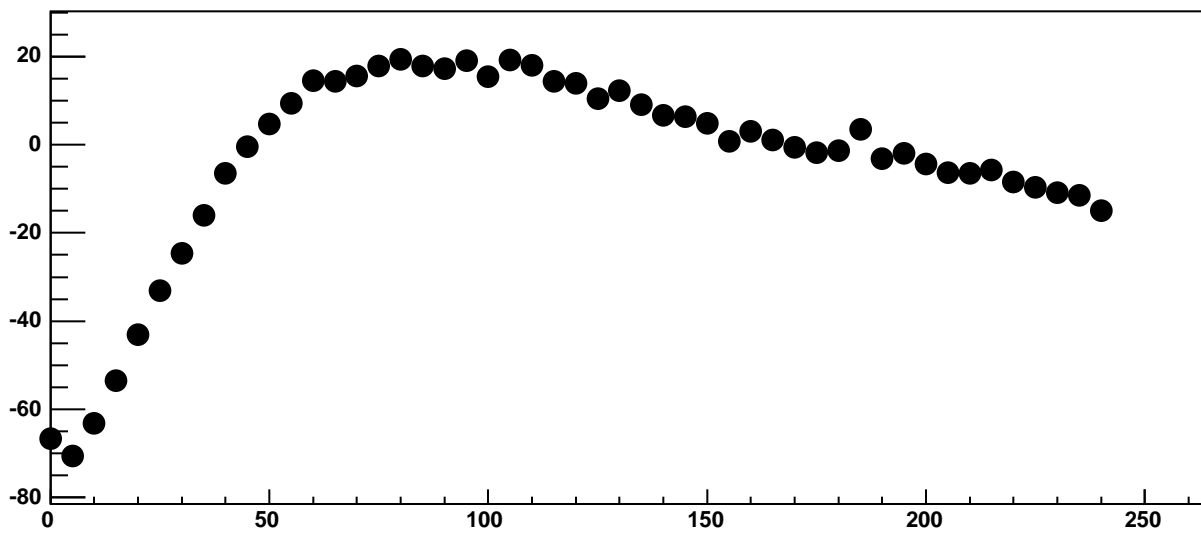


$\chi^2 / \text{ndf}$	4948 / 41
p0	$299.4 \pm 0.2184$
p1	$311.8 \pm 1.574$
p2	$640.1 \pm 84.85$
p3	$22.03 \pm 1.414$
p4	$0.1388 \pm 0.001149$

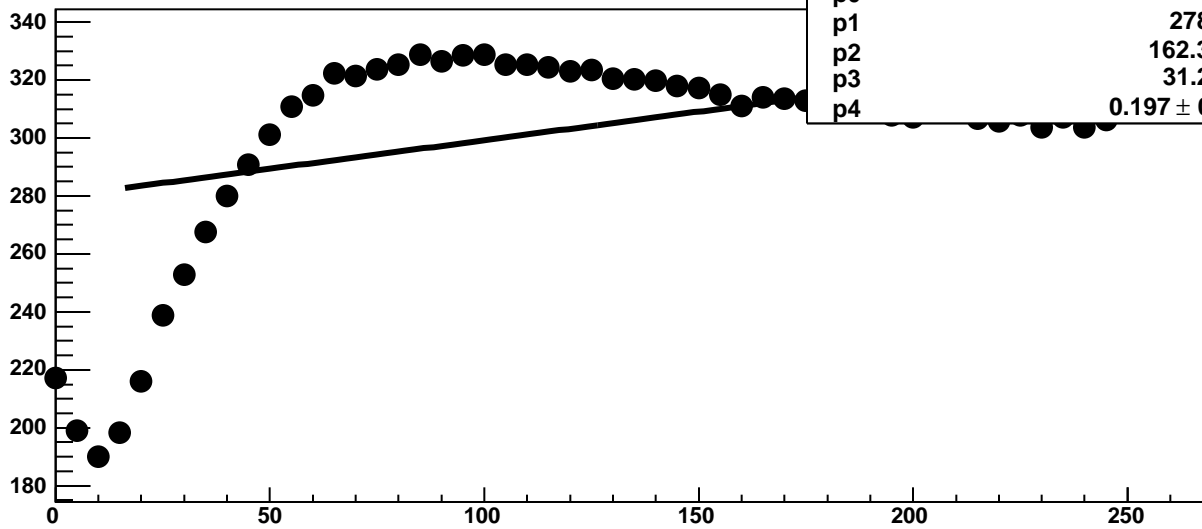
Chip 4, Channel 6, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 6, Enable 5, DAC=4000, ADC Residuals vs Hold

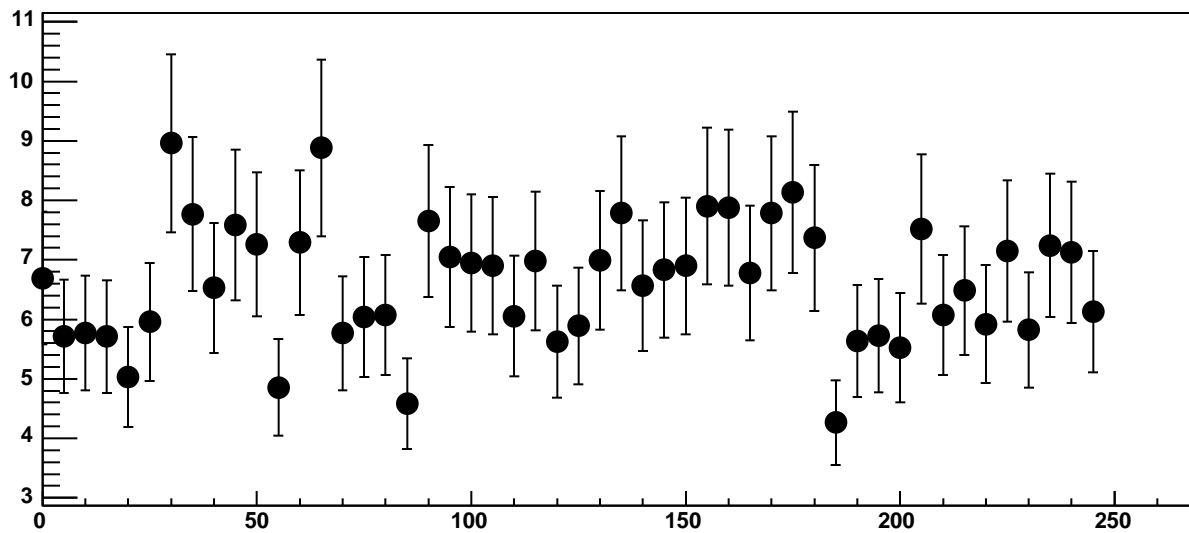


Chip 4, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

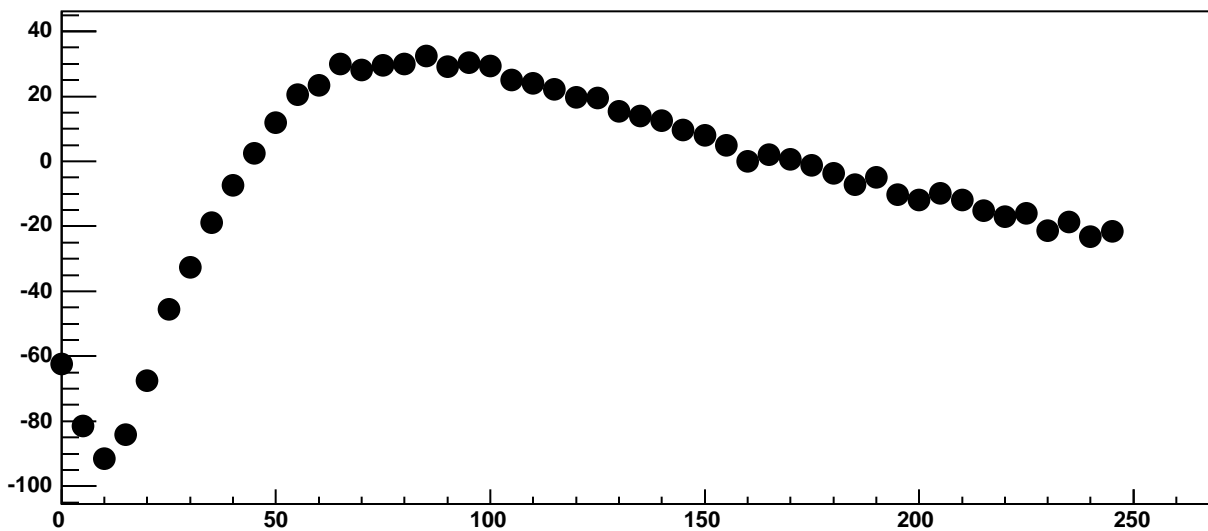


$\chi^2 / \text{ndf}$	1.587e+04 / 41
p0	334.3 ± 0.4314
p1	278 ± 0.787
p2	162.3 ± 3.183
p3	31.2 ± 0.219
p4	0.197 ± 0.003222

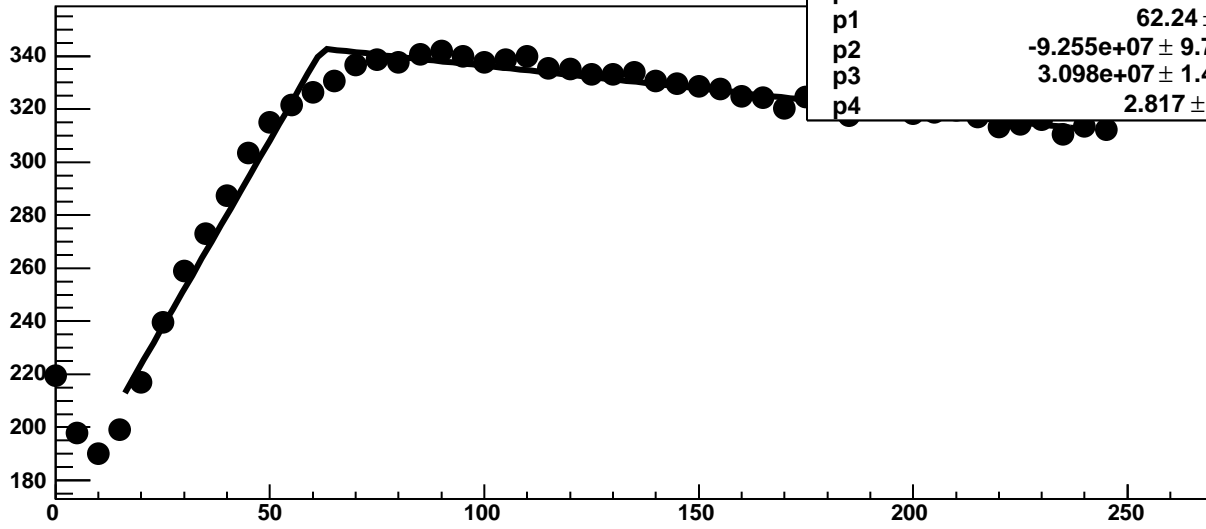
Chip 4, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

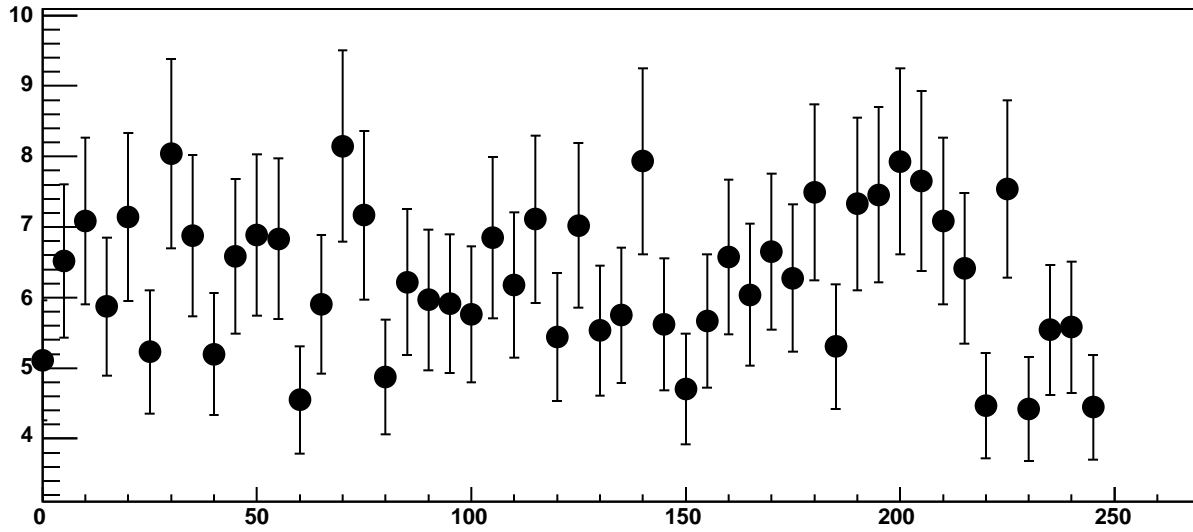


Chip 4, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold

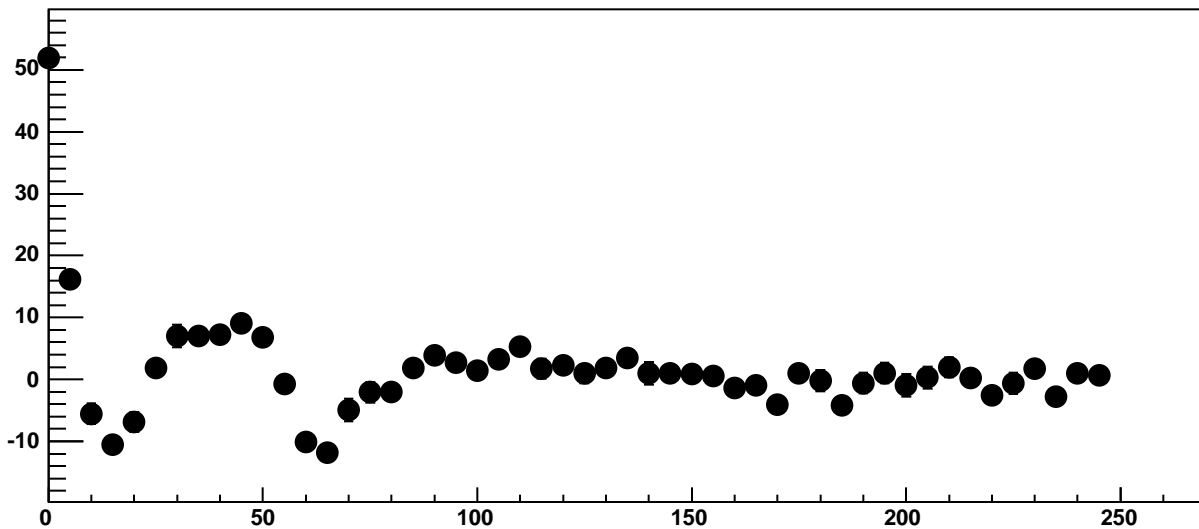


$\chi^2 / \text{ndf}$	475.9 / 41
p0	$342.8 \pm 0.4398$
p1	$62.24 \pm 0.3082$
p2	$-9.255e+07 \pm 9.724e+05$
p3	$3.098e+07 \pm 1.434e+05$
p4	$2.817 \pm 0.02891$

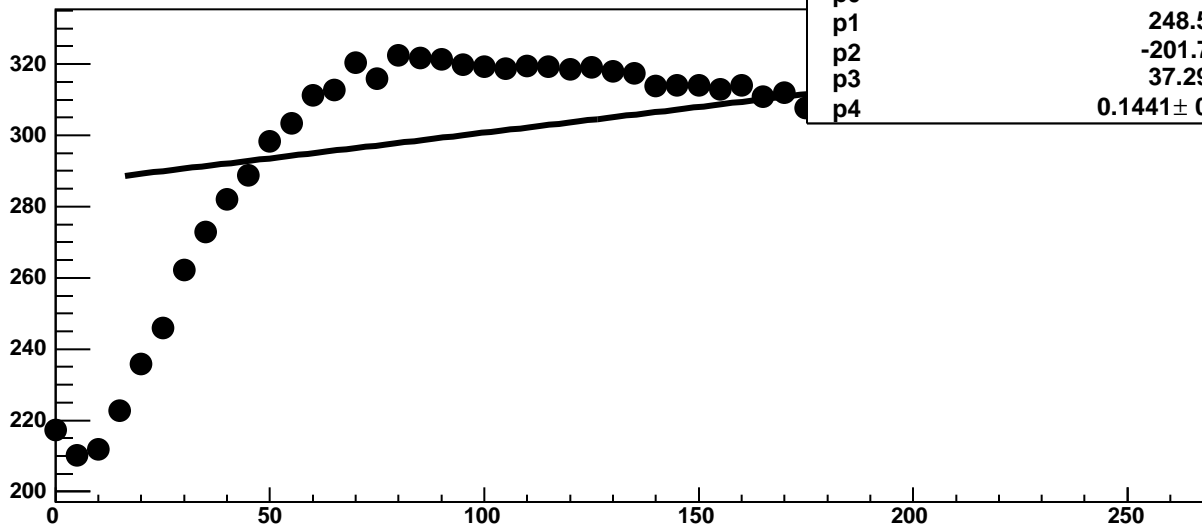
Chip 4, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

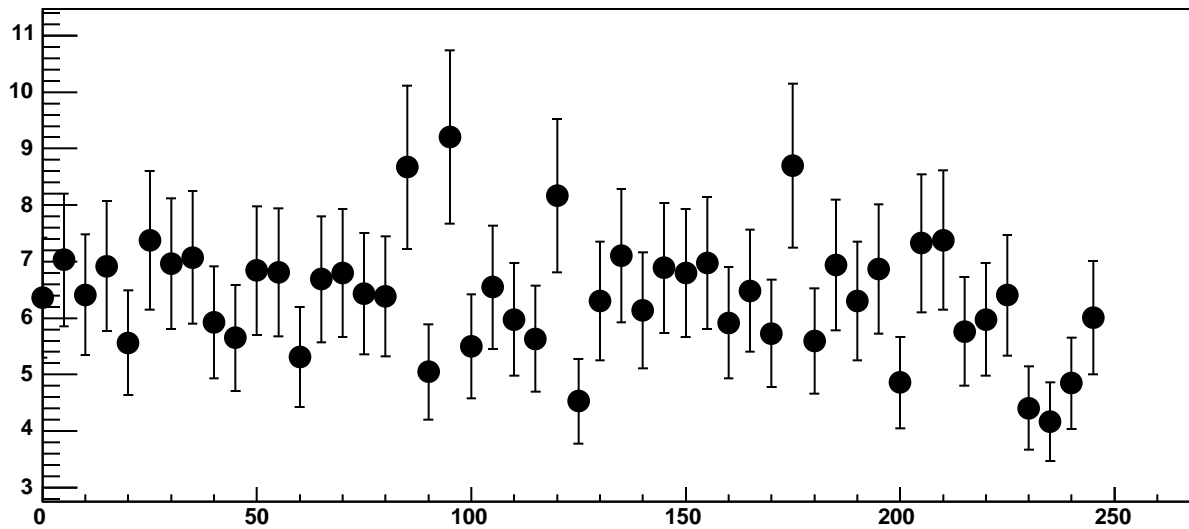


Chip 4, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold

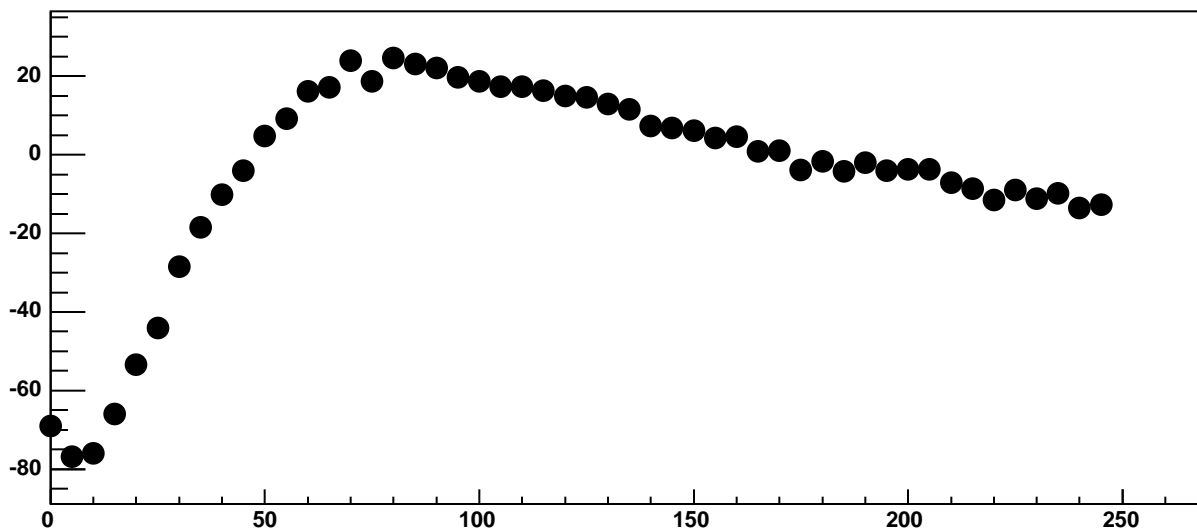


$\chi^2 / \text{ndf}$	7993 / 41
p0	$322.2 \pm 0.2068$
p1	$248.5 \pm 1.435$
p2	$-201.7 \pm 84.85$
p3	$37.29 \pm 1.414$
p4	$0.1441 \pm 0.001558$

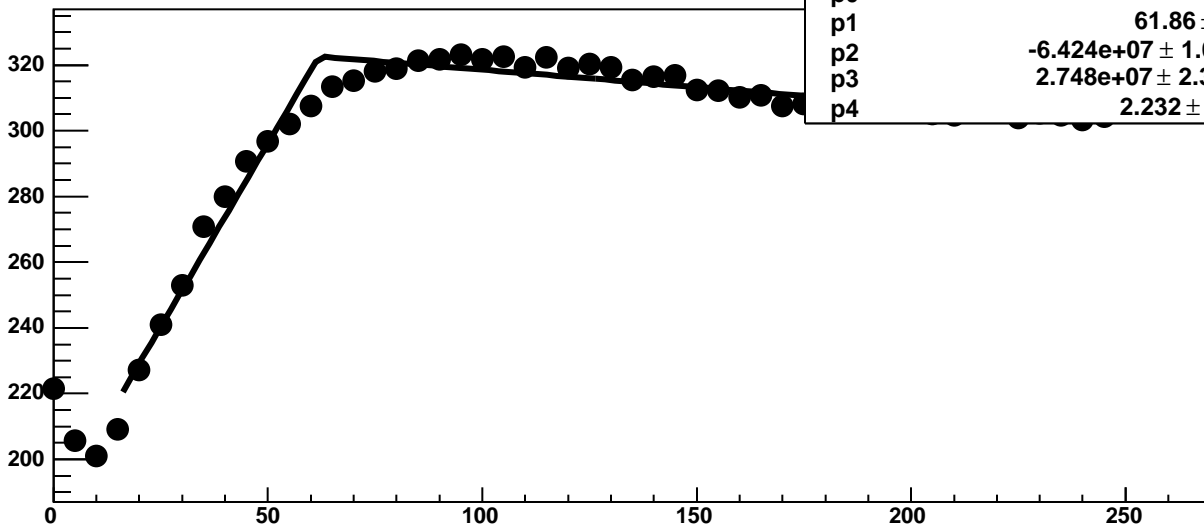
Chip 4, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold

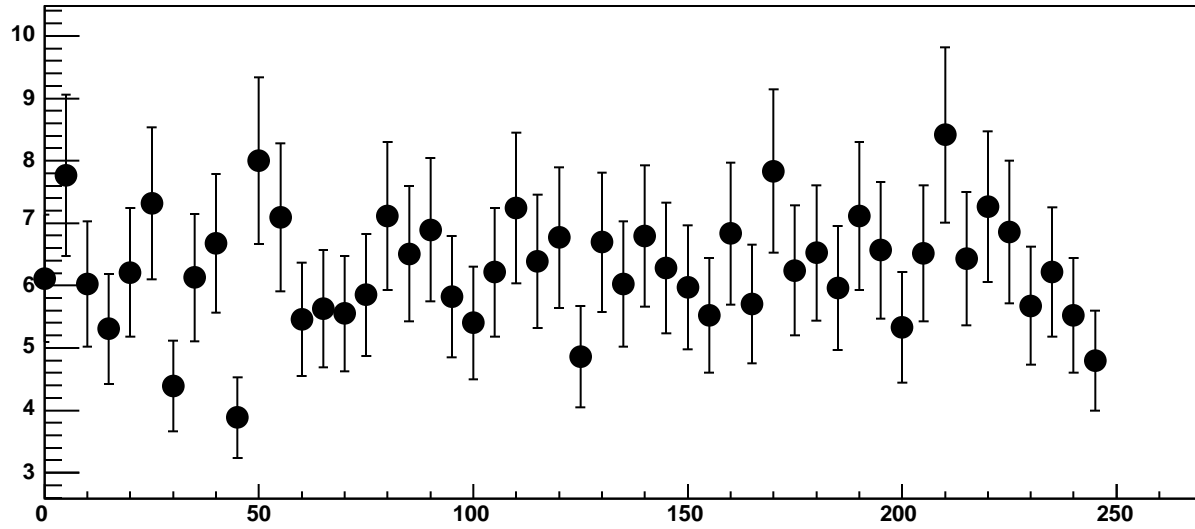


Chip 4, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

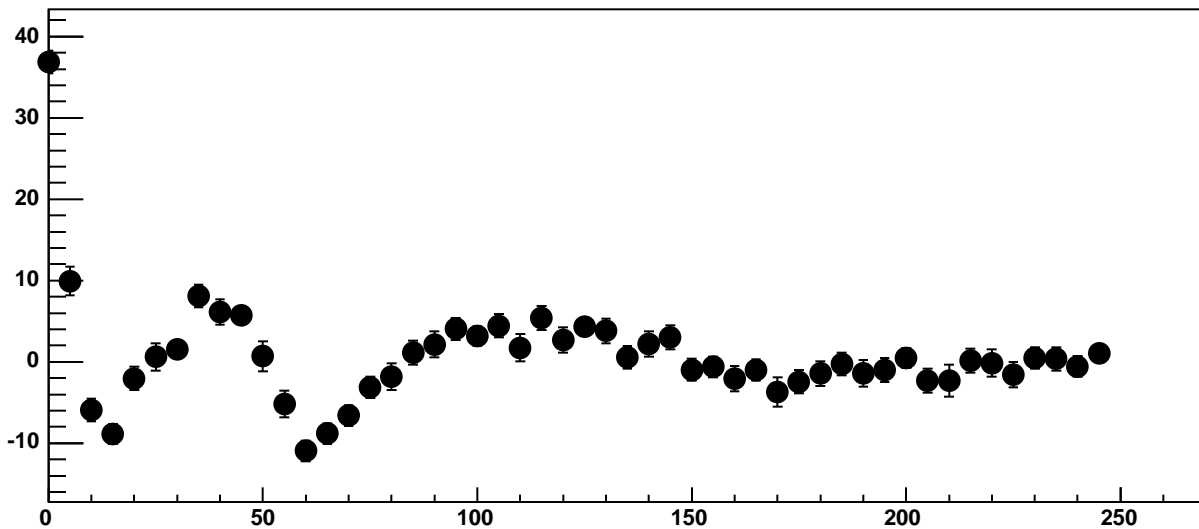


$\chi^2 / \text{ndf}$	405.6 / 41
p0	$322.6 \pm 0.4492$
p1	$61.86 \pm 0.4073$
p2	$-6.424\text{e}+07 \pm 1.018\text{e}+06$
p3	$2.748\text{e}+07 \pm 2.327\text{e}+05$
p4	$2.232 \pm 0.02949$

Chip 4, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold

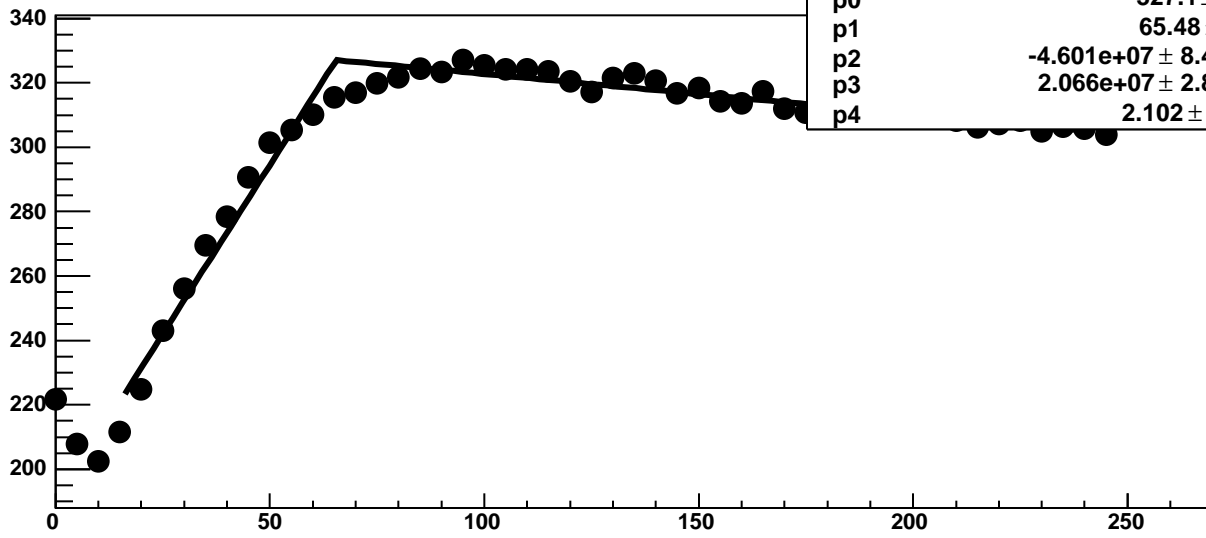


Chip 4, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold



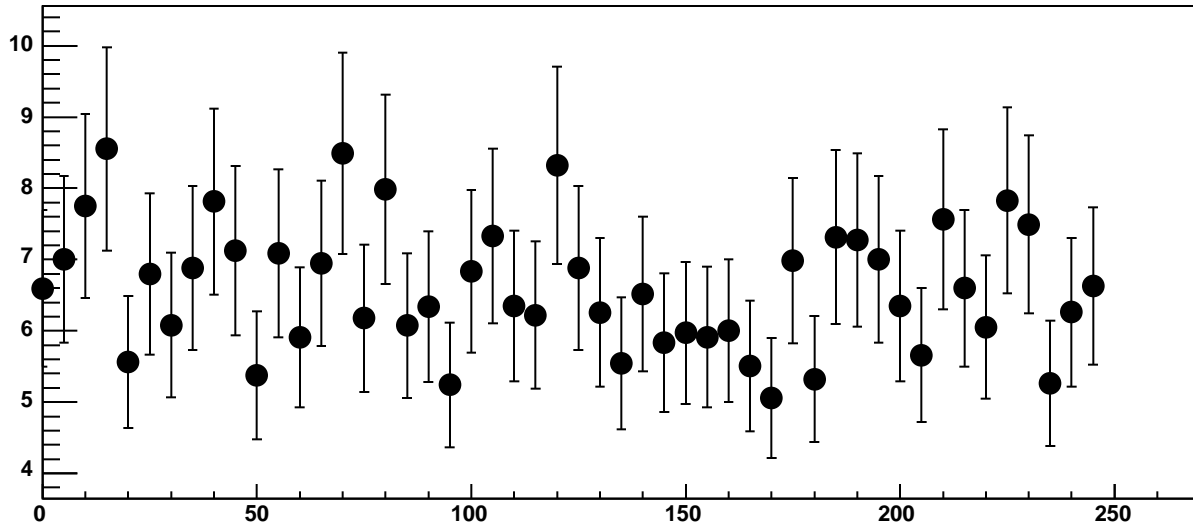


Chip 4, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

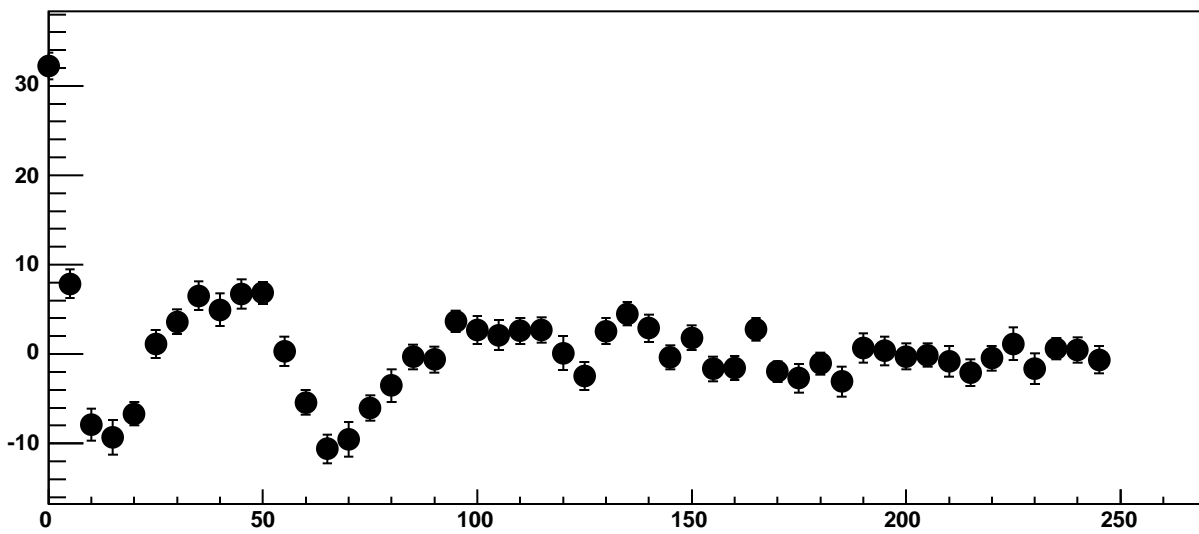


$\chi^2 / \text{ndf}$	301.5 / 41
p0	$327.1 \pm 0.4904$
p1	$65.48 \pm 0.4501$
p2	$-4.601\text{e}+07 \pm 8.497\text{e}+05$
p3	$2.066\text{e}+07 \pm 2.873\text{e}+05$
p4	$2.102 \pm 0.02913$

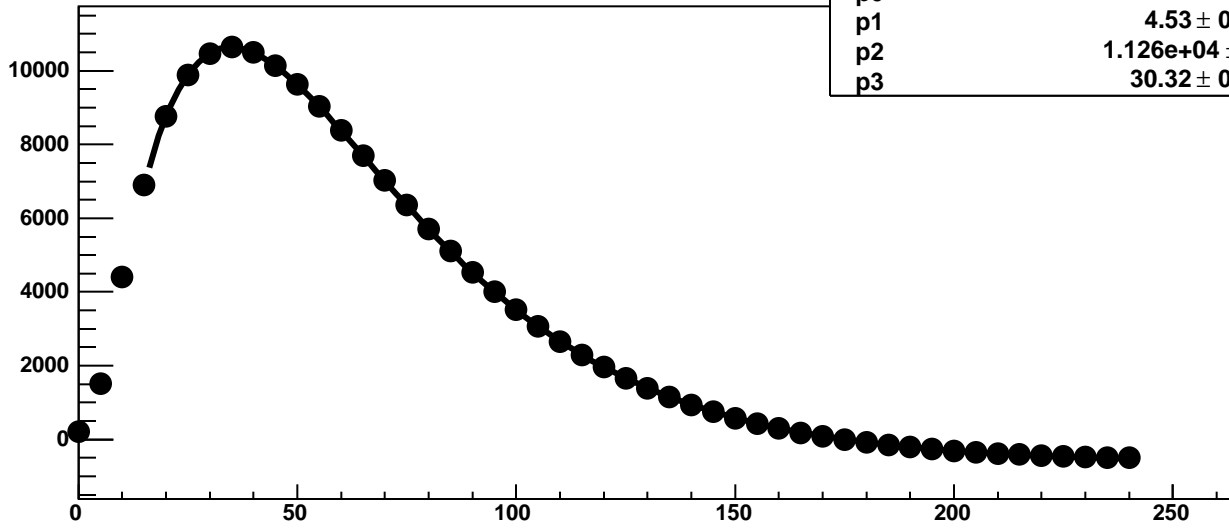
Chip 4, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

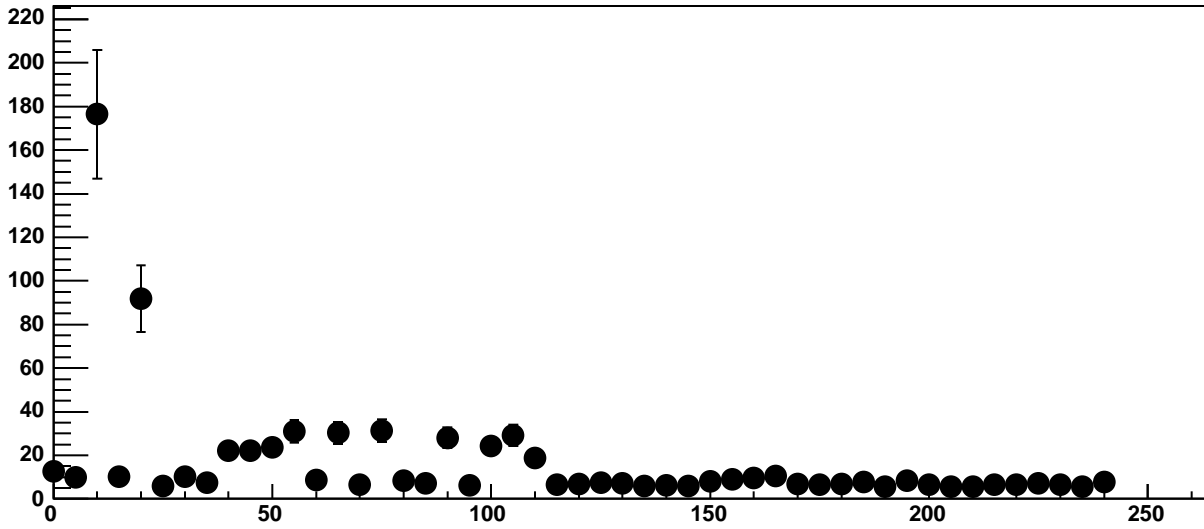


Chip 4, Channel 7, Enable 5!, DAC=4000, ADC Mean vs Hold

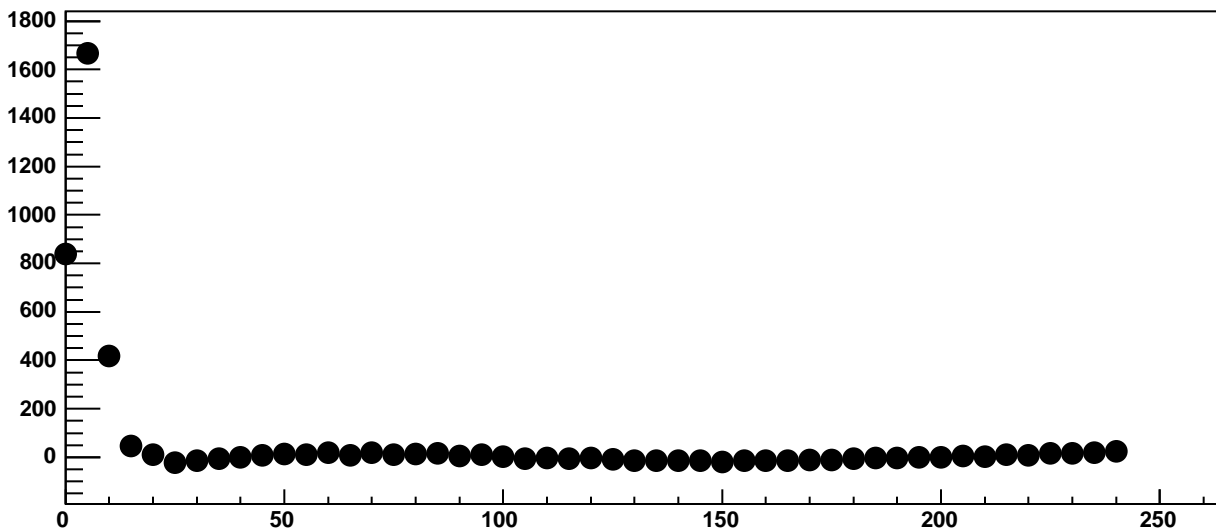


$\chi^2 / \text{ndf}$	2605 / 42
p0	-618.1 ± 0.4814
p1	4.53 ± 0.006014
p2	1.126e+04 ± 0.9352
p3	30.32 ± 0.004253

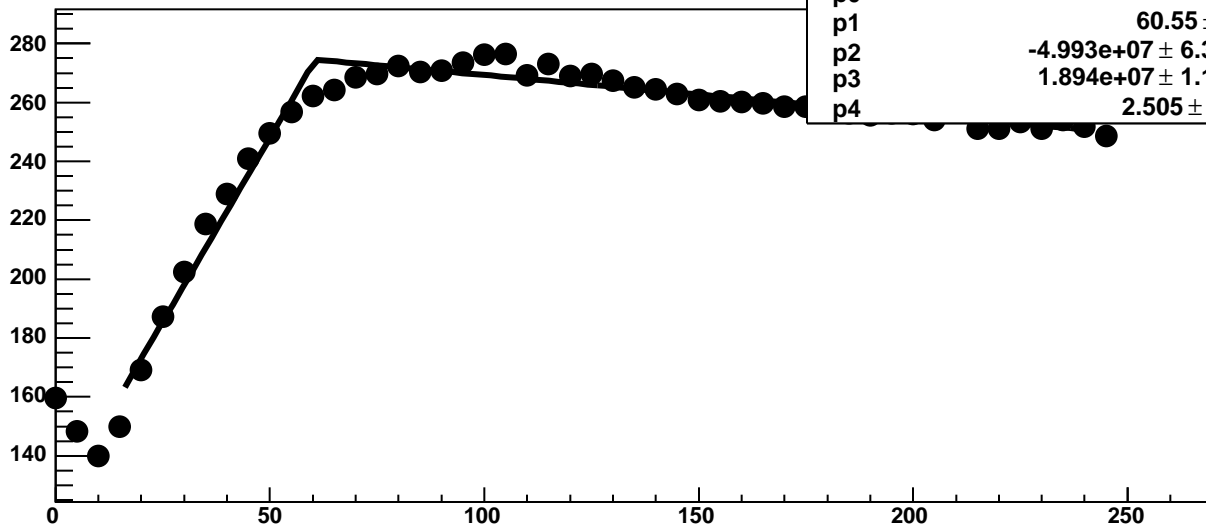
Chip 4, Channel 7, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 7, Enable 5!, DAC=4000, ADC Residuals vs Hold

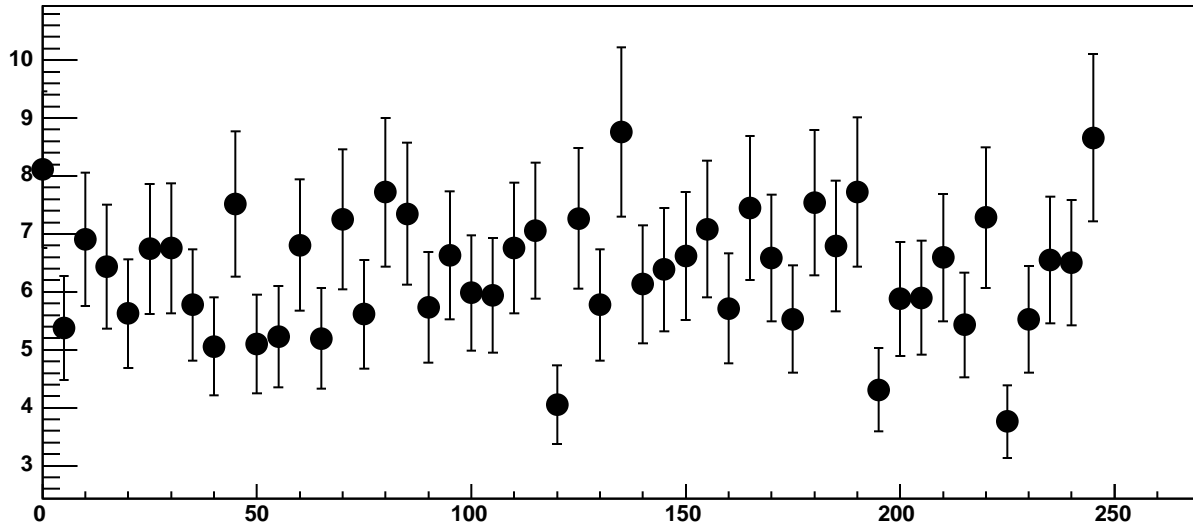


Chip 4, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

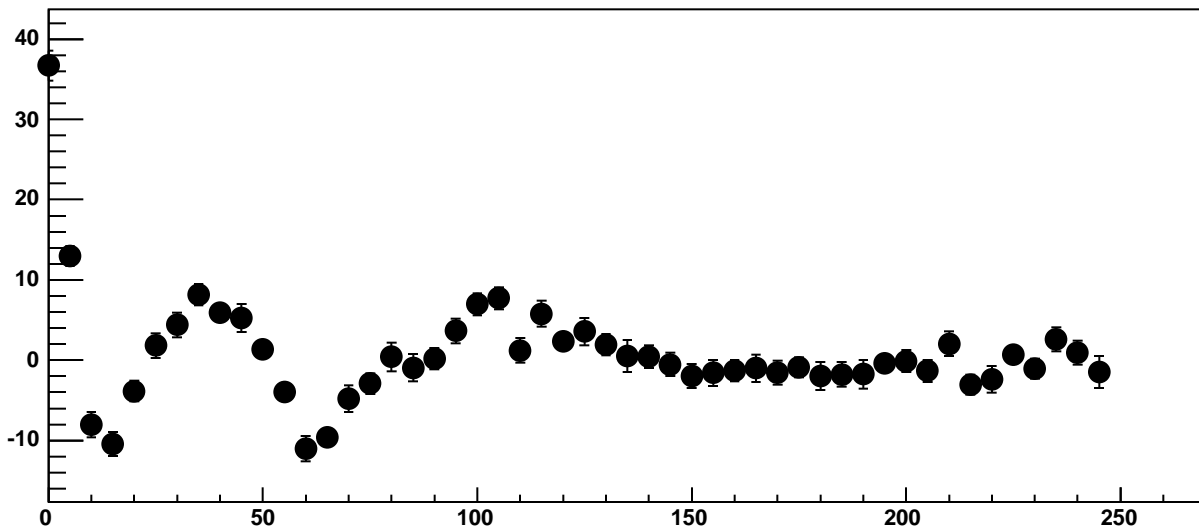


$\chi^2 / \text{ndf}$	397 / 41
p0	$274.5 \pm 0.4531$
p1	$60.55 \pm 0.3562$
p2	$-4.993\text{e}+07 \pm 6.359\text{e}+05$
p3	$1.894\text{e}+07 \pm 1.149\text{e}+05$
p4	$2.505 \pm 0.03069$

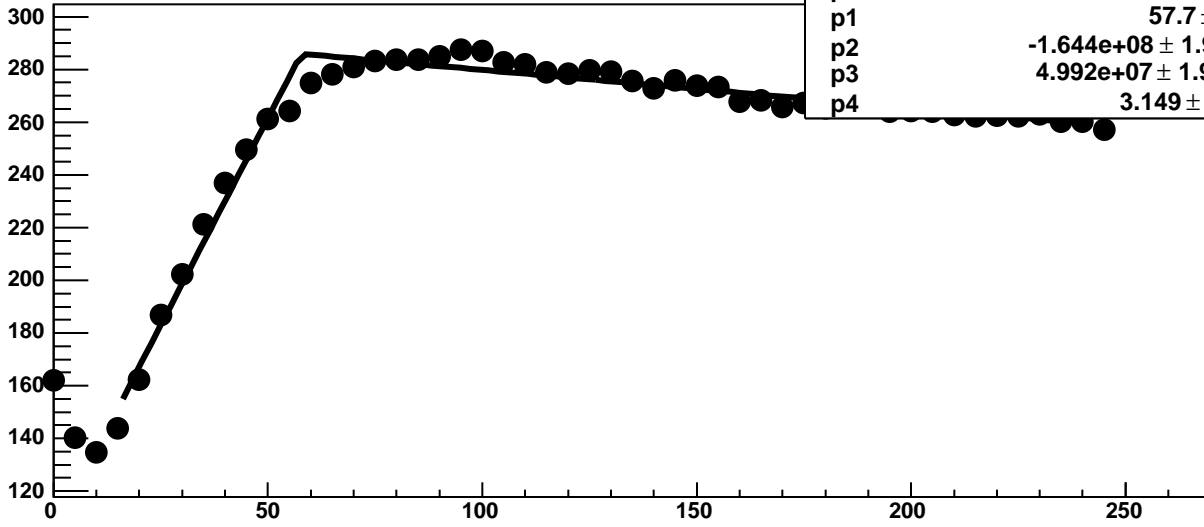
Chip 4, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

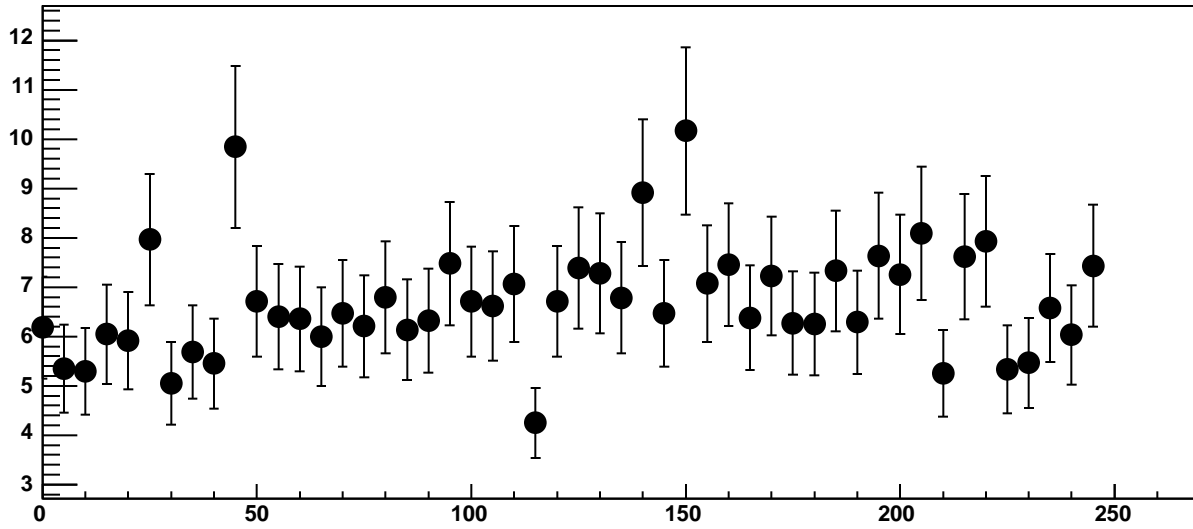


Chip 4, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

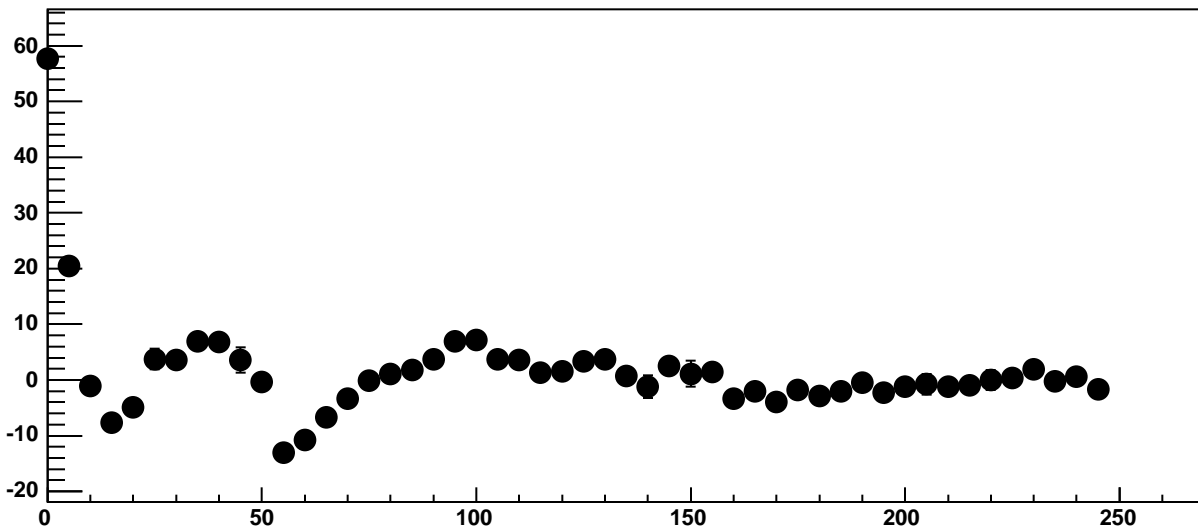


$\chi^2 / \text{ndf}$	378 / 41
p0	$285.9 \pm 0.4608$
p1	$57.7 \pm 0.3427$
p2	$-1.644\text{e}+08 \pm 1.989\text{e}+06$
p3	$4.992\text{e}+07 \pm 1.952\text{e}+05$
p4	$3.149 \pm 0.03809$

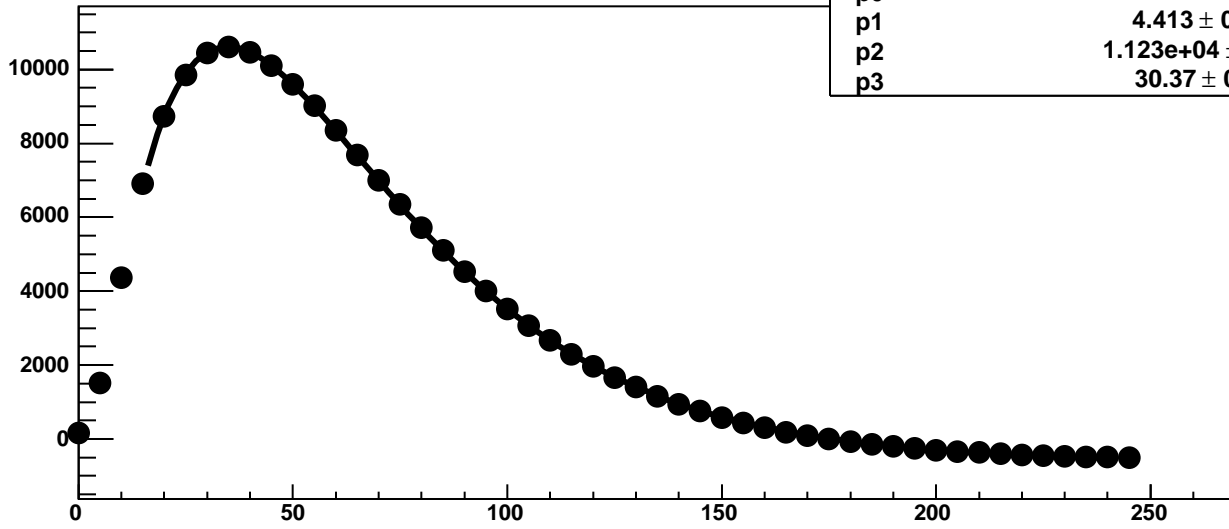
Chip 4, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

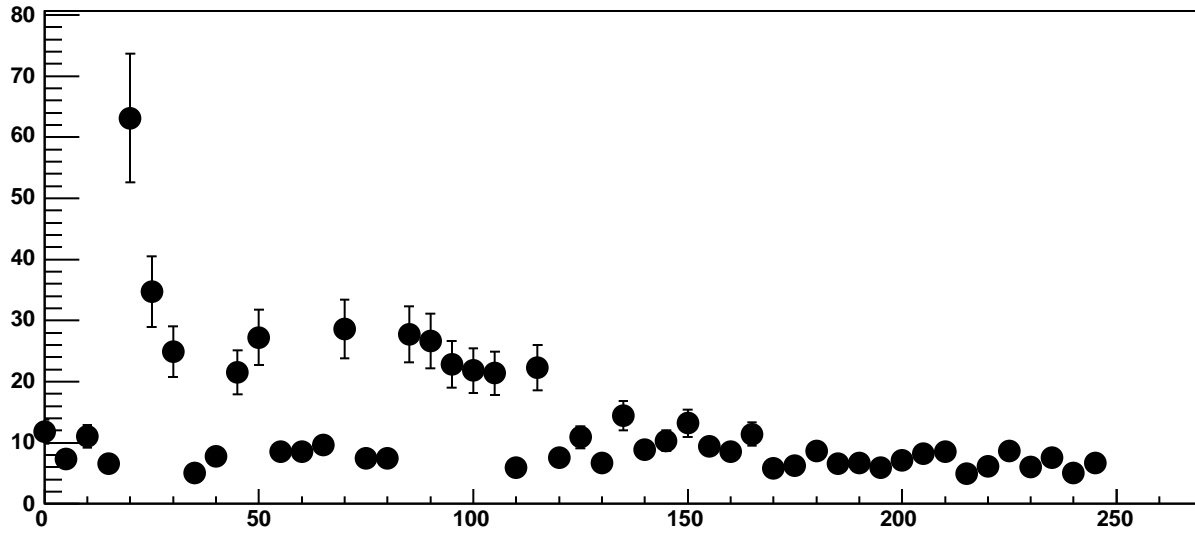


Chip 4, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold

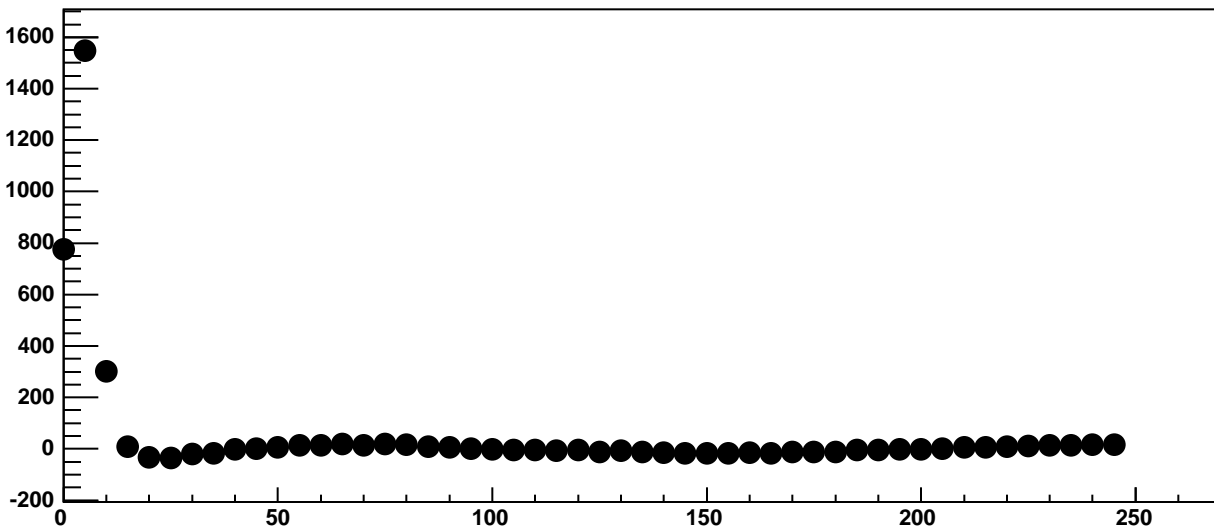


$\chi^2 / \text{ndf}$	1781 / 42
p0	-611 ± 0.4831
p1	4.413 ± 0.004362
p2	1.123e+04 ± 0.8727
p3	30.37 ± 0.004151

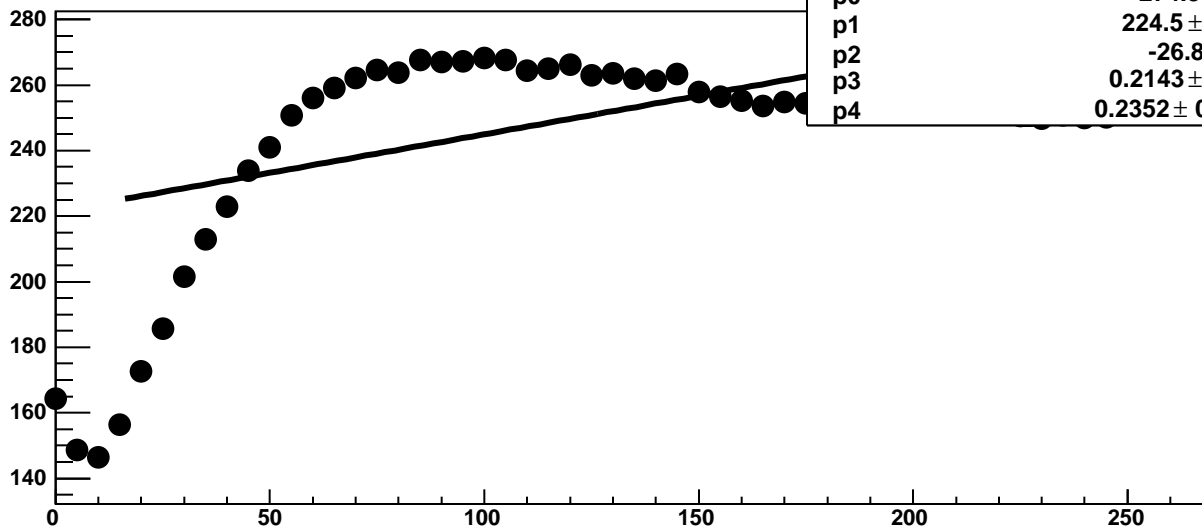
Chip 4, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold

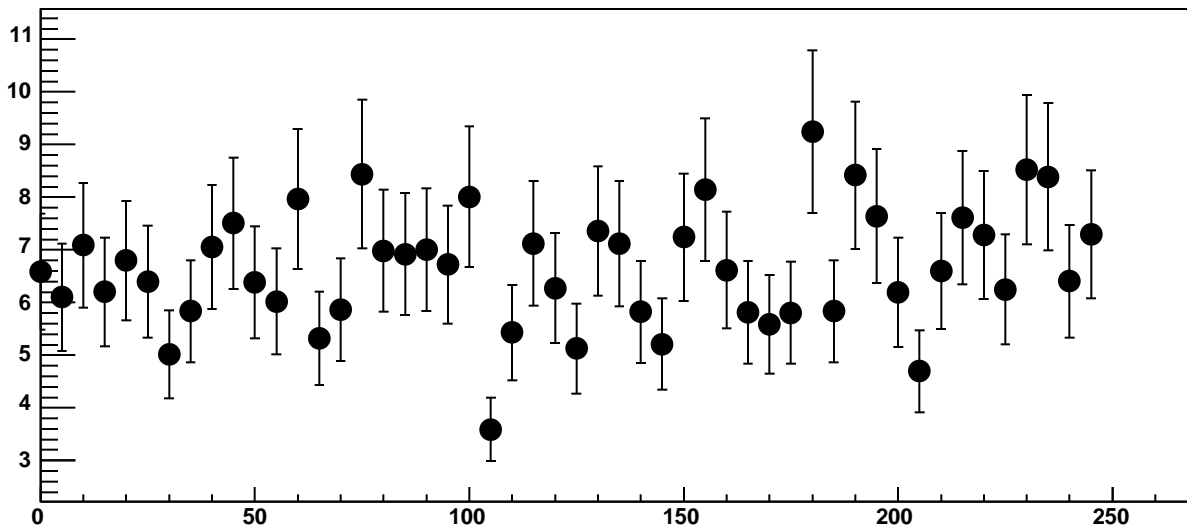


Chip 4, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

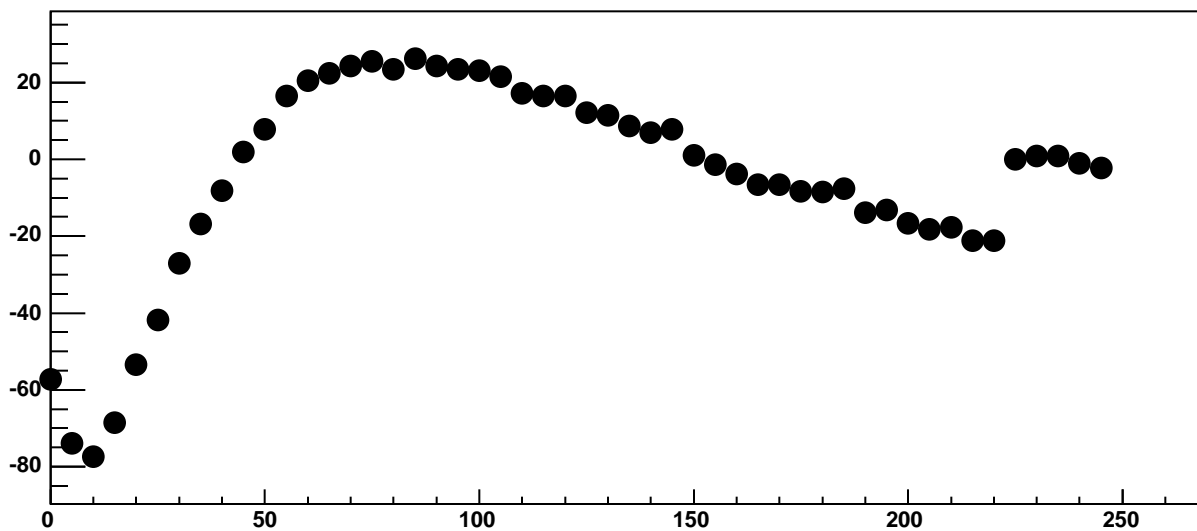


$\chi^2 / \text{ndf}$	9752 / 41
p0	$274.3 \pm 0.4641$
p1	$224.5 \pm 0.03905$
p2	$-26.82 \pm 1.12$
p3	$0.2143 \pm 0.05263$
p4	$0.2352 \pm 0.003729$

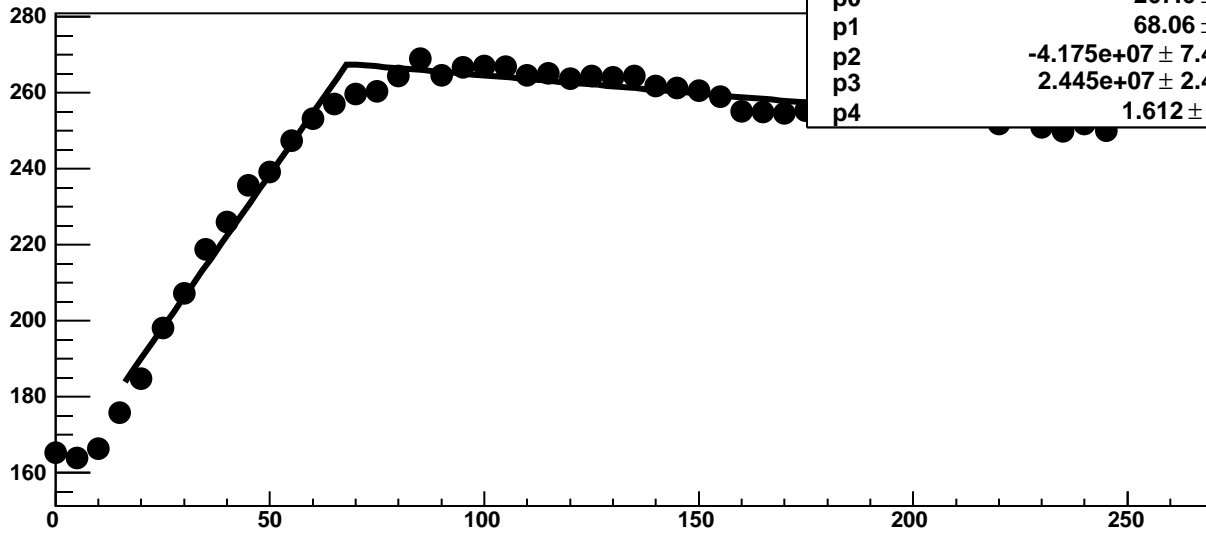
Chip 4, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

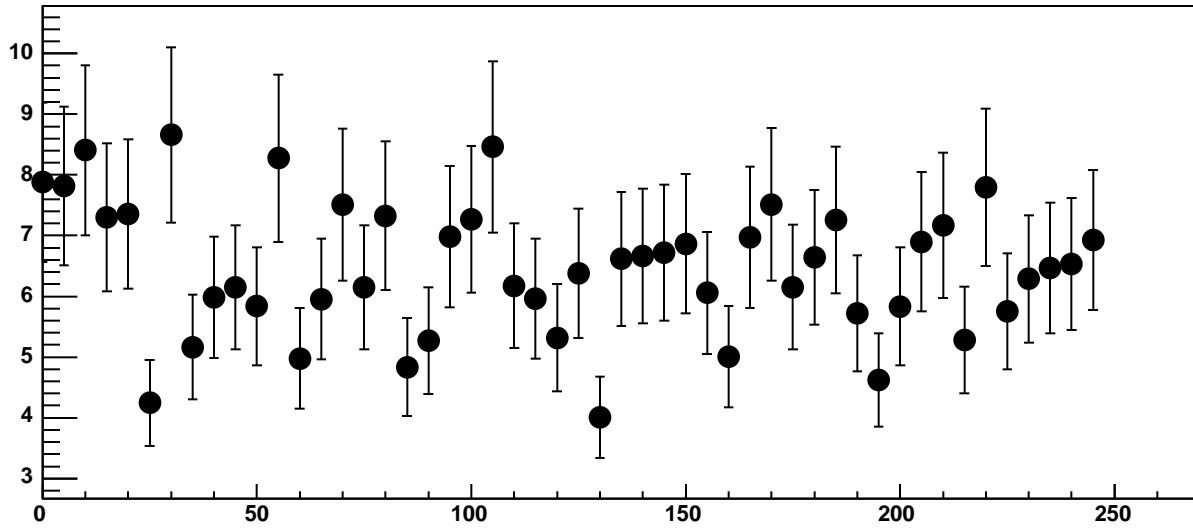


Chip 4, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

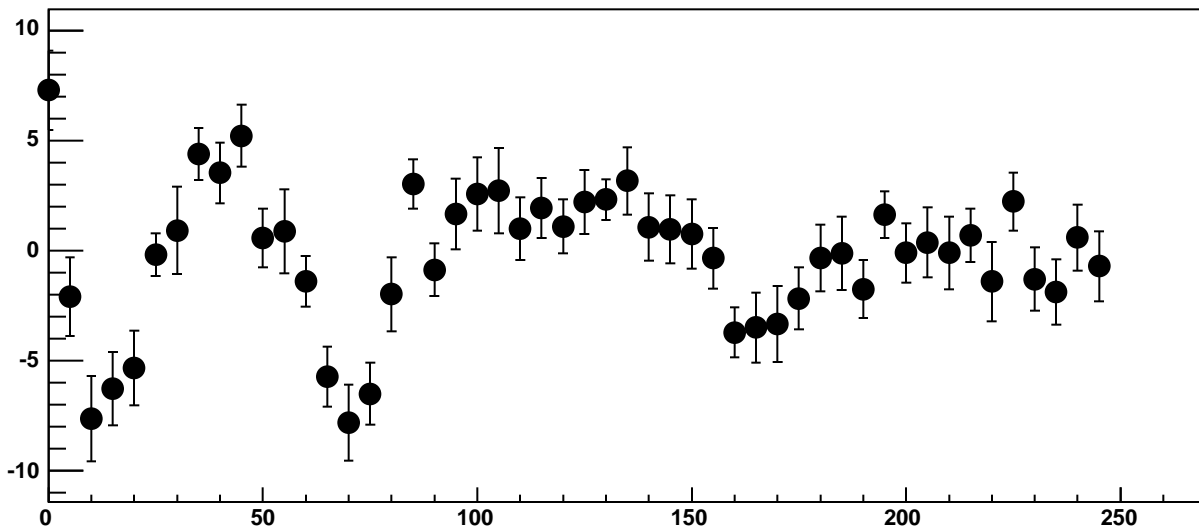


$\chi^2 / \text{ndf}$	183.6 / 41
p0	$267.6 \pm 0.4516$
p1	$68.06 \pm 0.5596$
p2	$-4.175\text{e}+07 \pm 7.444\text{e}+05$
p3	$2.445\text{e}+07 \pm 2.464\text{e}+05$
p4	$1.612 \pm 0.02653$

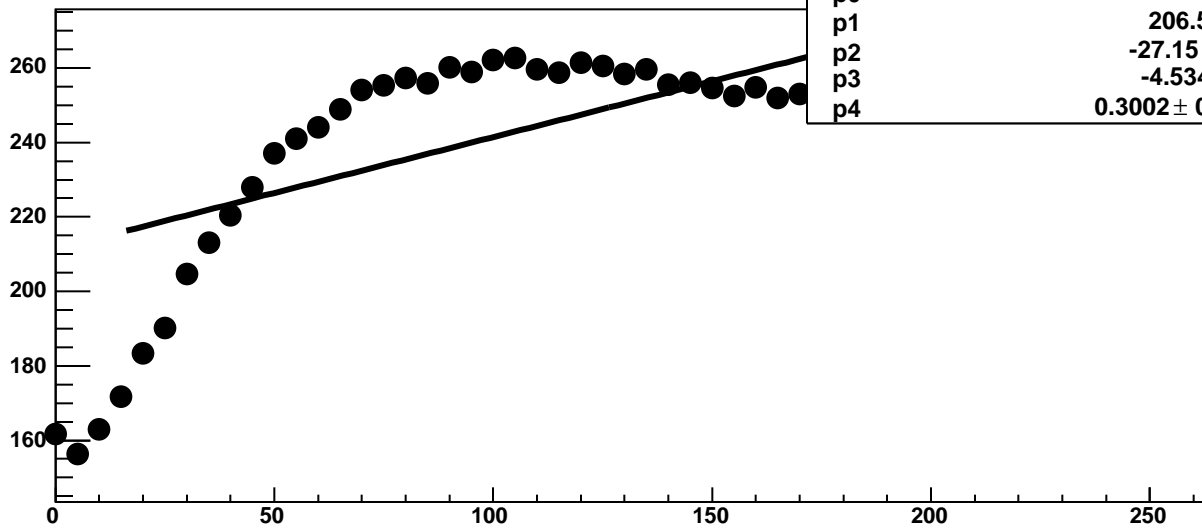
Chip 4, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

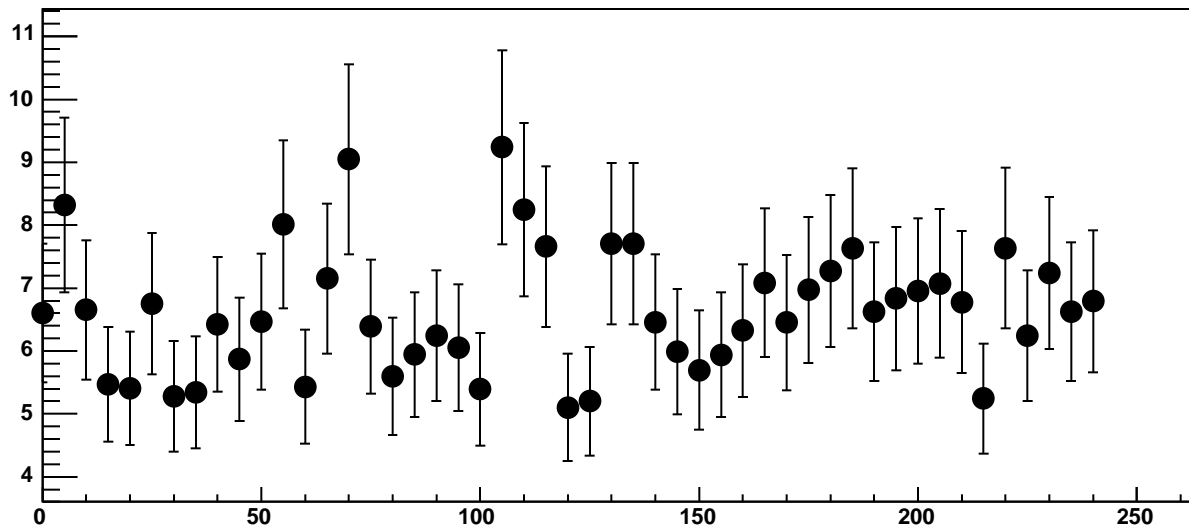


Chip 4, Channel 8, Enable 5, DAC=4000, ADC Mean vs Hold

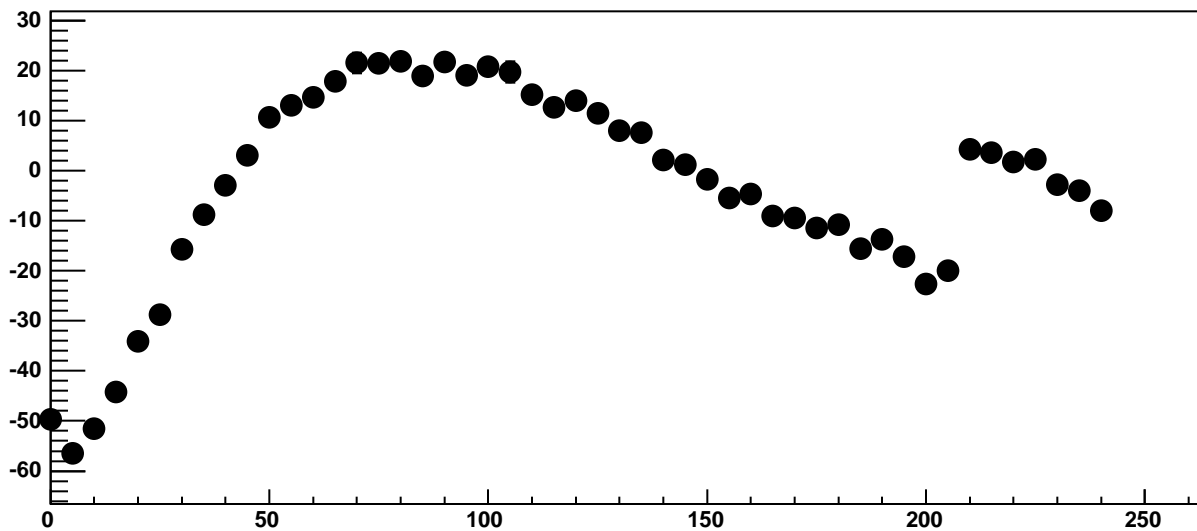


$\chi^2 / \text{ndf}$	5802 / 41
p0	$273.4 \pm 0.5718$
p1	$206.5 \pm 1.396$
p2	$-27.15 \pm 0.6697$
p3	$-4.534 \pm 1.495$
p4	$0.3002 \pm 0.003788$

Chip 4, Channel 8, Enable 5, DAC=4000, ADC Noise vs Hold

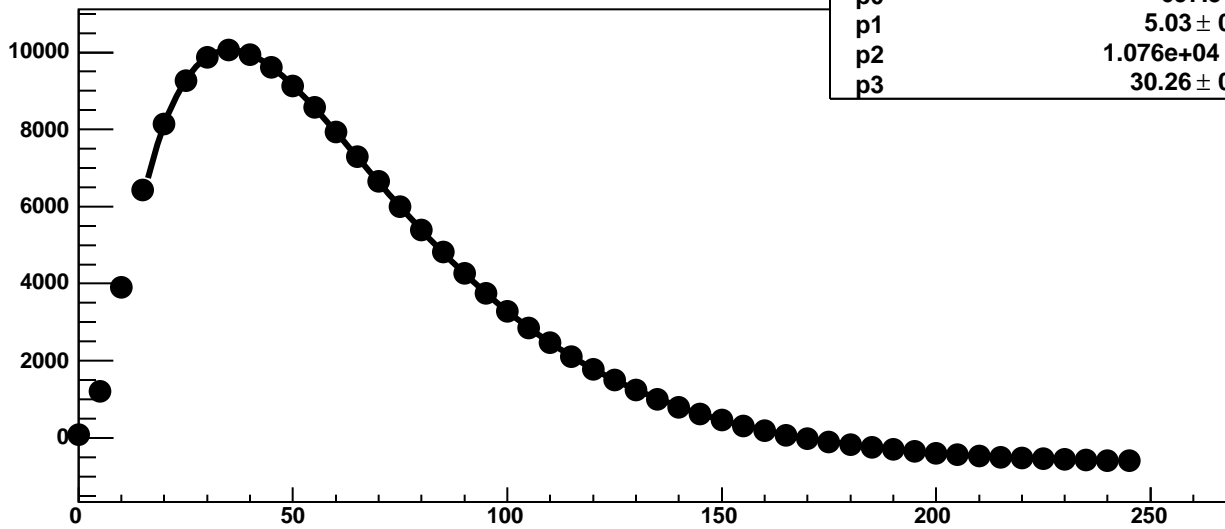


Chip 4, Channel 8, Enable 5, DAC=4000, ADC Residuals vs Hold



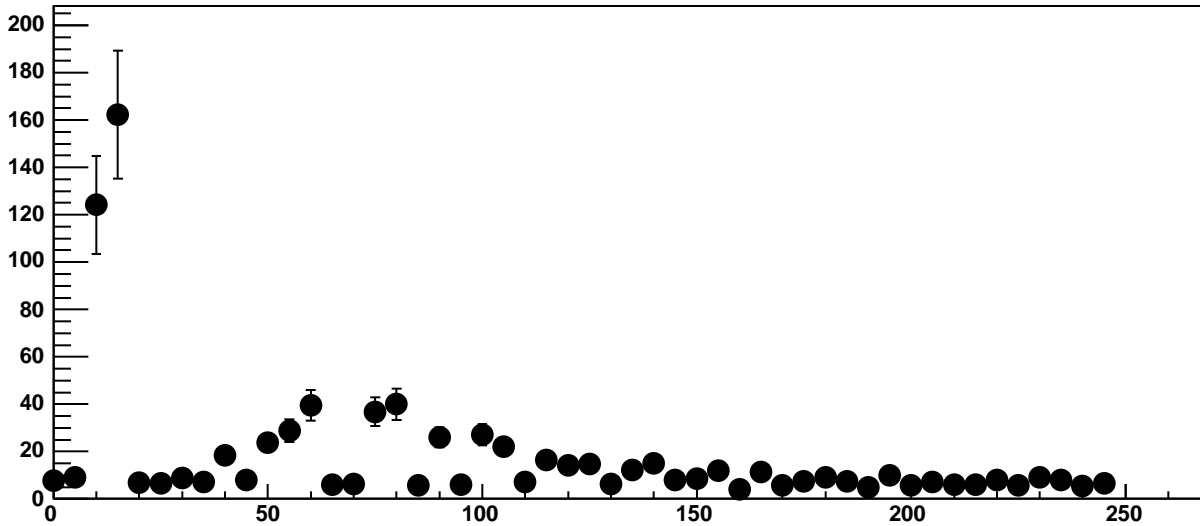


Chip 4, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold

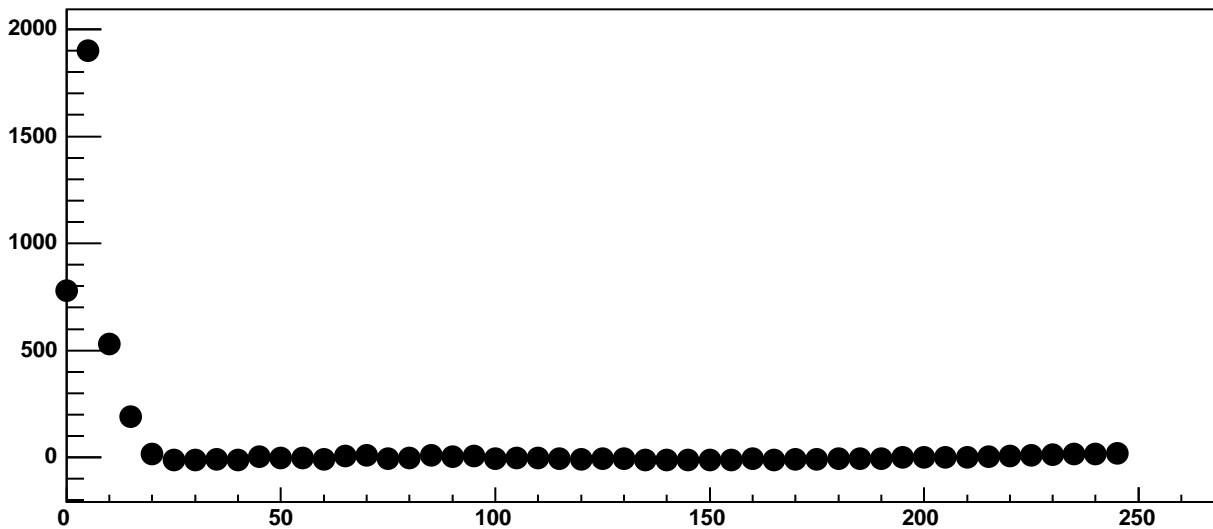


$\chi^2 / \text{ndf}$	1091 / 42
p0	-697.5 ± 0.4982
p1	5.03 ± 0.007276
p2	1.076e+04 ± 0.8499
p3	30.26 ± 0.005108

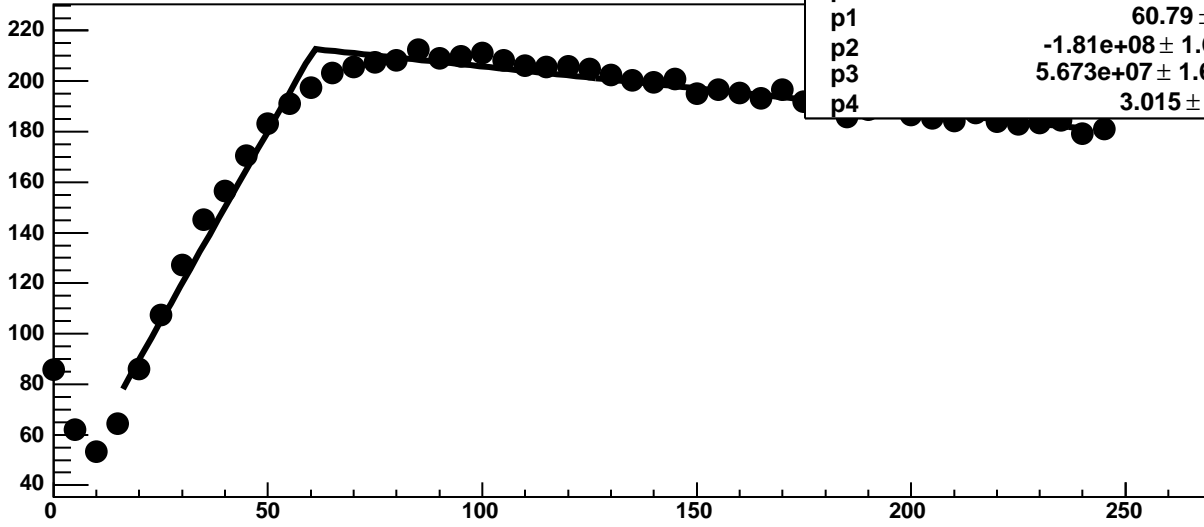
Chip 4, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold



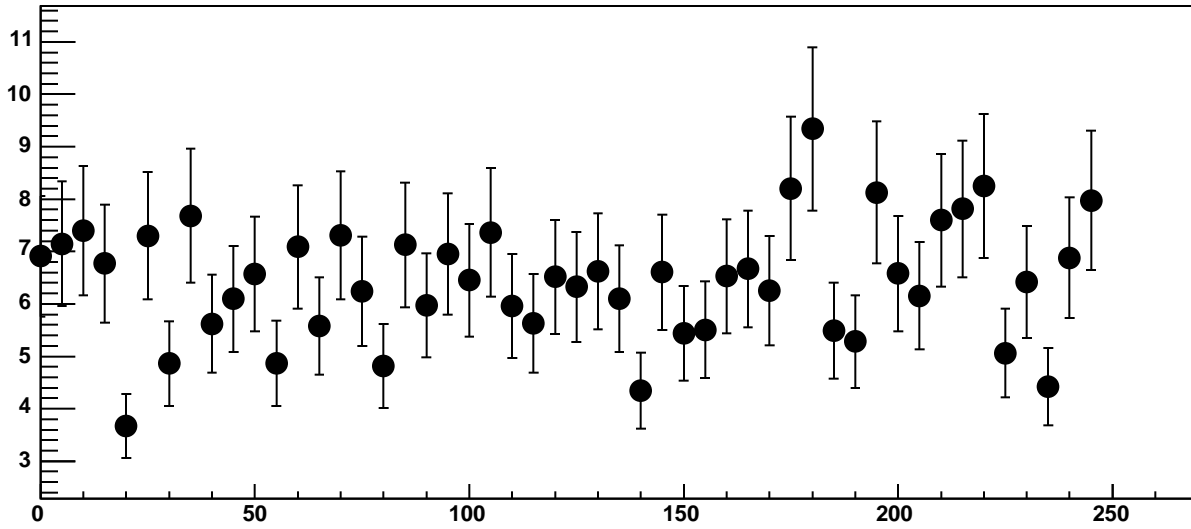
Chip 4, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold



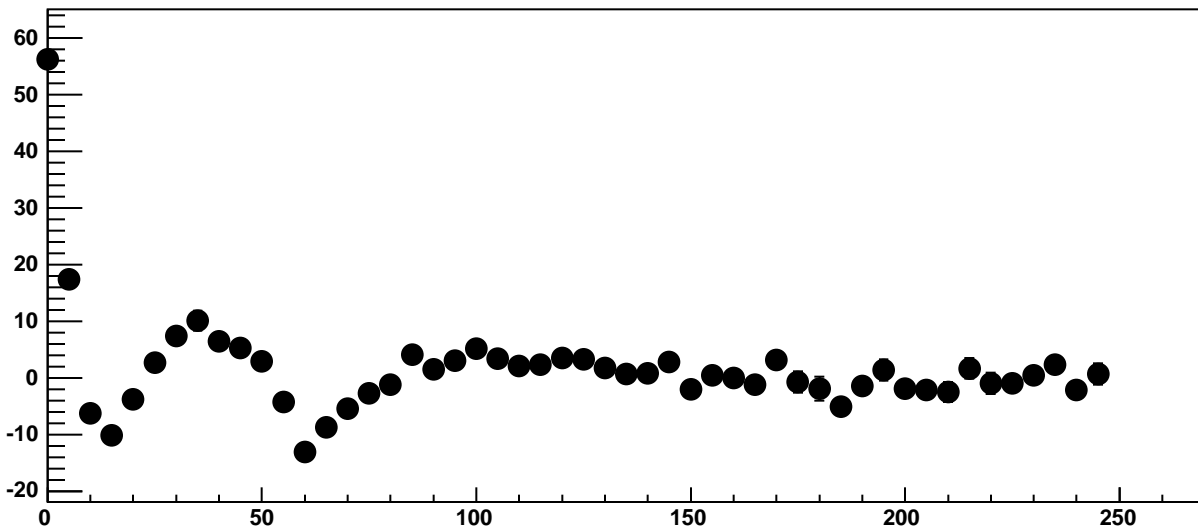
Chip 4, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold



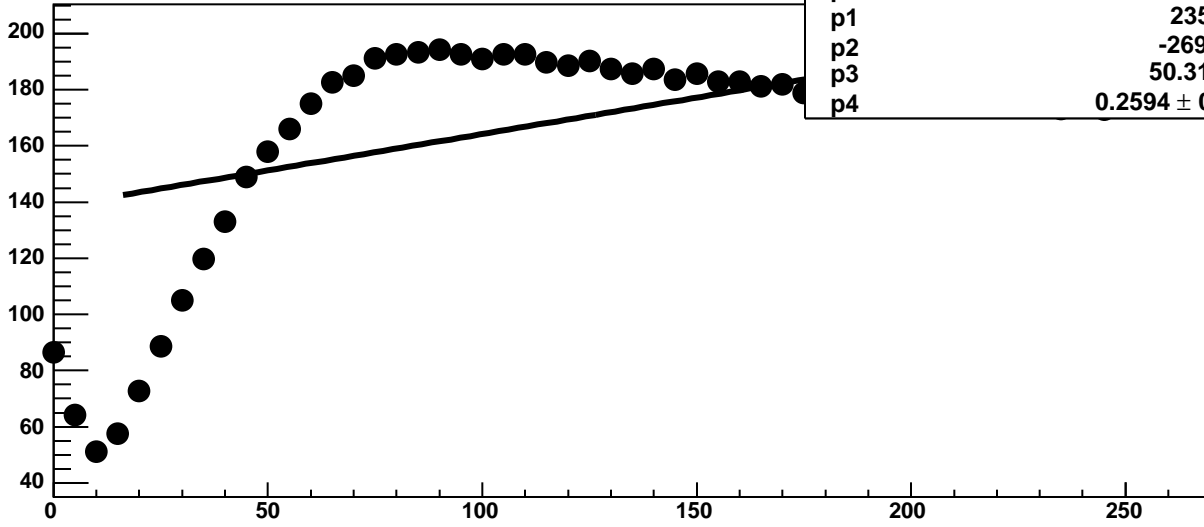
Chip 4, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

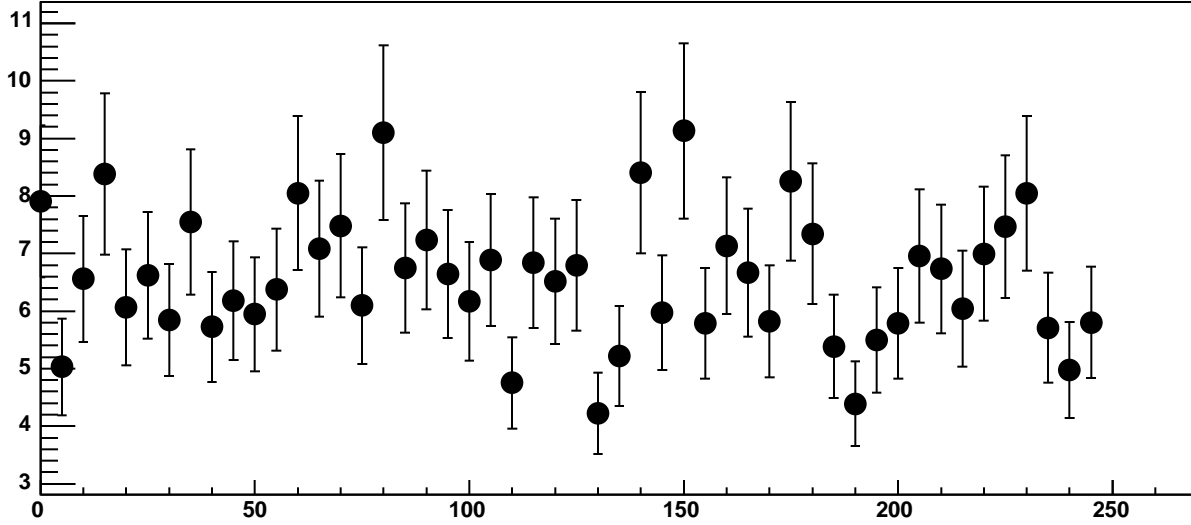


Chip 4, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

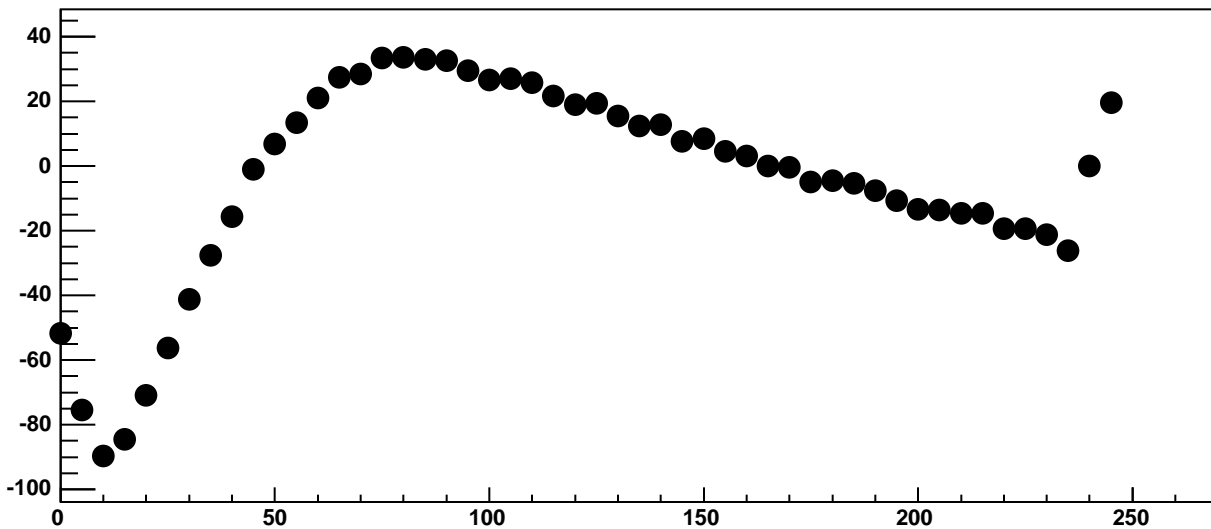


$\chi^2 / \text{ndf}$	1.36e+04 / 41
p0	199.2 ± 1.104
p1	235 ± 3.941
p2	-269 ± 164.8
p3	50.31 ± 23.56
p4	0.2594 ± 0.003411

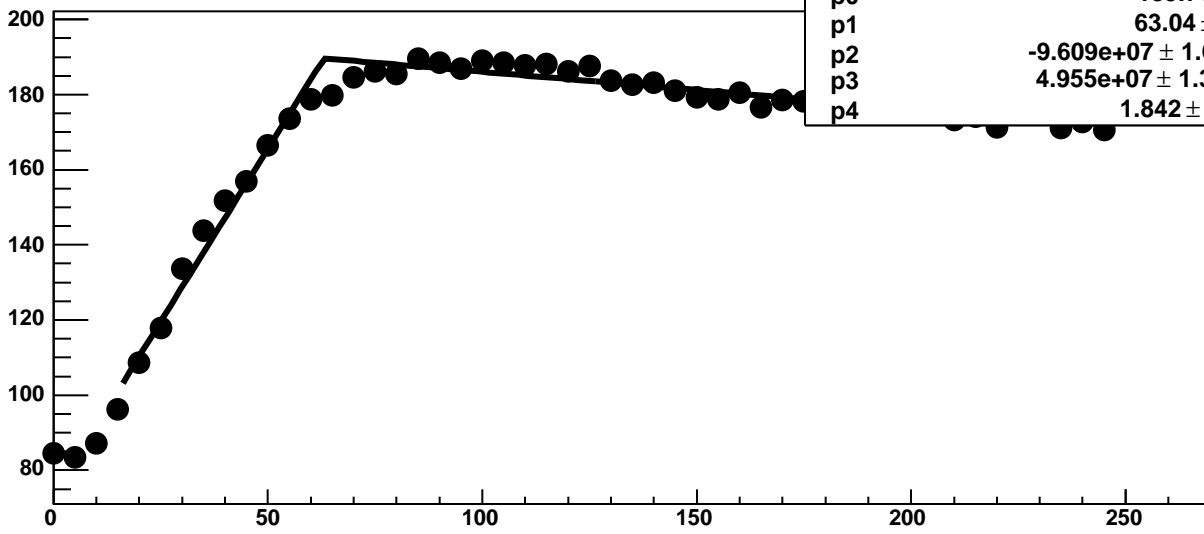
Chip 4, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold

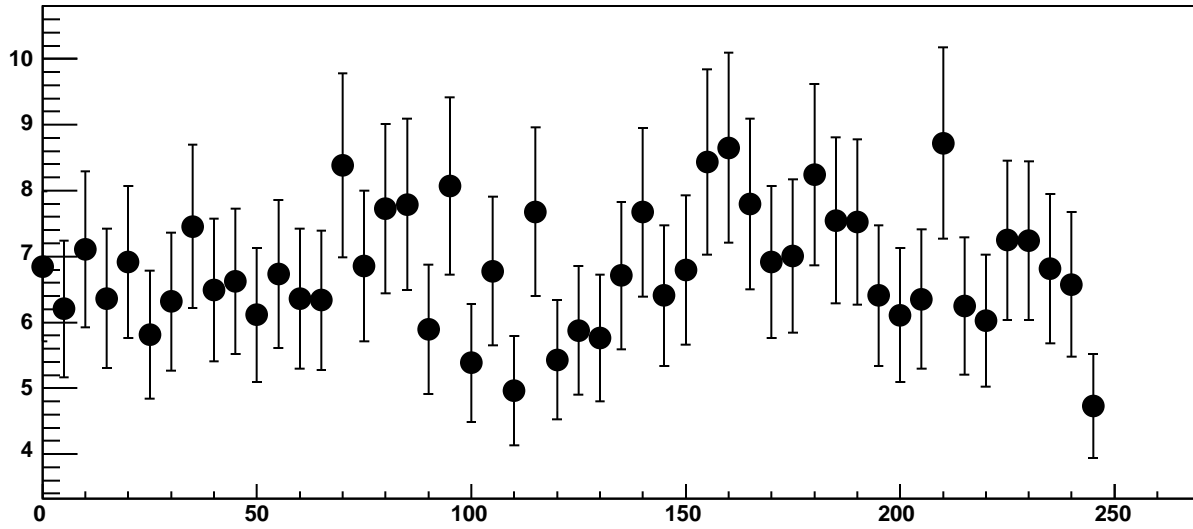


Chip 4, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold

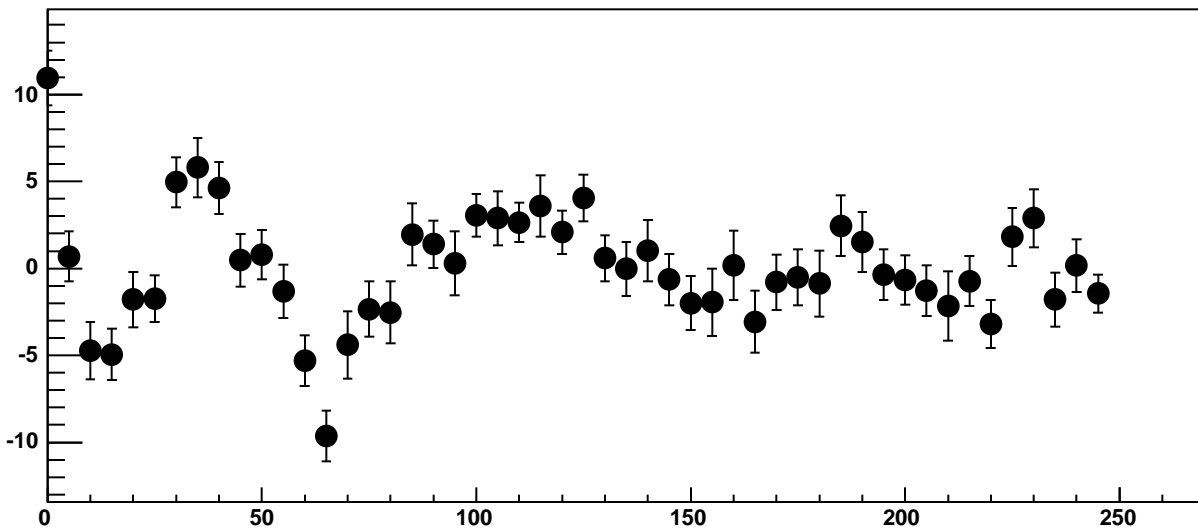


$\chi^2 / \text{ndf}$	171.2 / 41
p0	$189.7 \pm 0.4851$
p1	$63.04 \pm 0.5597$
p2	$-9.609\text{e}+07 \pm 1.638\text{e}+06$
p3	$4.955\text{e}+07 \pm 1.305\text{e}+05$
p4	$1.842 \pm 0.03255$

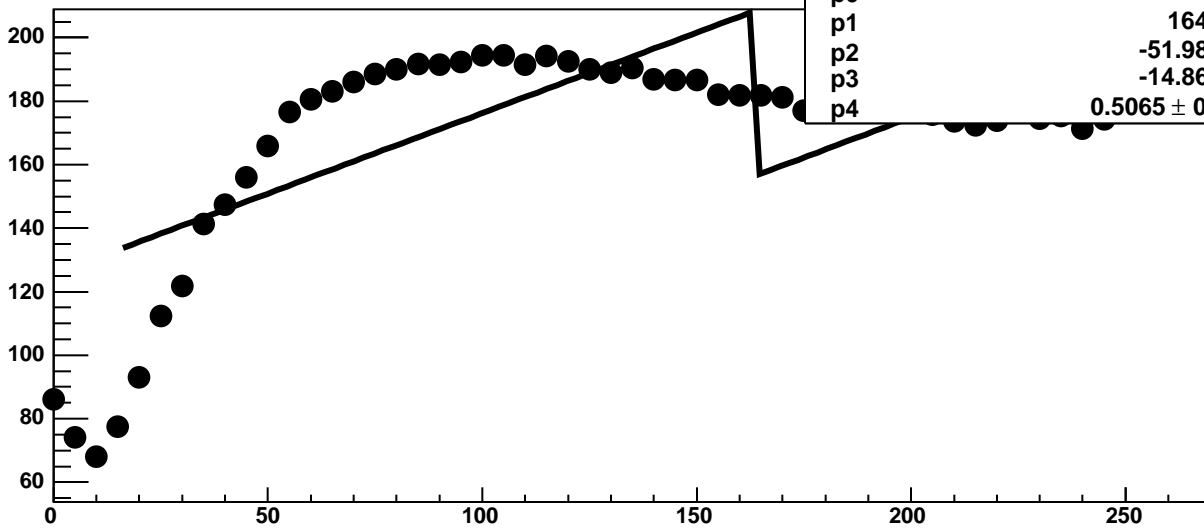
Chip 4, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold

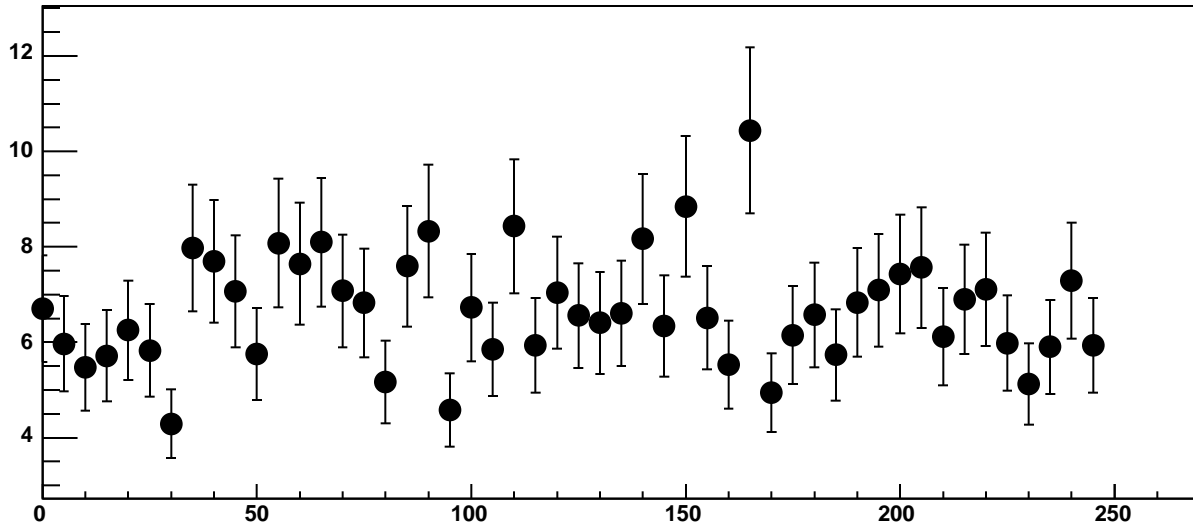


Chip 4, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

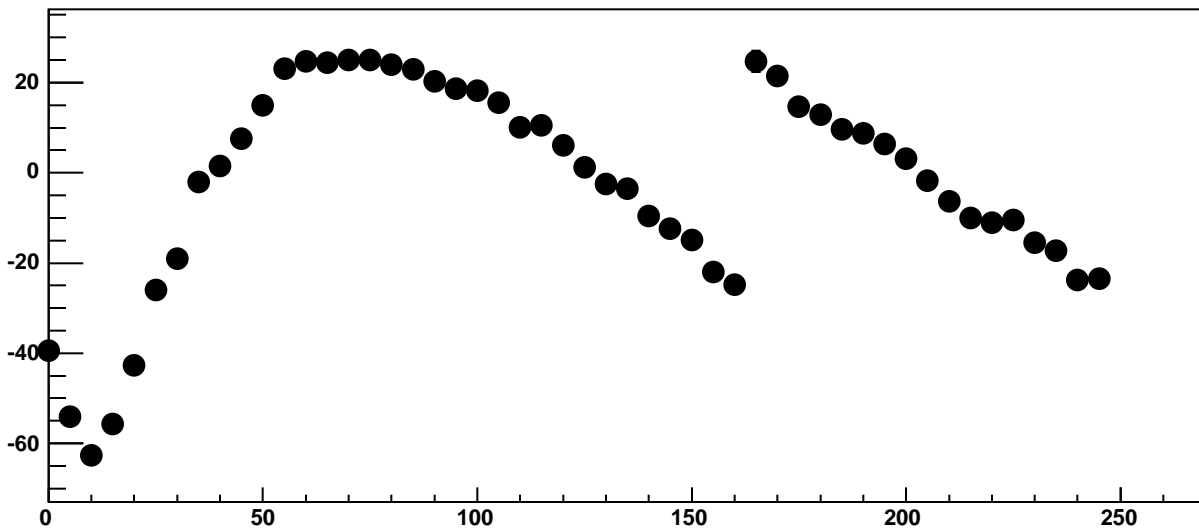


$\chi^2 / \text{ndf}$	8209 / 41
p0	$208.6 \pm 1.1$
p1	$164 \pm 1.393$
p2	$-51.98 \pm 2.212$
p3	$-14.86 \pm 48.36$
p4	$0.5065 \pm 0.002978$

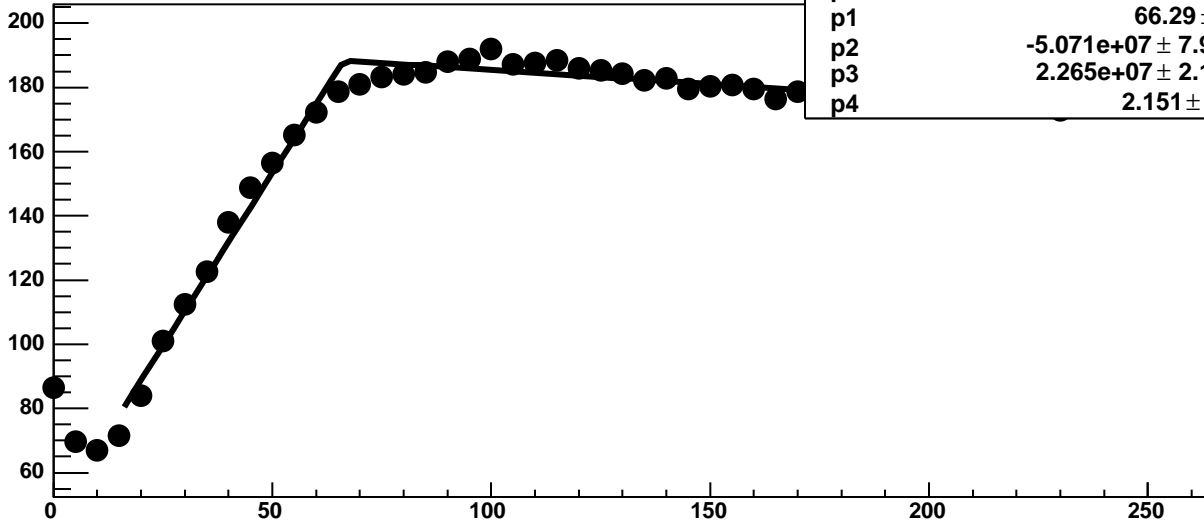
Chip 4, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

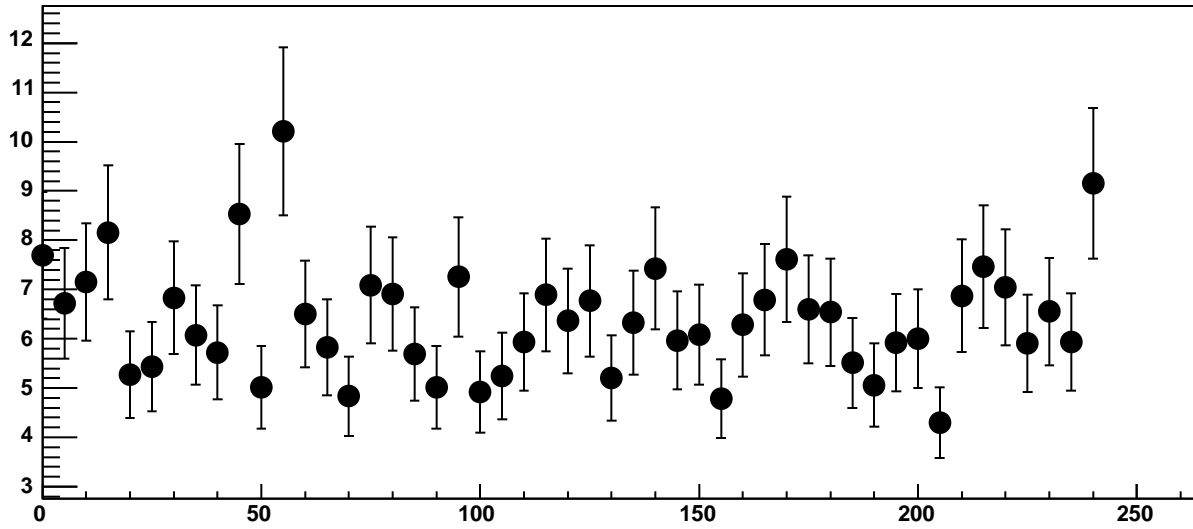


Chip 4, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold

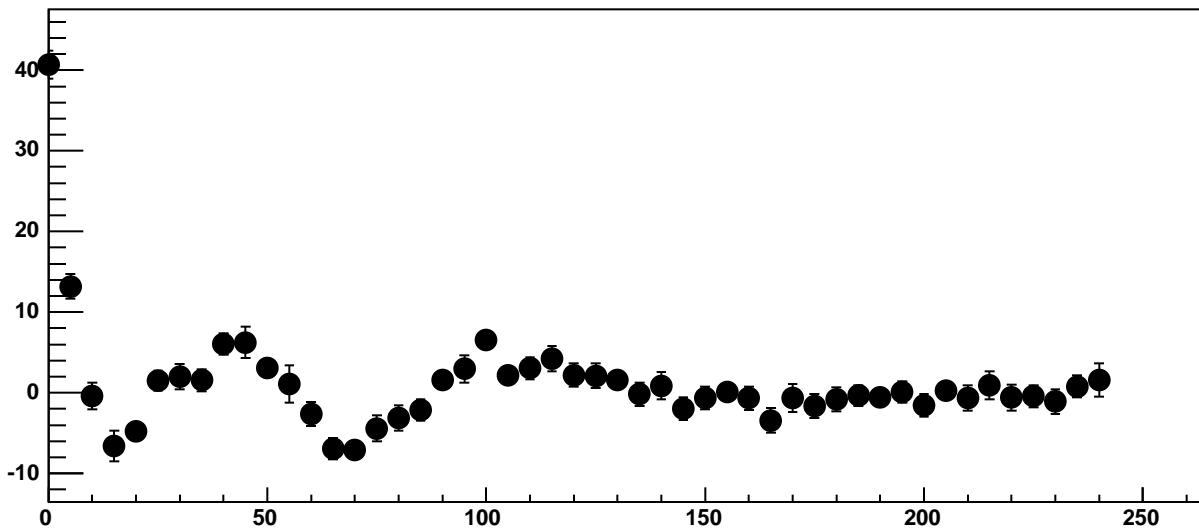


$\chi^2 / \text{ndf}$	230 / 41
p0	$188.4 \pm 0.4454$
p1	$66.29 \pm 0.4358$
p2	$-5.071\text{e}+07 \pm 7.938\text{e}+05$
p3	$2.265\text{e}+07 \pm 2.141\text{e}+05$
p4	$2.151 \pm 0.02772$

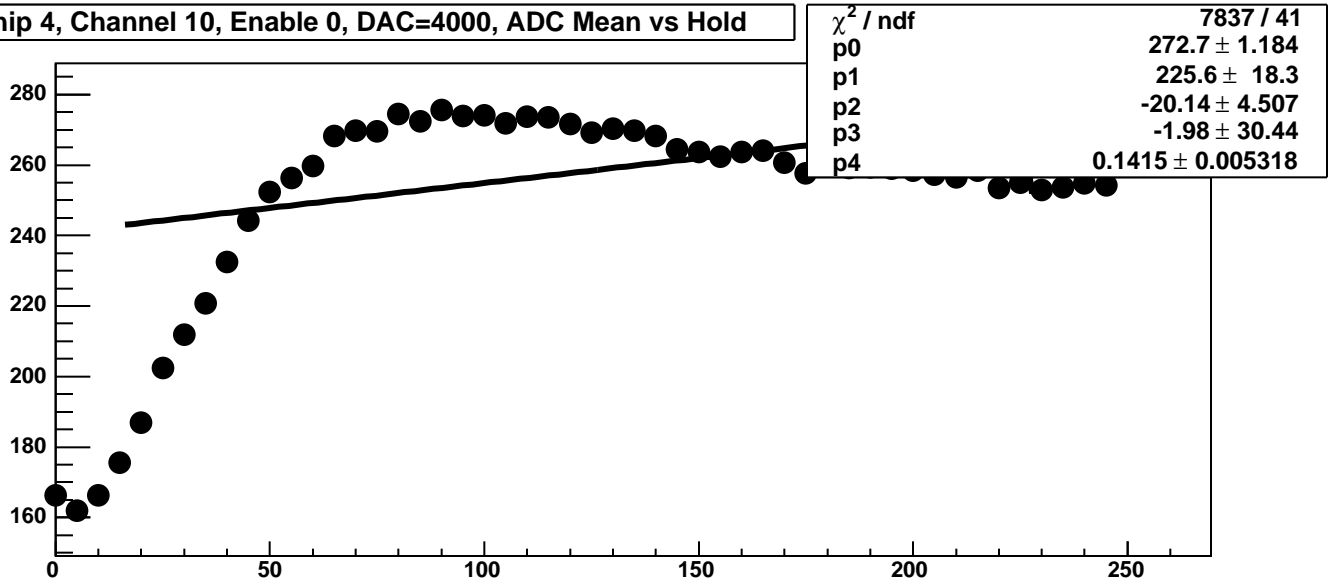
Chip 4, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



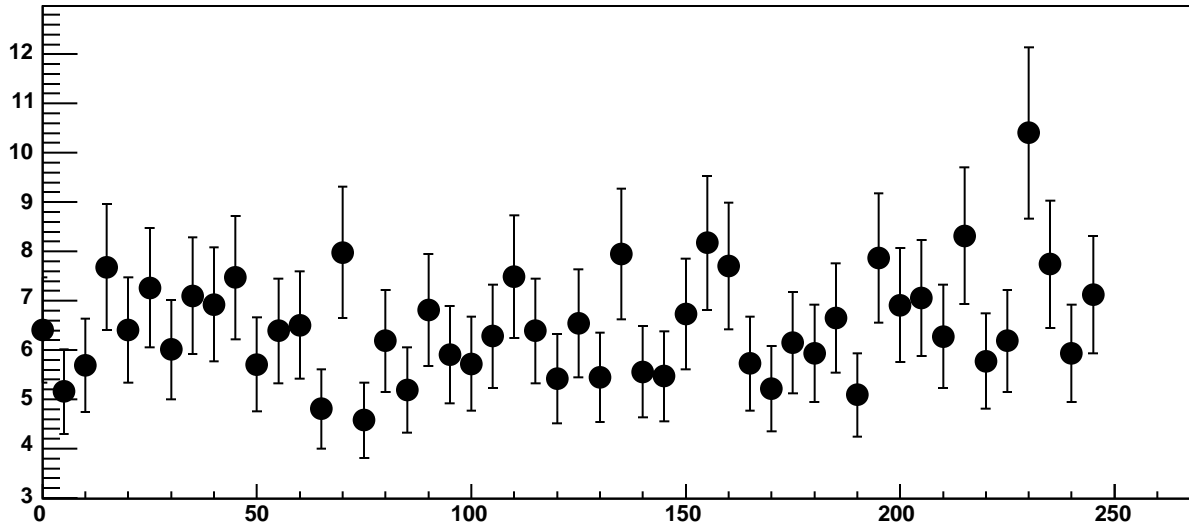
Chip 4, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold



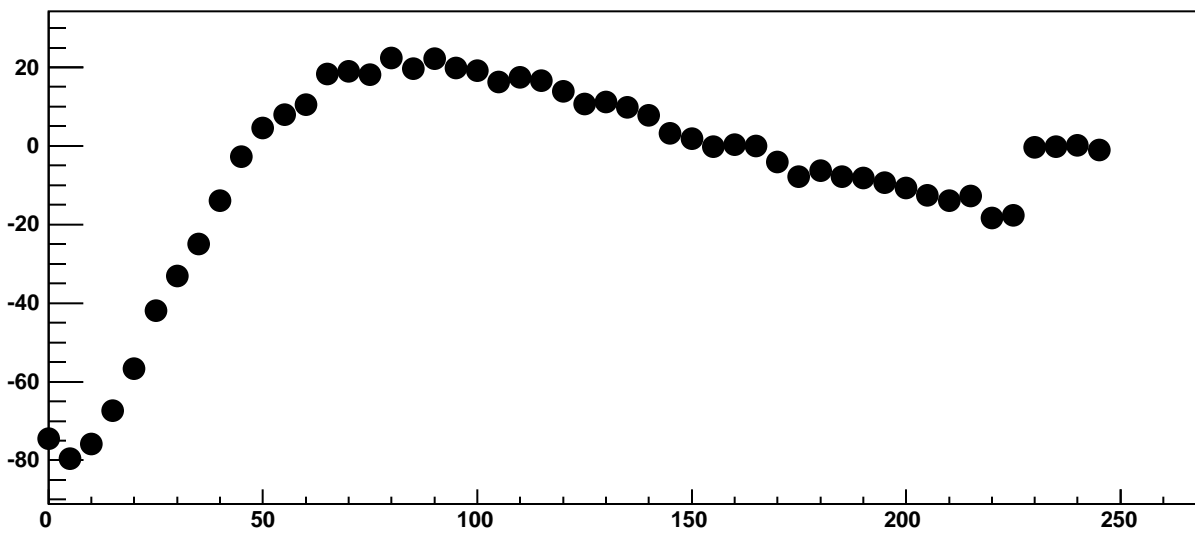
Chip 4, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold



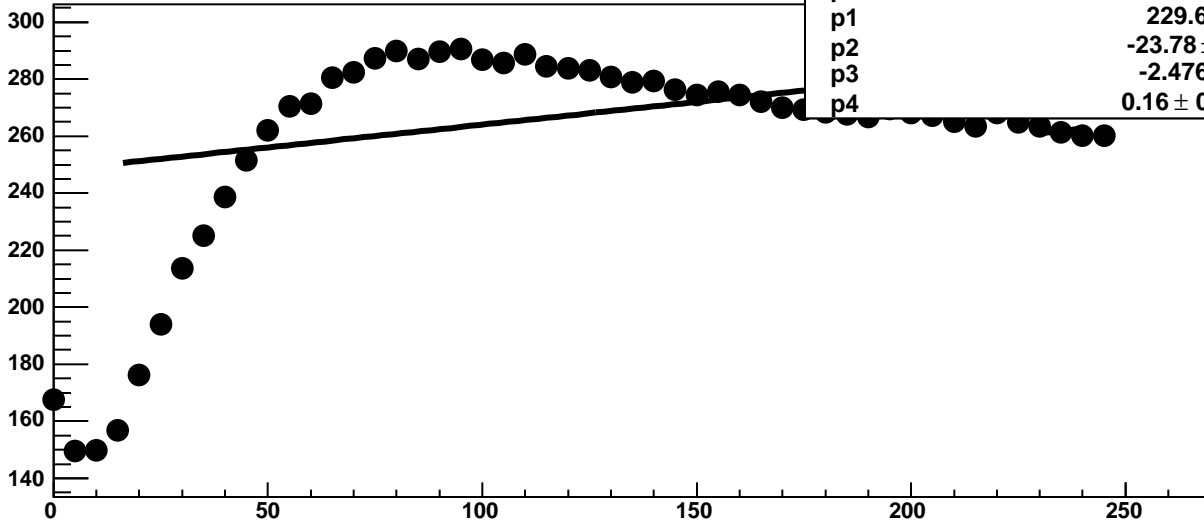
Chip 4, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold

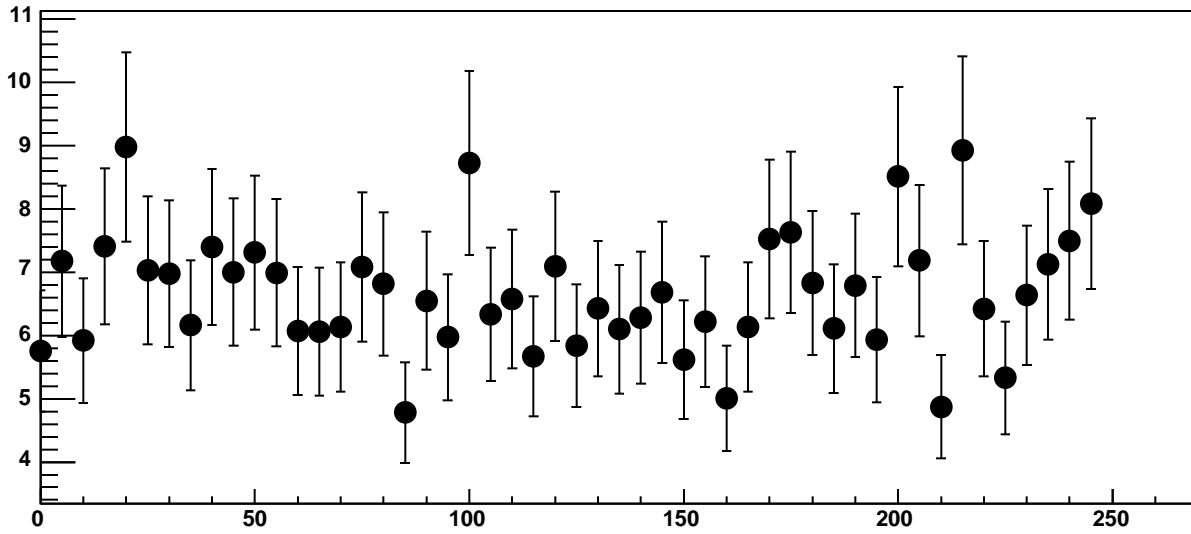


Chip 4, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

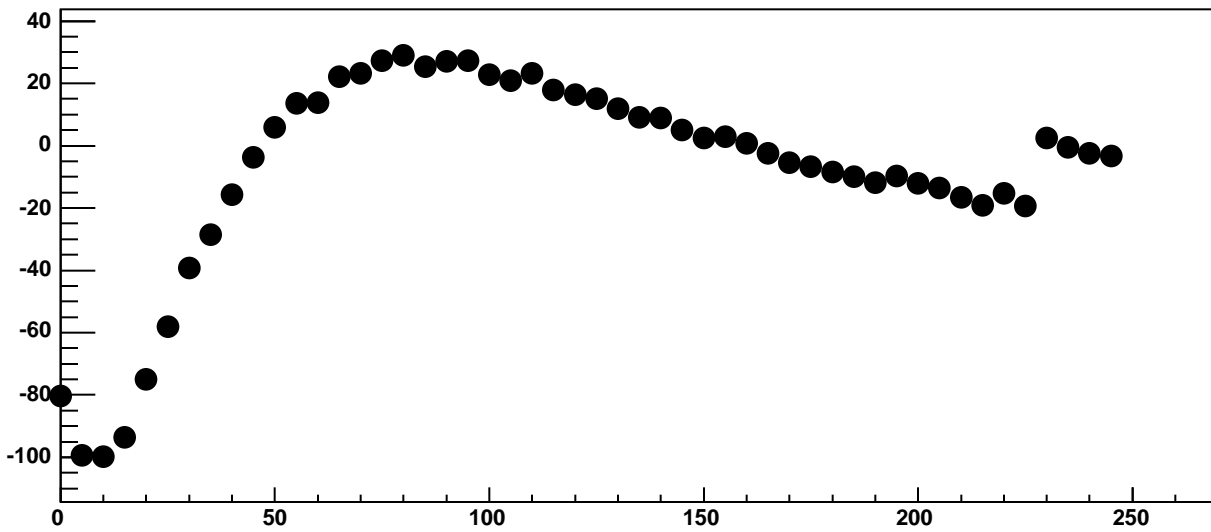


$\chi^2 / \text{ndf}$	1.149e+04 / 41
p0	284.8 ± 0.1969
p1	229.6 ± 9.376
p2	-23.78 ± 0.4783
p3	-2.476 ± 4.164
p4	0.16 ± 0.001137

Chip 4, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold

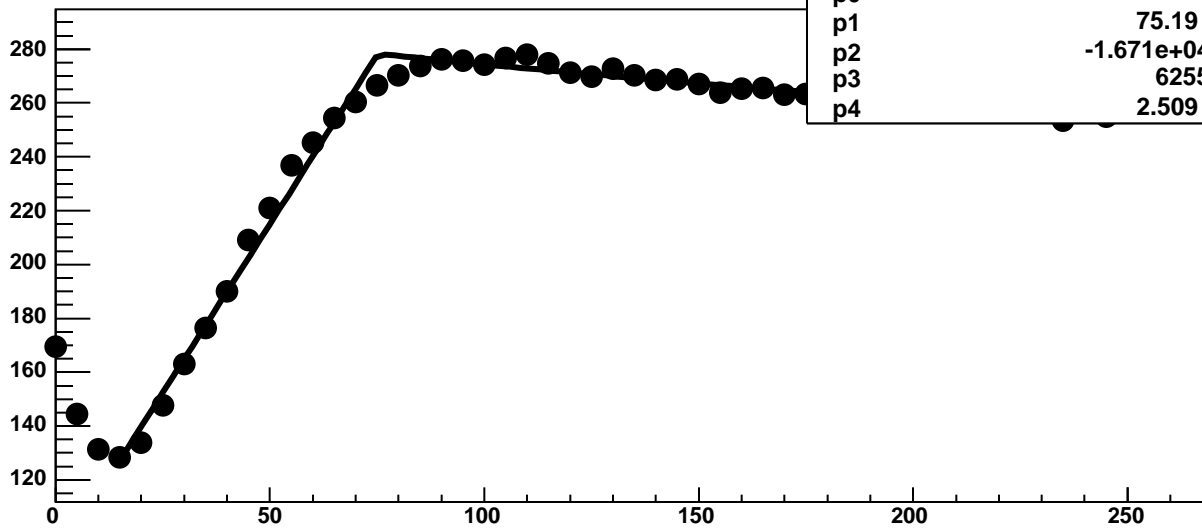


Chip 4, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold



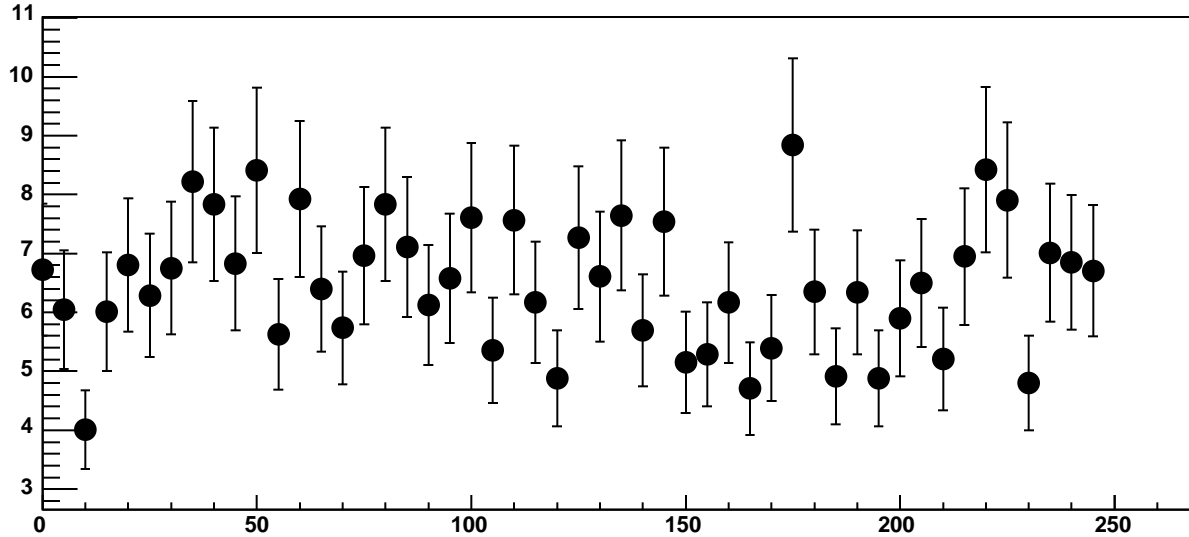


Chip 4, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

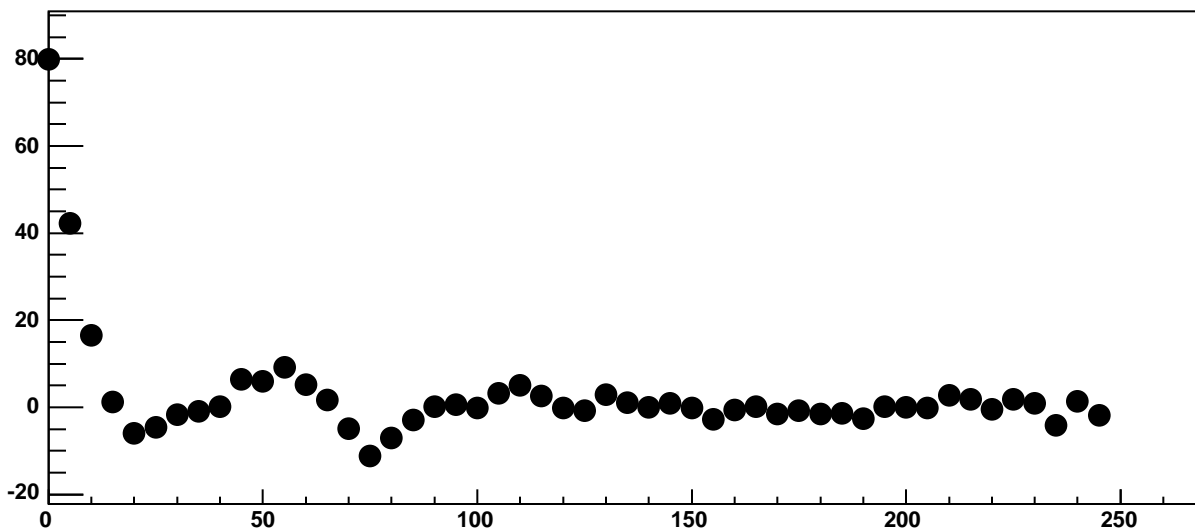


$\chi^2 / \text{ndf}$	248.8 / 41
p0	$278.3 \pm 0.5244$
p1	$75.19 \pm 0.3567$
p2	$-1.671\text{e}+04 \pm 2307$
p3	$6255 \pm 874.6$
p4	$2.509 \pm 0.0219$

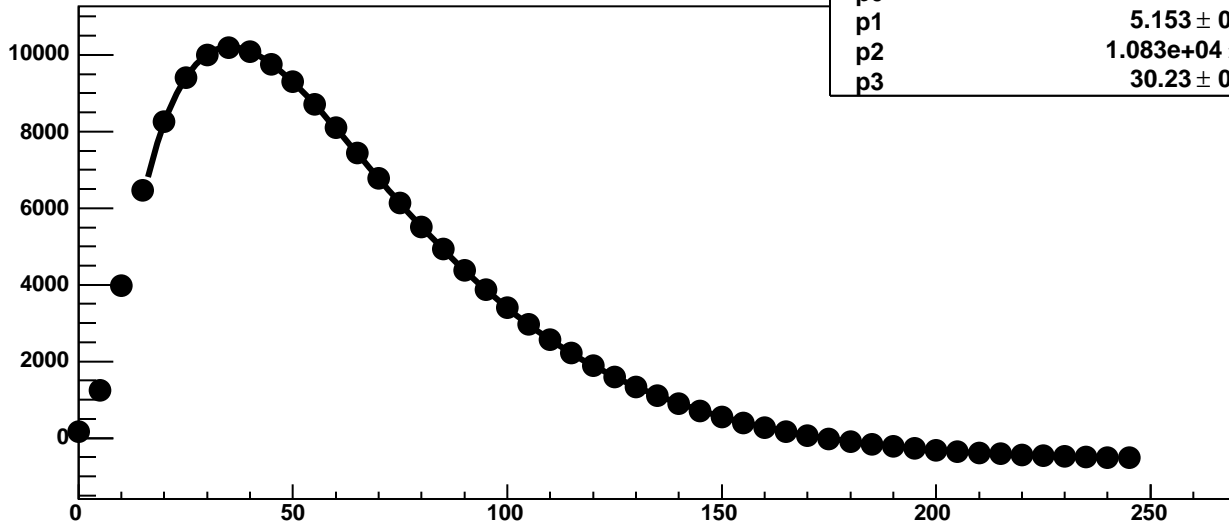
Chip 4, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

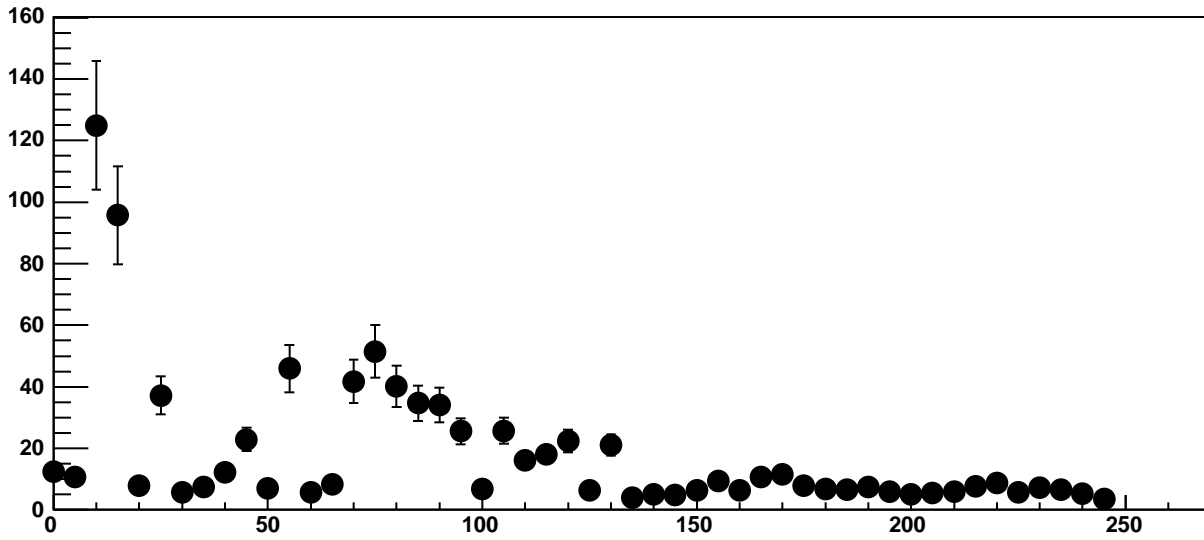


Chip 4, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold

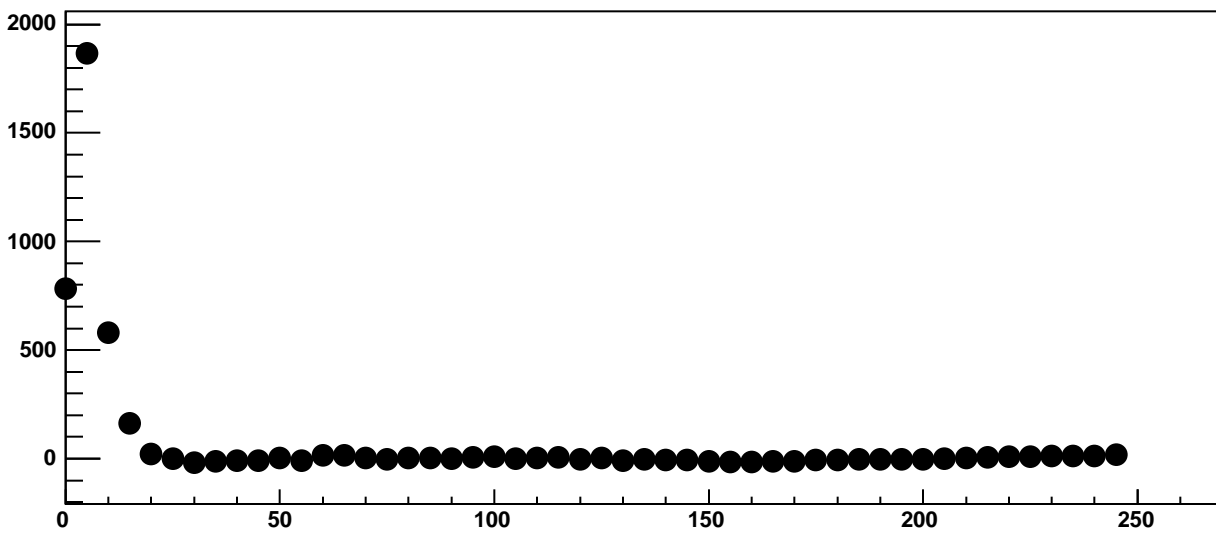


$\chi^2 / \text{ndf}$	1364 / 42
p0	-614.7 ± 0.5062
p1	5.153 ± 0.007409
p2	1.083e+04 ± 0.8131
p3	30.23 ± 0.005058

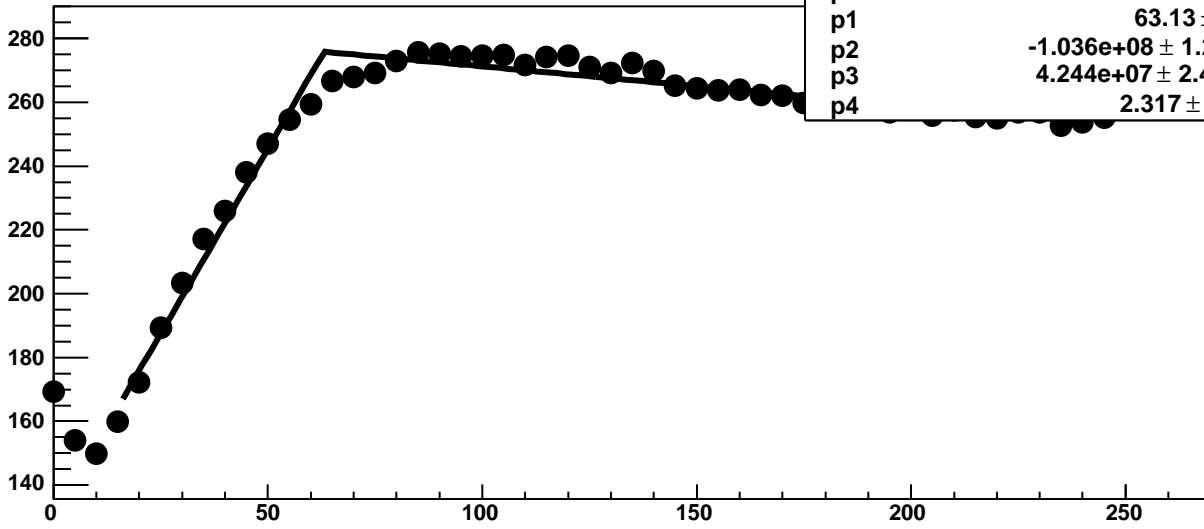
Chip 4, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold

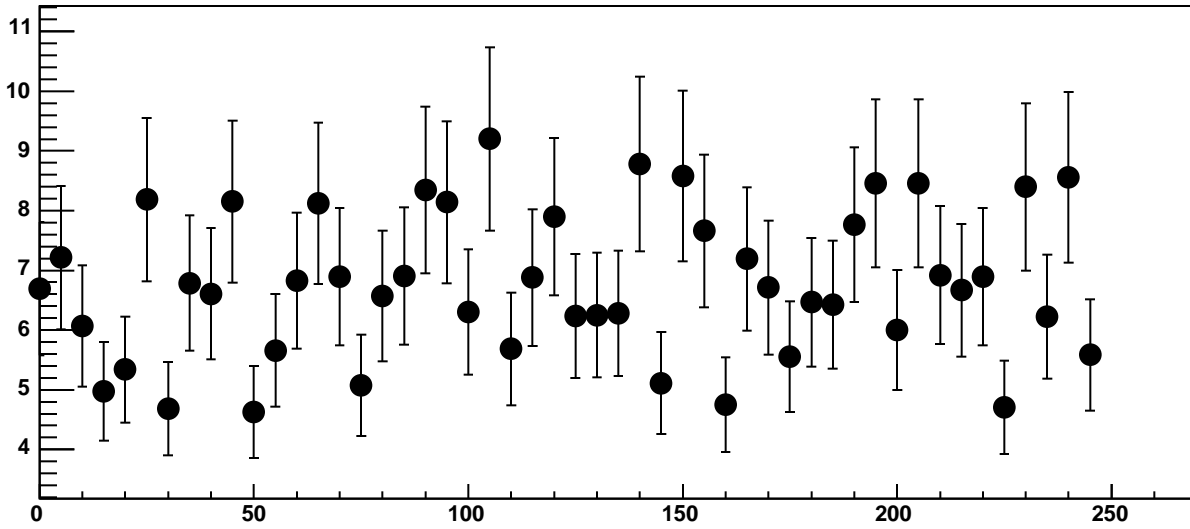


Chip 4, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

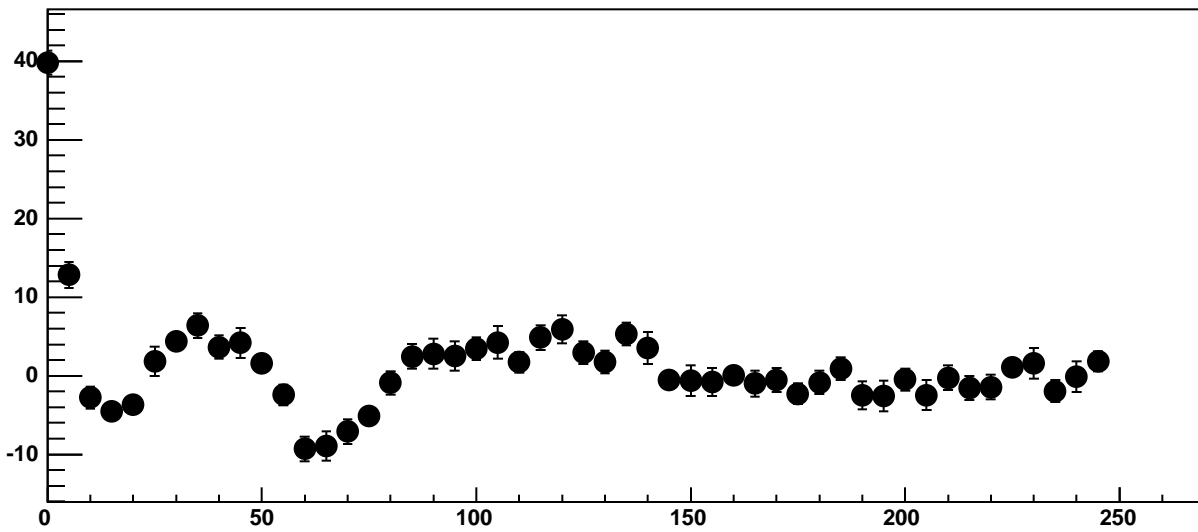


$\chi^2 / \text{ndf}$	249.1 / 41
p0	$275.8 \pm 0.4901$
p1	$63.13 \pm 0.4179$
p2	$-1.036\text{e}+08 \pm 1.297\text{e}+06$
p3	$4.244\text{e}+07 \pm 2.486\text{e}+05$
p4	$2.317 \pm 0.02884$

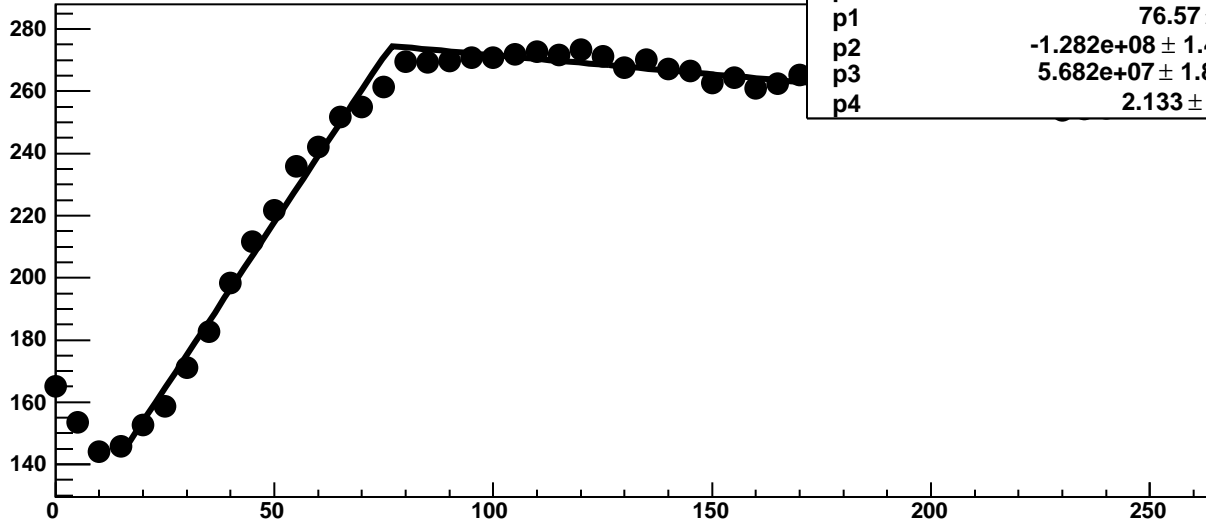
Chip 4, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

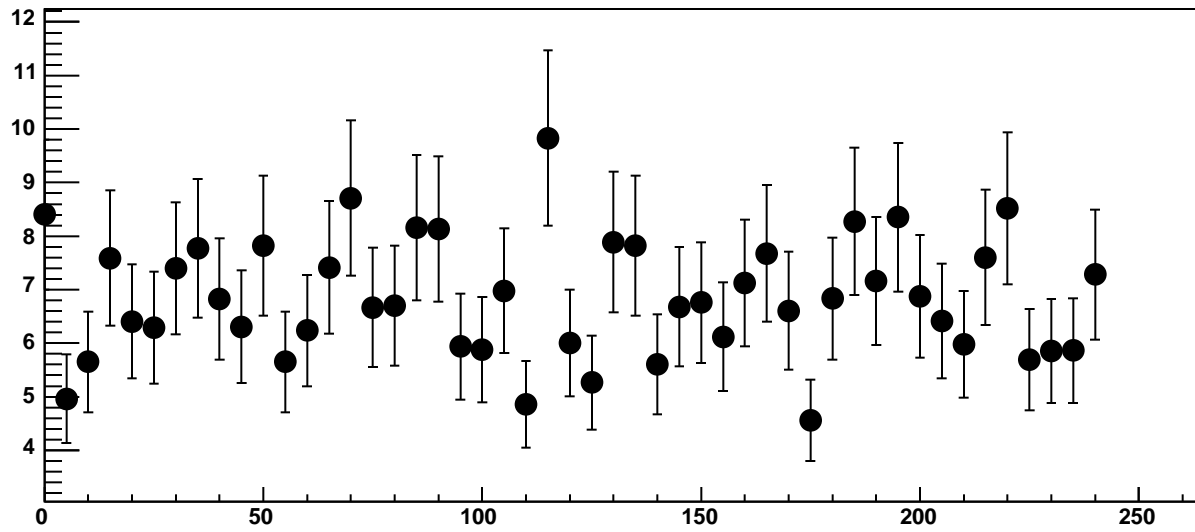


Chip 4, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold

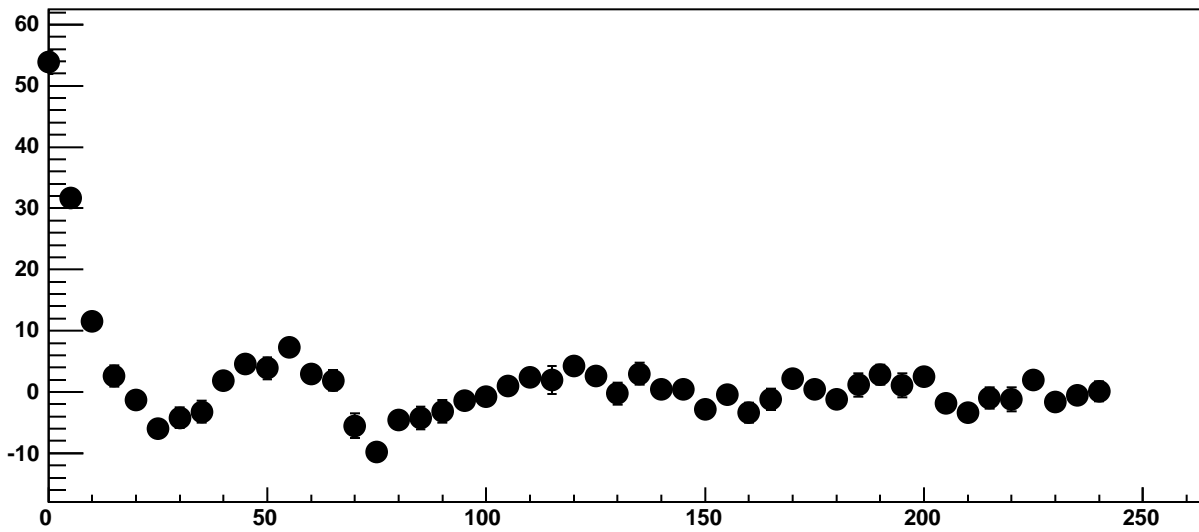


$\chi^2 / \text{ndf}$	202.2 / 41
p0	$274.5 \pm 0.5032$
p1	$76.57 \pm 0.4541$
p2	$-1.282\text{e}+08 \pm 1.452\text{e}+06$
p3	$5.682\text{e}+07 \pm 1.802\text{e}+05$
p4	$2.133 \pm 0.02382$

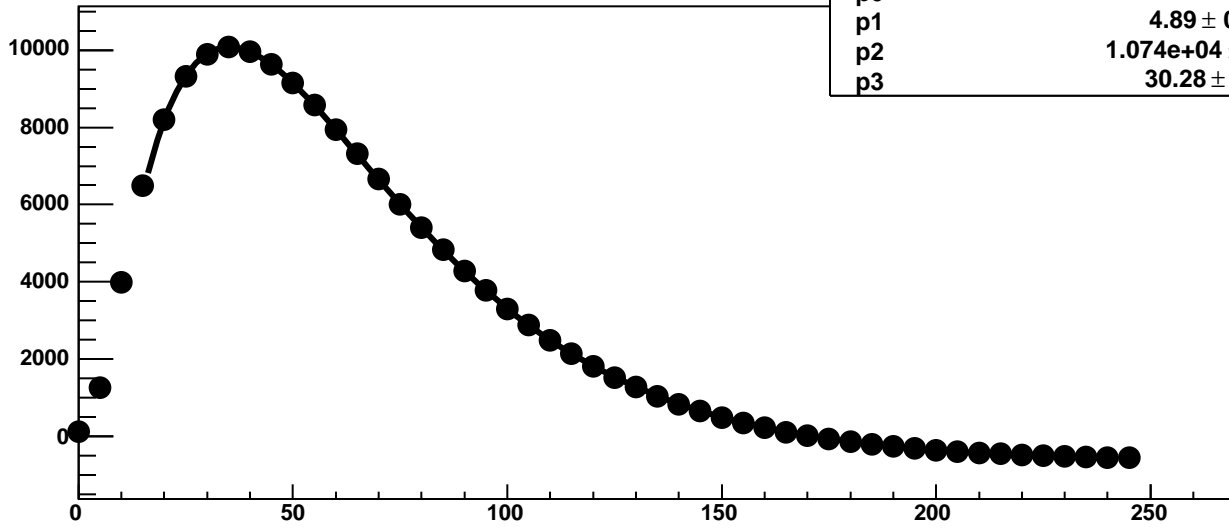
Chip 4, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

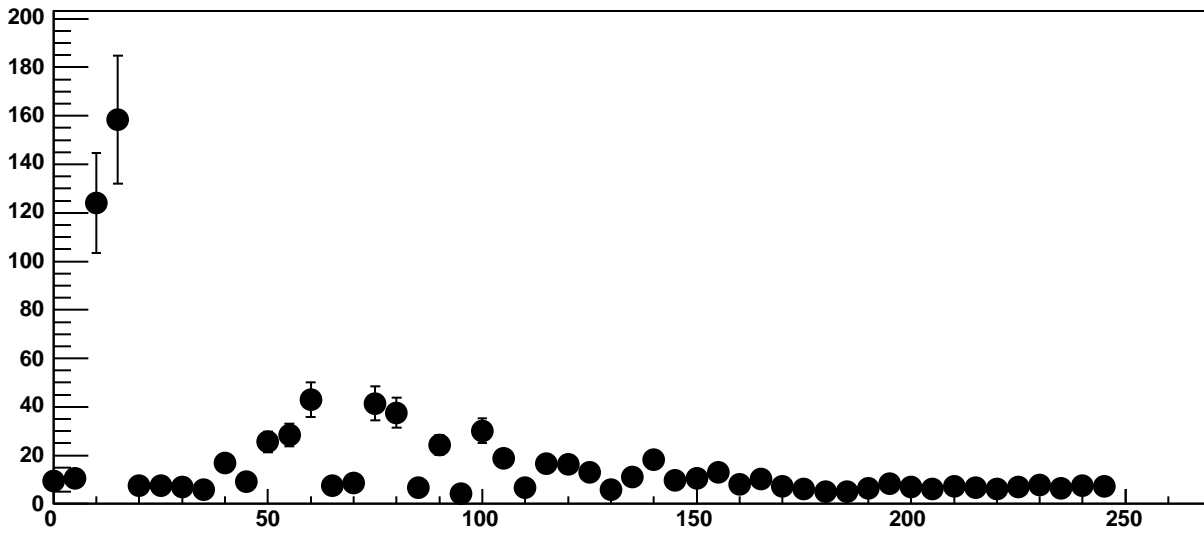


Chip 4, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold

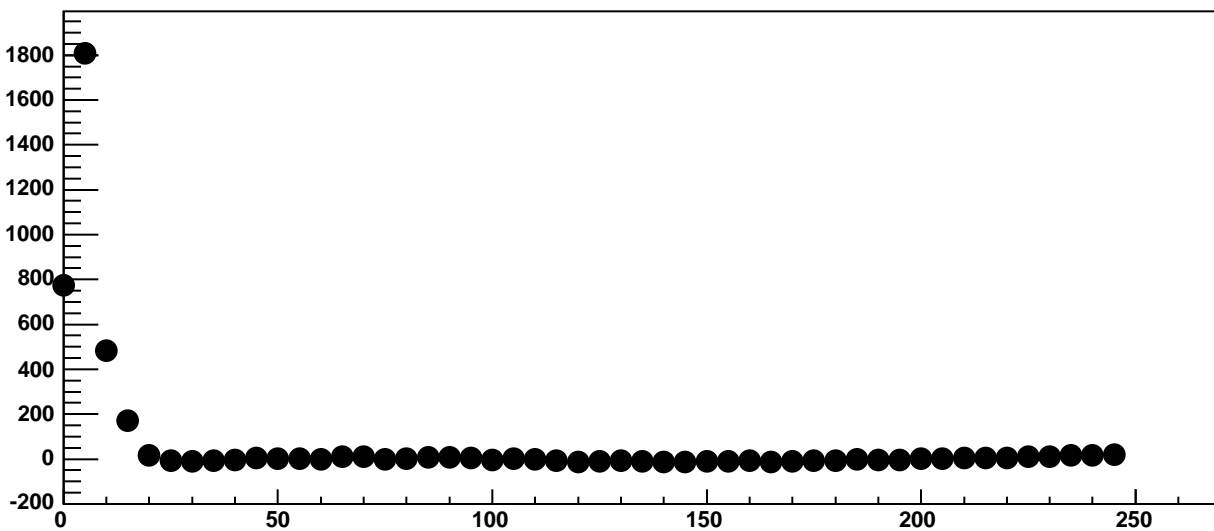


$\chi^2 / \text{ndf}$	1043 / 42
p0	-654.7 ± 0.4987
p1	4.89 ± 0.007731
p2	1.074e+04 ± 0.8461
p3	30.28 ± 0.00495

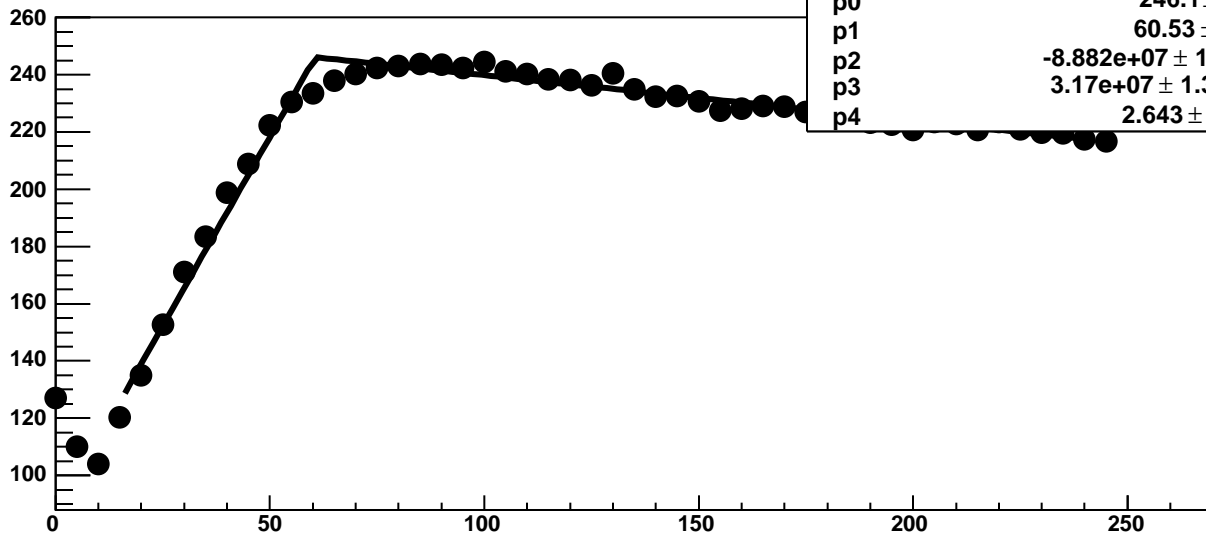
Chip 4, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold

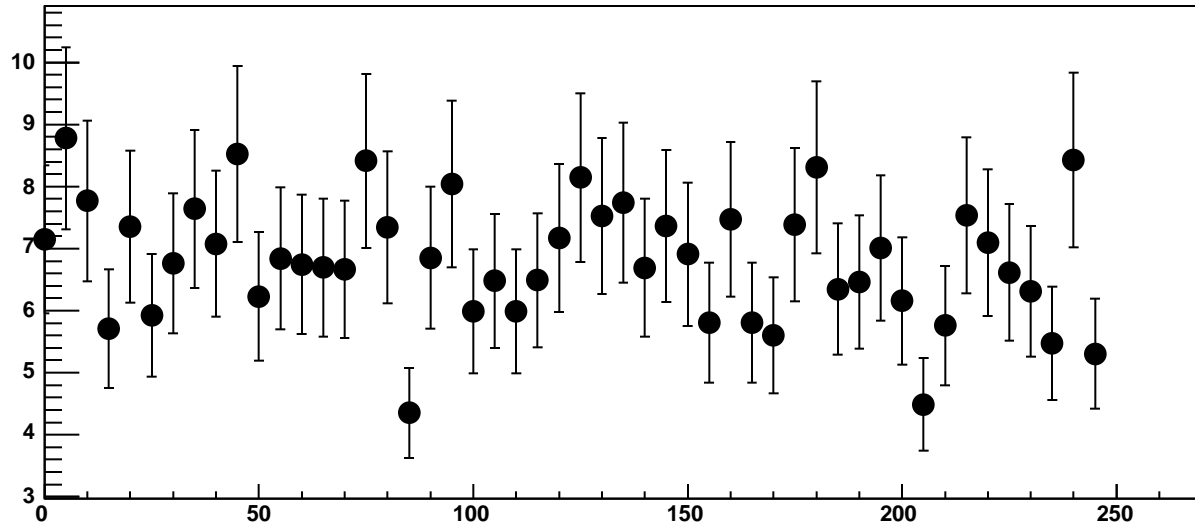


Chip 4, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

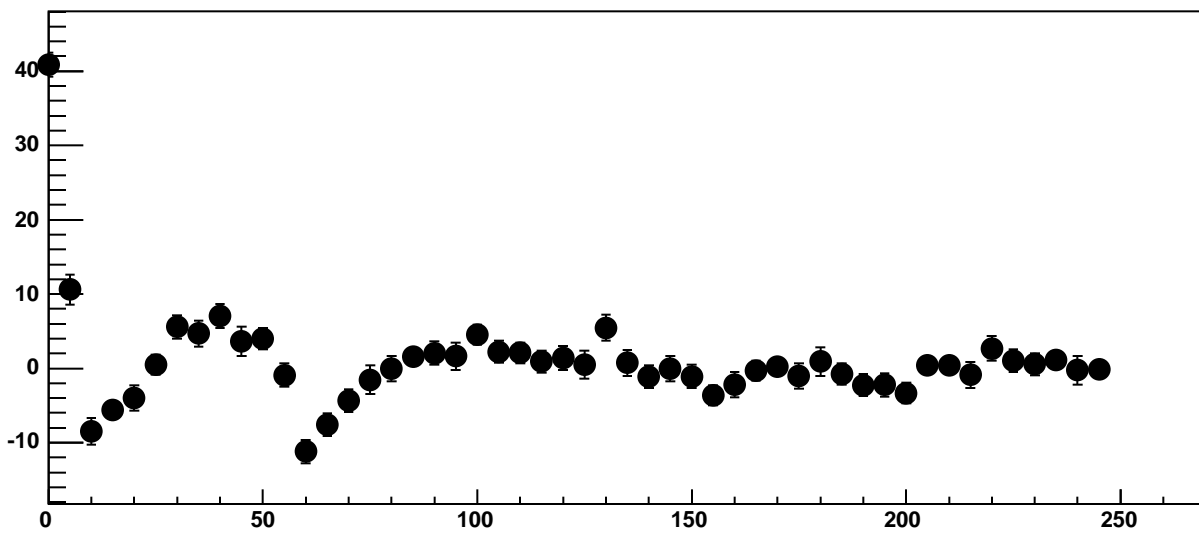


$\chi^2 / \text{ndf}$	217 / 41
p0	$246.1 \pm 0.4823$
p1	$60.53 \pm 0.3774$
p2	$-8.882e+07 \pm 1.09e+06$
p3	$3.17e+07 \pm 1.321e+05$
p4	$2.643 \pm 0.03267$

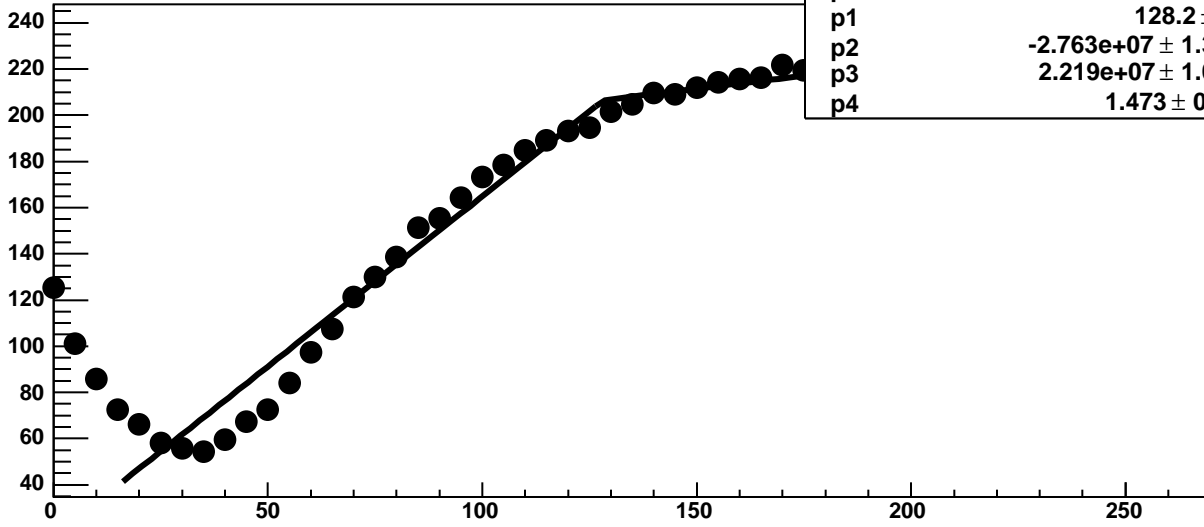
Chip 4, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold

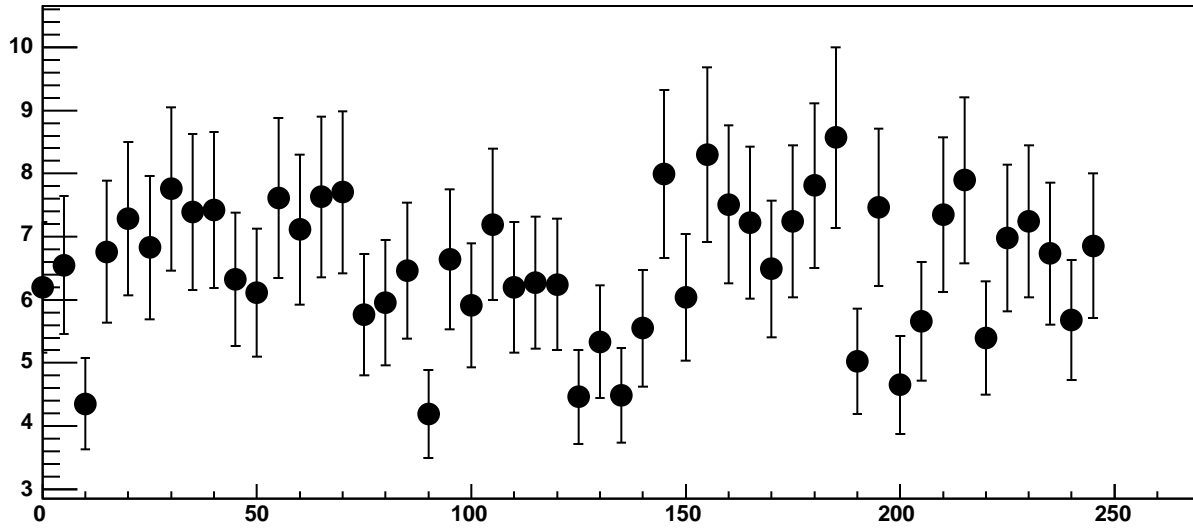


Chip 4, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

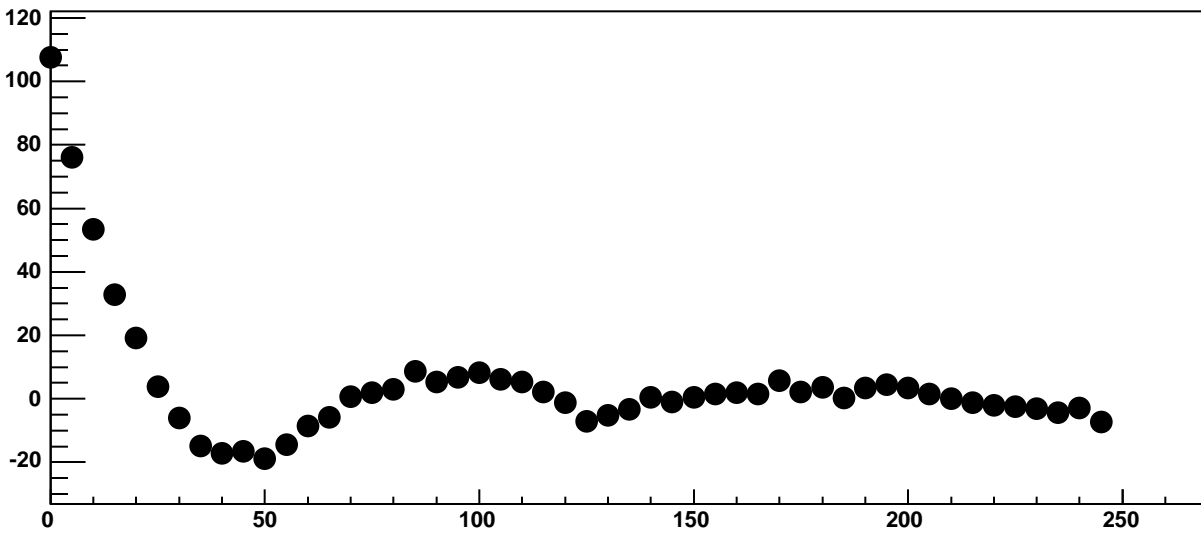


$\chi^2 / \text{ndf}$	1502 / 41
p0	$206.4 \pm 0.6327$
p1	$128.2 \pm 0.5939$
p2	$-2.763\text{e}+07 \pm 1.315\text{e}+06$
p3	$2.219\text{e}+07 \pm 1.094\text{e}+06$
p4	$1.473 \pm 0.008783$

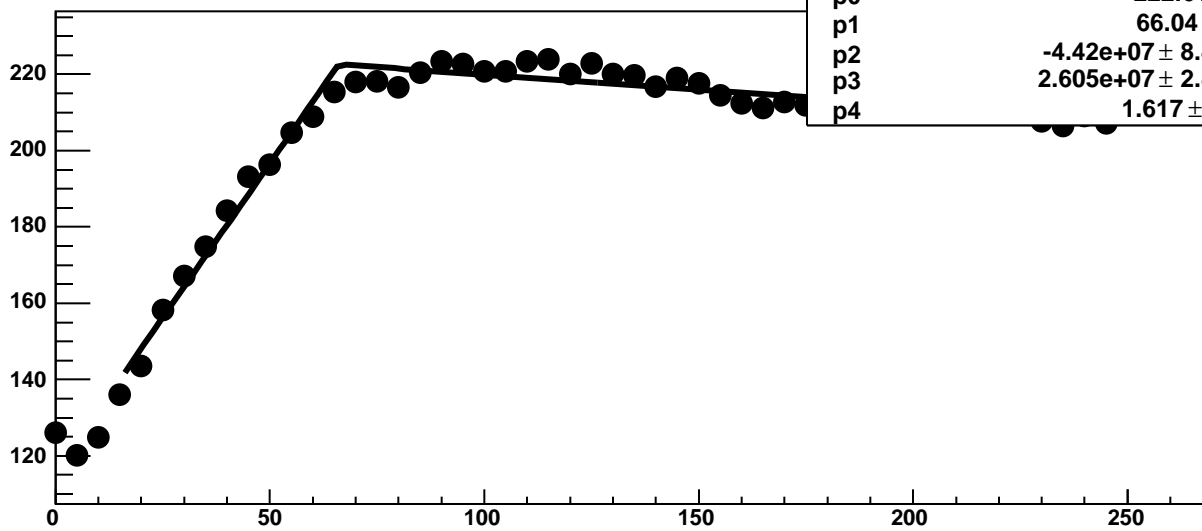
Chip 4, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

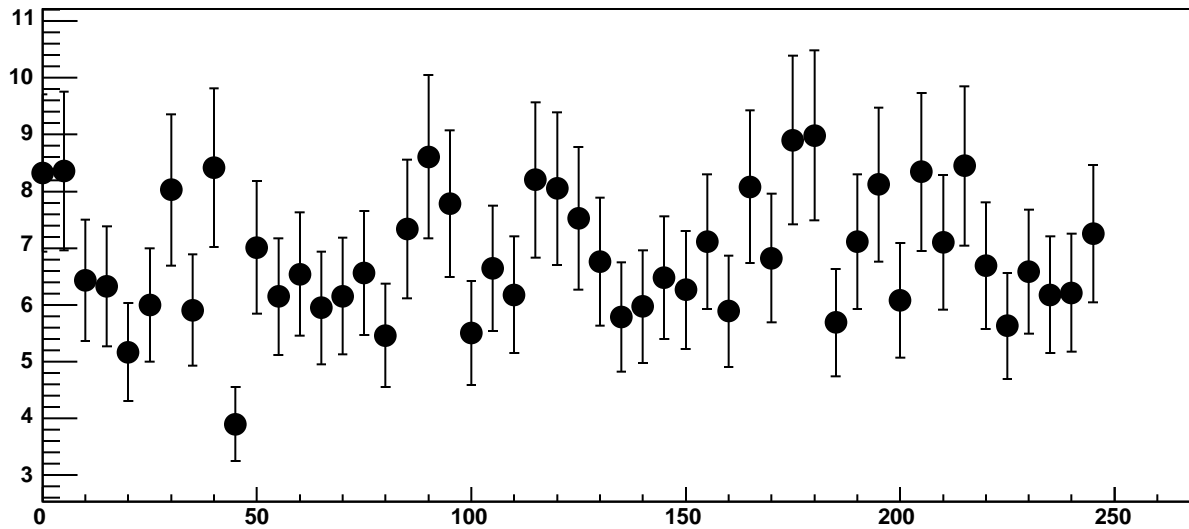


Chip 4, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold

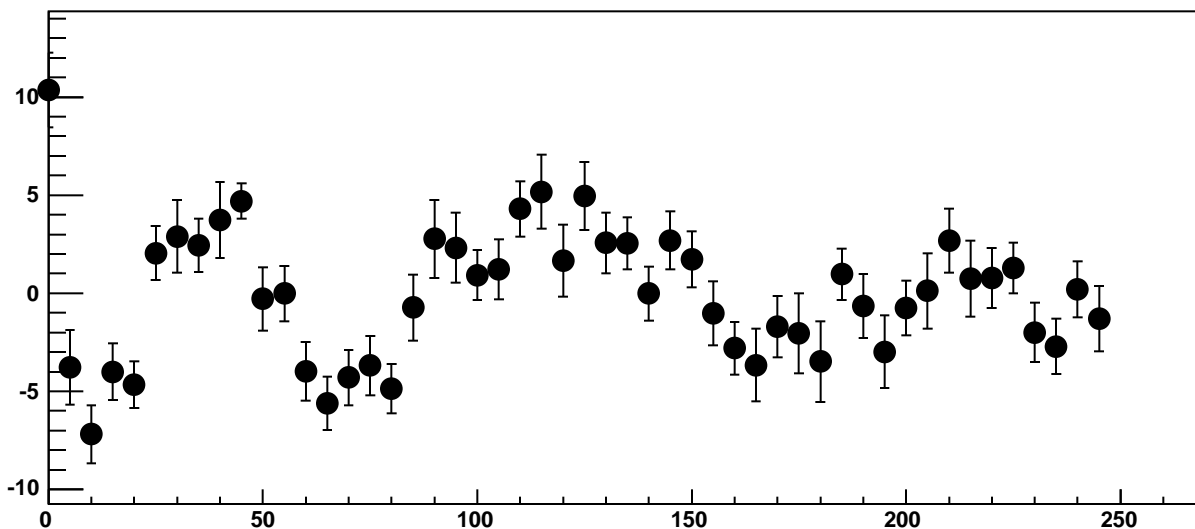


$\chi^2 / \text{ndf}$	185.1 / 41
p0	$222.6 \pm 0.4934$
p1	$66.04 \pm 0.5602$
p2	$-4.42\text{e}+07 \pm 8.497\text{e}+05$
p3	$2.605\text{e}+07 \pm 2.837\text{e}+05$
p4	$1.617 \pm 0.02635$

Chip 4, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold

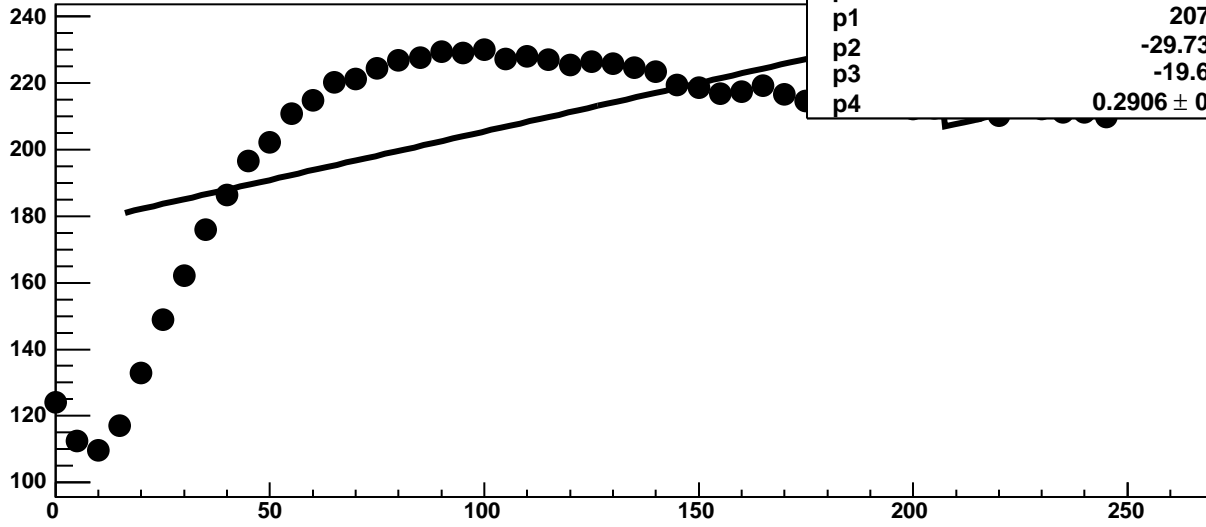


Chip 4, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold



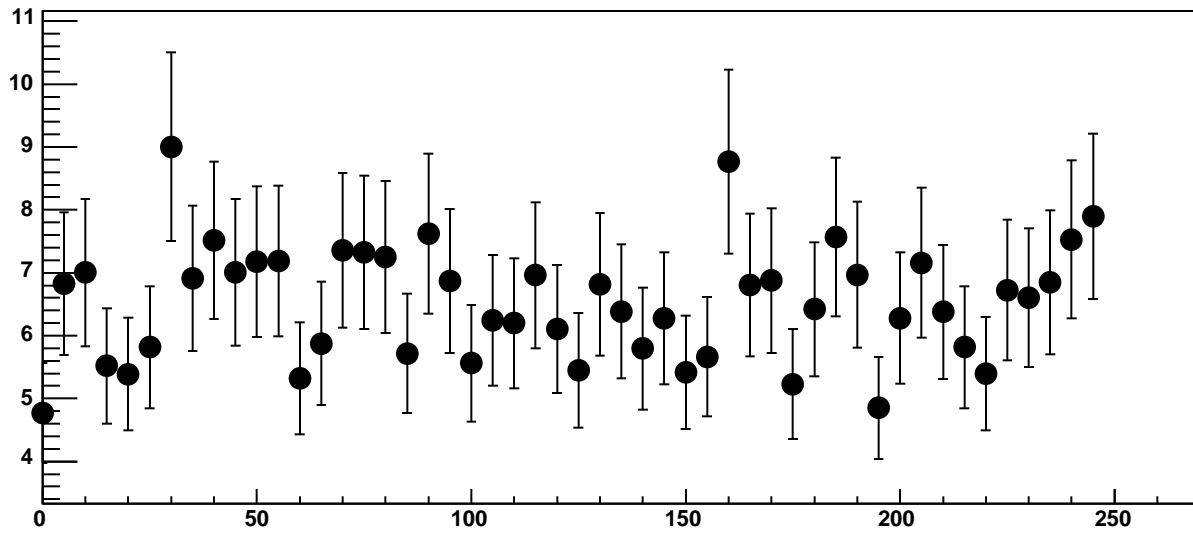


Chip 4, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

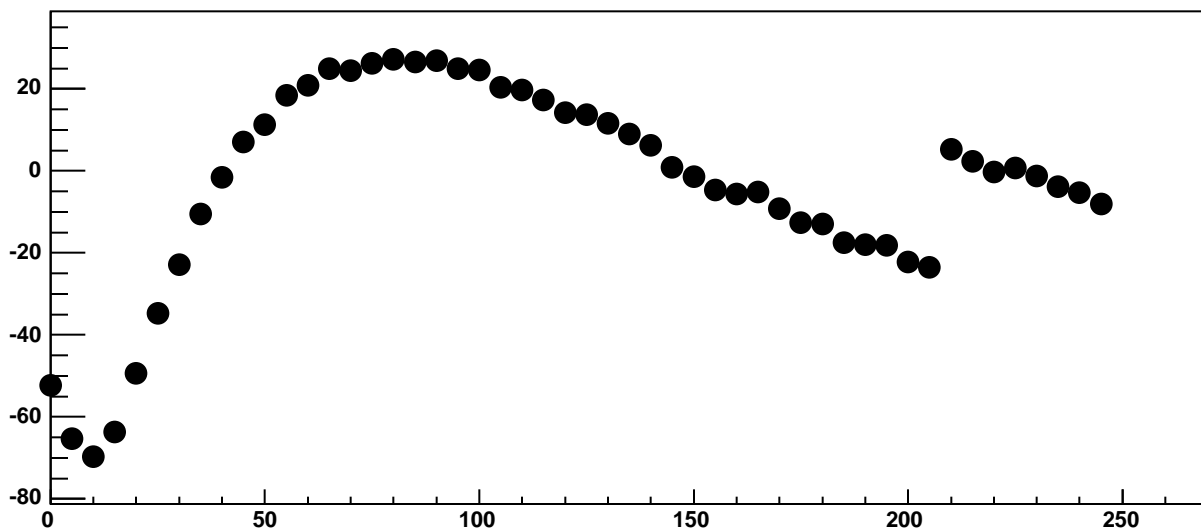


$\chi^2 / \text{ndf}$	9864 / 41
p0	$236.5 \pm 2.218$
p1	$207 \pm 8.809$
p2	$-29.73 \pm 2.122$
p3	$-19.6 \pm 21.37$
p4	$0.2906 \pm 0.002364$

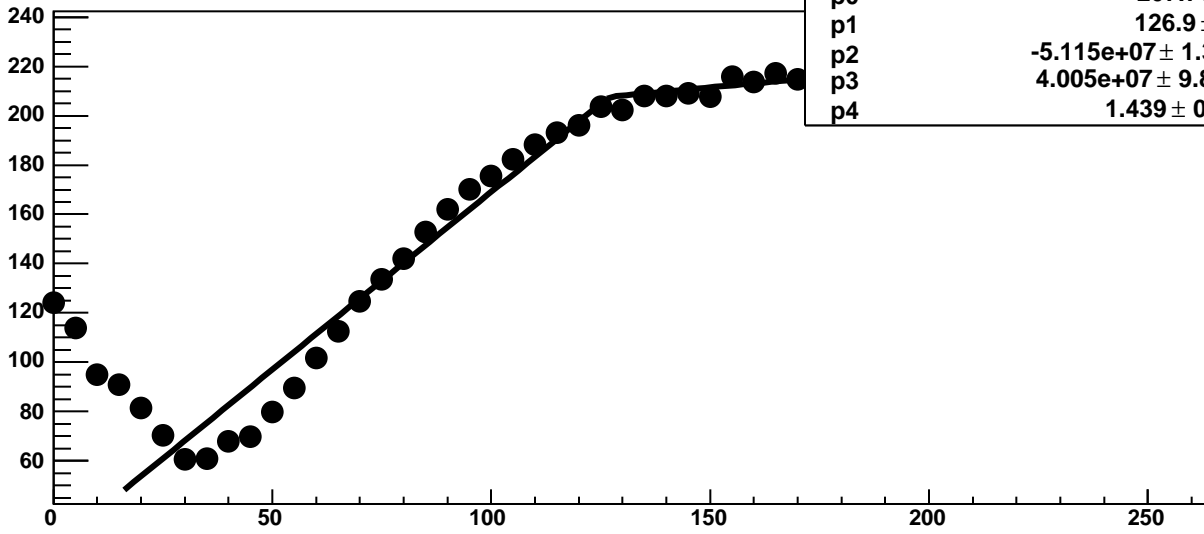
Chip 4, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

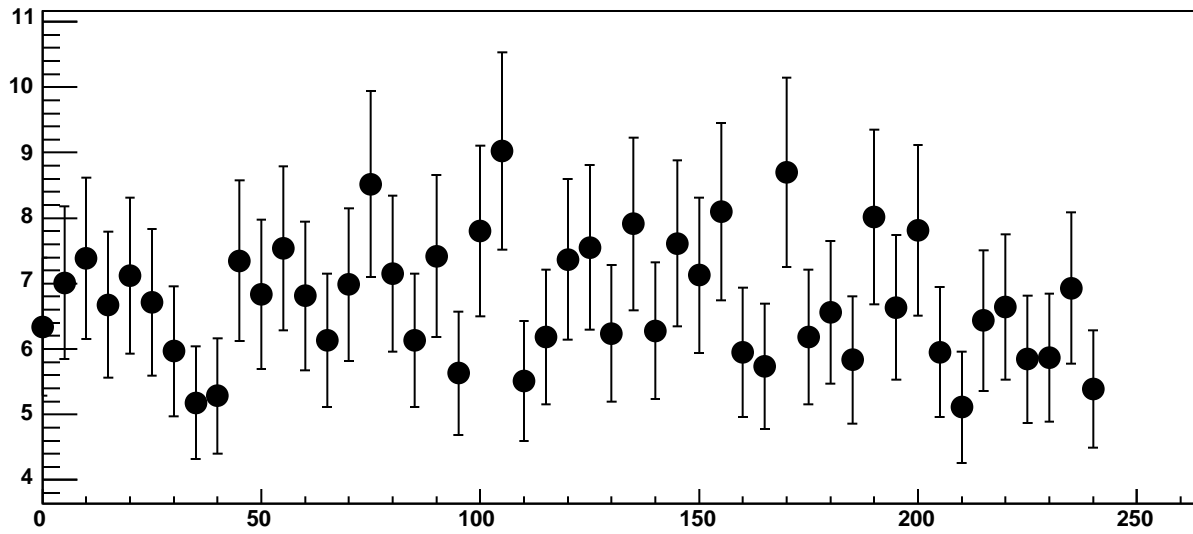


Chip 4, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold

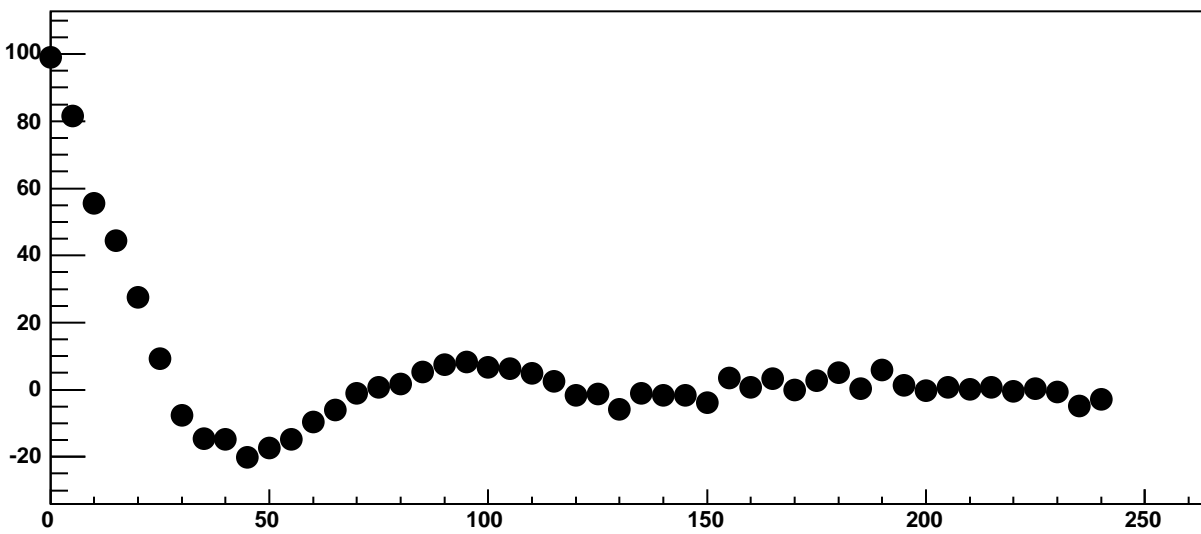


$\chi^2 / \text{ndf}$	2081 / 41
p0	$207.7 \pm 0.7341$
p1	$126.9 \pm 0.7158$
p2	$-5.115\text{e}+07 \pm 1.363\text{e}+06$
p3	$4.005\text{e}+07 \pm 9.826\text{e}+05$
p4	$1.439 \pm 0.009533$

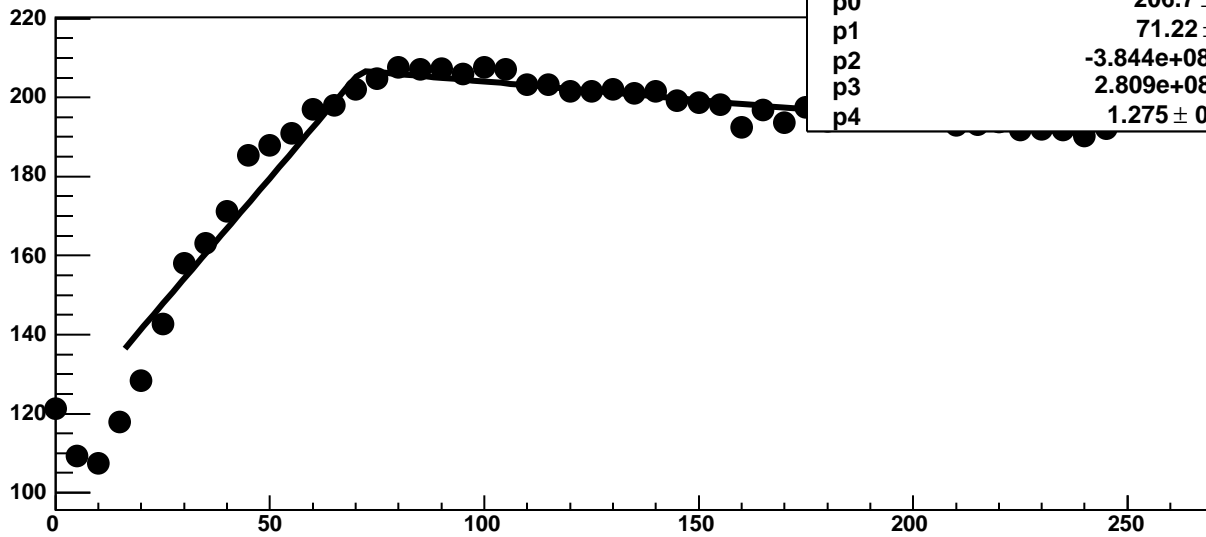
Chip 4, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

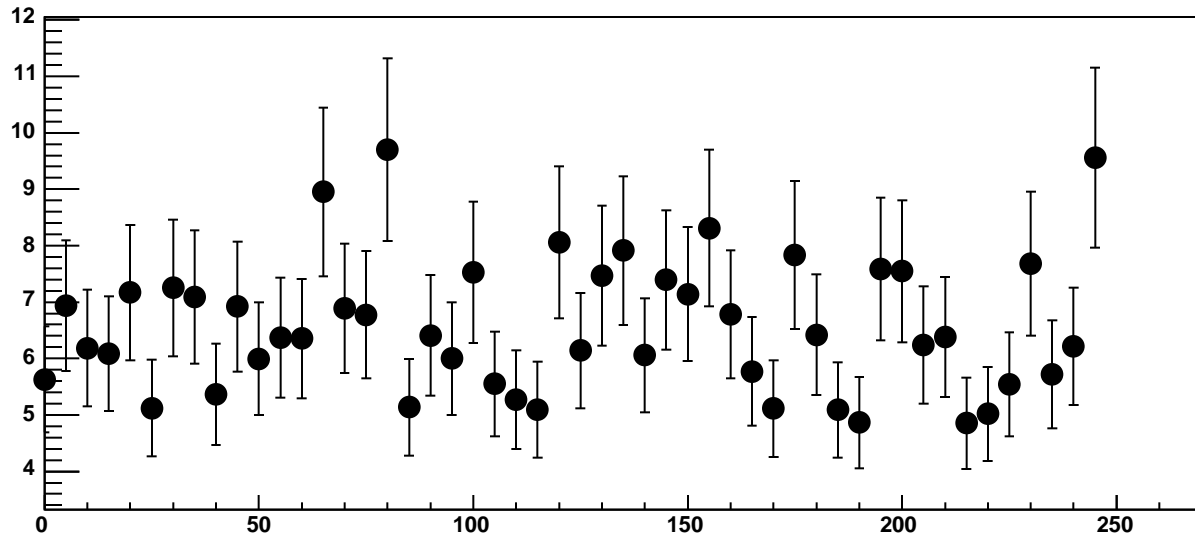


Chip 4, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

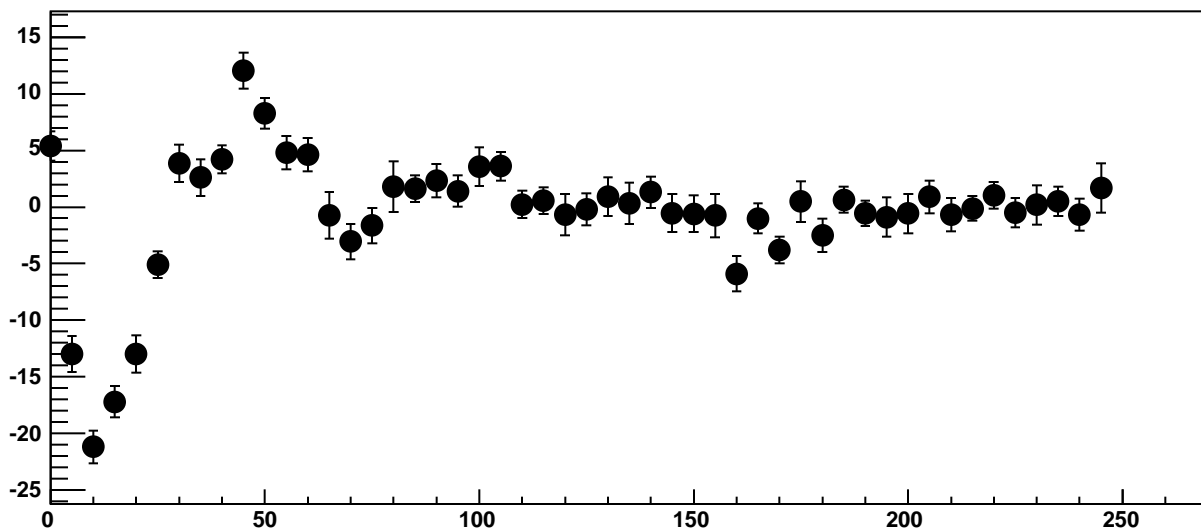


$\chi^2 / \text{ndf}$	425.2 / 41
p0	$206.7 \pm 0.4624$
p1	$71.22 \pm 0.5728$
p2	$-3.844\text{e}+08 \pm 1.414$
p3	$2.809\text{e}+08 \pm 1.414$
p4	$1.275 \pm 0.004916$

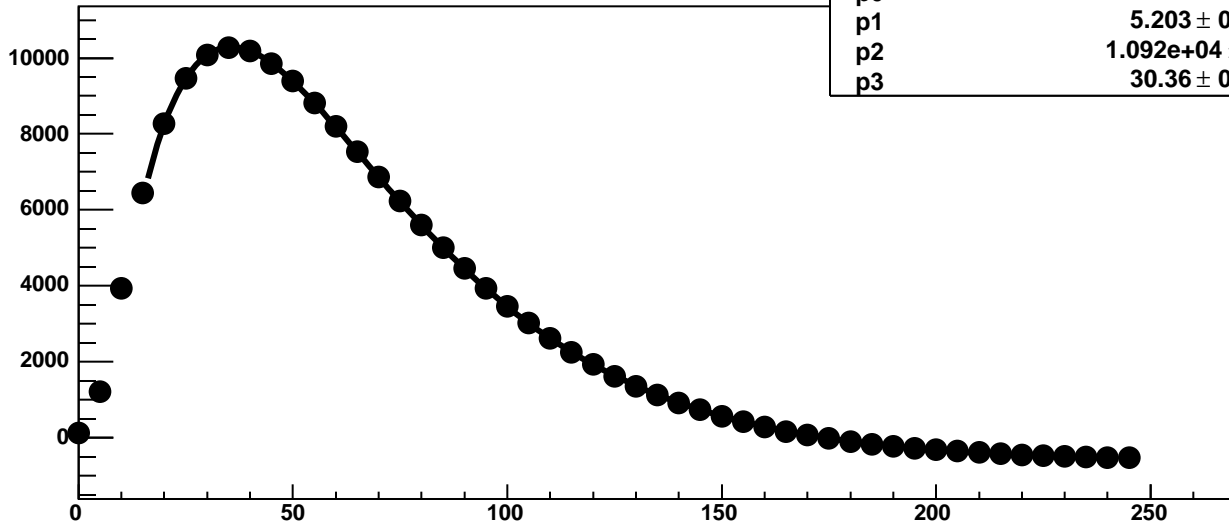
Chip 4, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

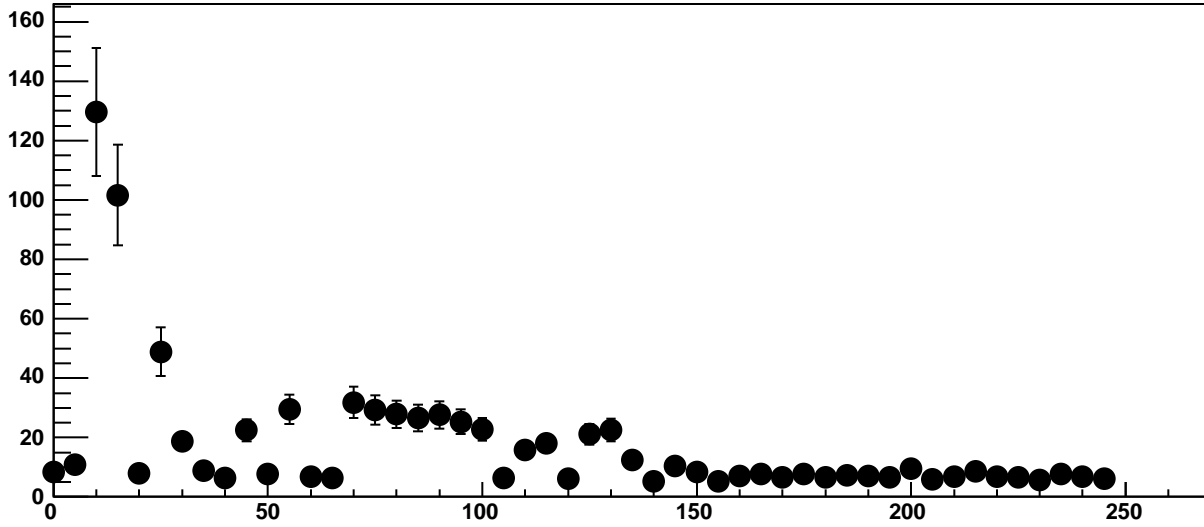


Chip 4, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold

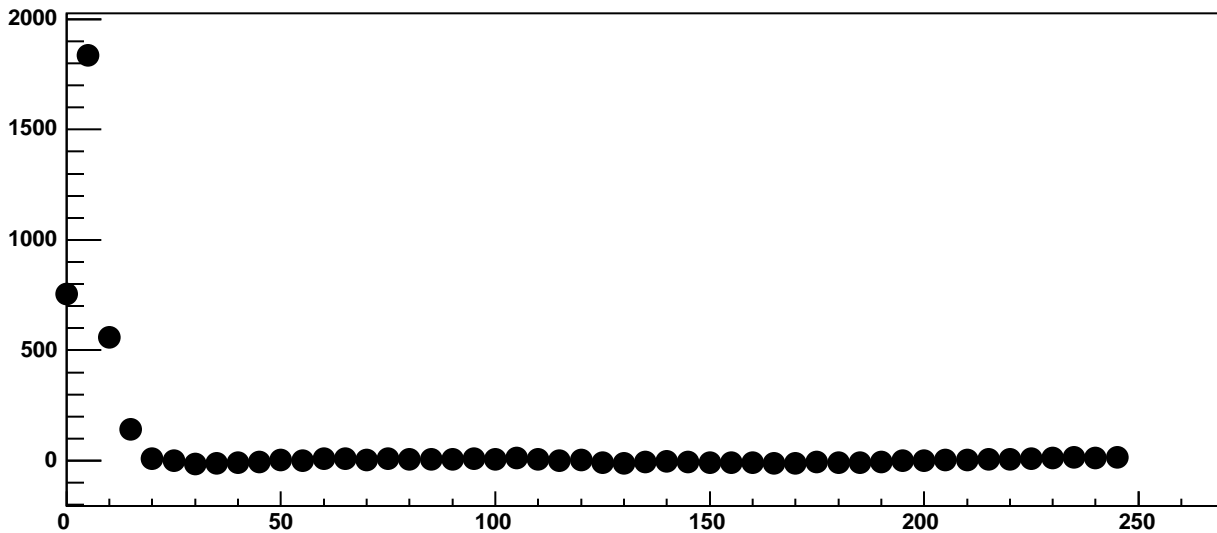


$\chi^2 / \text{ndf}$	886.2 / 42
p0	-628 ± 0.5405
p1	5.203 ± 0.007394
p2	1.092e+04 ± 0.8881
p3	30.36 ± 0.005339

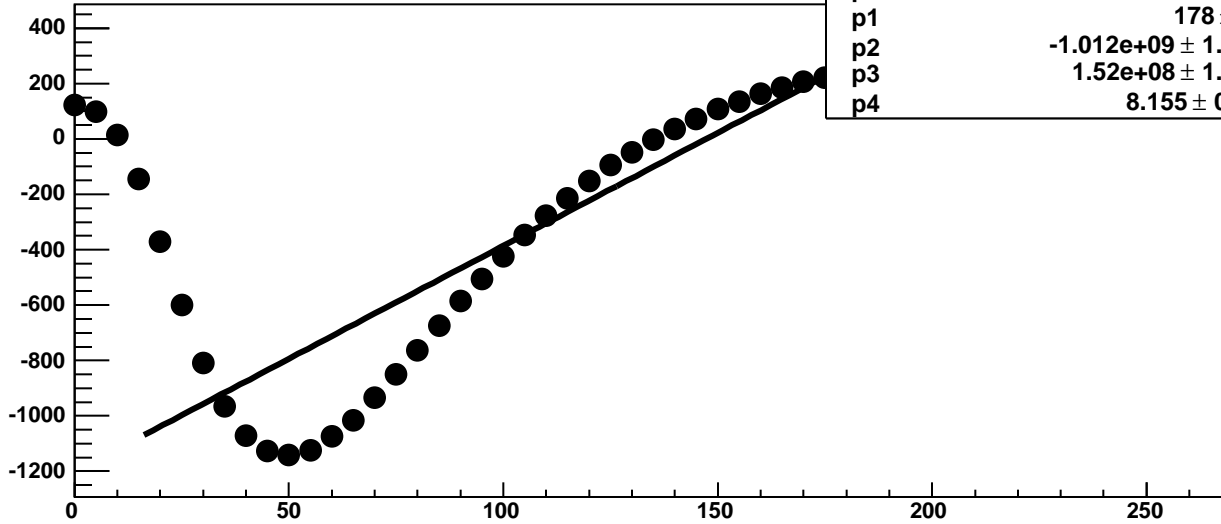
Chip 4, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold

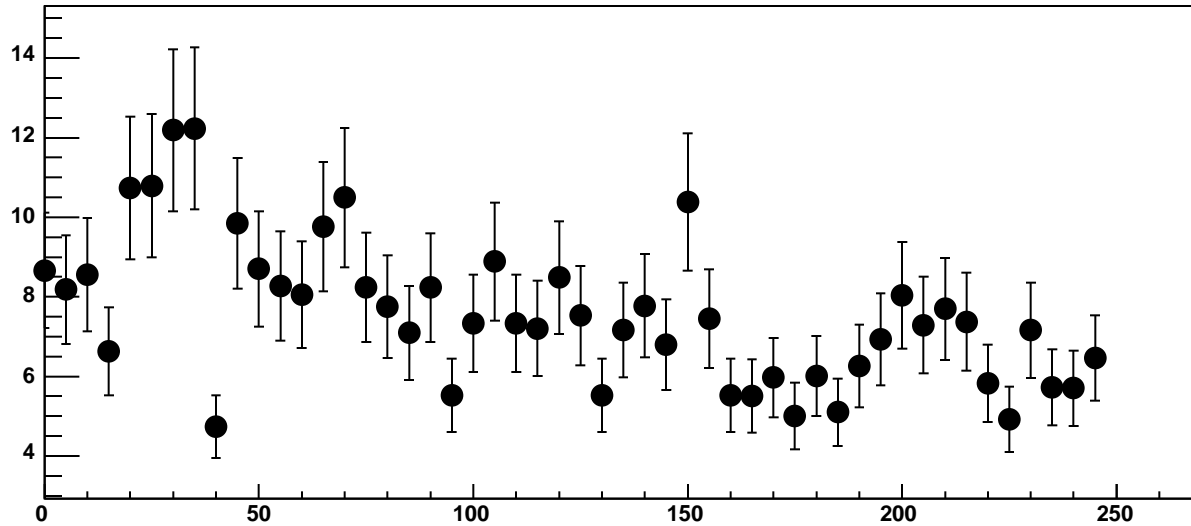


Chip 4, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

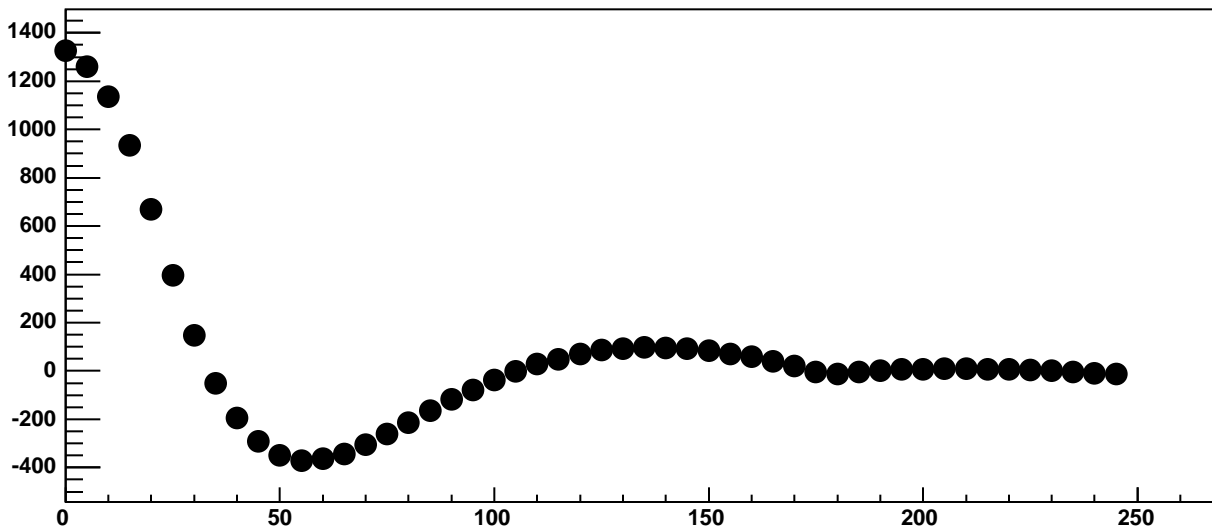


$\chi^2 / \text{ndf}$	7.536e+05 / 41
p0	250.4 ± 0.9402
p1	178 ± 0.1386
p2	-1.012e+09 ± 1.291e+07
p3	1.52e+08 ± 1.876e+06
p4	8.155 ± 0.005985

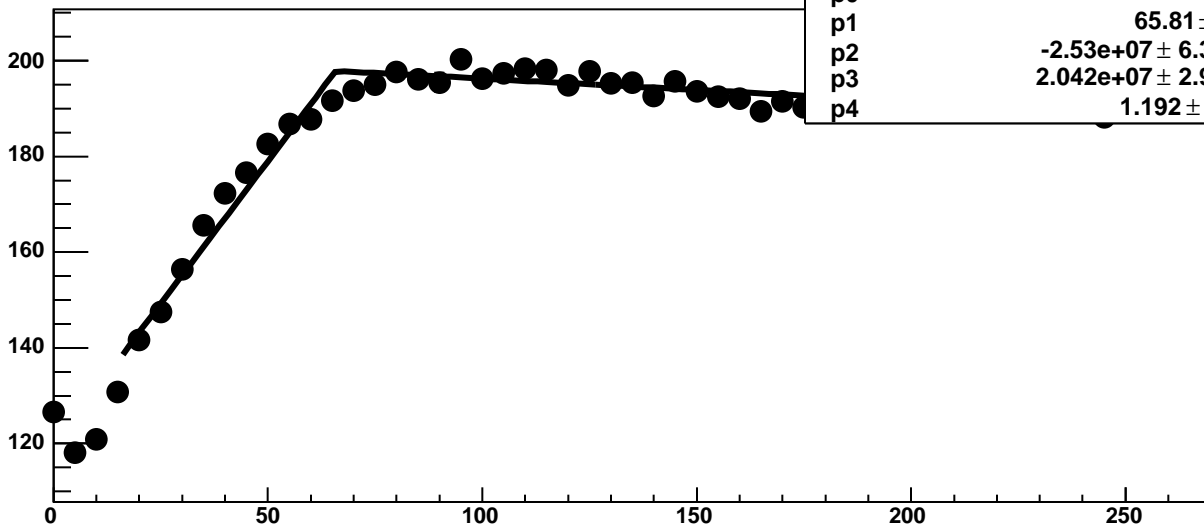
Chip 4, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

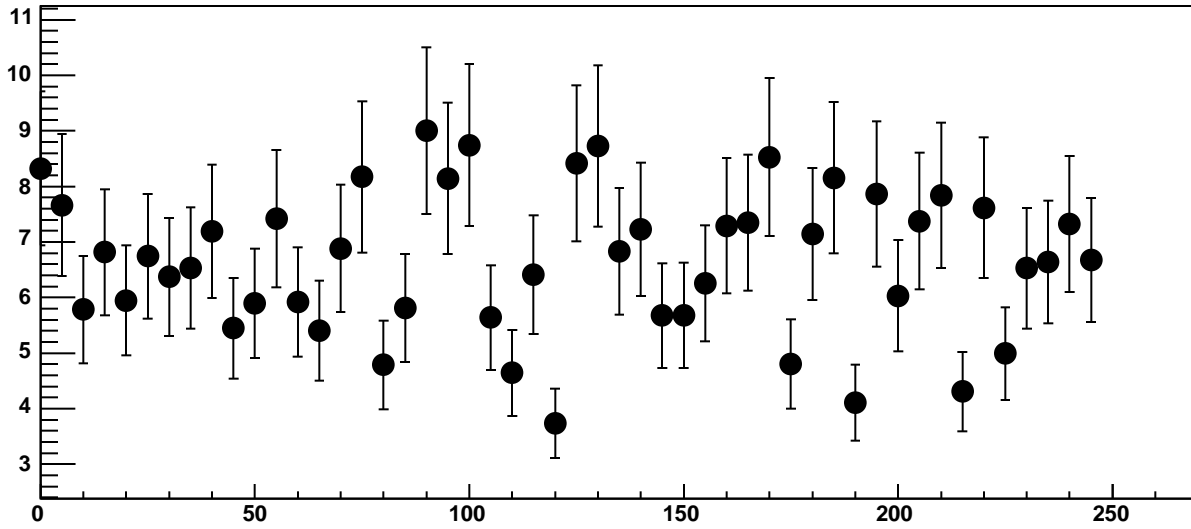


Chip 4, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

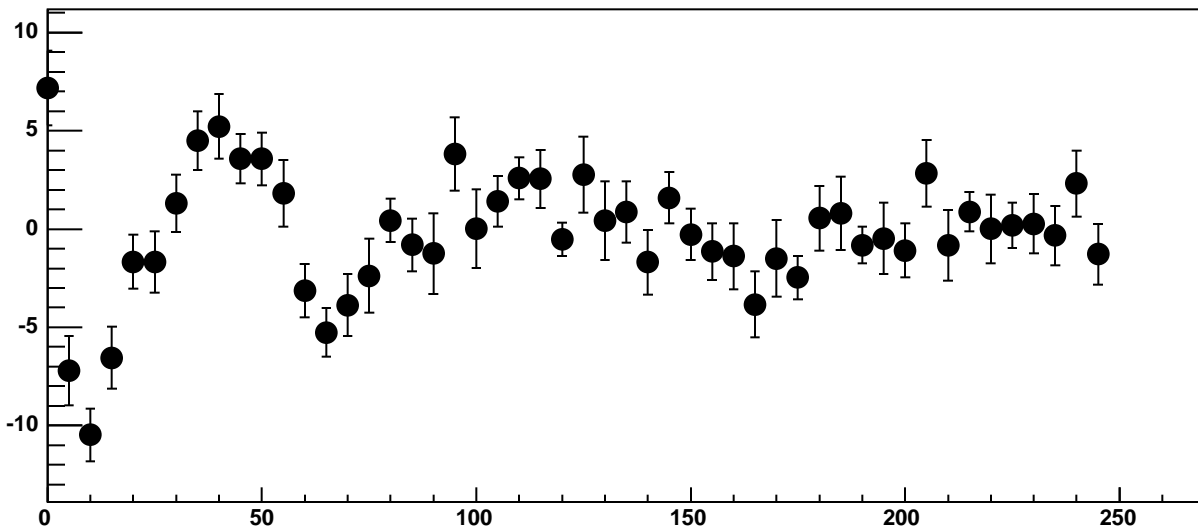


$\chi^2 / \text{ndf}$	127.5 / 41
p0	$197.9 \pm 0.4708$
p1	$65.81 \pm 0.7475$
p2	$-2.53\text{e}+07 \pm 6.318\text{e}+05$
p3	$2.042\text{e}+07 \pm 2.991\text{e}+05$
p4	$1.192 \pm 0.02678$

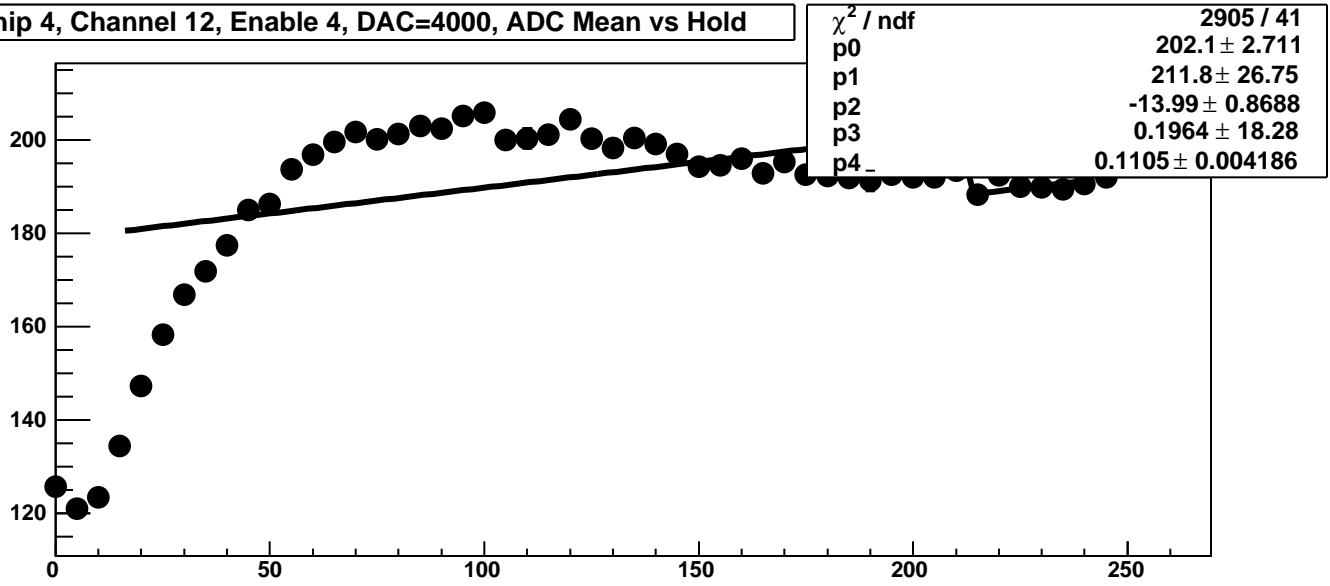
Chip 4, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



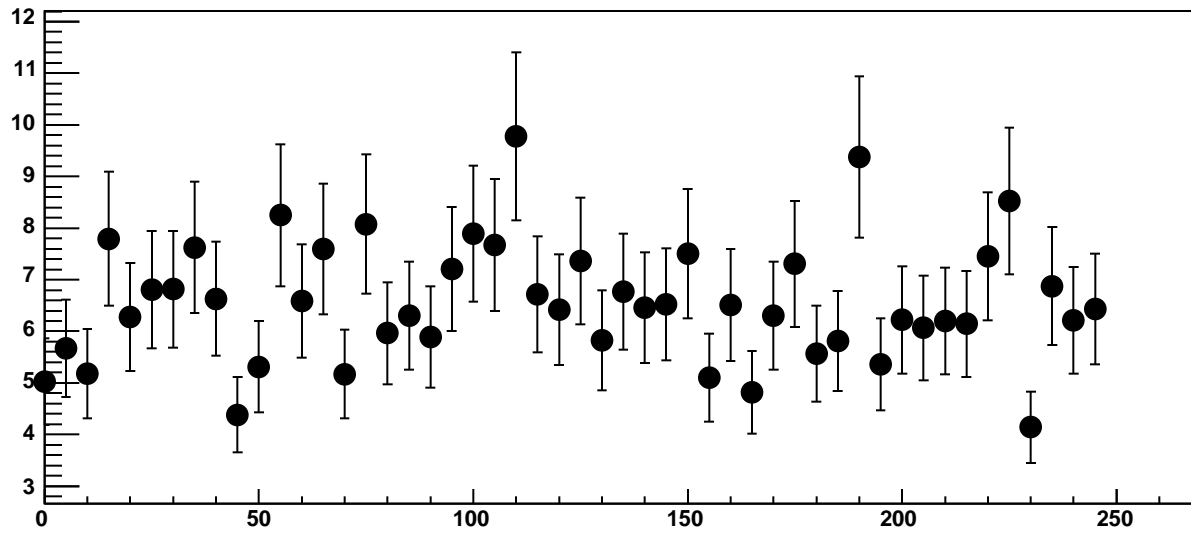
Chip 4, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold



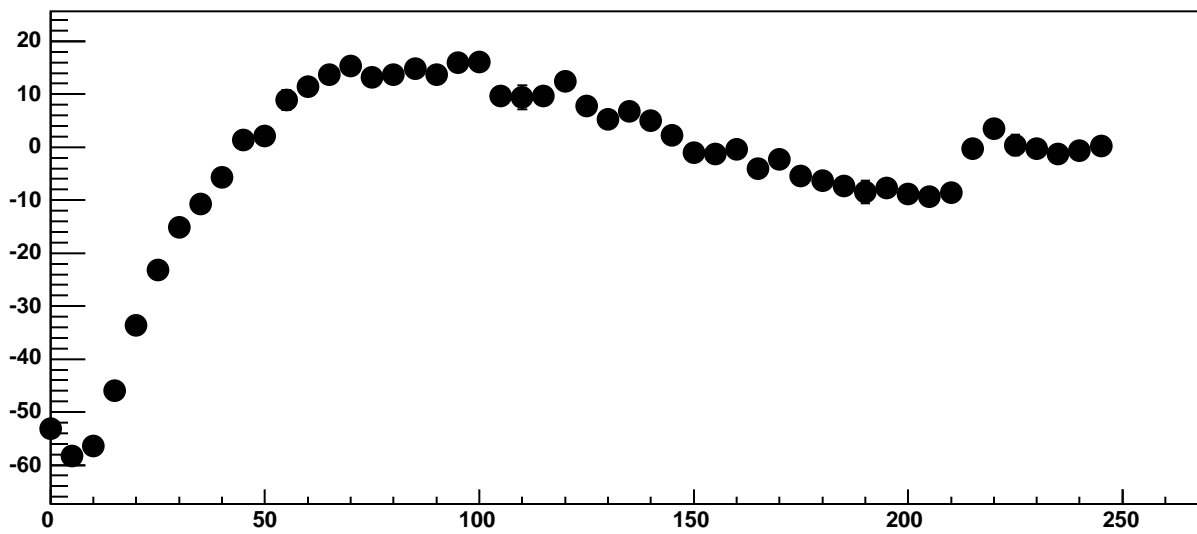
Chip 4, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold



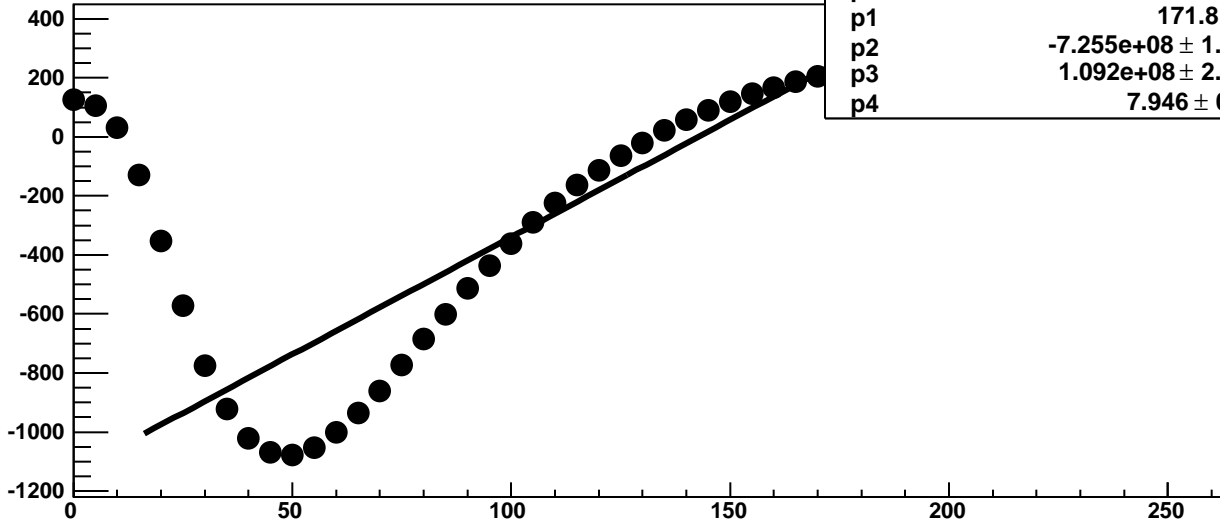
Chip 4, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold

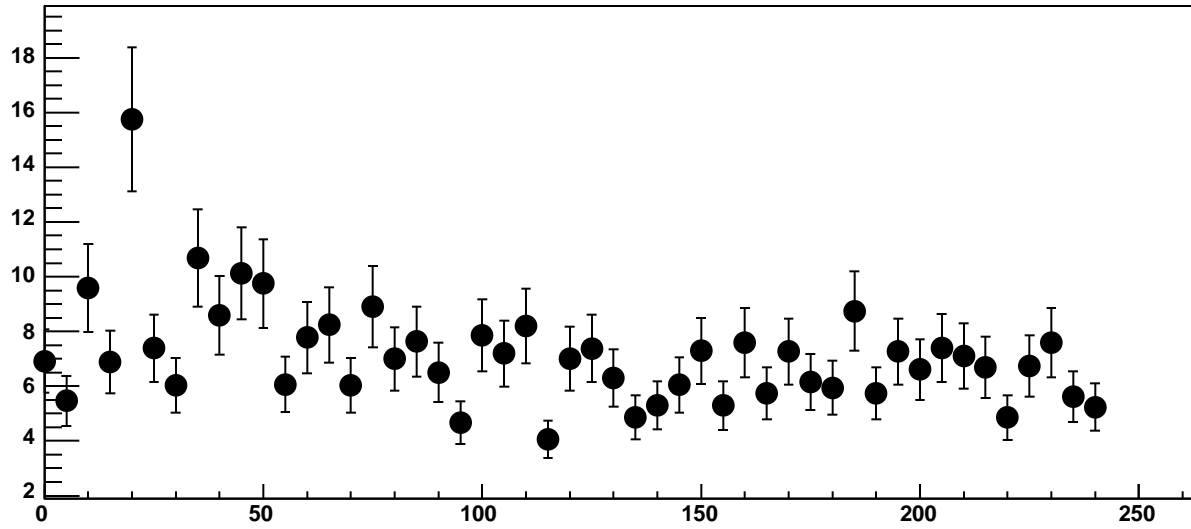


Chip 4, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

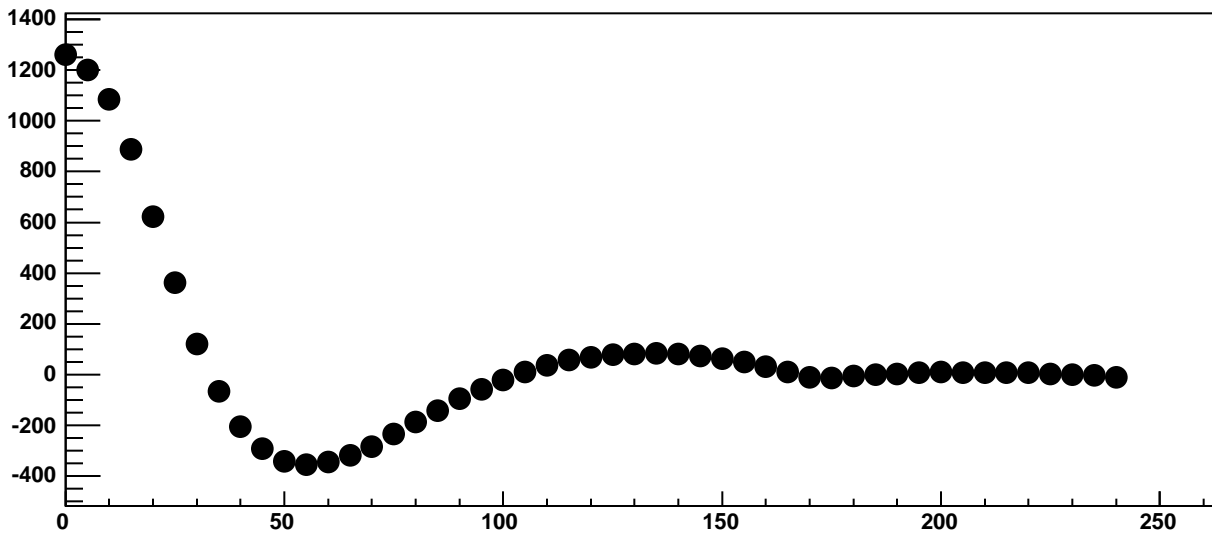


$\chi^2 / \text{ndf}$	6.905e+05 / 41
p0	231.3 ± 0.9574
p1	171.8 ± 0.1441
p2	-7.255e+08 ± 1.545e+07
p3	1.092e+08 ± 2.332e+06
p4	7.946 ± 0.006492

Chip 4, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold

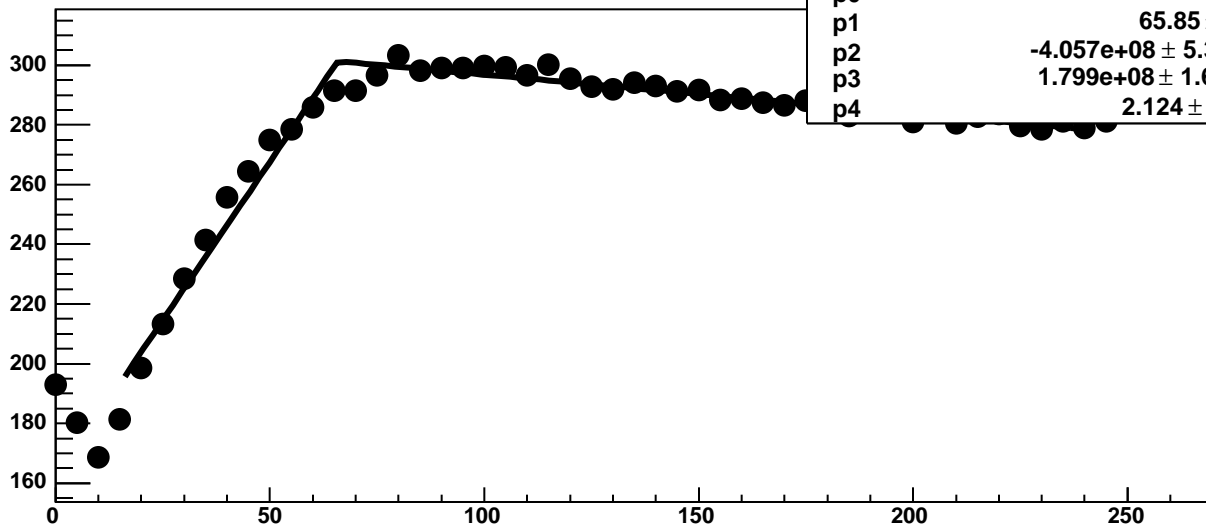


Chip 4, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold



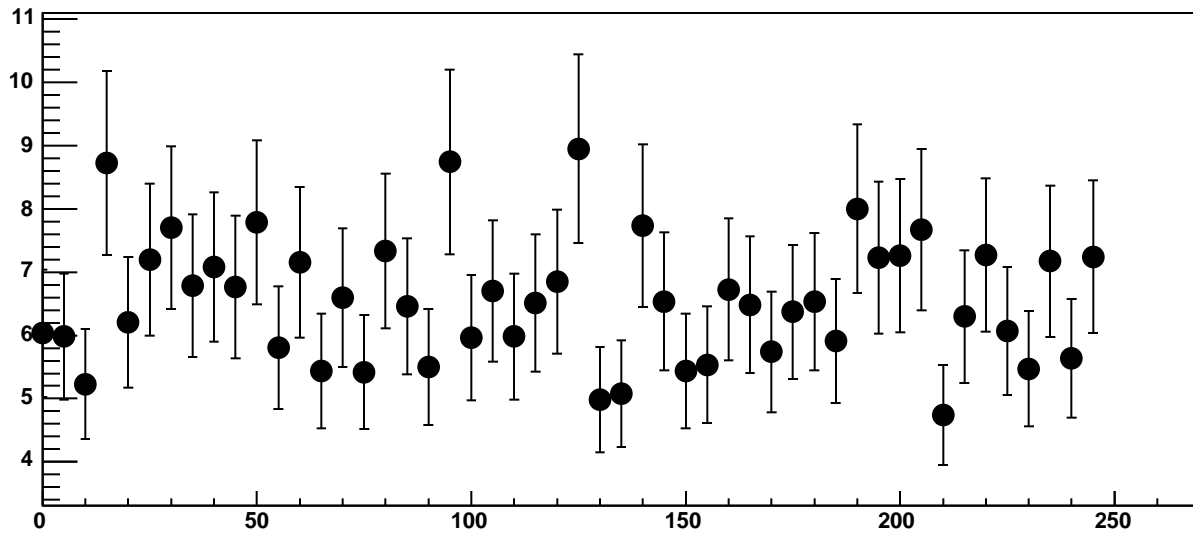


Chip 4, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold

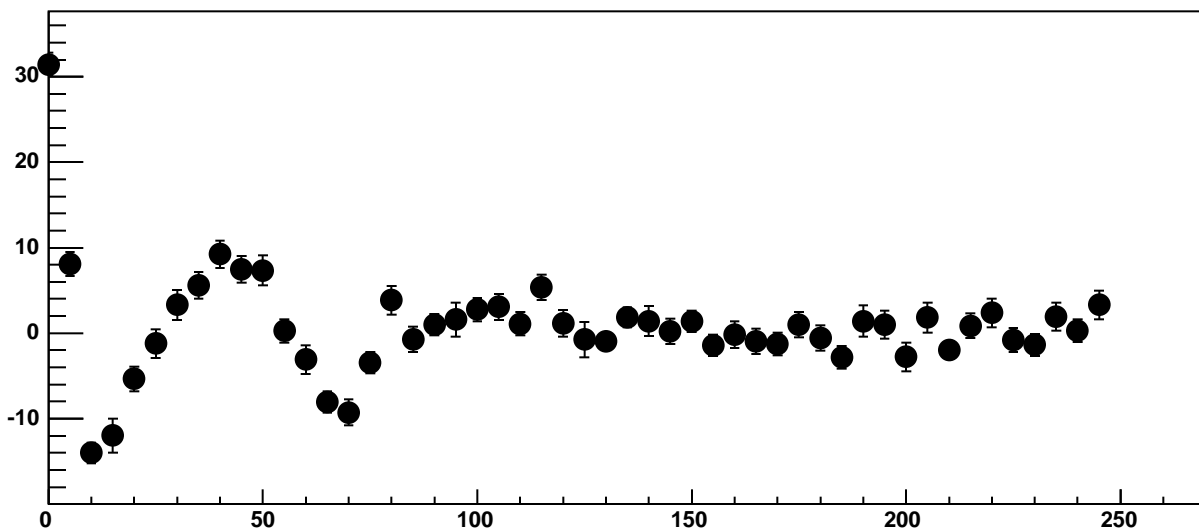


$\chi^2 / \text{ndf}$	285.8 / 41
p0	$301.3 \pm 0.4861$
p1	$65.85 \pm 0.4391$
p2	$-4.057\text{e}+08 \pm 5.324\text{e}+06$
p3	$1.799\text{e}+08 \pm 1.658\text{e}+05$
p4	$2.124 \pm 0.02936$

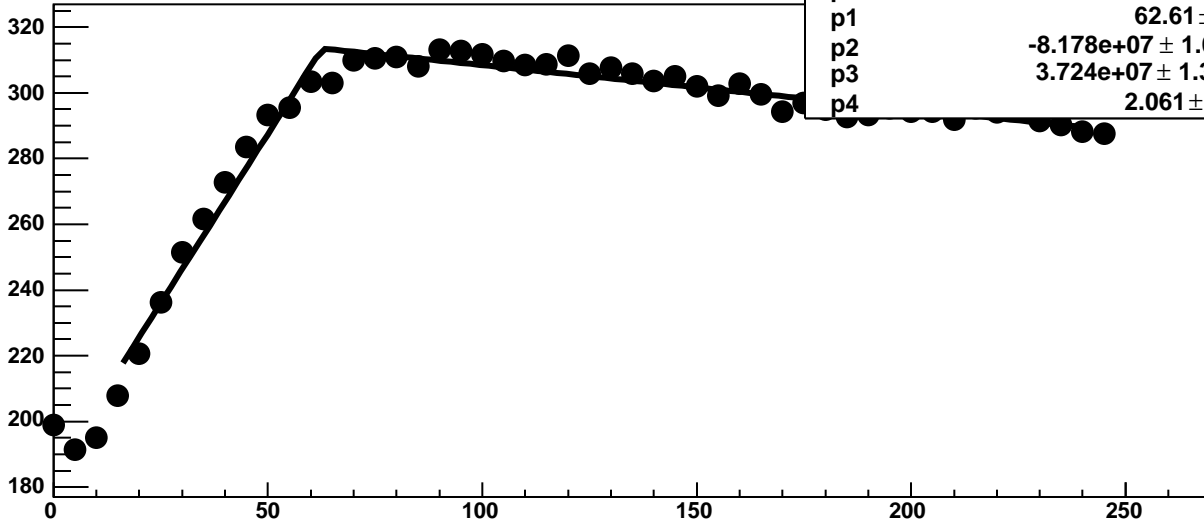
Chip 4, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold

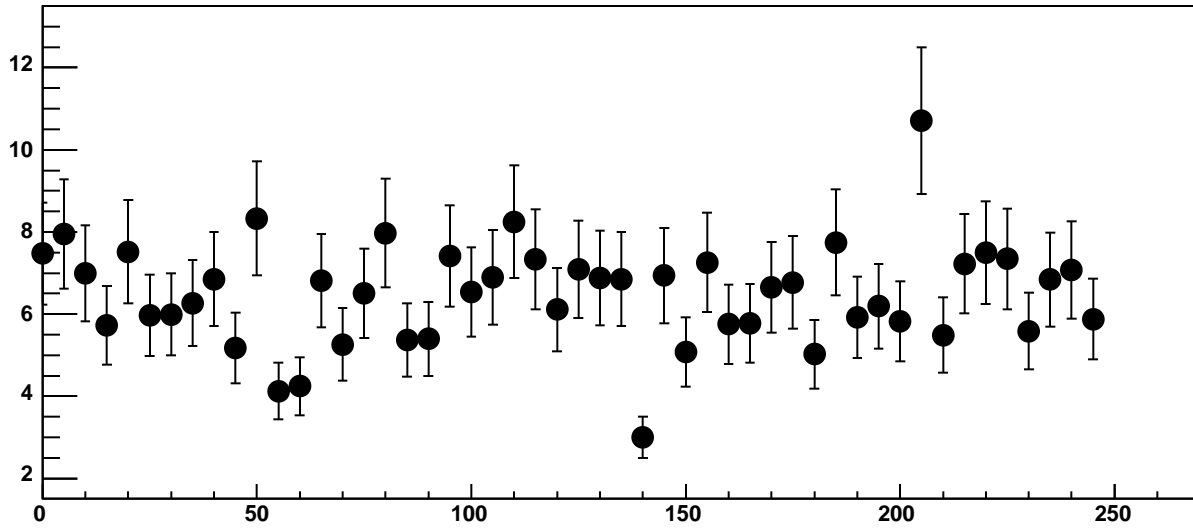


Chip 4, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold

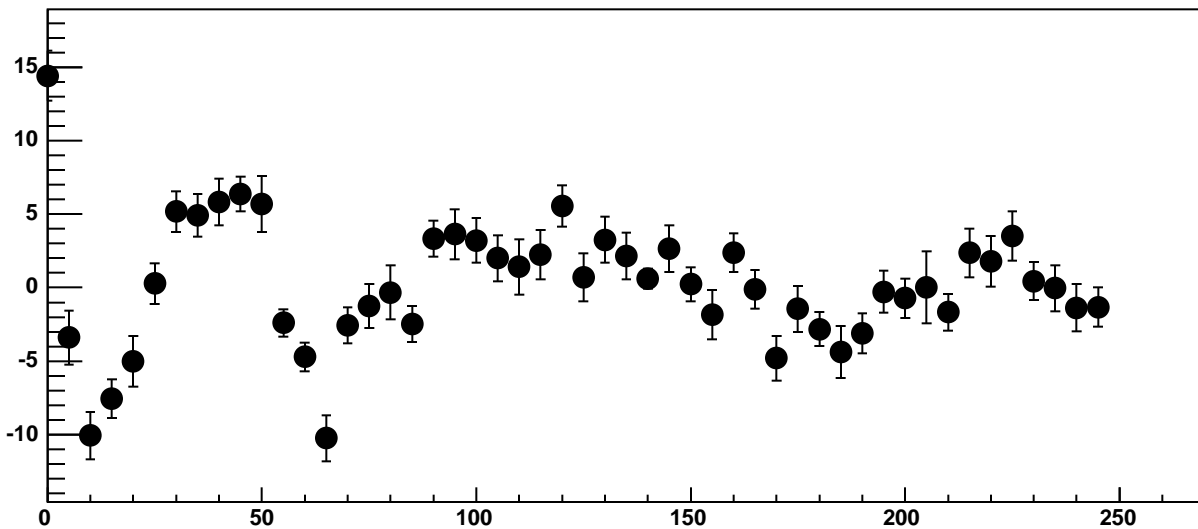


$\chi^2 / \text{ndf}$	289.5 / 41
p0	$313.5 \pm 0.4514$
p1	$62.61 \pm 0.3874$
p2	$-8.178e+07 \pm 1.046e+06$
p3	$3.724e+07 \pm 1.328e+05$
p4	$2.061 \pm 0.02681$

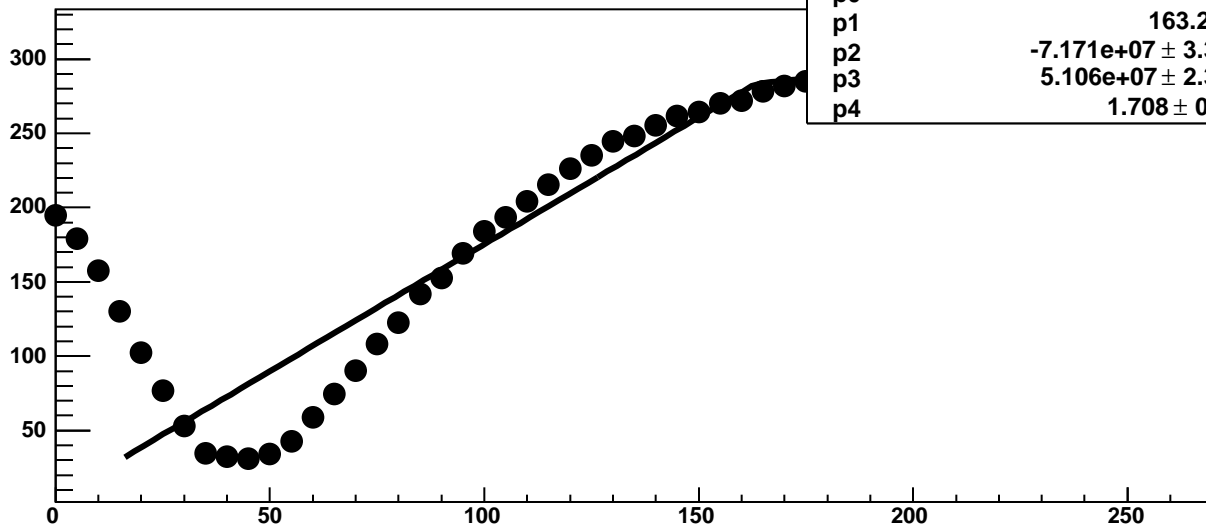
Chip 4, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold

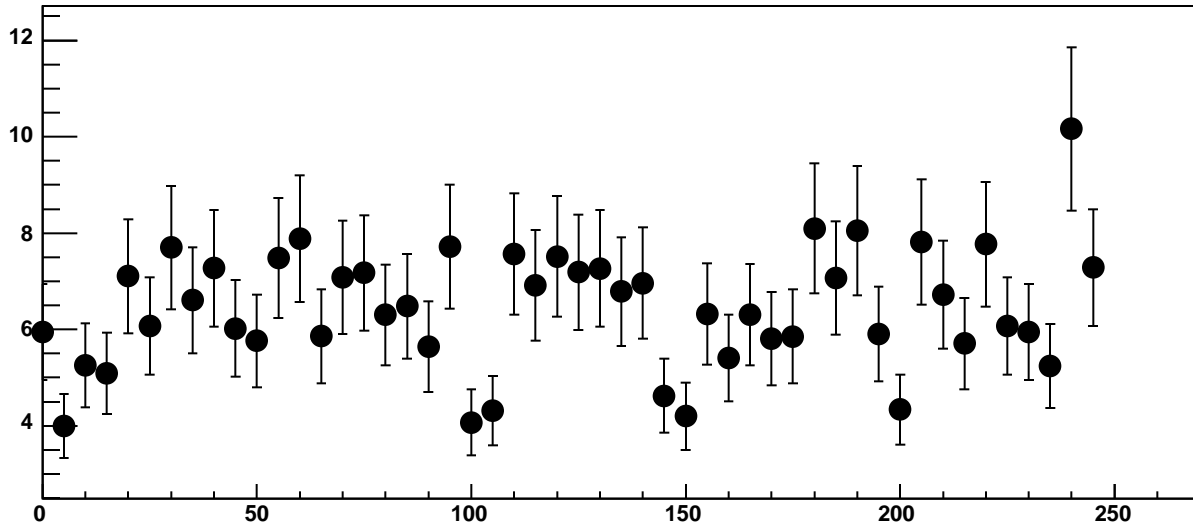


Chip 4, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

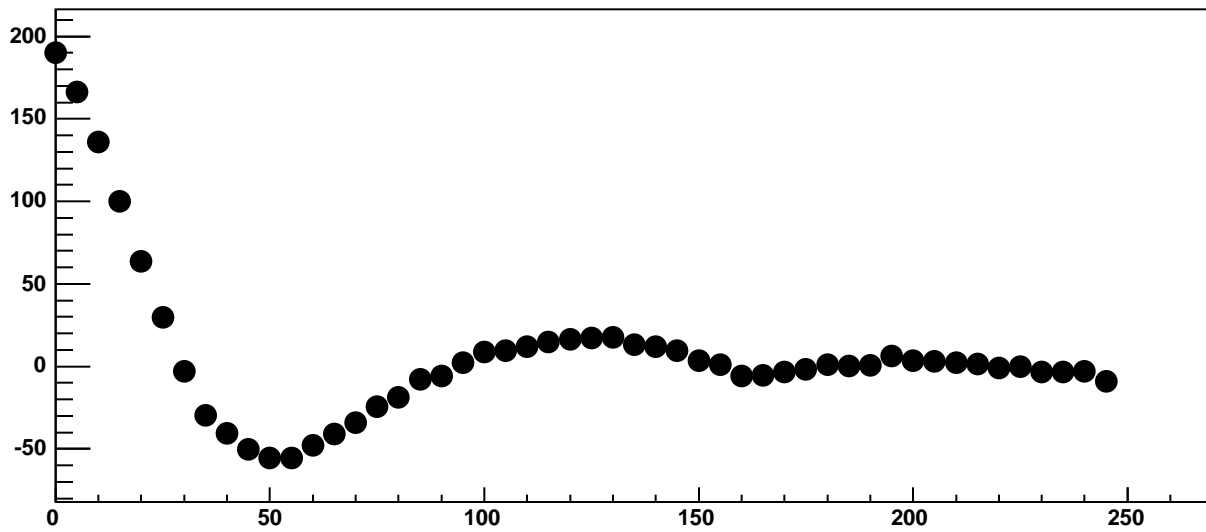


$\chi^2 / \text{ndf}$	1.789e+04 / 41
p0	283.3 ± 0.8998
p1	163.2 ± 0.627
p2	-7.171e+07 ± 3.353e+06
p3	5.106e+07 ± 2.385e+06
p4	1.708 ± 0.005743

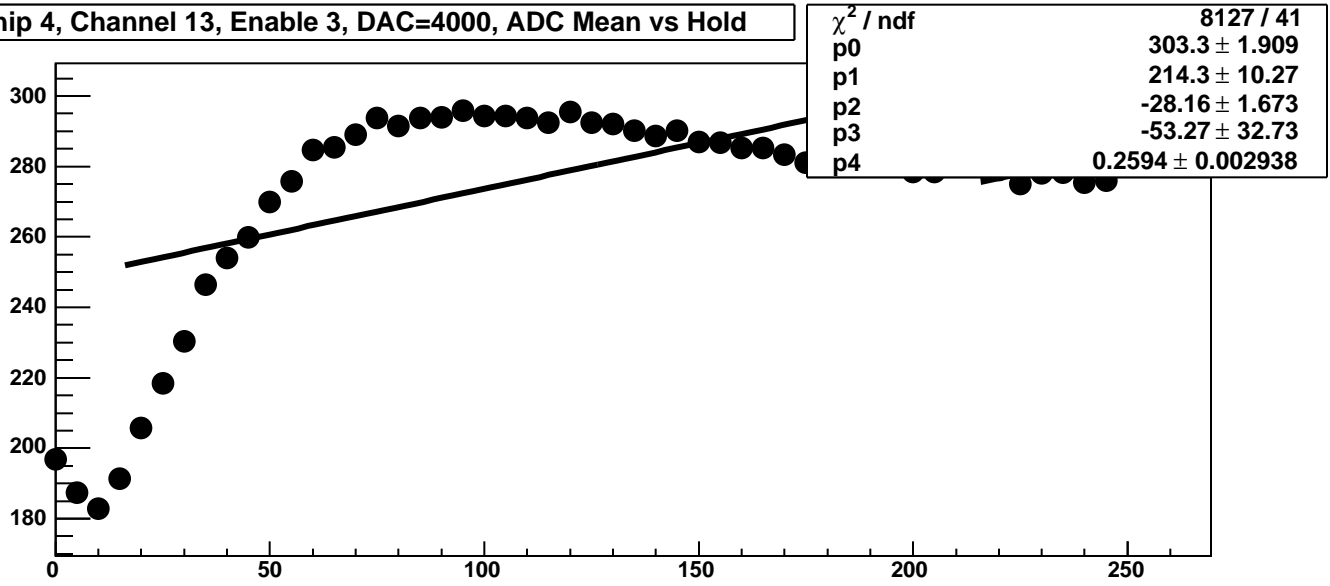
Chip 4, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



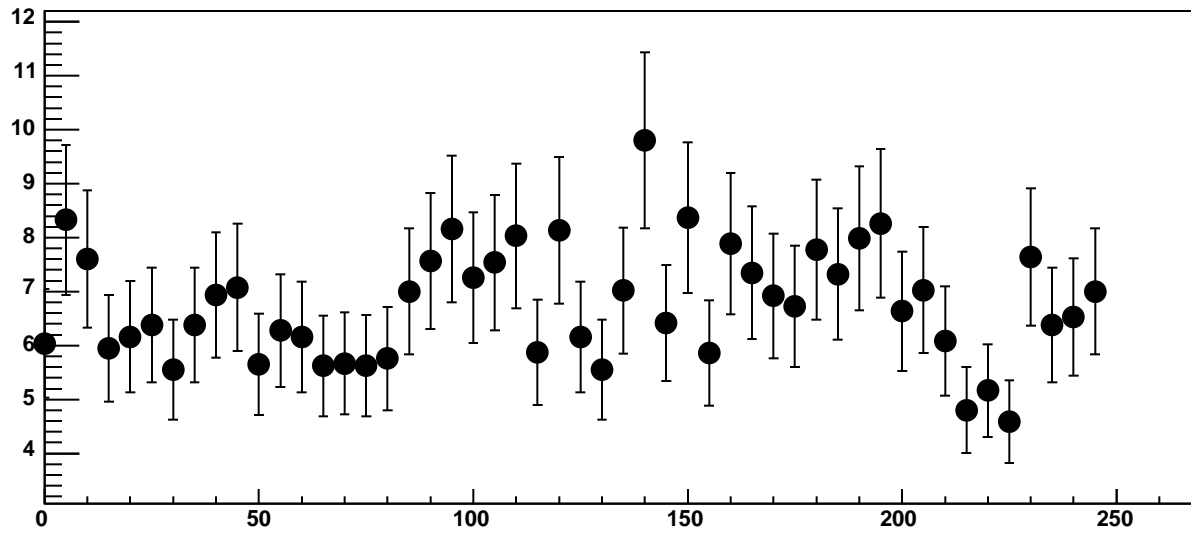
Chip 4, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



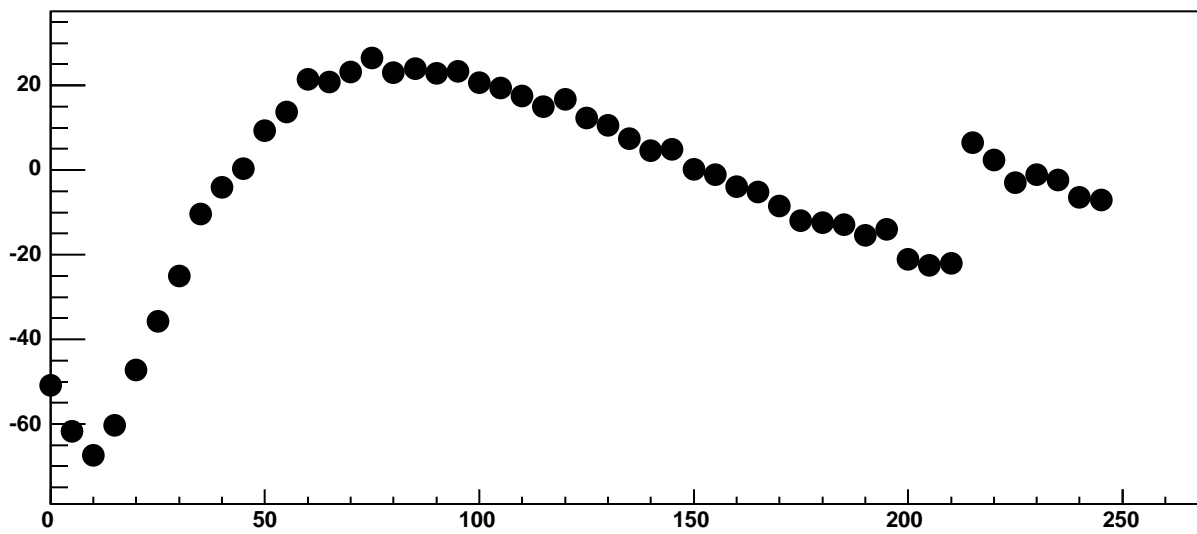
Chip 4, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold



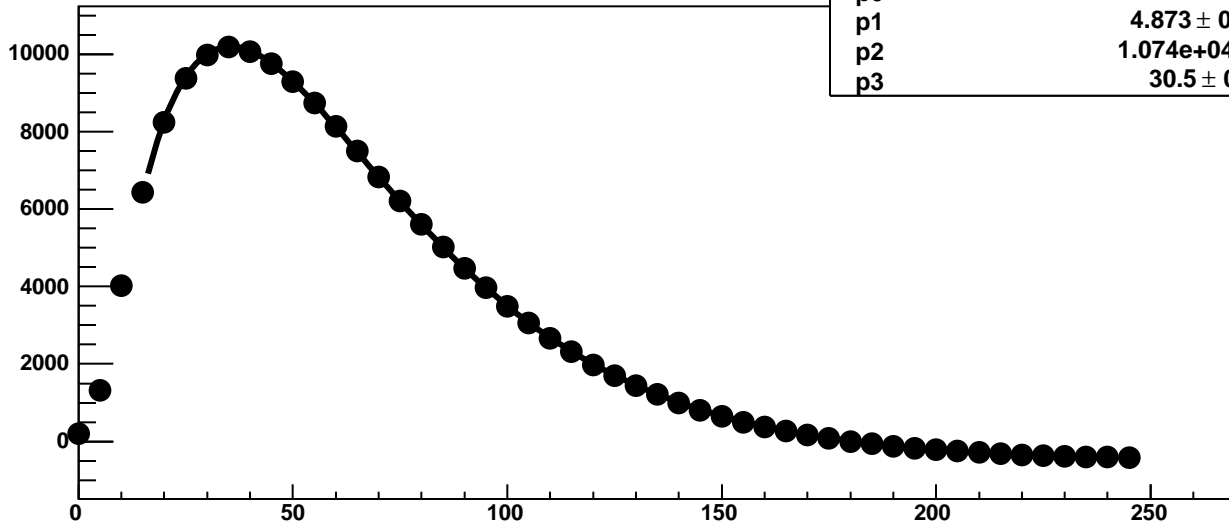
Chip 4, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

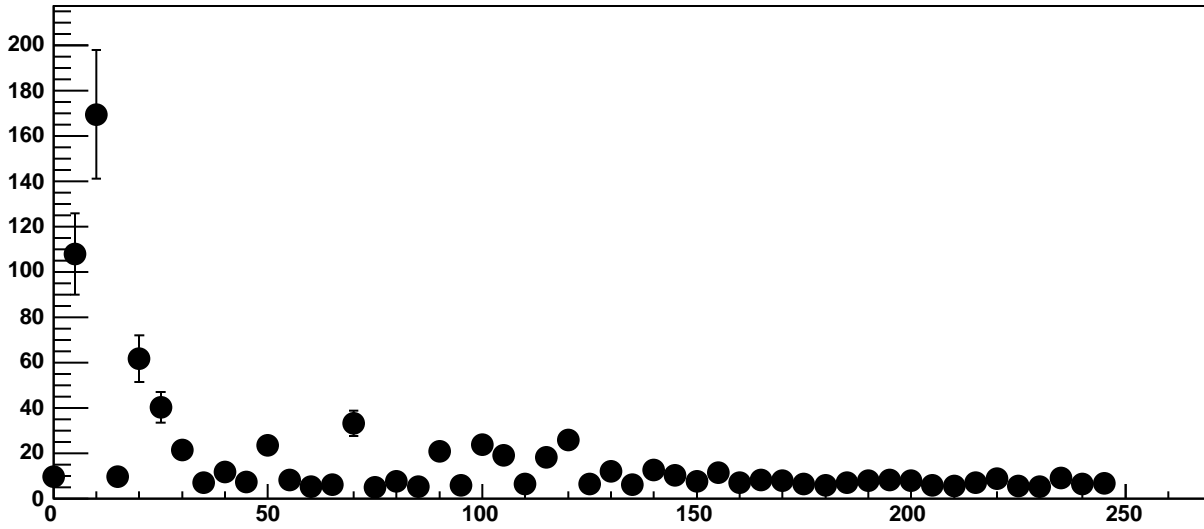


Chip 4, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold

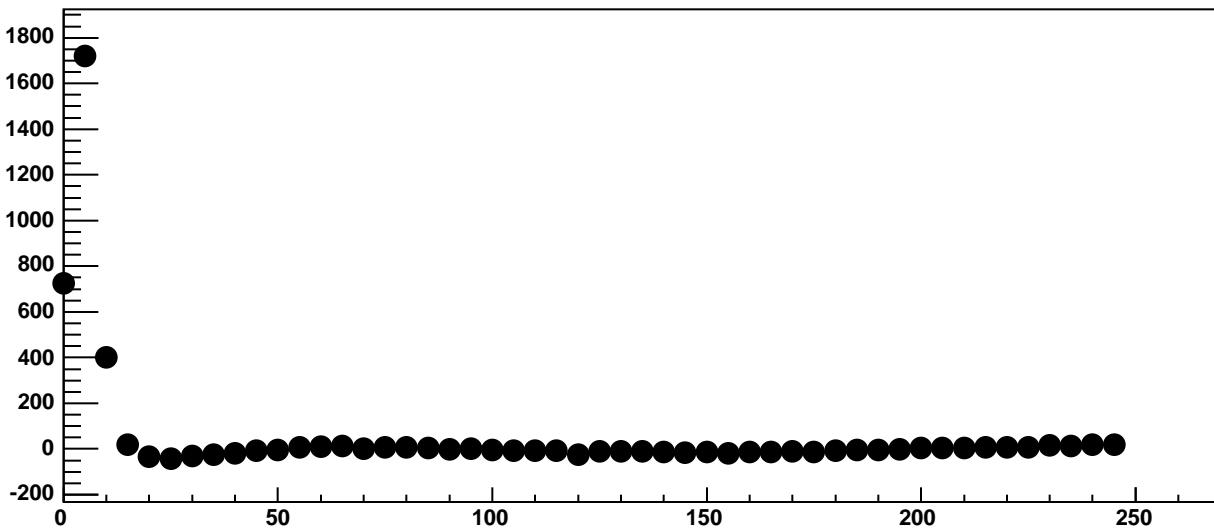


$\chi^2 / \text{ndf}$	1579 / 42
p0	-530.2 ± 0.4956
p1	4.873 ± 0.005793
p2	1.074e+04 ± 0.921
p3	30.5 ± 0.004381

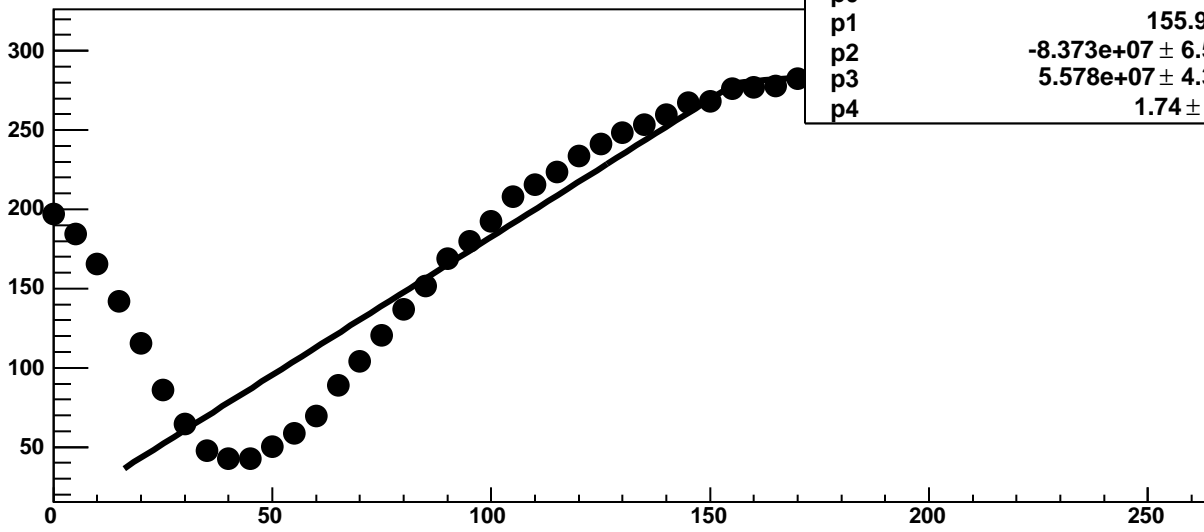
Chip 4, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold

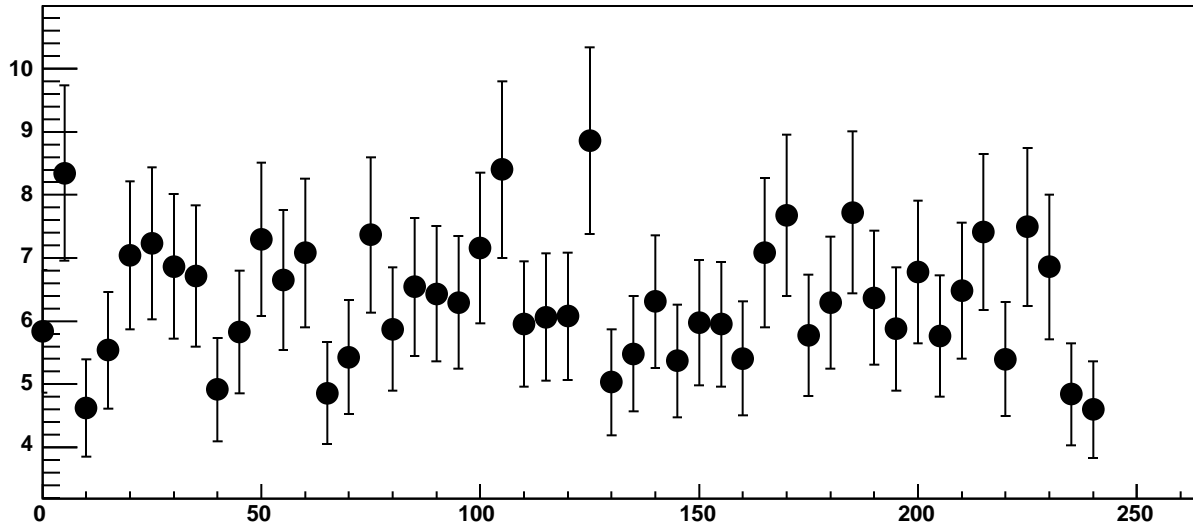


Chip 4, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

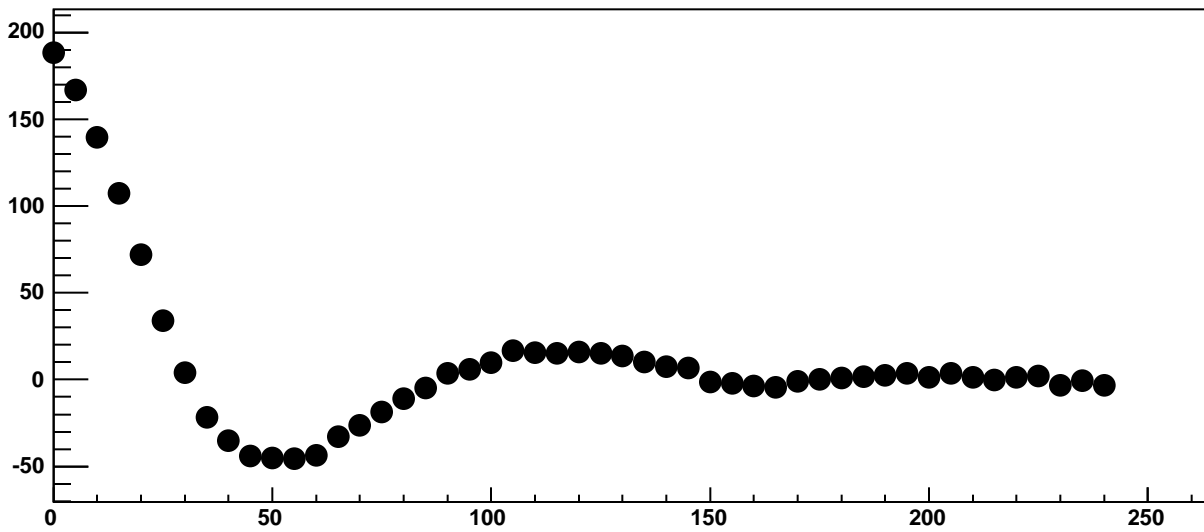


$\chi^2 / \text{ndf}$	1.656e+04 / 41
p0	279.9 ± 0.8217
p1	155.9 ± 0.578
p2	-8.373e+07 ± 6.579e+06
p3	5.578e+07 ± 4.352e+06
p4	1.74 ± 0.00619

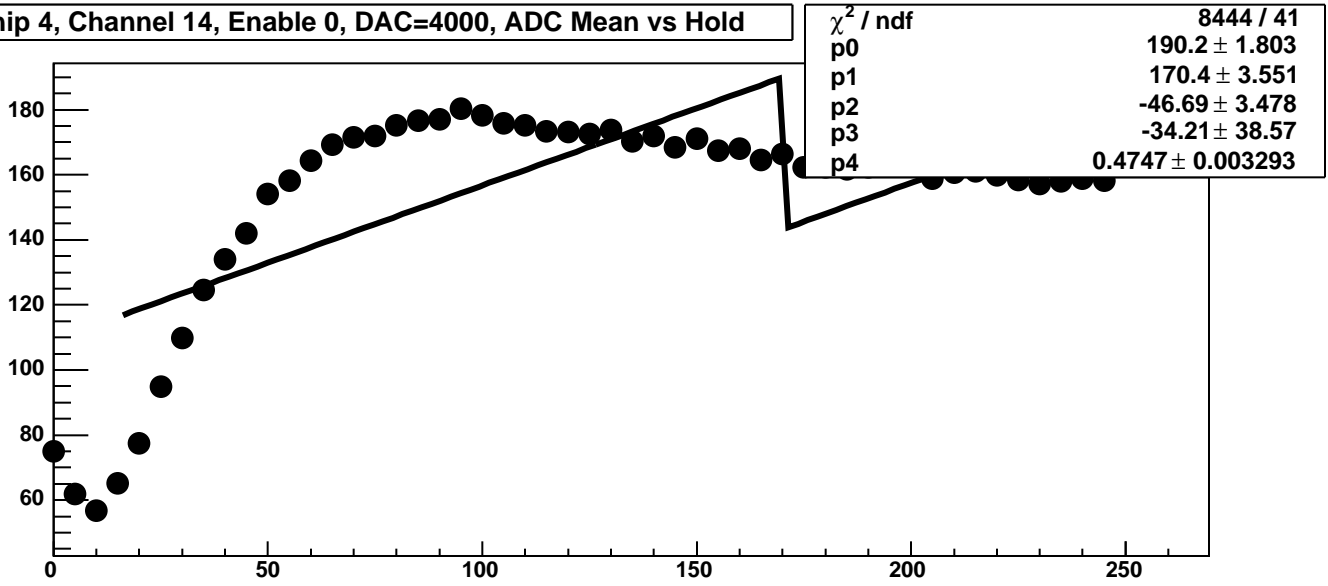
Chip 4, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



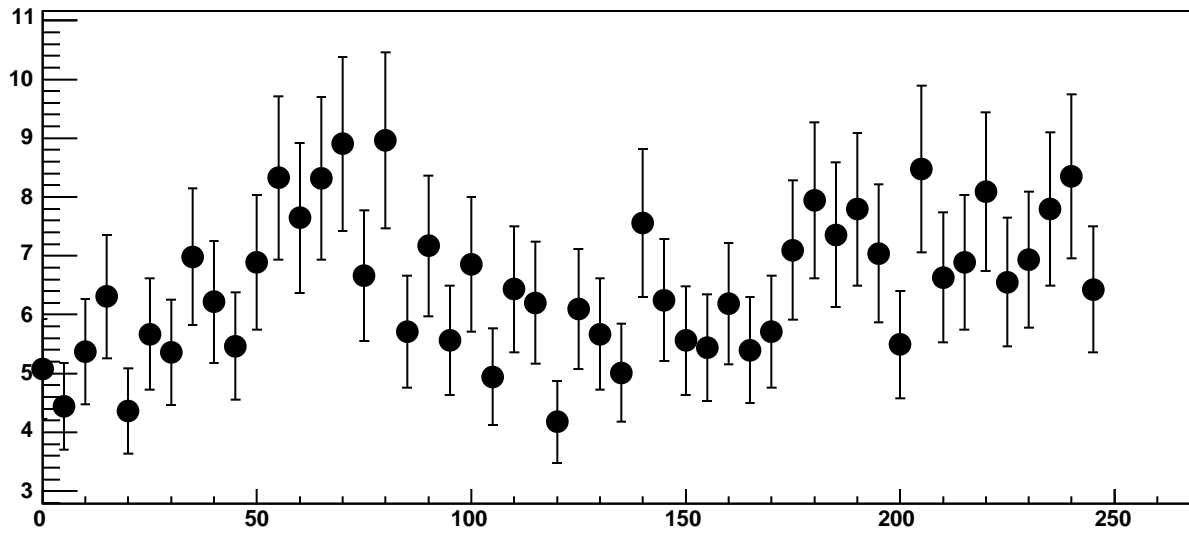
Chip 4, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold



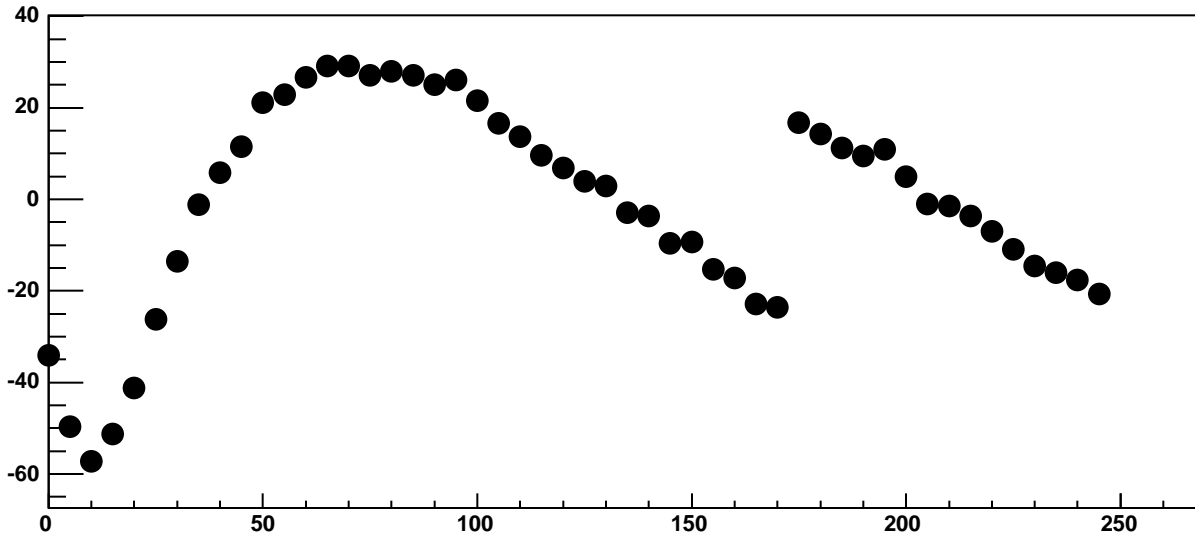
Chip 4, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold



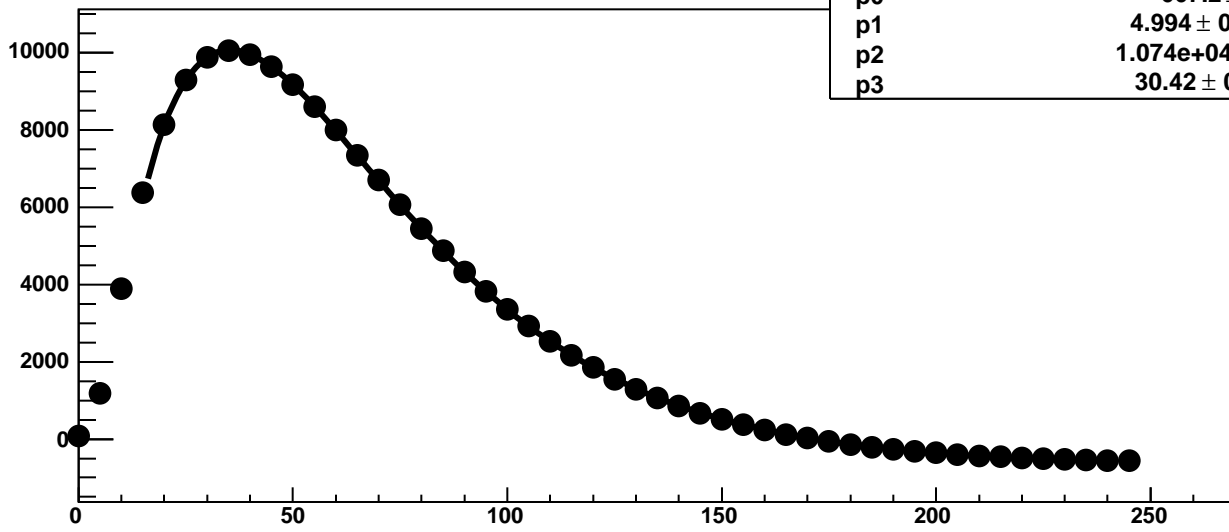
Chip 4, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

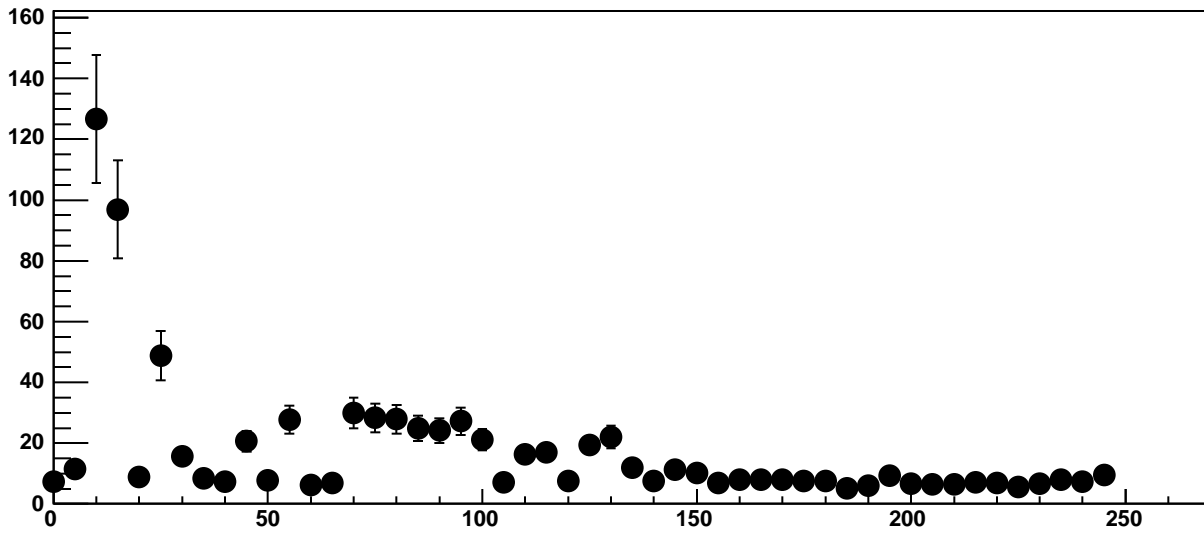


Chip 4, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold

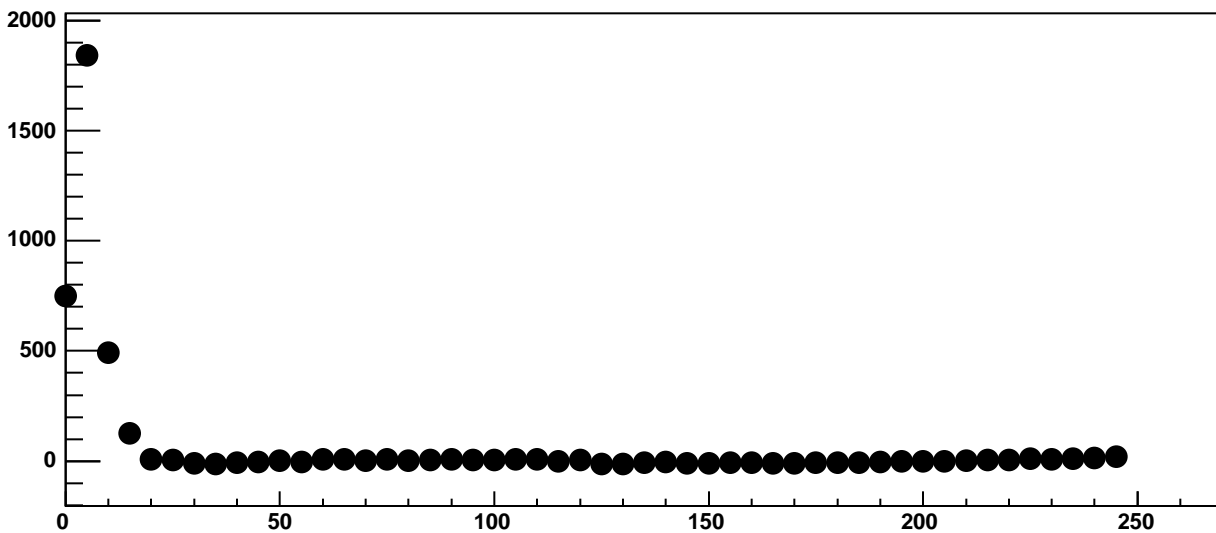


$\chi^2 / \text{ndf}$	699.3 / 42
p0	-667.2 ± 0.5438
p1	4.994 ± 0.008409
p2	1.074e+04 ± 0.922
p3	30.42 ± 0.005971

Chip 4, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold

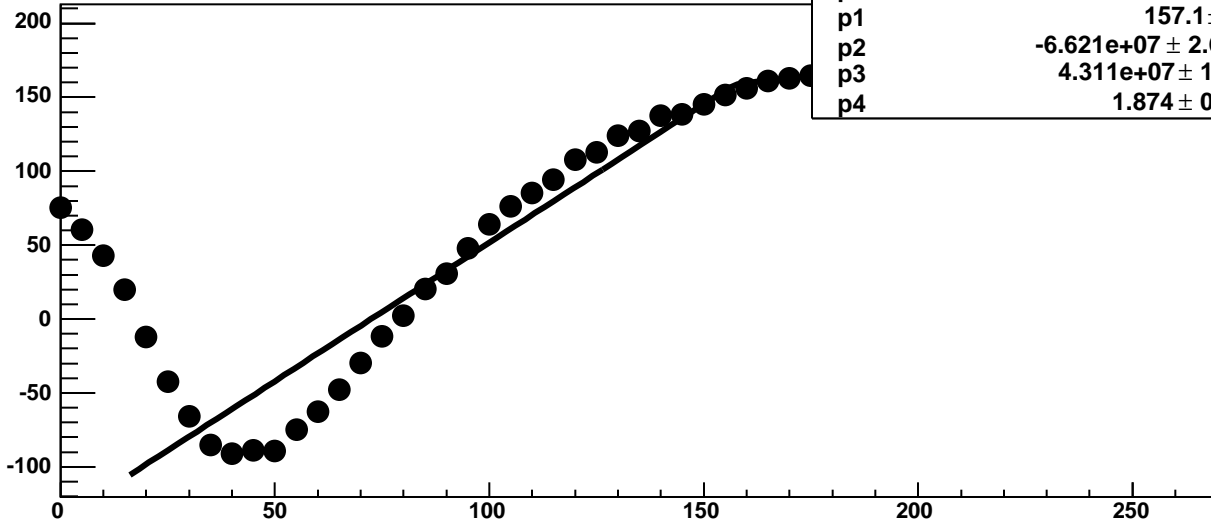


Chip 4, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold



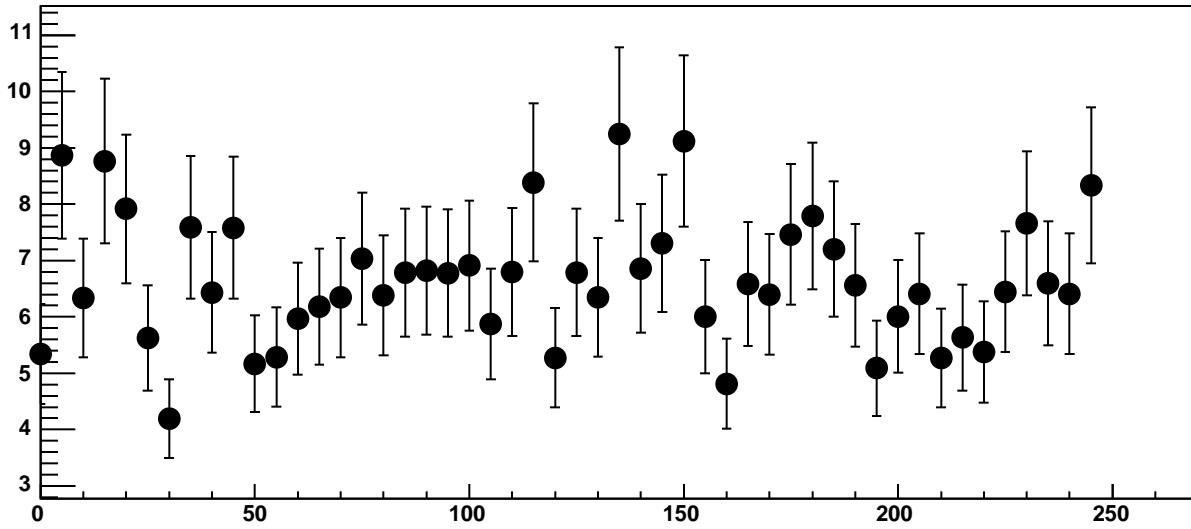


Chip 4, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

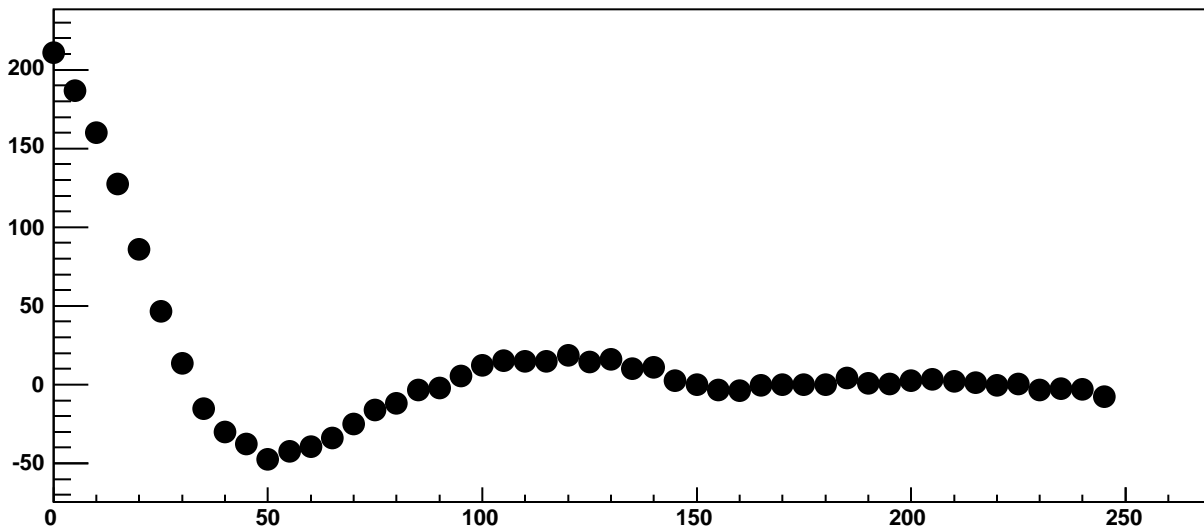


$\chi^2 / \text{ndf}$	1.433e+04 / 41
p0	158.6 ± 0.8483
p1	157.1 ± 0.5867
p2	-6.621e+07 ± 2.621e+06
p3	4.311e+07 ± 1.65e+06
p4	1.874 ± 0.006737

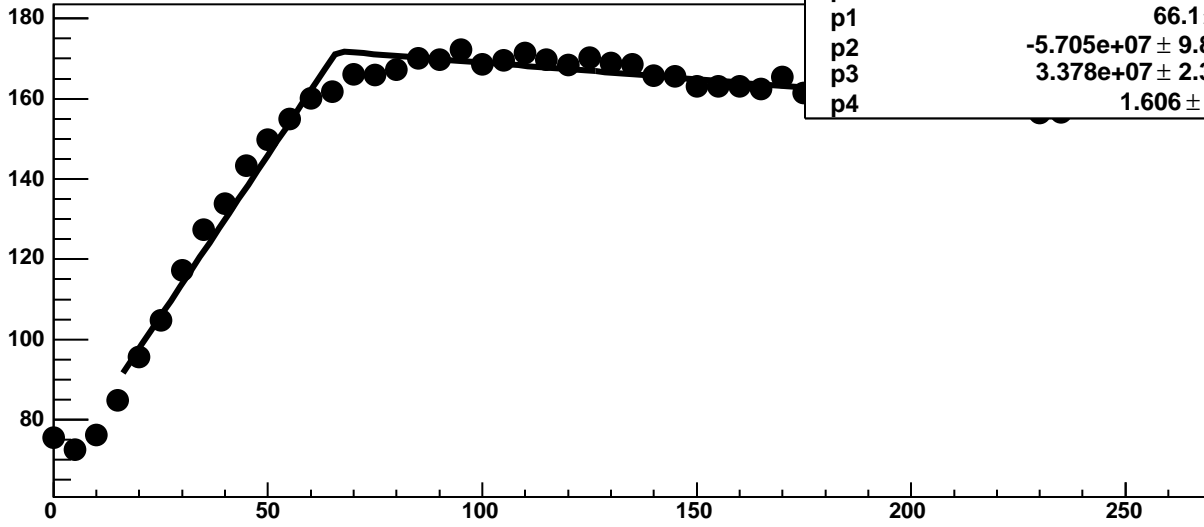
Chip 4, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

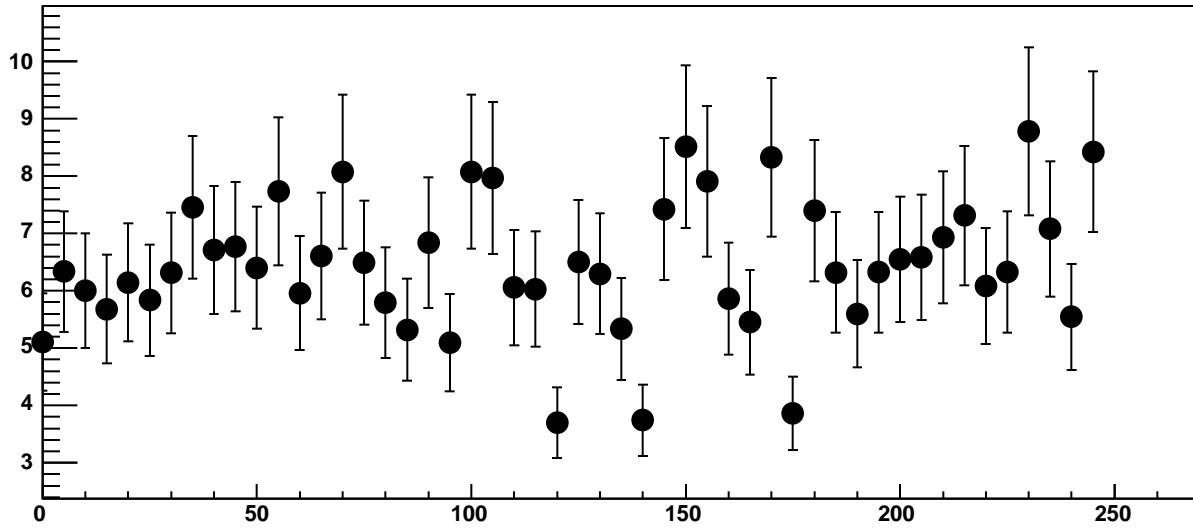


Chip 4, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

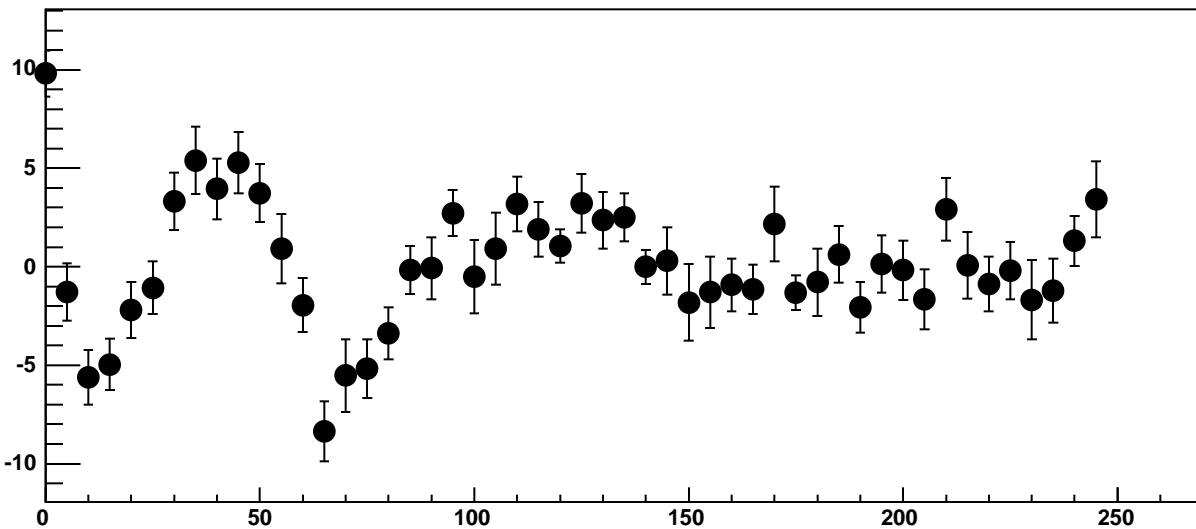


$\chi^2 / \text{ndf}$	159.9 / 41
p0	$171.9 \pm 0.4573$
p1	$66.1 \pm 0.5881$
p2	$-5.705e+07 \pm 9.852e+05$
p3	$3.378e+07 \pm 2.312e+05$
p4	$1.606 \pm 0.02714$

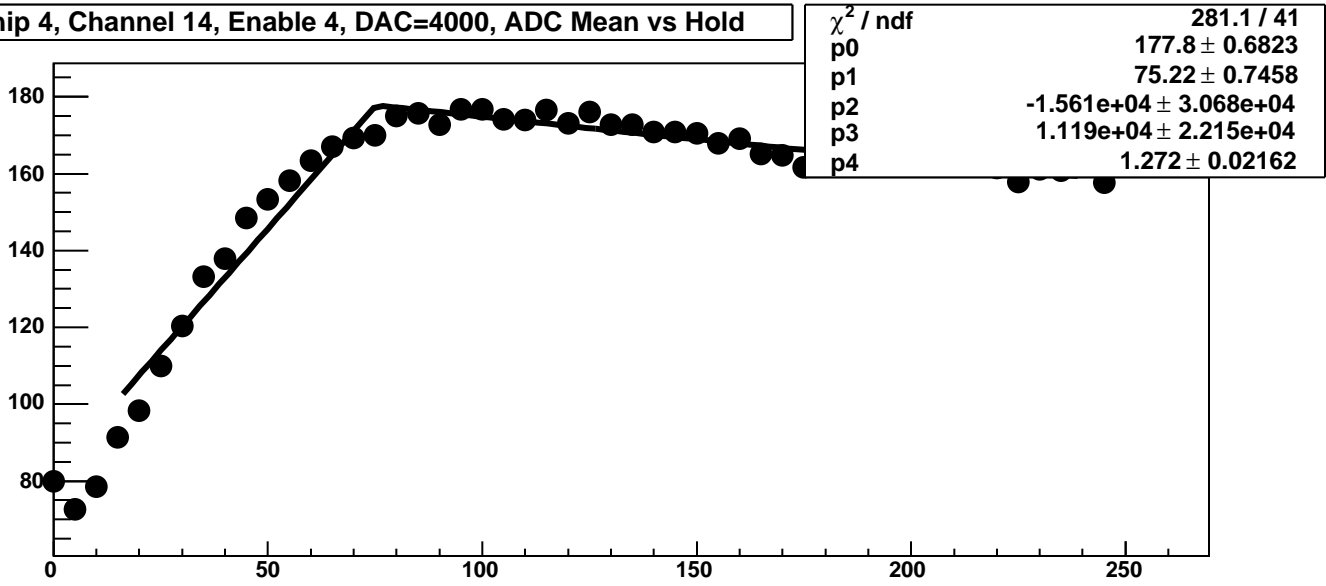
Chip 4, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



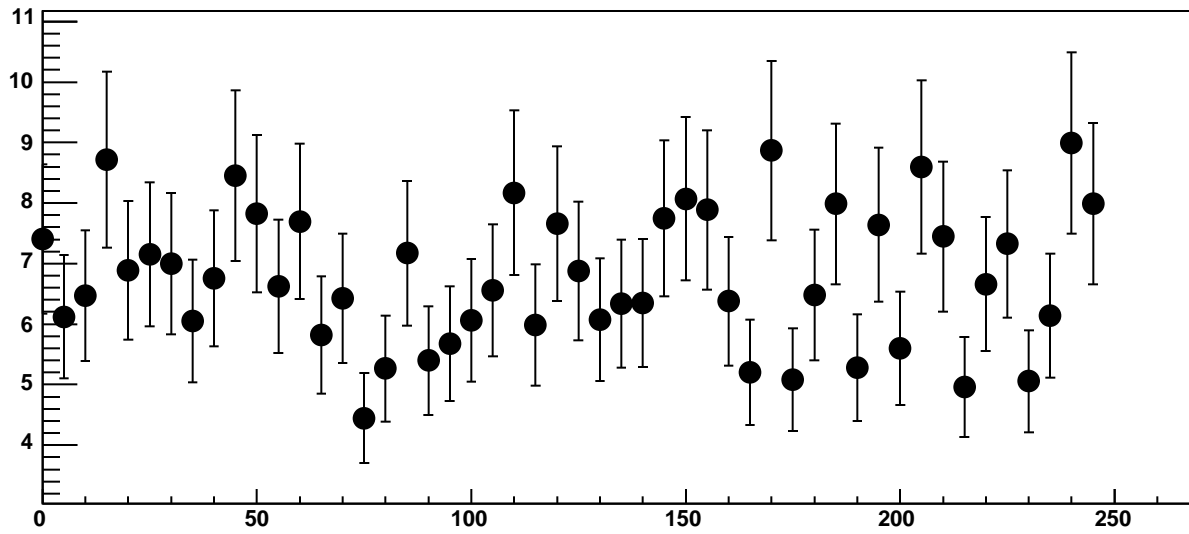
Chip 4, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold



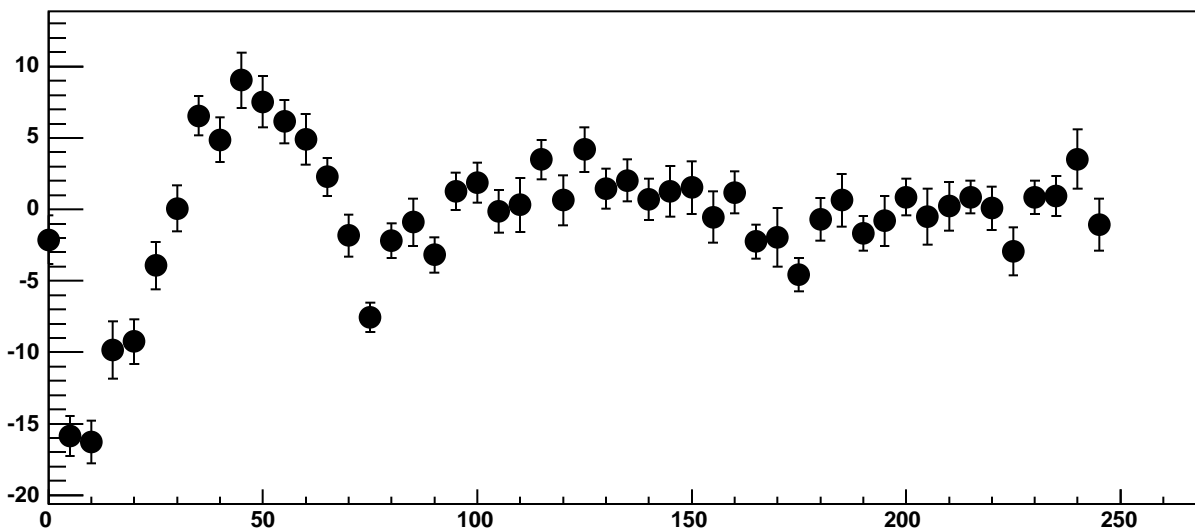
Chip 4, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold



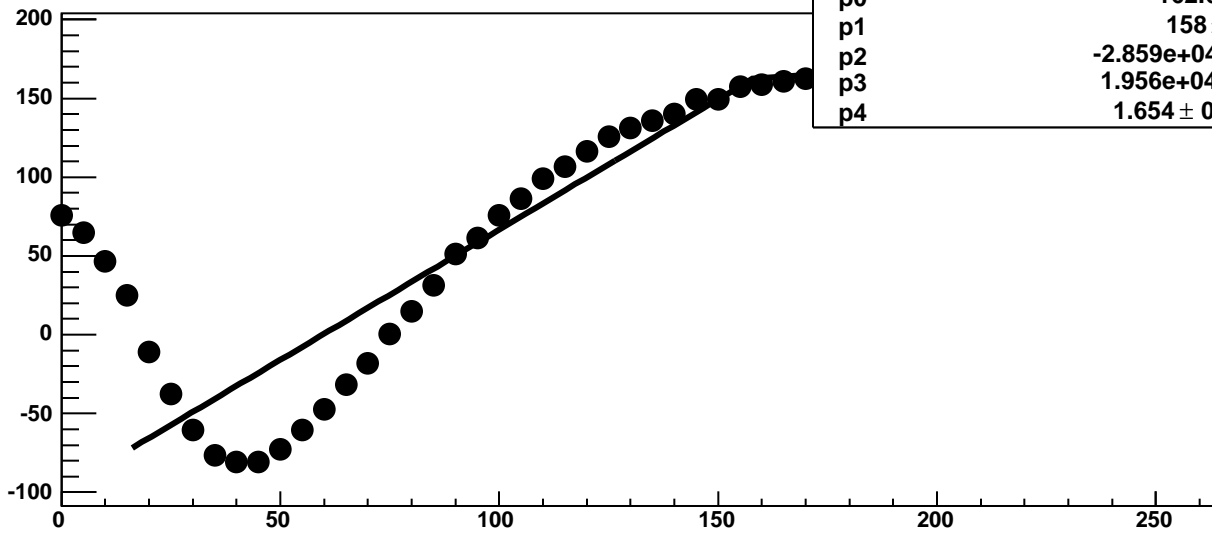
Chip 4, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold

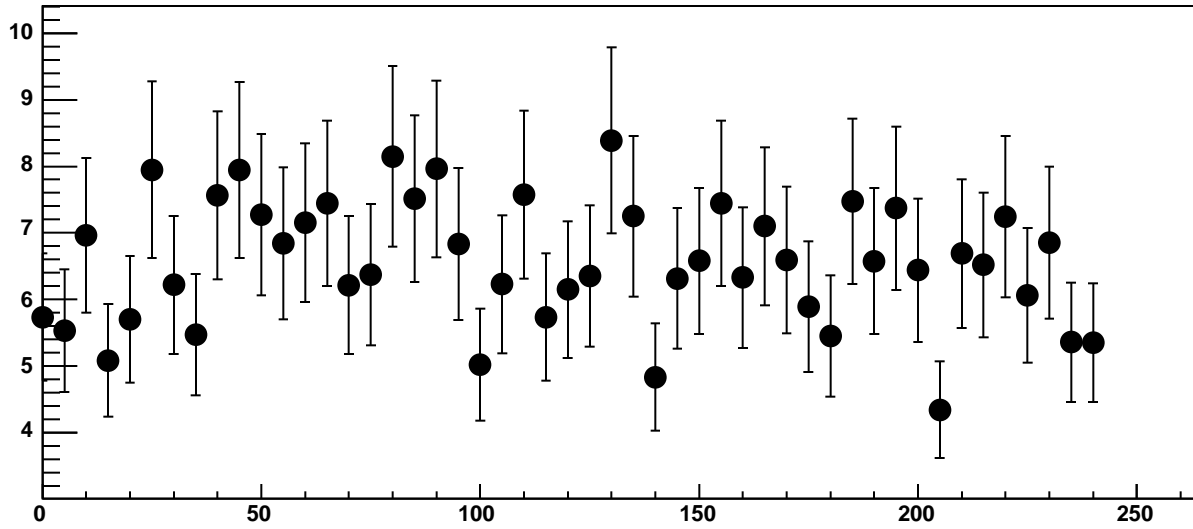


Chip 4, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

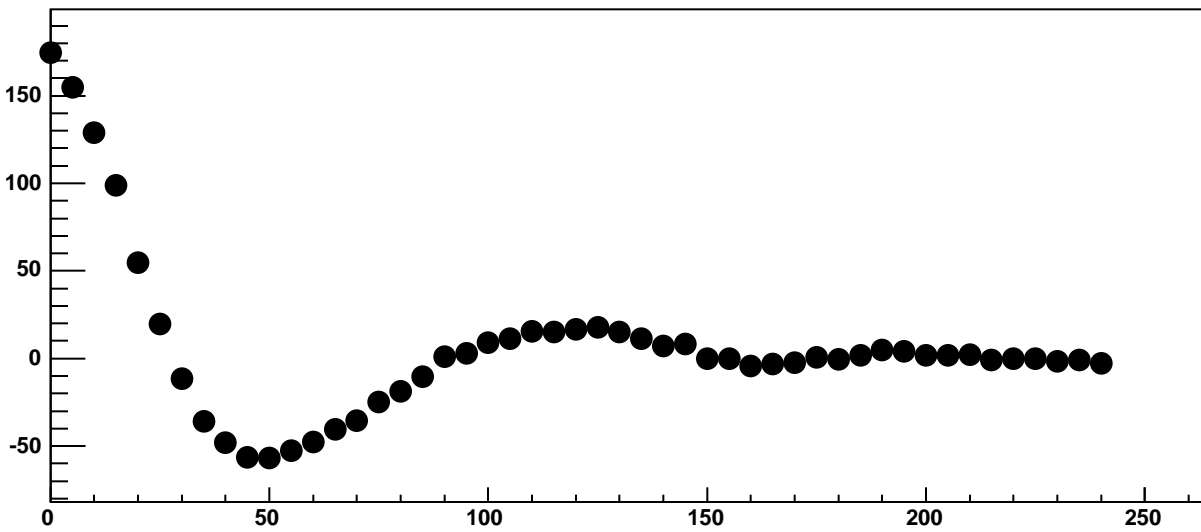


$\chi^2 / \text{ndf}$	1.725e+04 / 41
p0	162.6 ± 0.791
p1	158 ± 0.6031
p2	-2.859e+04 ± 211.3
p3	1.956e+04 ± 60.61
p4	1.654 ± 0.006399

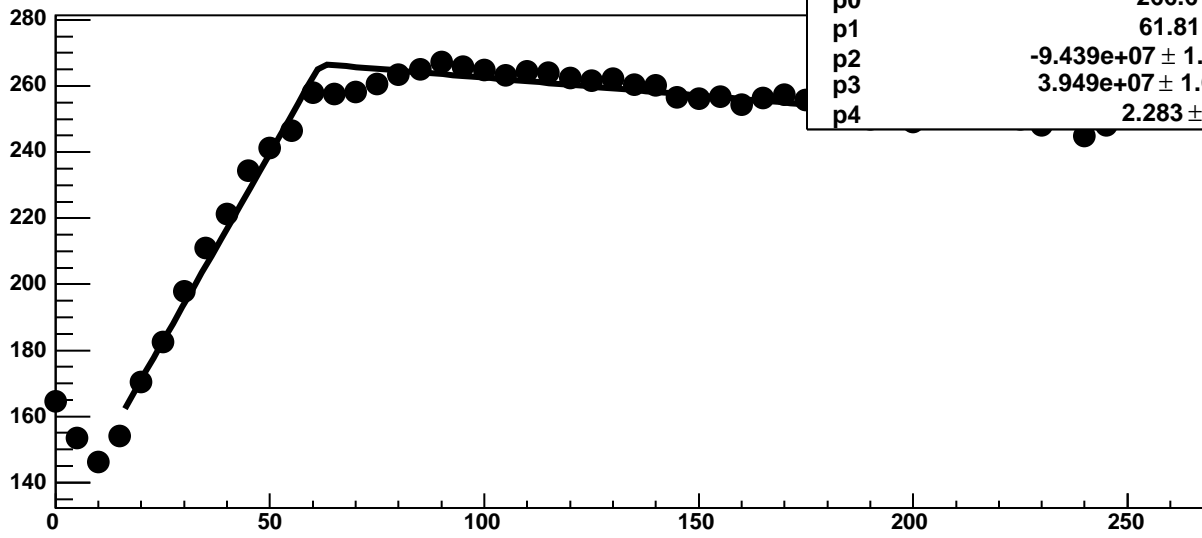
Chip 4, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

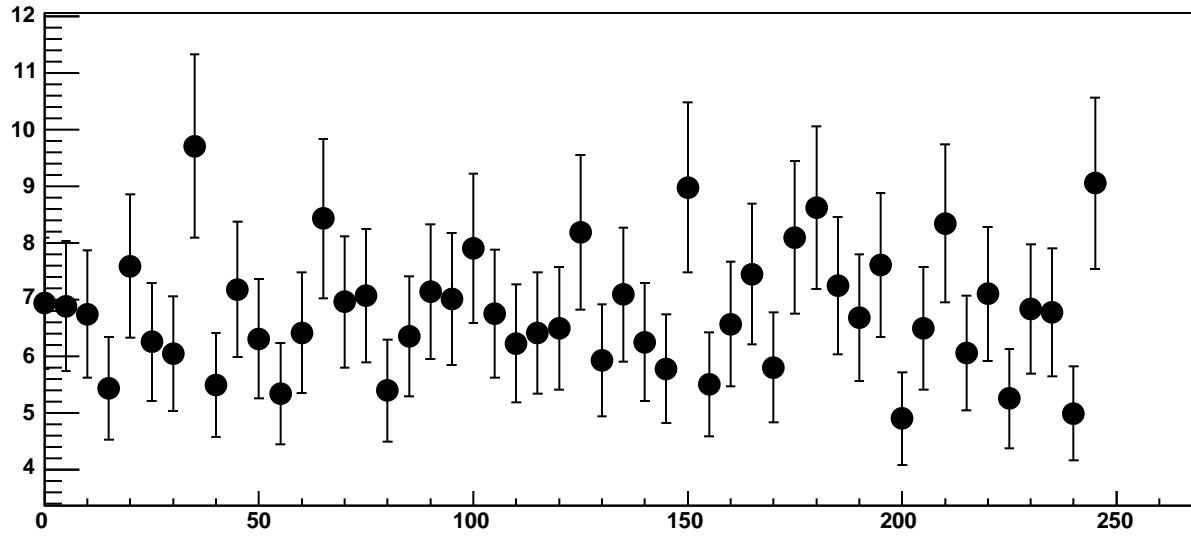


Chip 4, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

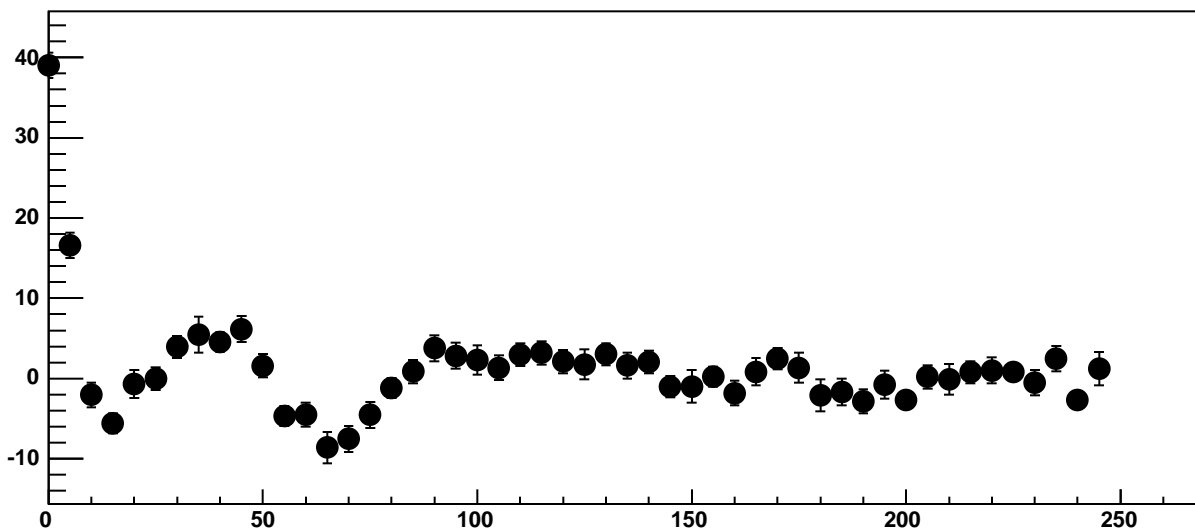


$\chi^2 / \text{ndf}$	195.3 / 41
p0	$266.6 \pm 0.4968$
p1	$61.81 \pm 0.4133$
p2	$-9.439\text{e}+07 \pm 1.262\text{e}+06$
p3	$3.949\text{e}+07 \pm 1.684\text{e}+05$
p4	$2.283 \pm 0.03053$

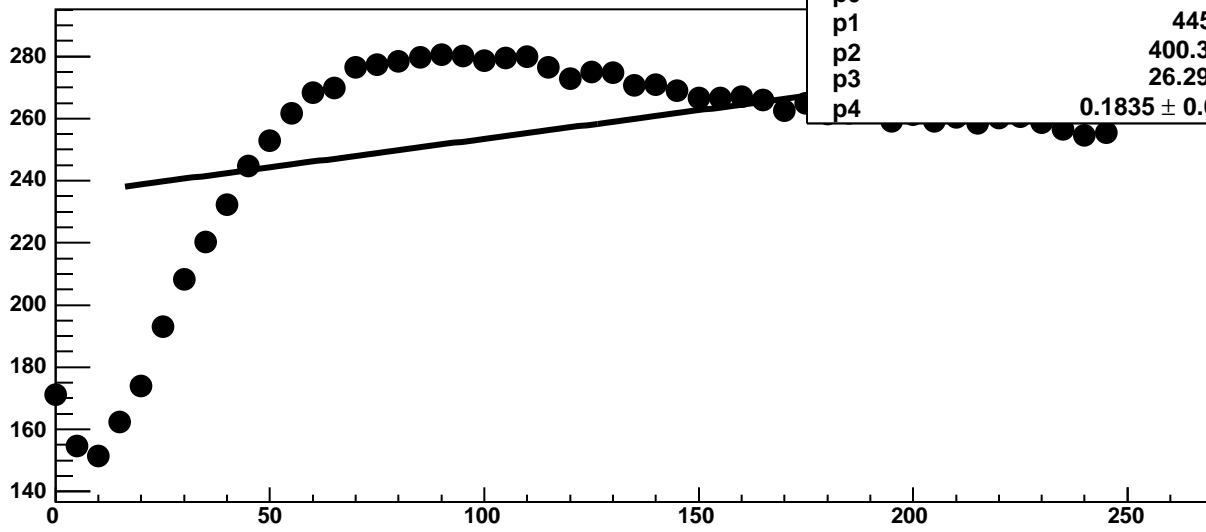
Chip 4, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold

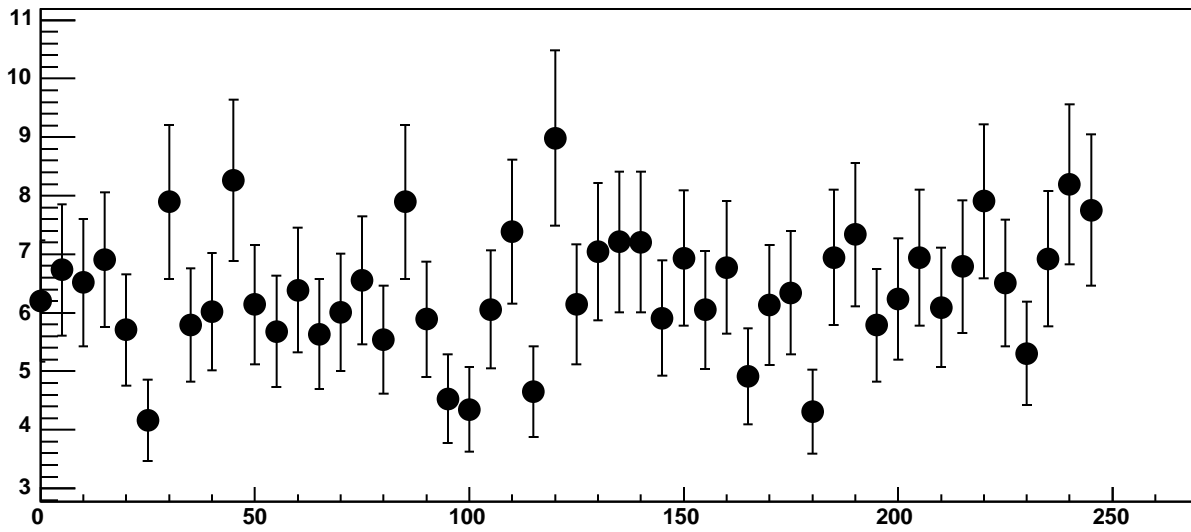


Chip 4, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

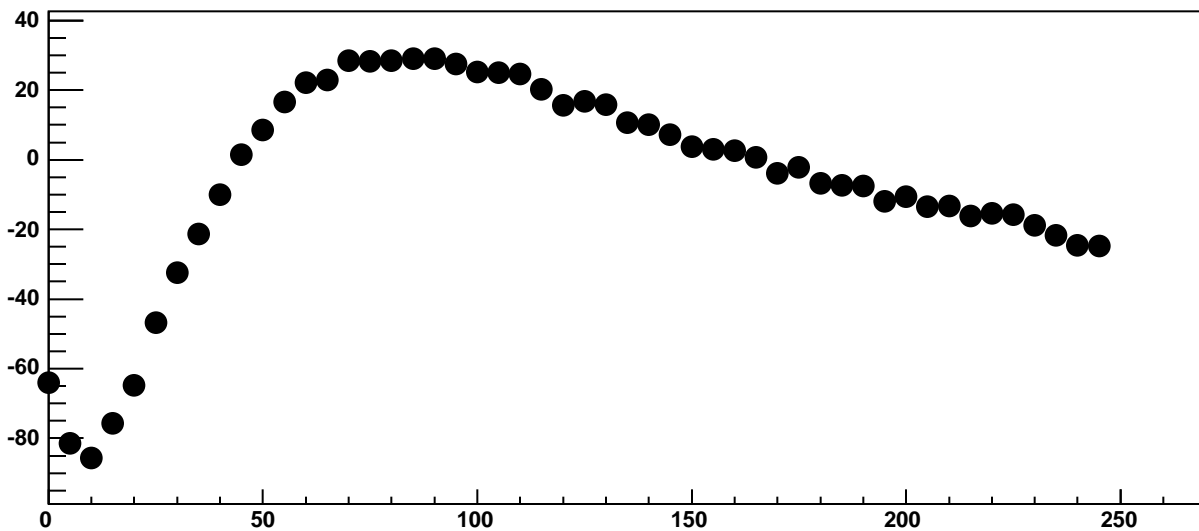


$\chi^2 / \text{ndf}$	1.443e+04 / 41
p0	316.8 ± 0.2051
p1	445 ± 1.118
p2	400.3 ± 84.85
p3	26.29 ± 1.414
p4	0.1835 ± 0.0006248

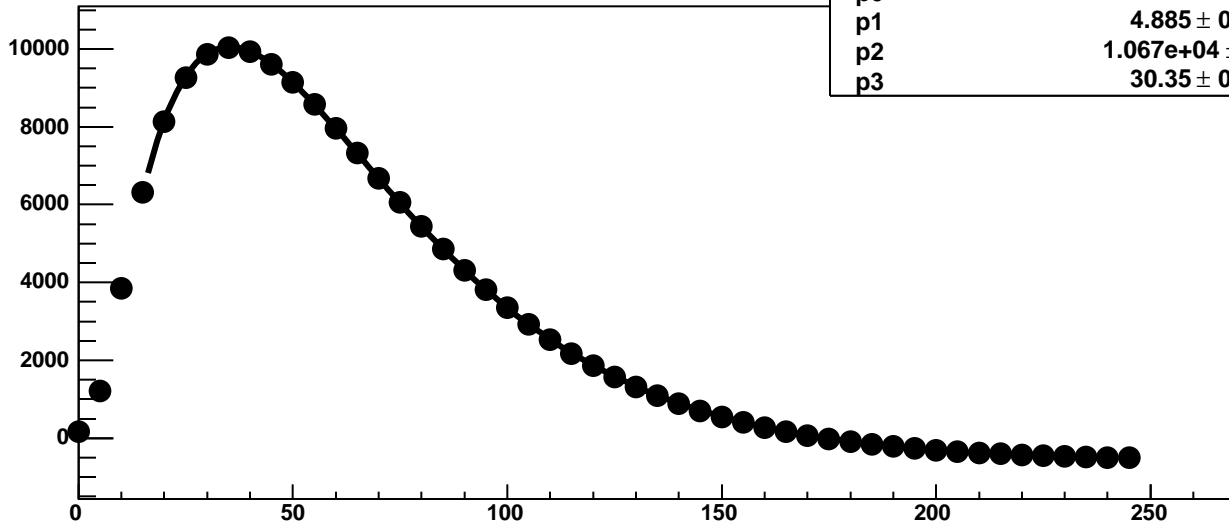
Chip 4, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

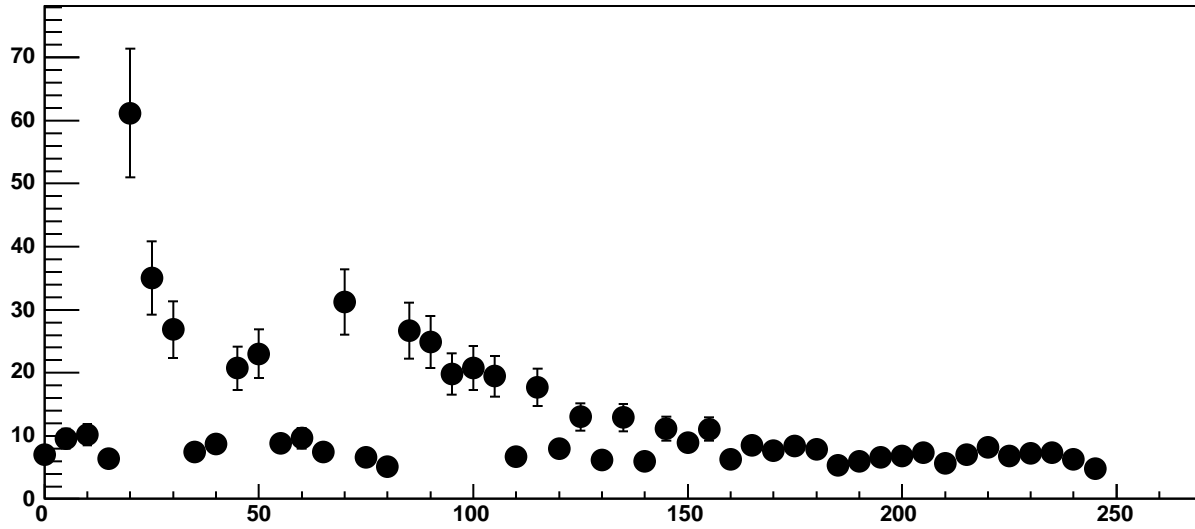


Chip 4, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold

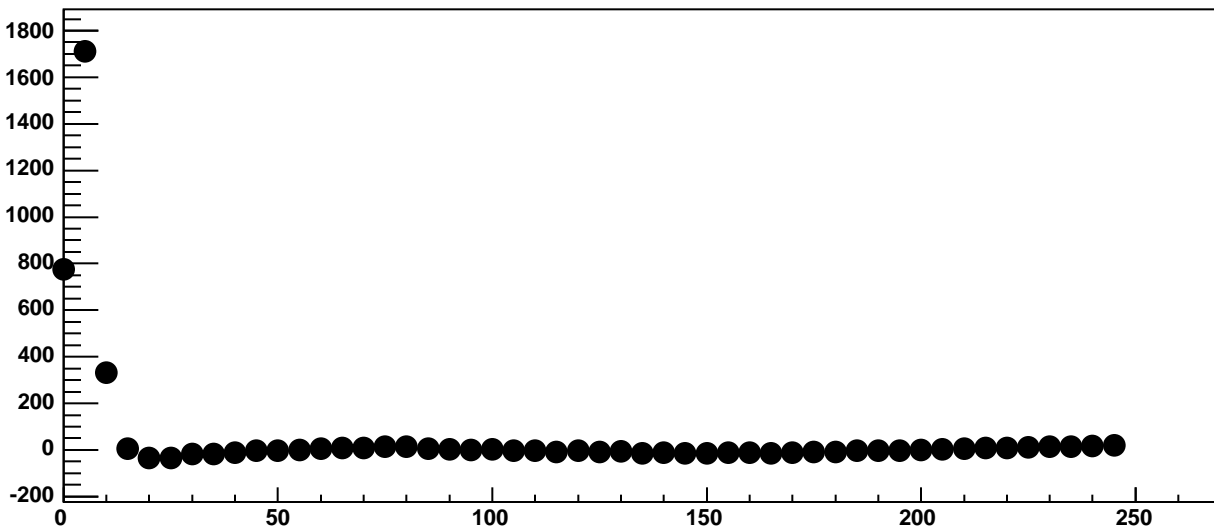


$\chi^2 / \text{ndf}$	1238 / 42
p0	-606.2 ± 0.502
p1	4.885 ± 0.004554
p2	1.067e+04 ± 0.9902
p3	30.35 ± 0.004553

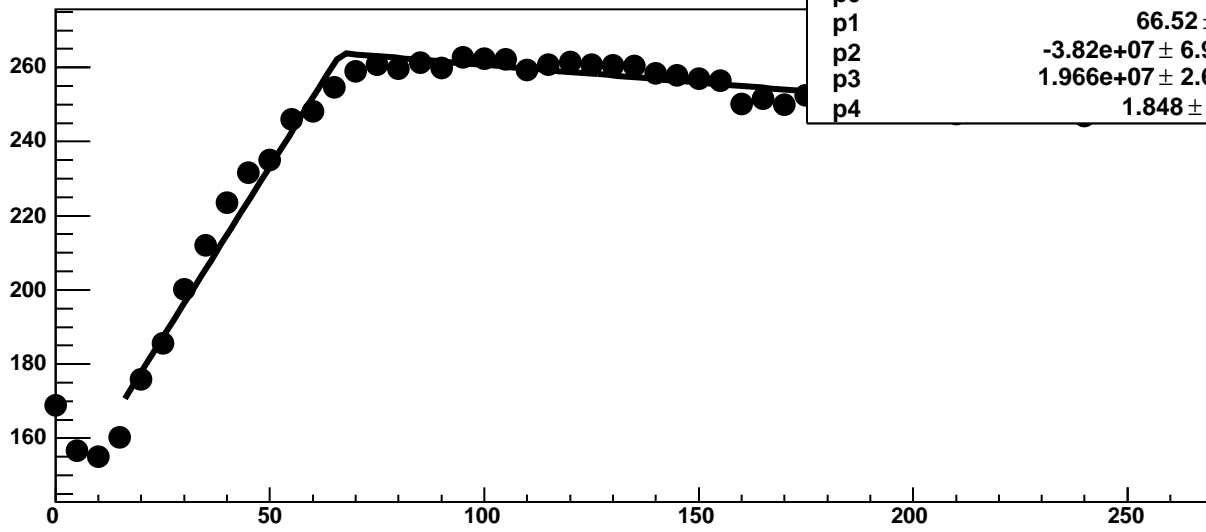
Chip 4, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold

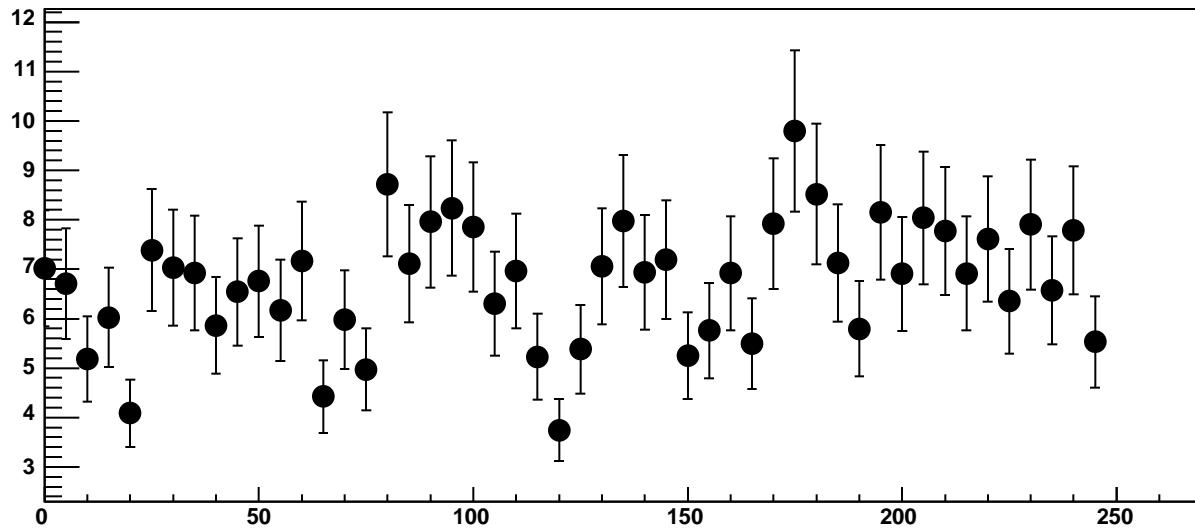


Chip 4, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

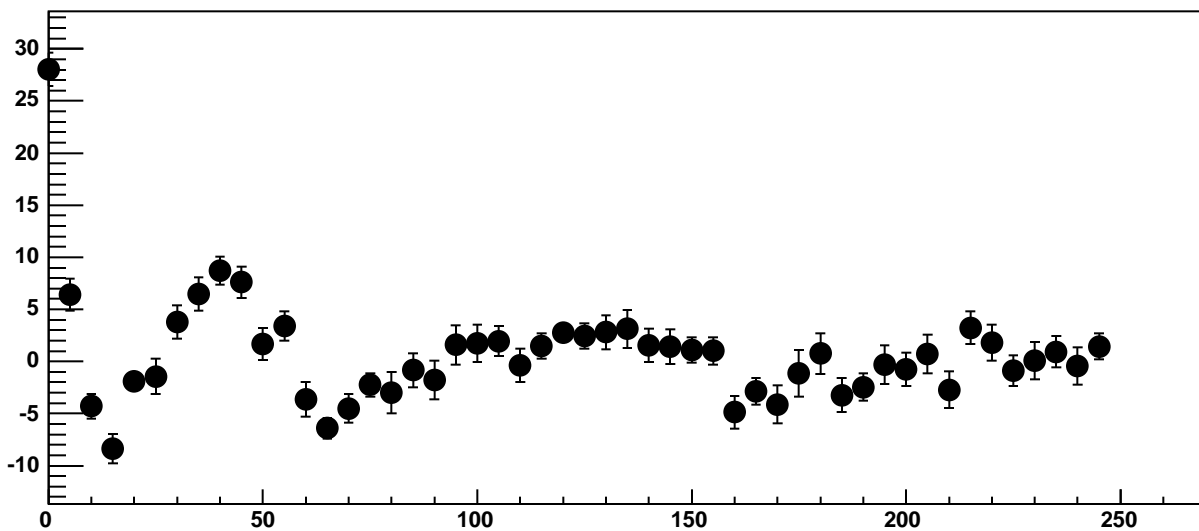


$\chi^2 / \text{ndf}$	266.1 / 41
p0	$263.8 \pm 0.4771$
p1	$66.52 \pm 0.4618$
p2	$-3.82\text{e}+07 \pm 6.945\text{e}+05$
p3	$1.966\text{e}+07 \pm 2.607\text{e}+05$
p4	$1.848 \pm 0.02339$

Chip 4, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold

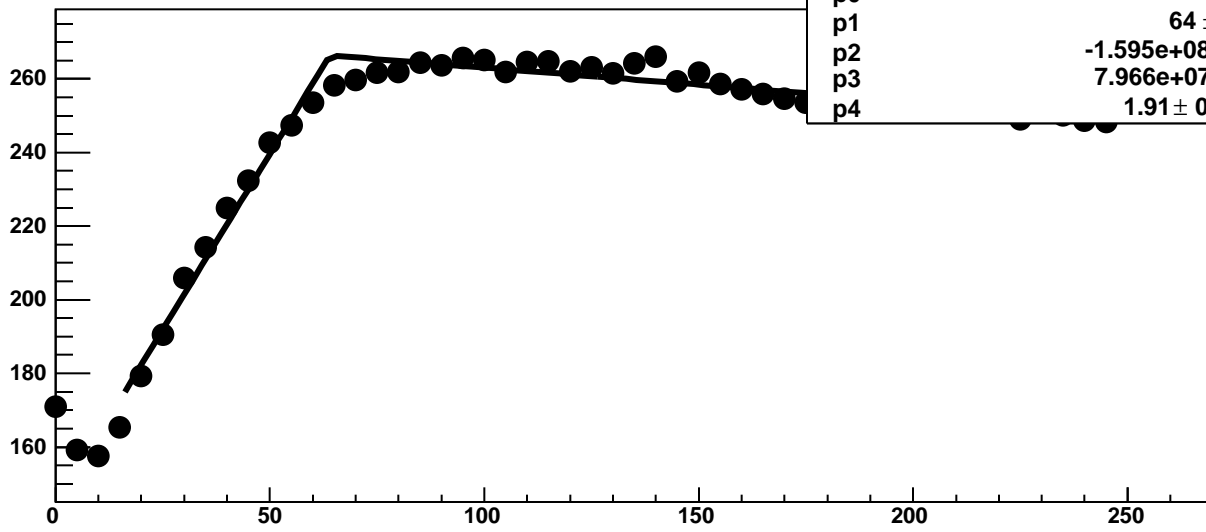


Chip 4, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold



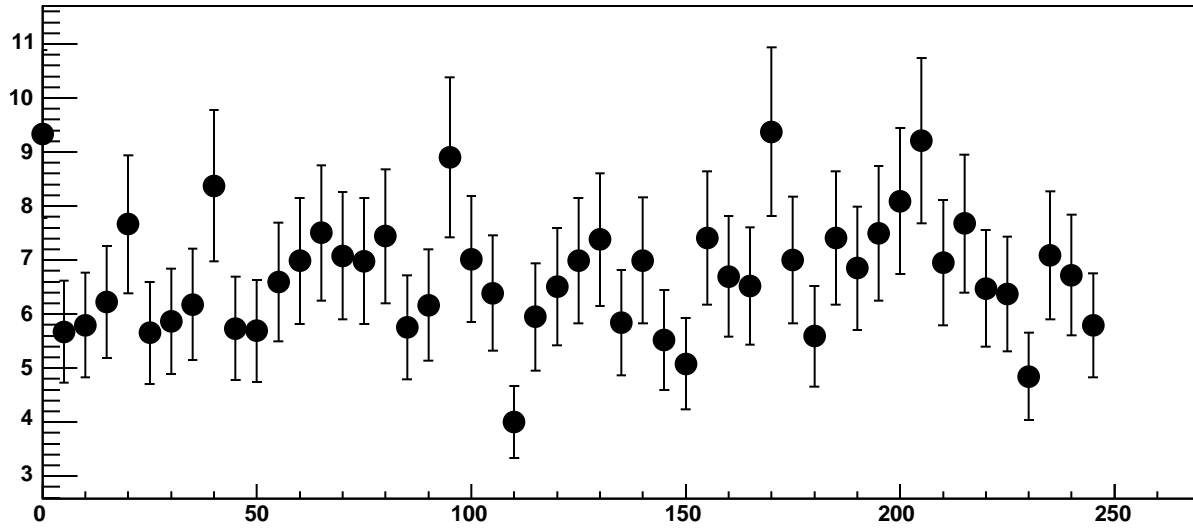


Chip 4, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

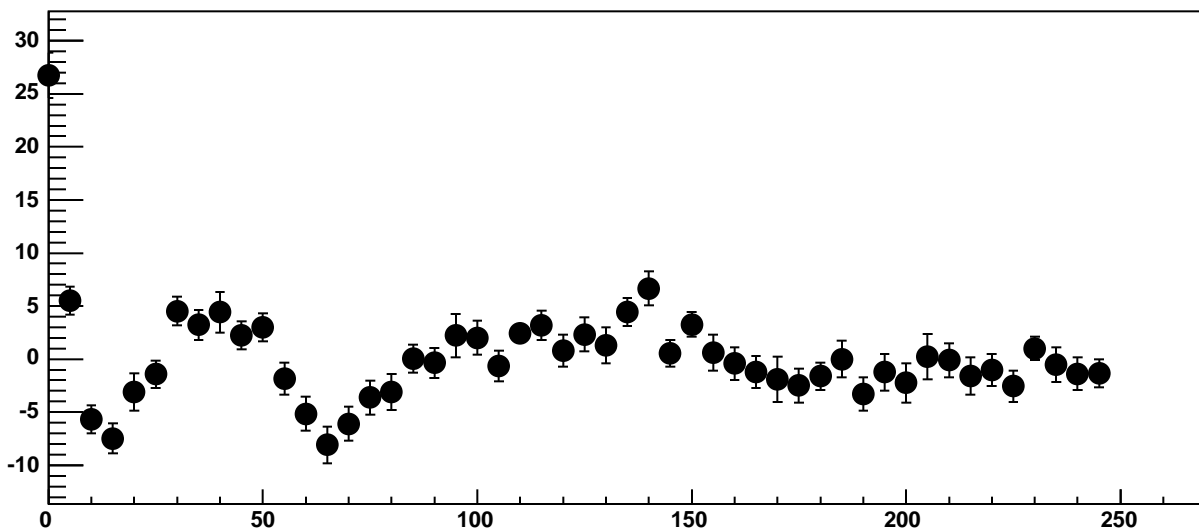


$\chi^2 / \text{ndf}$	190.3 / 41
p0	$266.4 \pm 0.4602$
p1	$64 \pm 0.3792$
p2	$-1.595\text{e}+08 \pm 1.414$
p3	$7.966\text{e}+07 \pm 1.414$
p4	$1.91 \pm 0.004816$

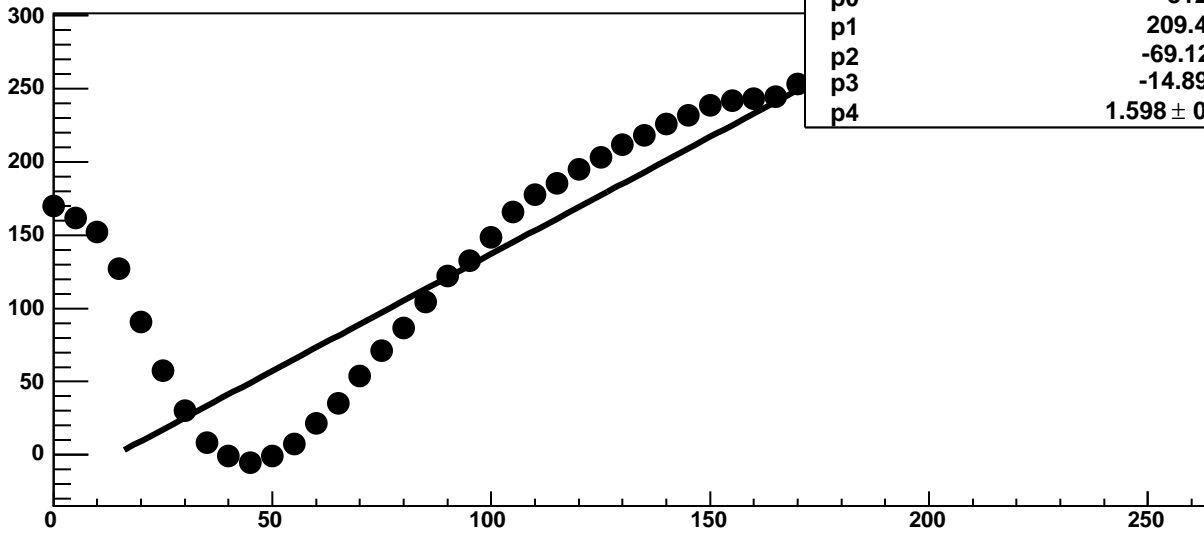
Chip 4, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

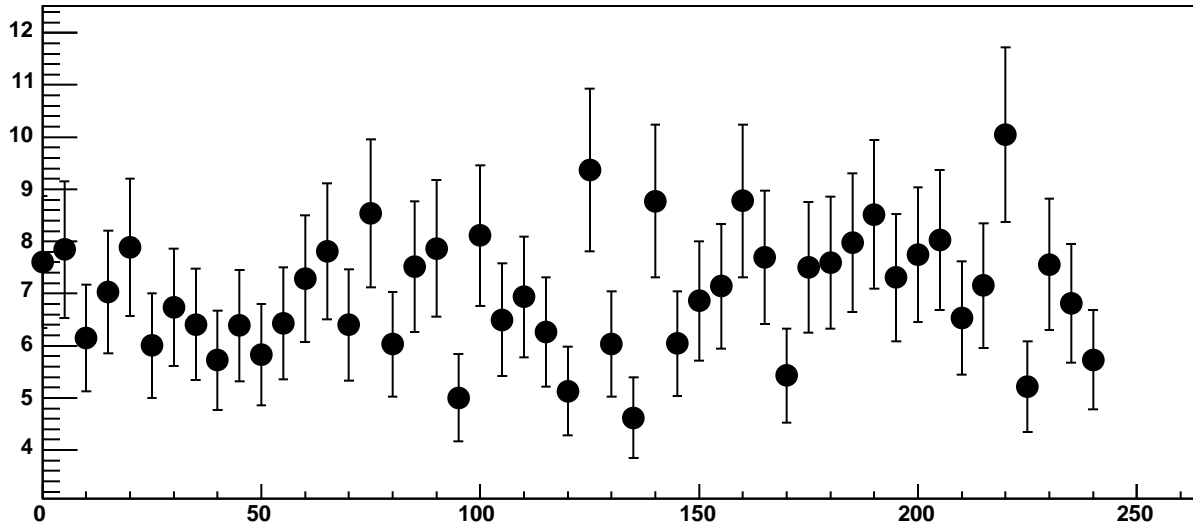


Chip 4, Channel 15, Enable 5, DAC=4000, ADC Mean vs Hold

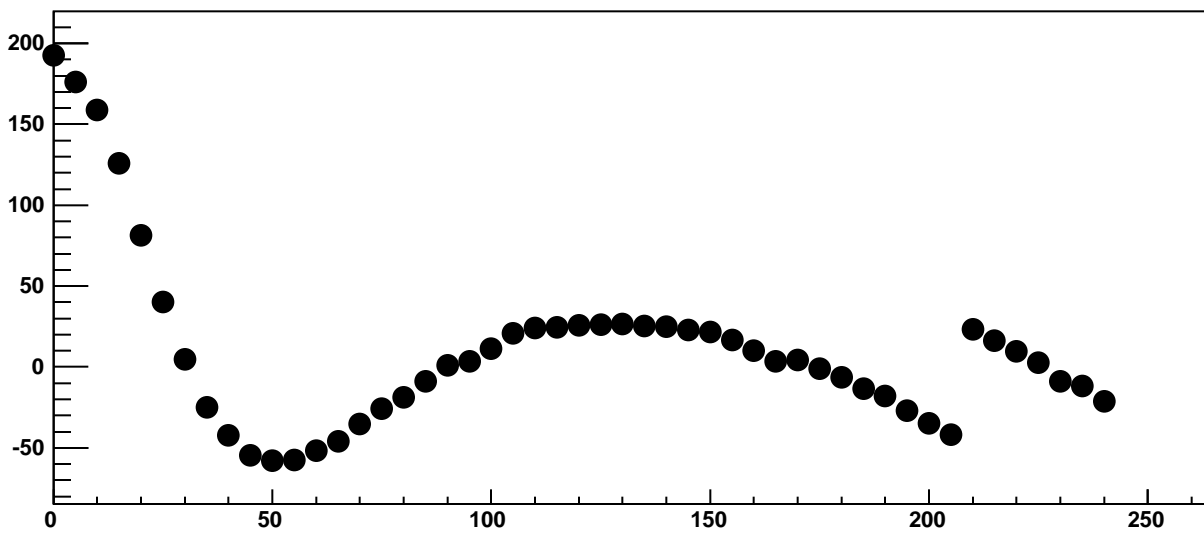


$\chi^2 / \text{ndf}$	2.278e+04 / 41
p0	312 ± 2.088
p1	209.4 ± 1.473
p2	-69.12 ± 3.831
p3	-14.89 ± 10.69
p4	1.598 ± 0.002399

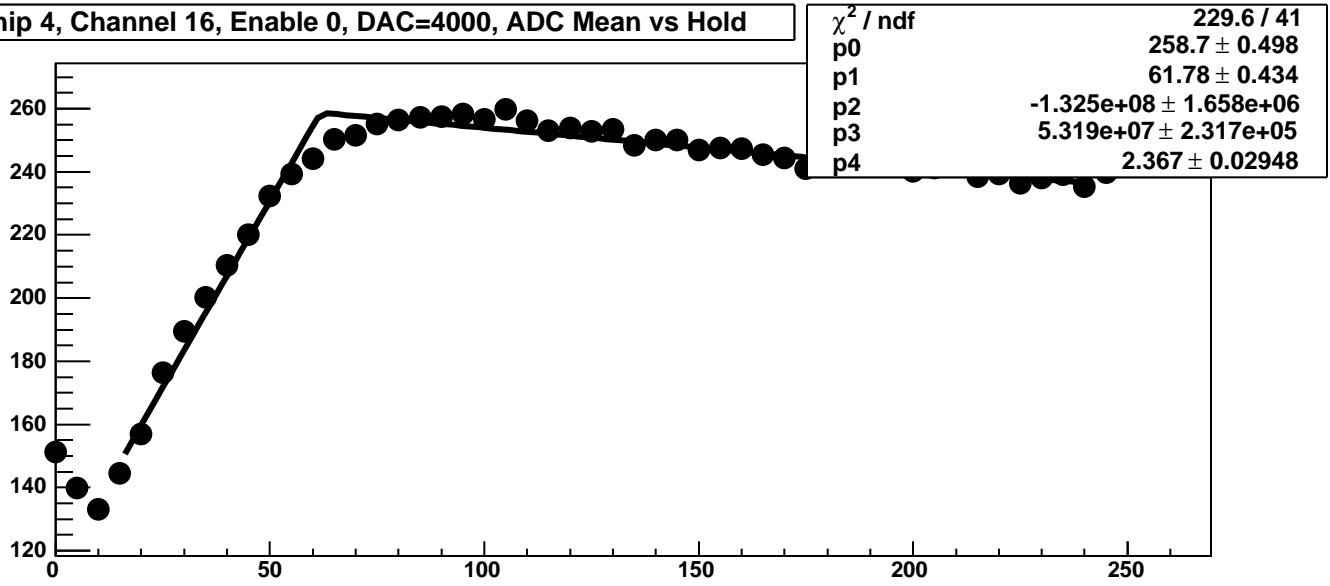
Chip 4, Channel 15, Enable 5, DAC=4000, ADC Noise vs Hold



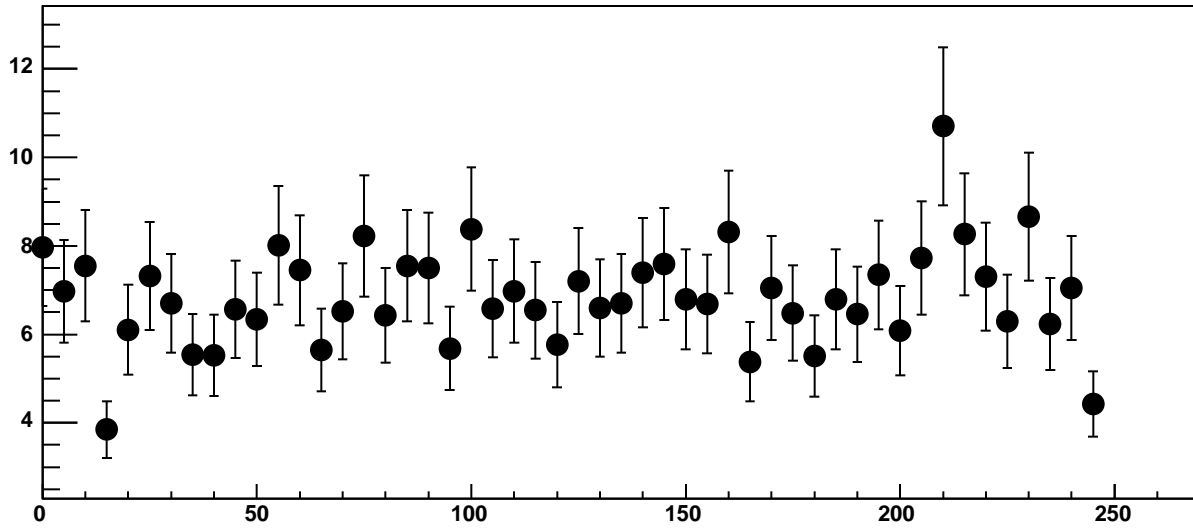
Chip 4, Channel 15, Enable 5, DAC=4000, ADC Residuals vs Hold



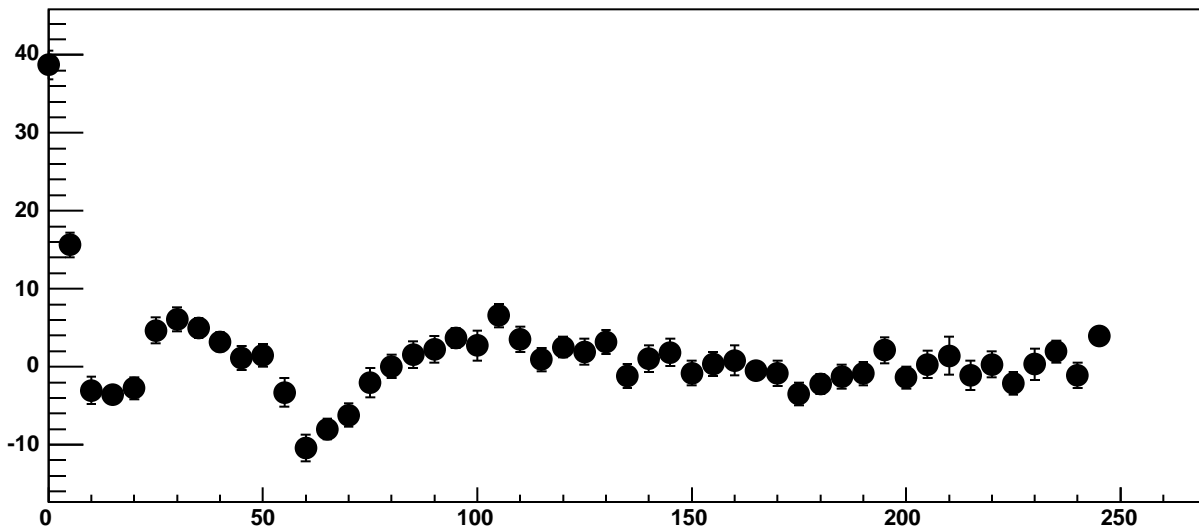
Chip 4, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold



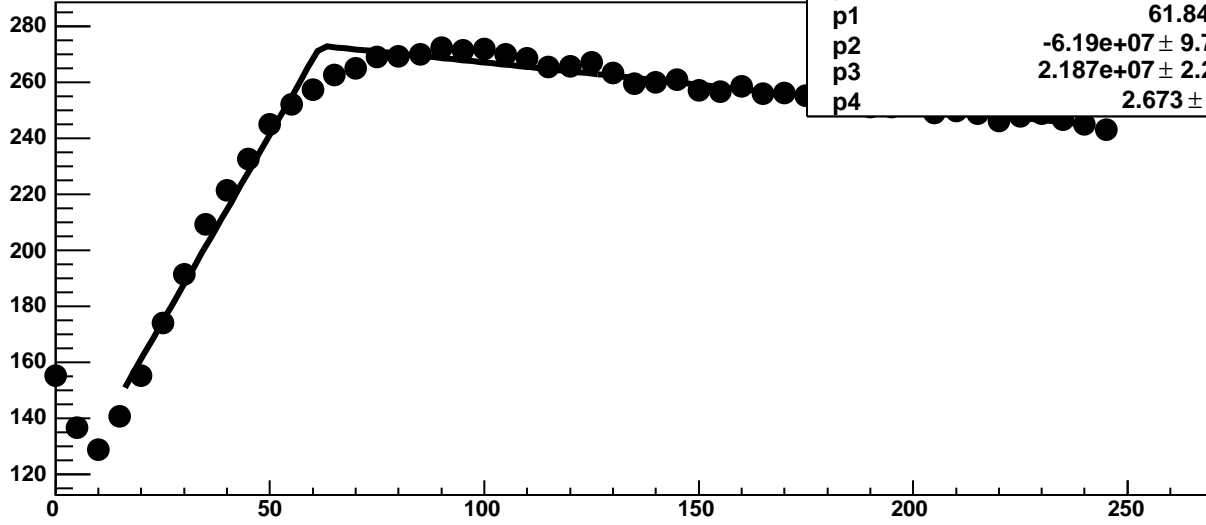
Chip 4, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

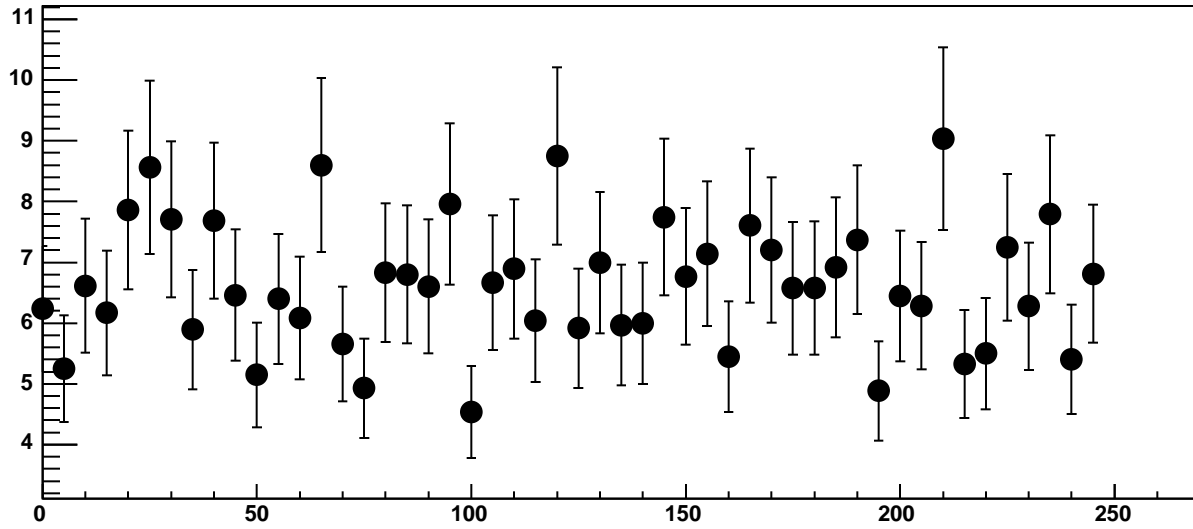


Chip 4, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold

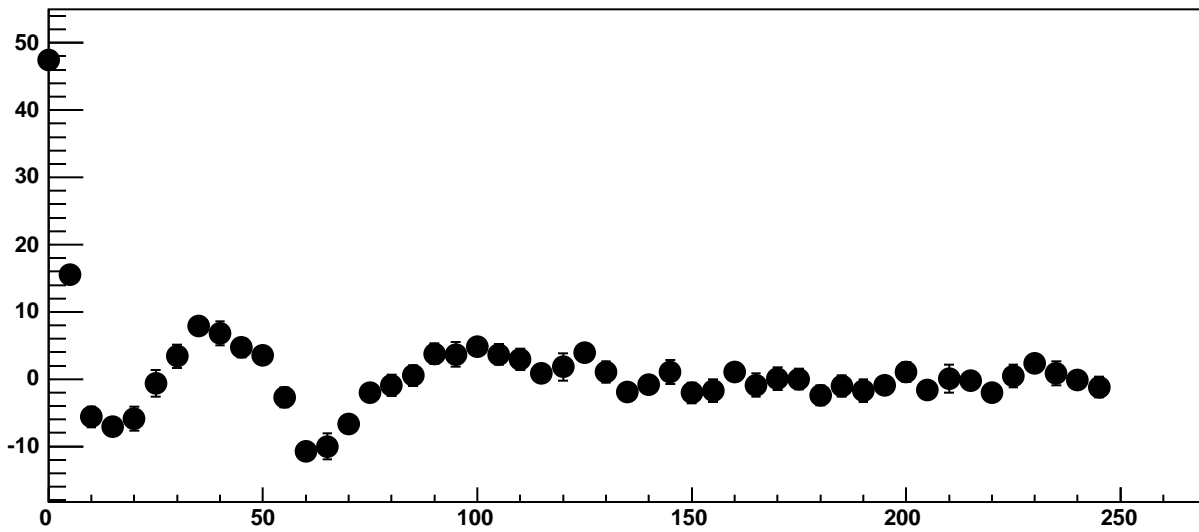


$\chi^2 / \text{ndf}$	295.3 / 41
p0	$273.1 \pm 0.4589$
p1	$61.84 \pm 0.352$
p2	$-6.19\text{e}+07 \pm 9.718\text{e}+05$
p3	$2.187\text{e}+07 \pm 2.273\text{e}+05$
p4	$2.673 \pm 0.03278$

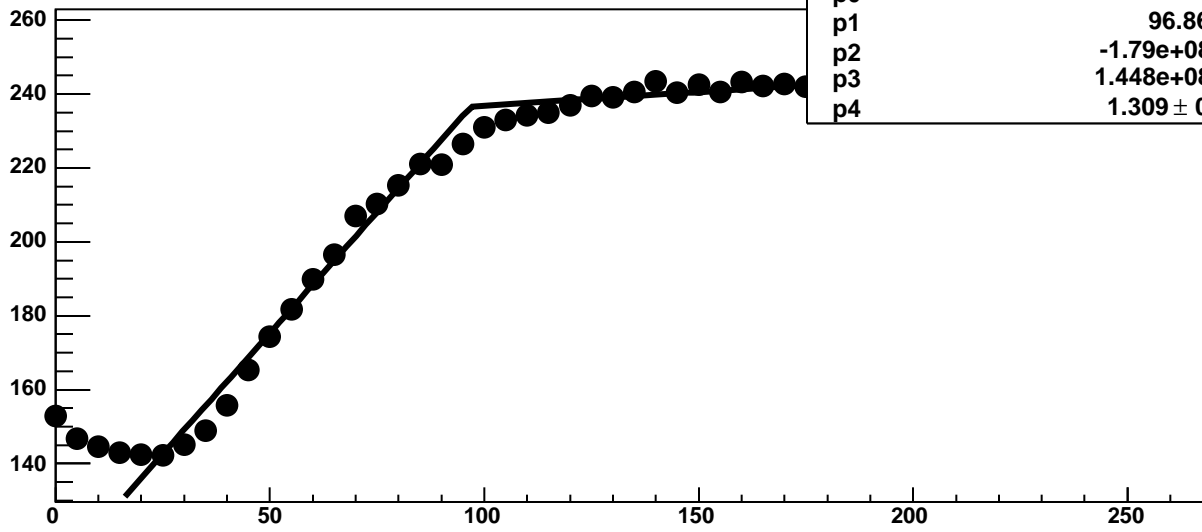
Chip 4, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold

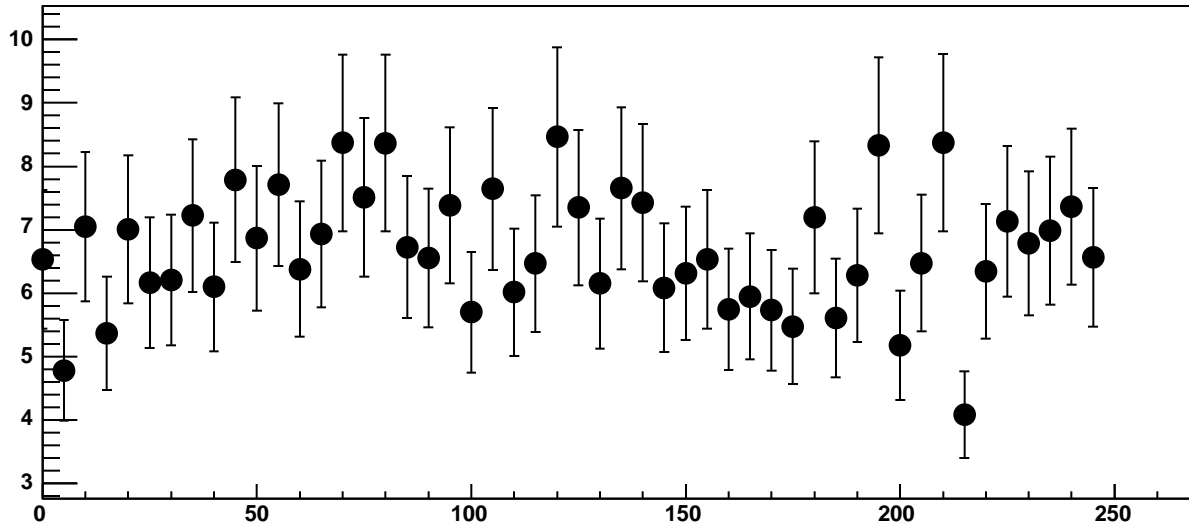


Chip 4, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold

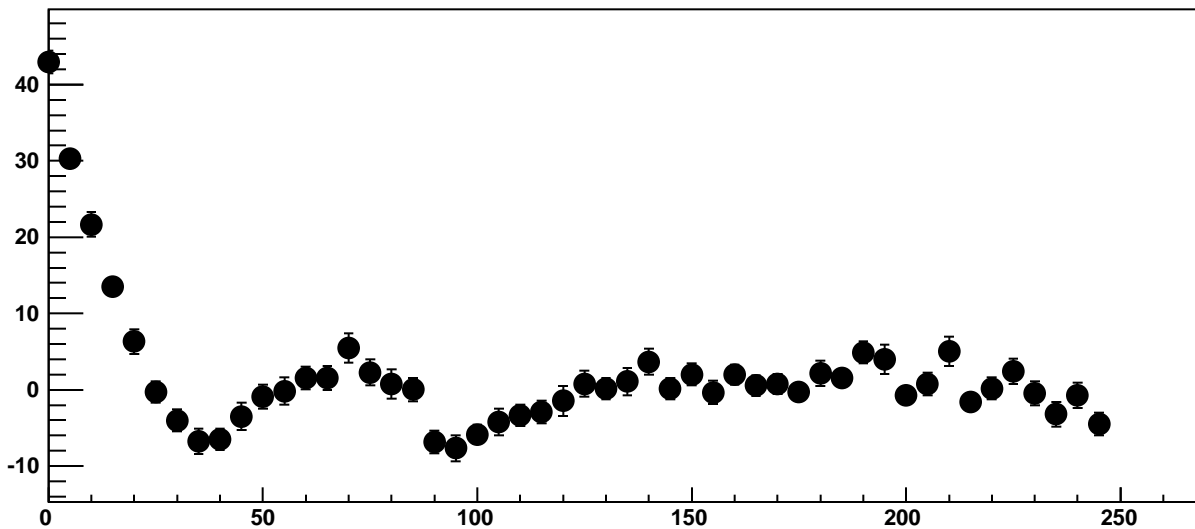


$\chi^2 / \text{ndf}$	322.2 / 41
p0	$236.7 \pm 0.5849$
p1	$96.86 \pm 0.708$
p2	$-1.79\text{e}+08 \pm 1.415$
p3	$1.448\text{e}+08 \pm 1.415$
p4	$1.309 \pm 0.006154$

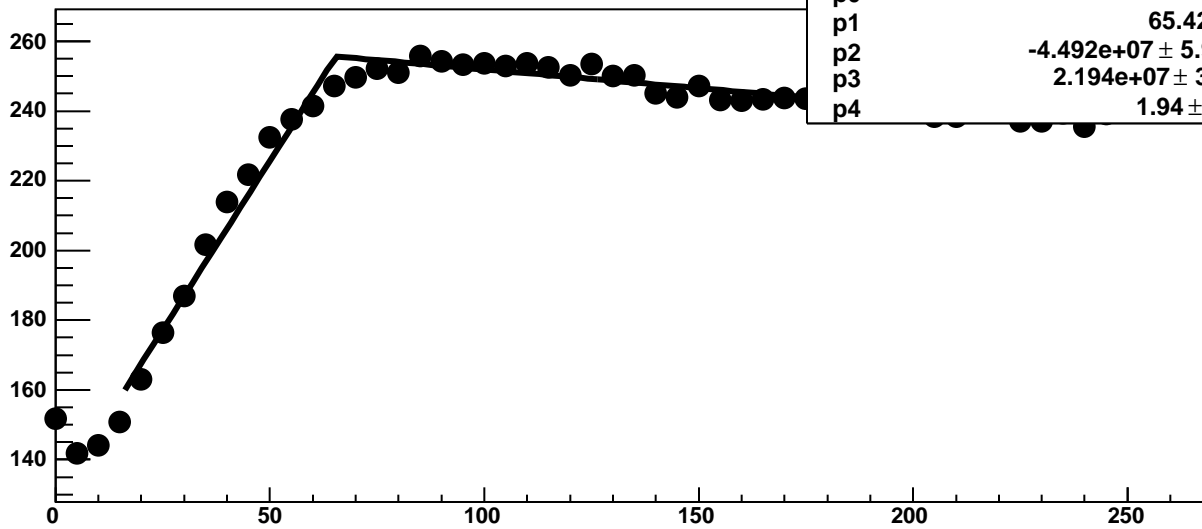
Chip 4, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold

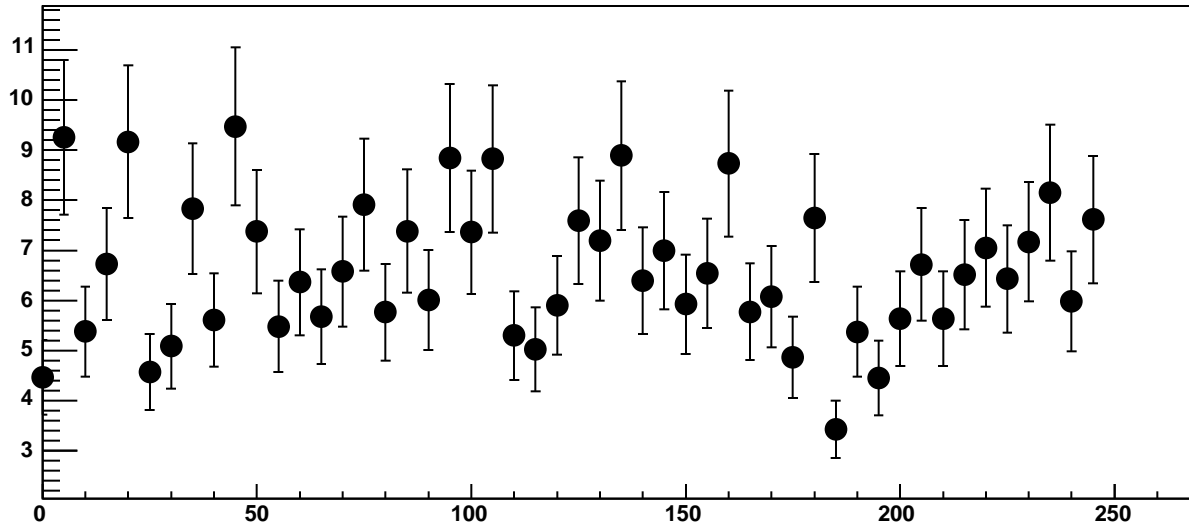


Chip 4, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

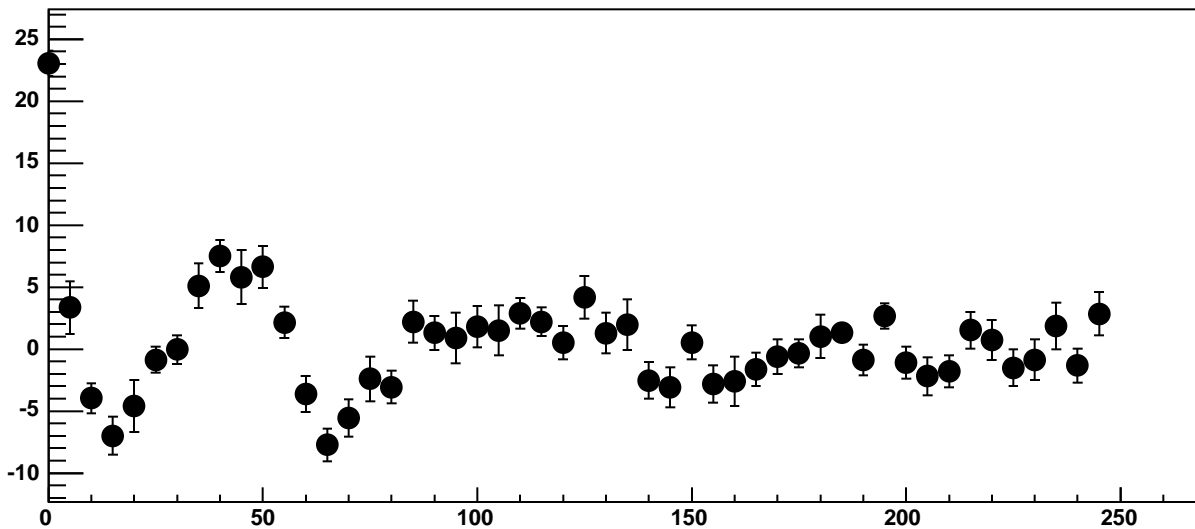


$\chi^2 / \text{ndf}$	210.5 / 41
p0	$255.6 \pm 0.4903$
p1	$65.42 \pm 0.407$
p2	$-4.492\text{e}+07 \pm 5.936\text{e}+05$
p3	$2.194\text{e}+07 \pm 3.13\text{e}+05$
p4	$1.94 \pm 0.01926$

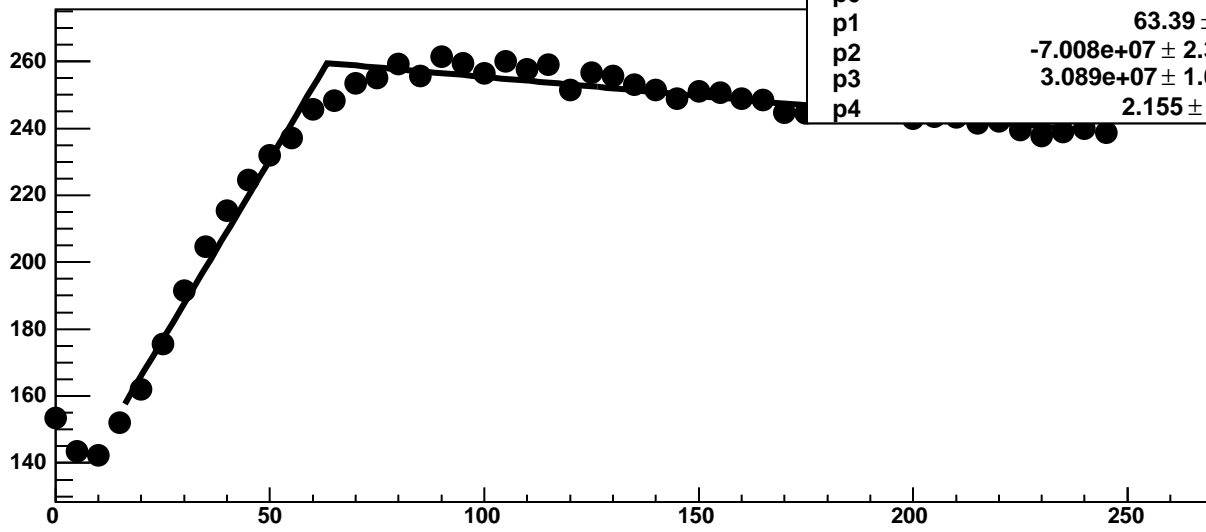
Chip 4, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

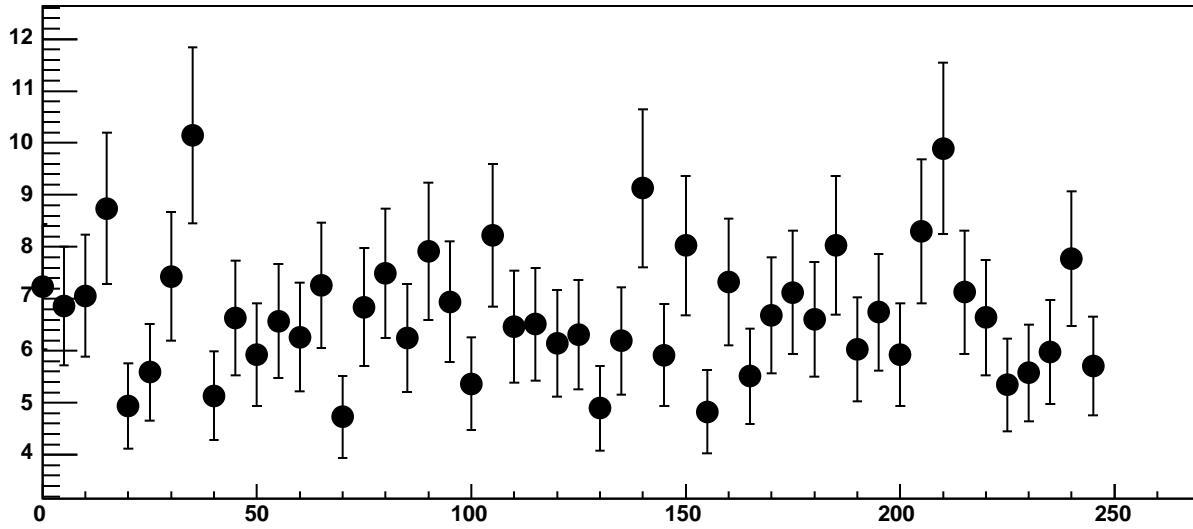


Chip 4, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

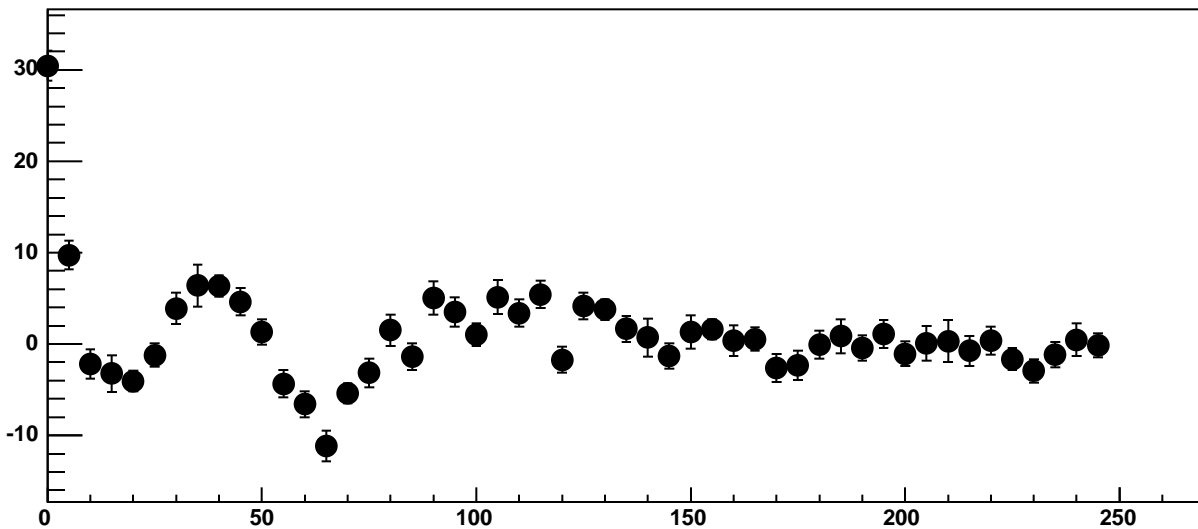


$\chi^2 / \text{ndf}$	253 / 41
p0	$259.5 \pm 0.4583$
p1	$63.39 \pm 0.4475$
p2	$-7.008\text{e}+07 \pm 2.388\text{e}+06$
p3	$3.089\text{e}+07 \pm 1.017\text{e}+06$
p4	$2.155 \pm 0.03139$

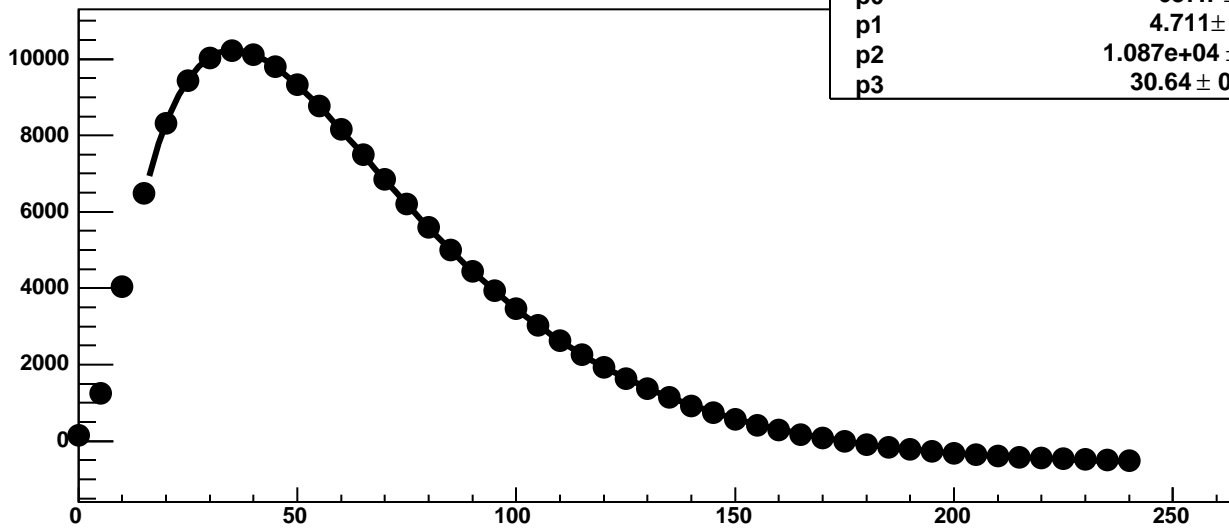
Chip 4, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

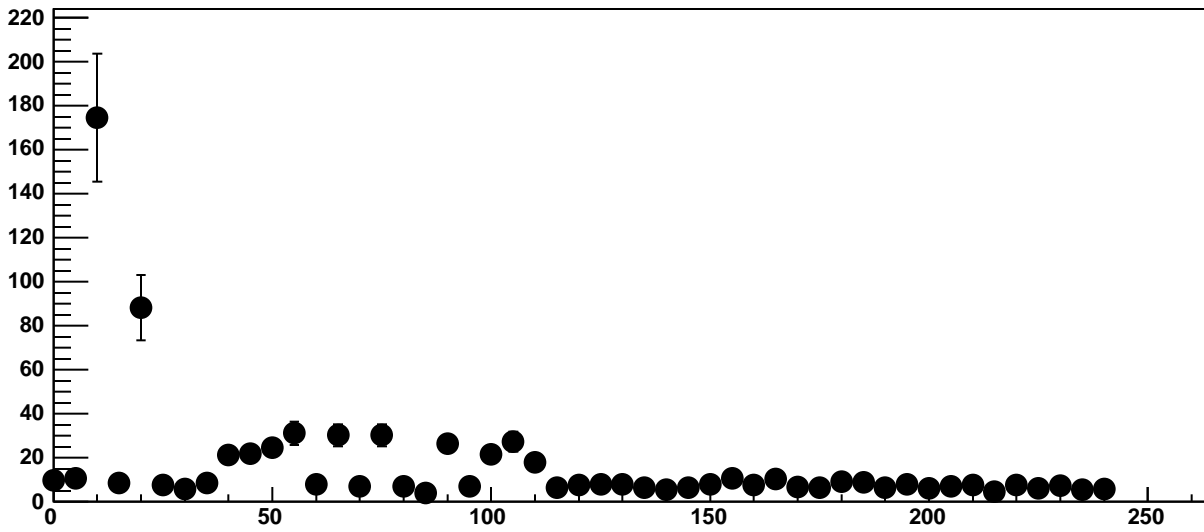


Chip 4, Channel 16, Enable 5!, DAC=4000, ADC Mean vs Hold

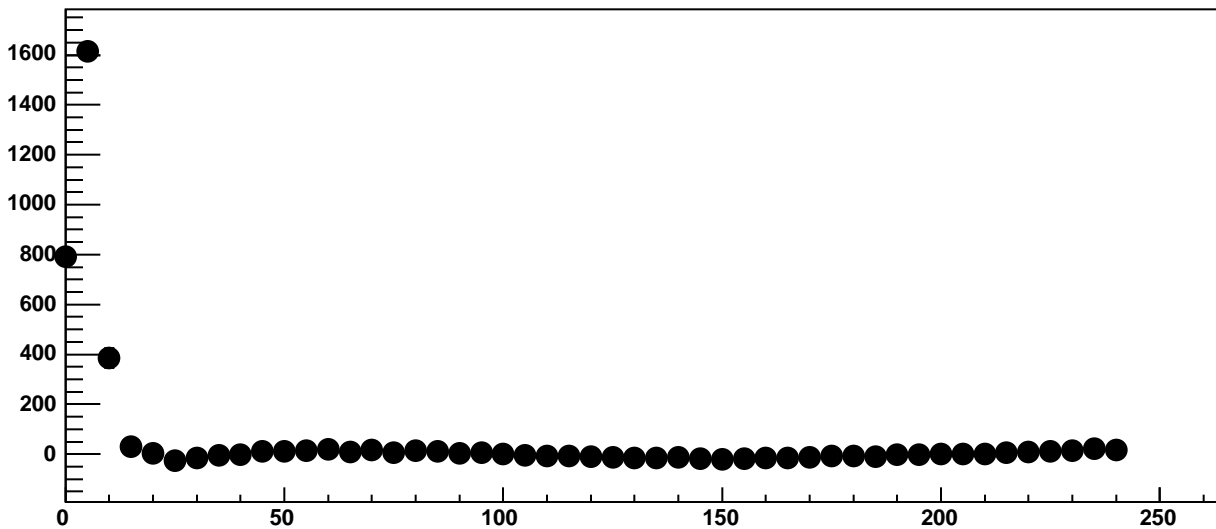


$\chi^2 / \text{ndf}$	2547 / 42
p0	$-637.7 \pm 0.4704$
p1	$4.711 \pm 0.00547$
p2	$1.087\text{e}+04 \pm 0.8912$
p3	$30.64 \pm 0.004027$

Chip 4, Channel 16, Enable 5!, DAC=4000, ADC Noise vs Hold

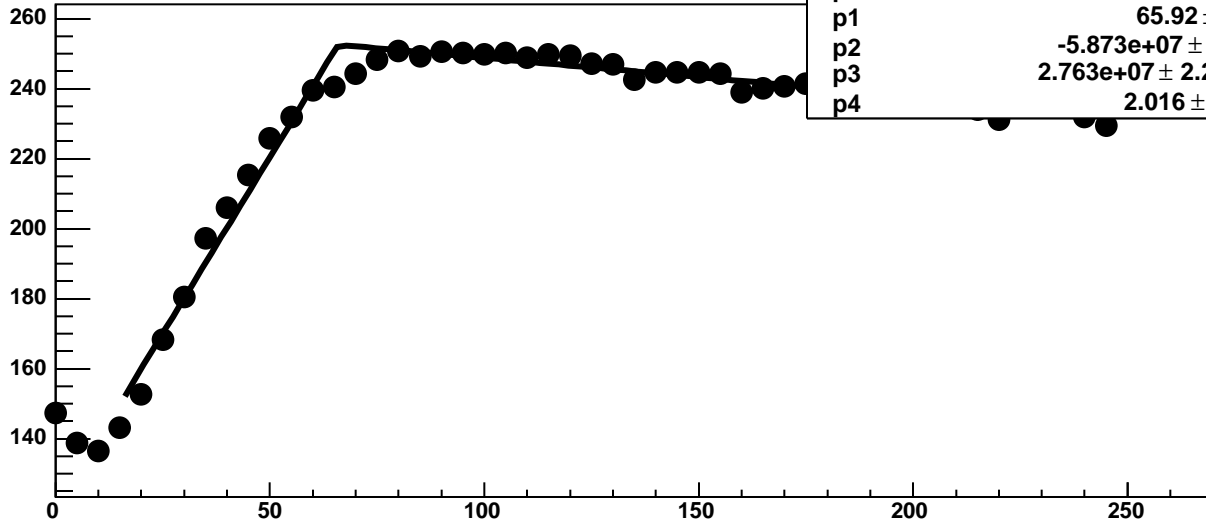


Chip 4, Channel 16, Enable 5!, DAC=4000, ADC Residuals vs Hold



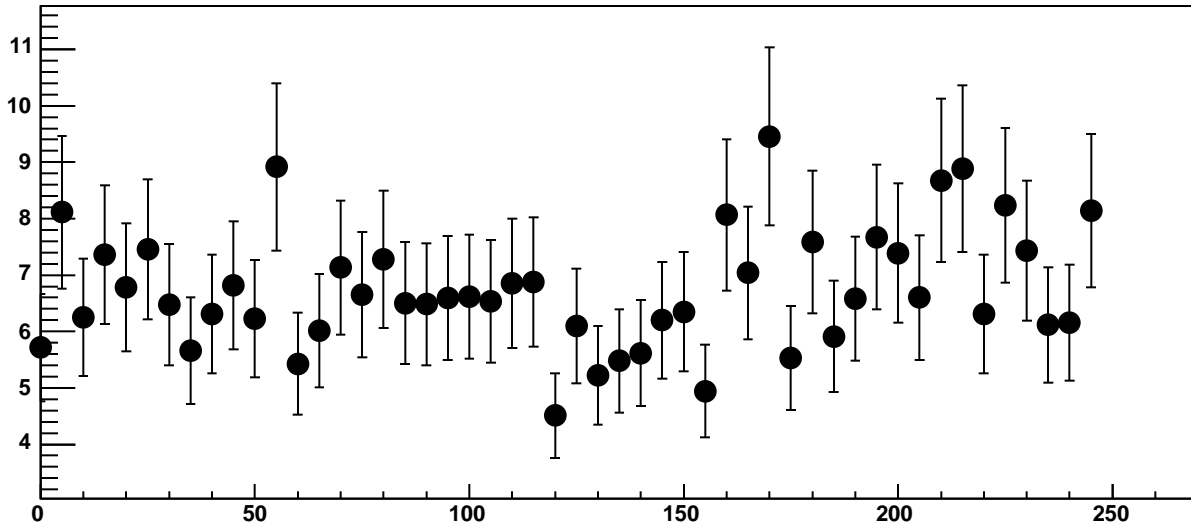


Chip 4, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

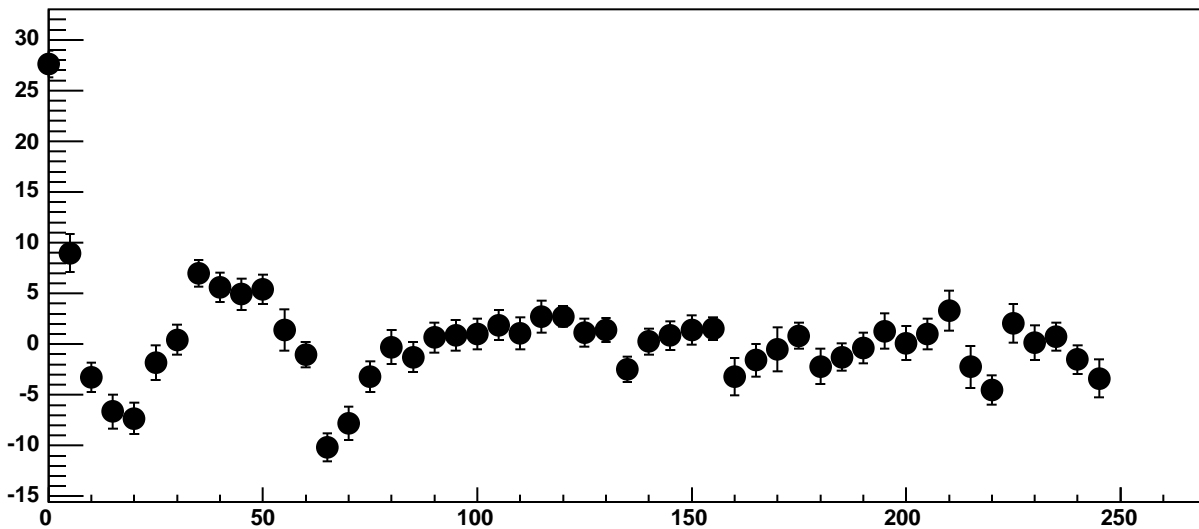


$\chi^2 / \text{ndf}$	236.5 / 41
p0	$252.6 \pm 0.4862$
p1	$65.92 \pm 0.4603$
p2	$-5.873\text{e}+07 \pm 9.4\text{e}+05$
p3	$2.763\text{e}+07 \pm 2.255\text{e}+05$
p4	$2.016 \pm 0.02871$

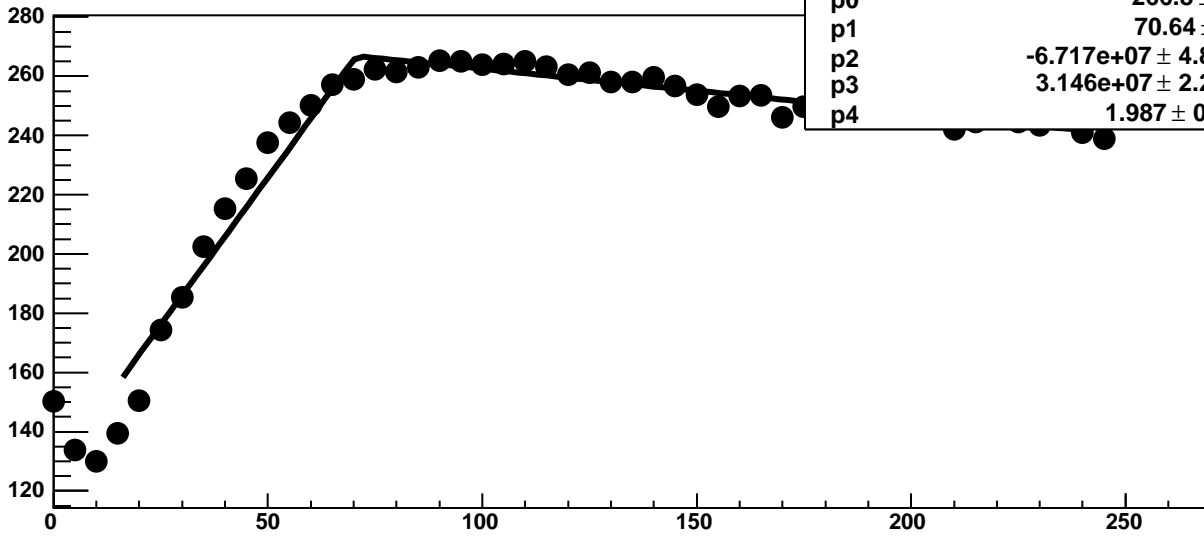
Chip 4, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

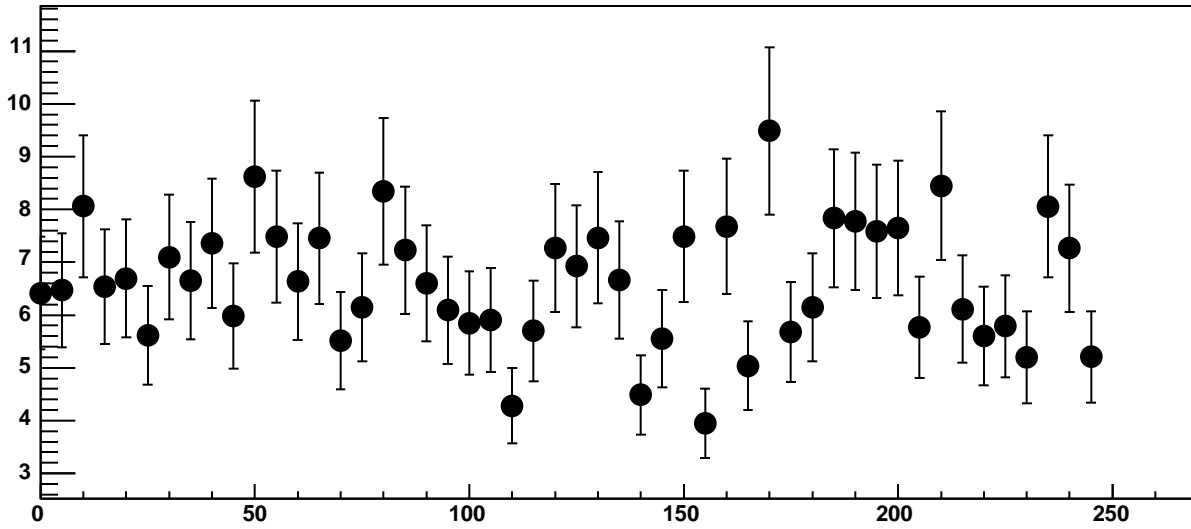


Chip 4, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

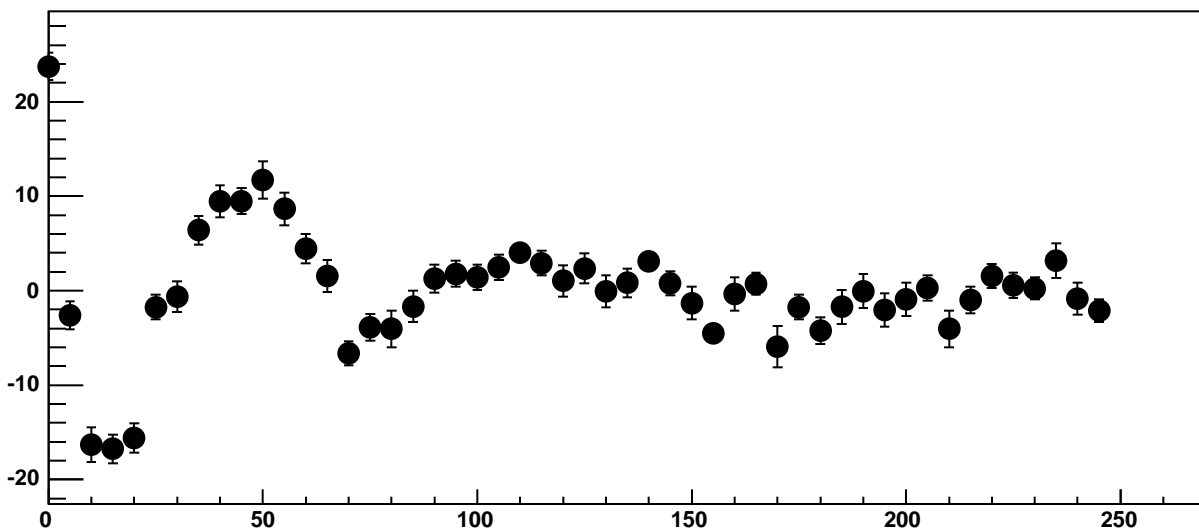


$\chi^2 / \text{ndf}$	534.3 / 41
p0	$266.8 \pm 0.4454$
p1	$70.64 \pm 0.3517$
p2	$-6.717\text{e}+07 \pm 4.867\text{e}+05$
p3	$3.146\text{e}+07 \pm 2.279\text{e}+05$
p4	$1.987 \pm 0.005043$

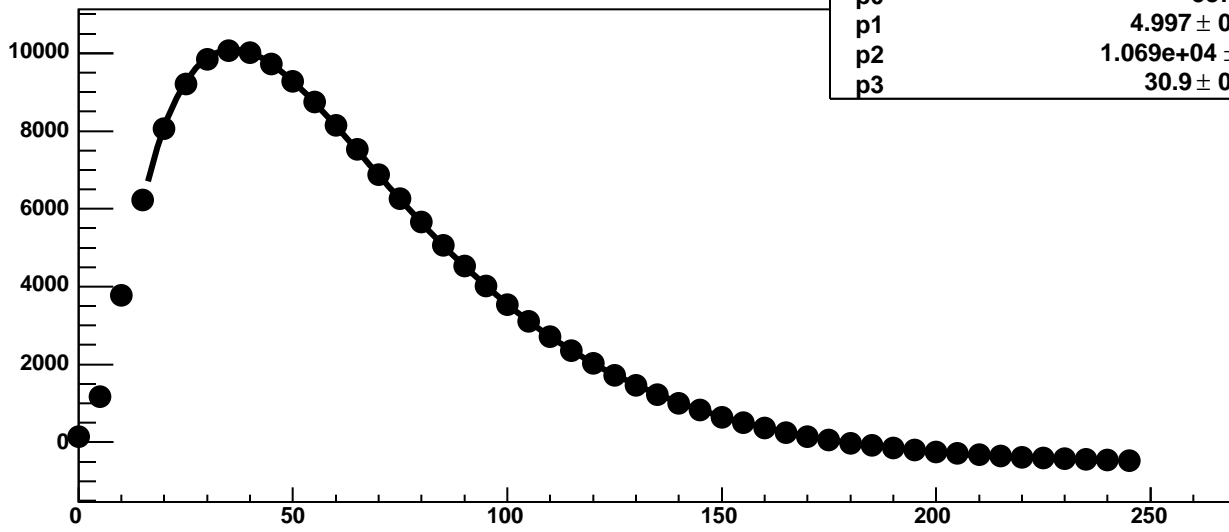
Chip 4, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

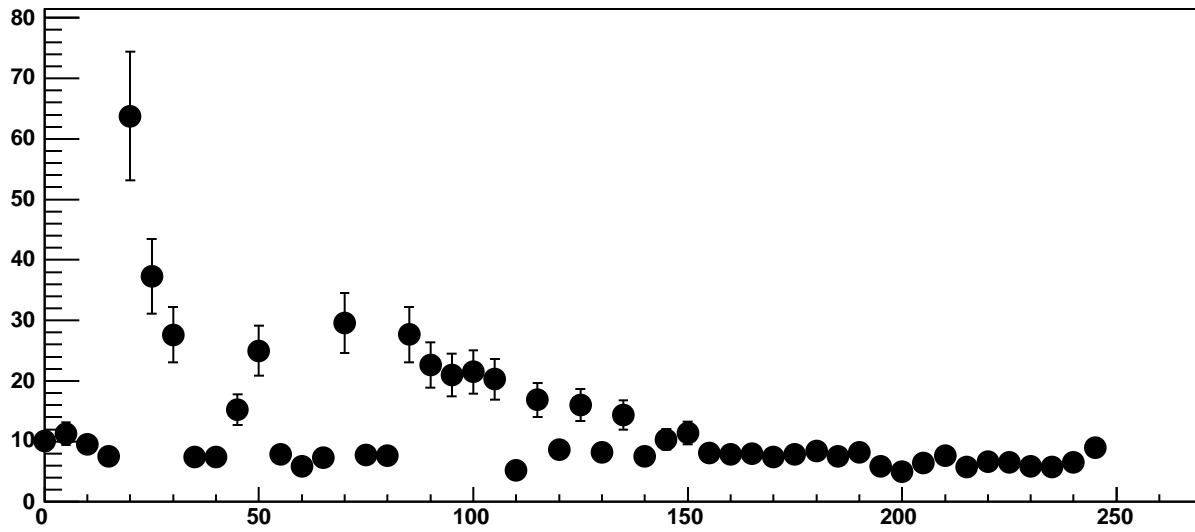


Chip 4, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold

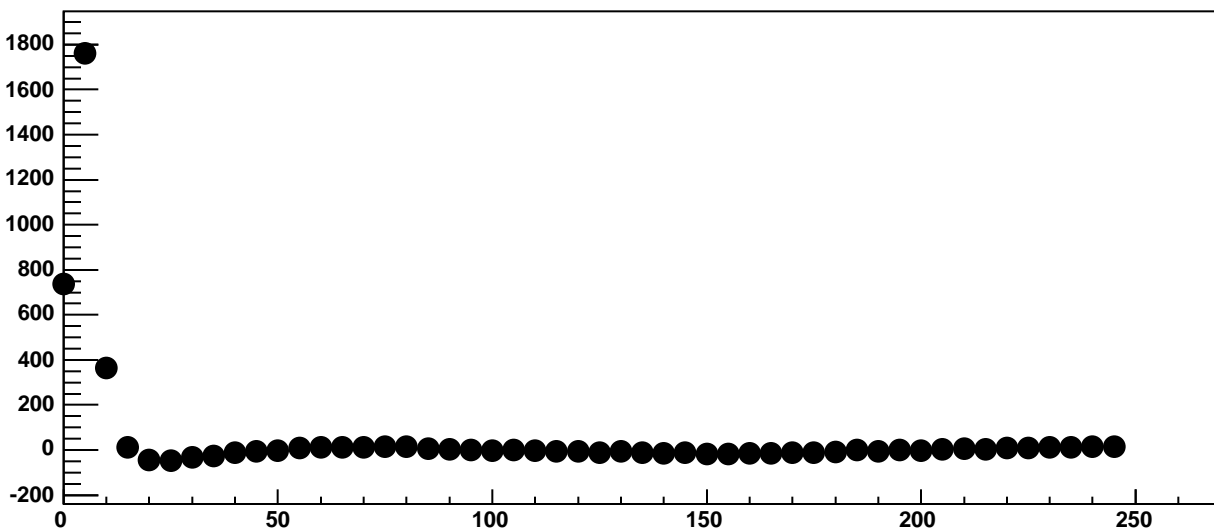


$\chi^2 / \text{ndf}$  1574 / 42  
p0  $-587 \pm 0.492$   
p1  $4.997 \pm 0.004815$   
p2  $1.069\text{e}+04 \pm 0.9096$   
p3  $30.9 \pm 0.004576$

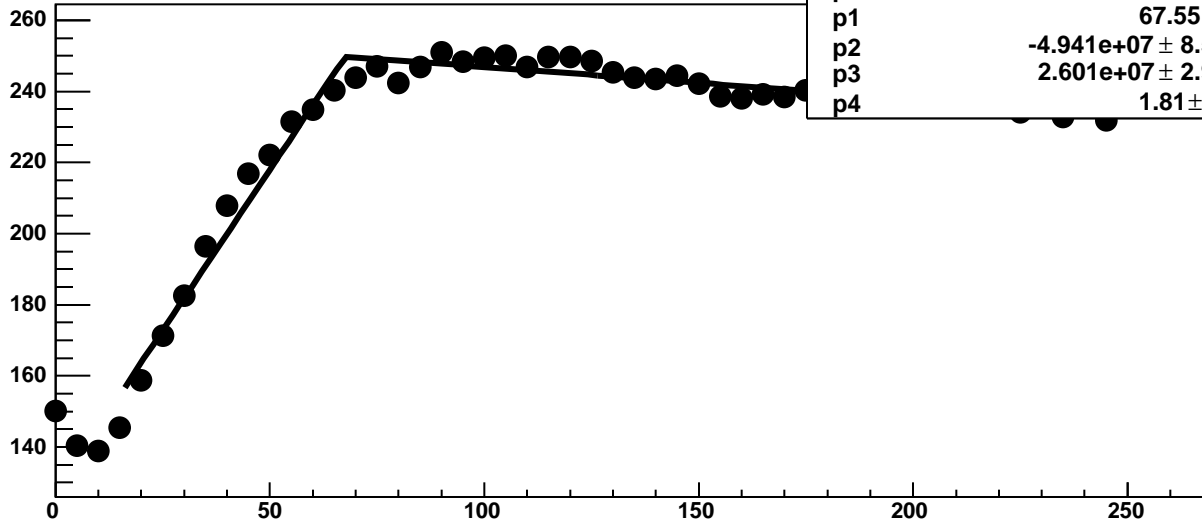
Chip 4, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold

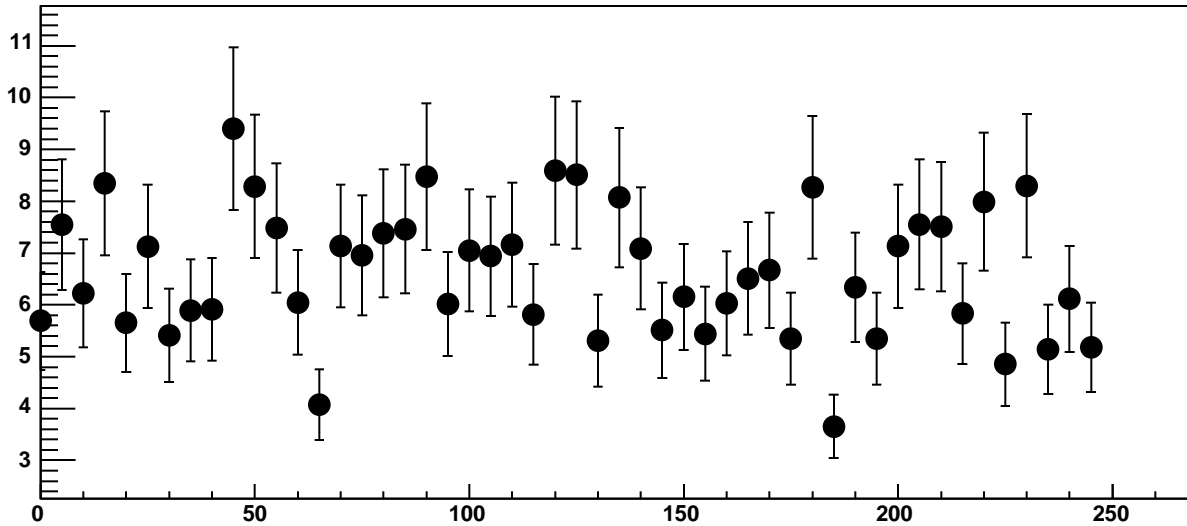


Chip 4, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold

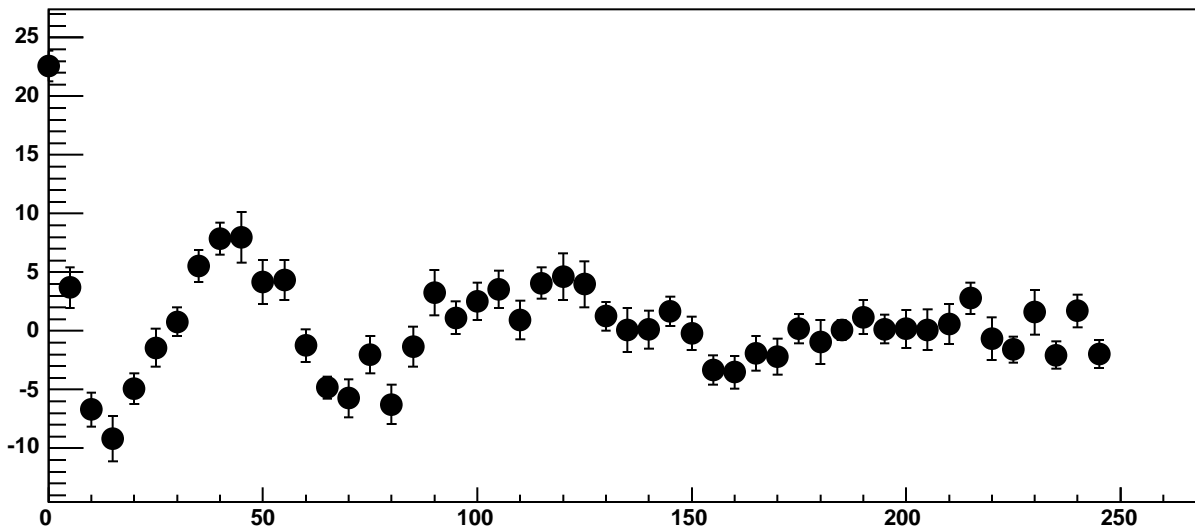


$\chi^2 / \text{ndf}$	232.3 / 41
p0	$249.8 \pm 0.5088$
p1	$67.55 \pm 0.4901$
p2	$-4.941\text{e}+07 \pm 8.575\text{e}+05$
p3	$2.601\text{e}+07 \pm 2.994\text{e}+05$
p4	$1.81 \pm 0.02529$

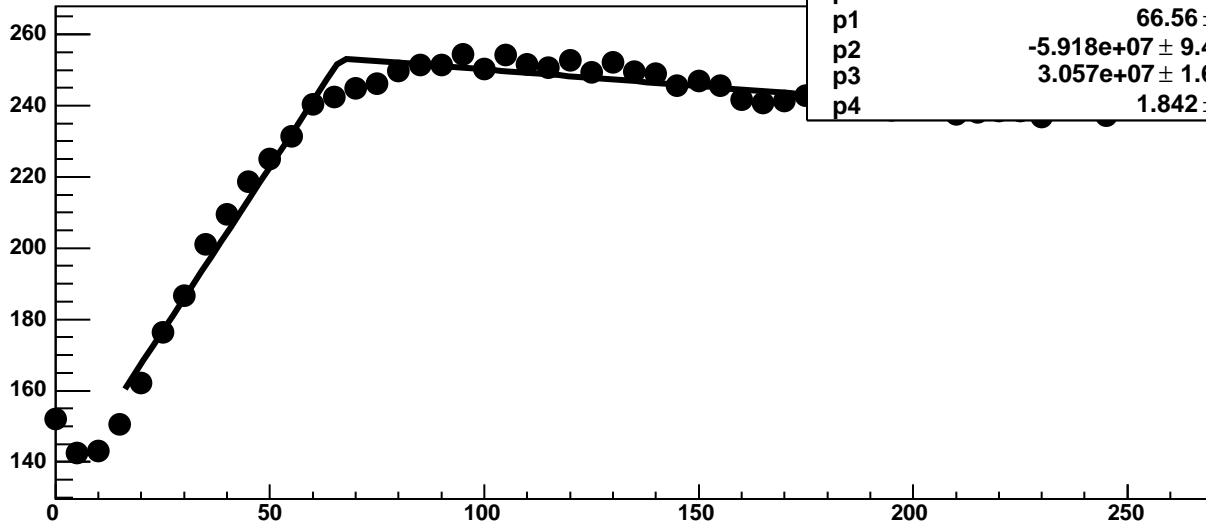
Chip 4, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold

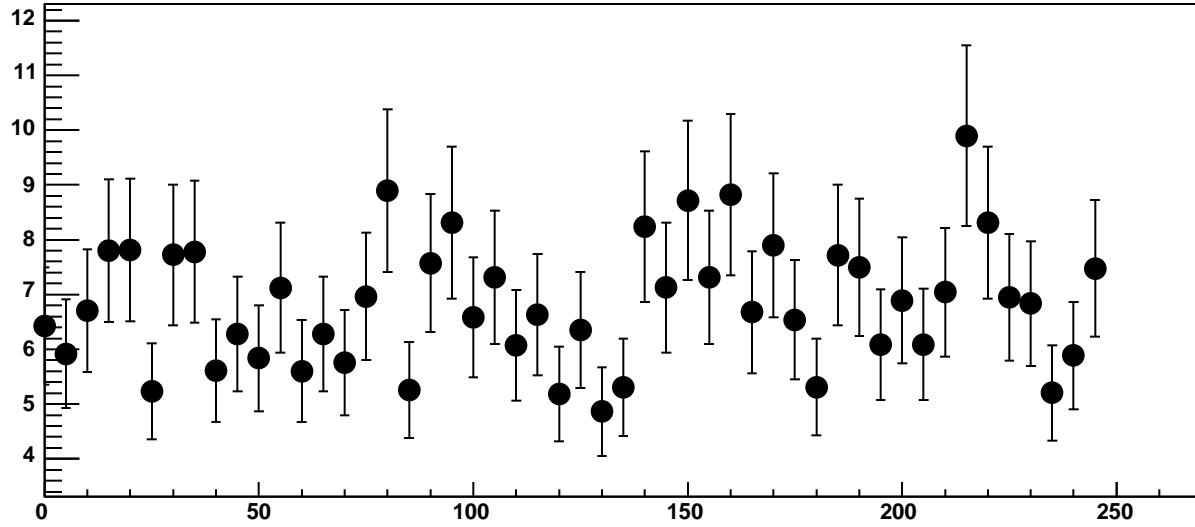


Chip 4, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold

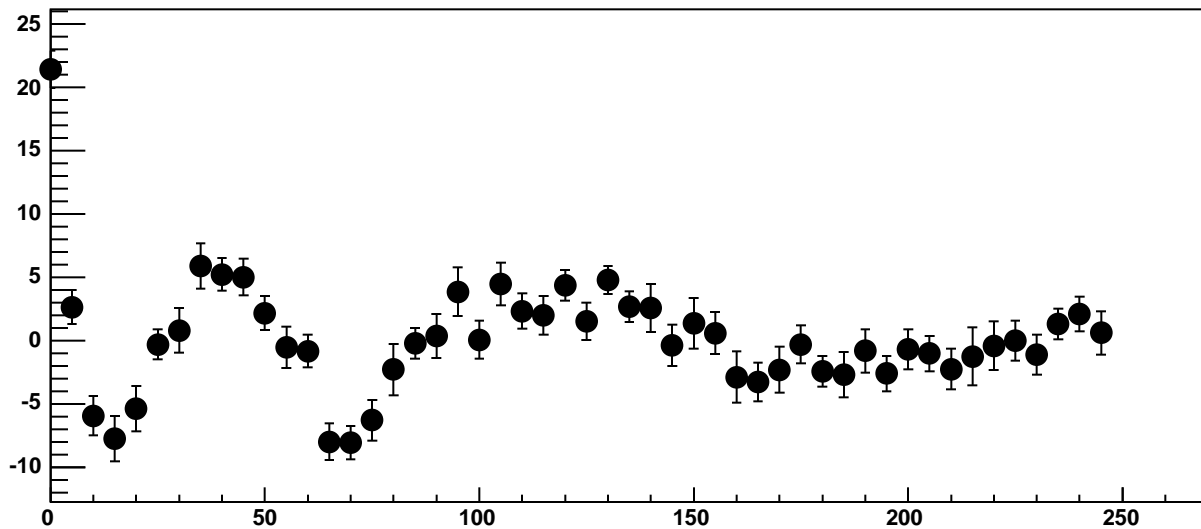


$\chi^2 / \text{ndf}$	236.5 / 41
p0	$253.3 \pm 0.4843$
p1	$66.56 \pm 0.5088$
p2	$-5.918\text{e}+07 \pm 9.495\text{e}+05$
p3	$3.057\text{e}+07 \pm 1.645\text{e}+05$
p4	$1.842 \pm 0.0289$

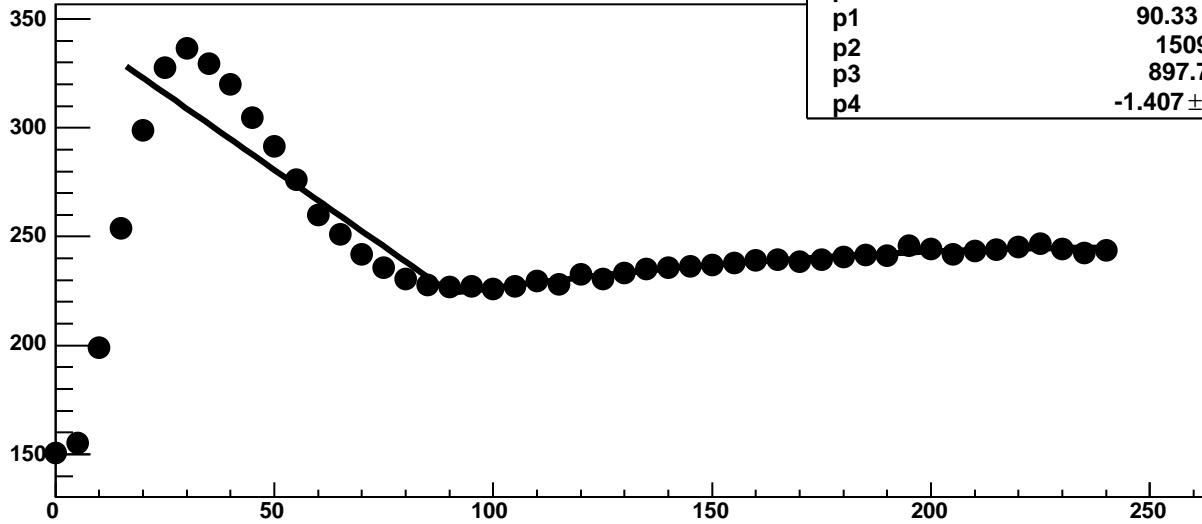
Chip 4, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

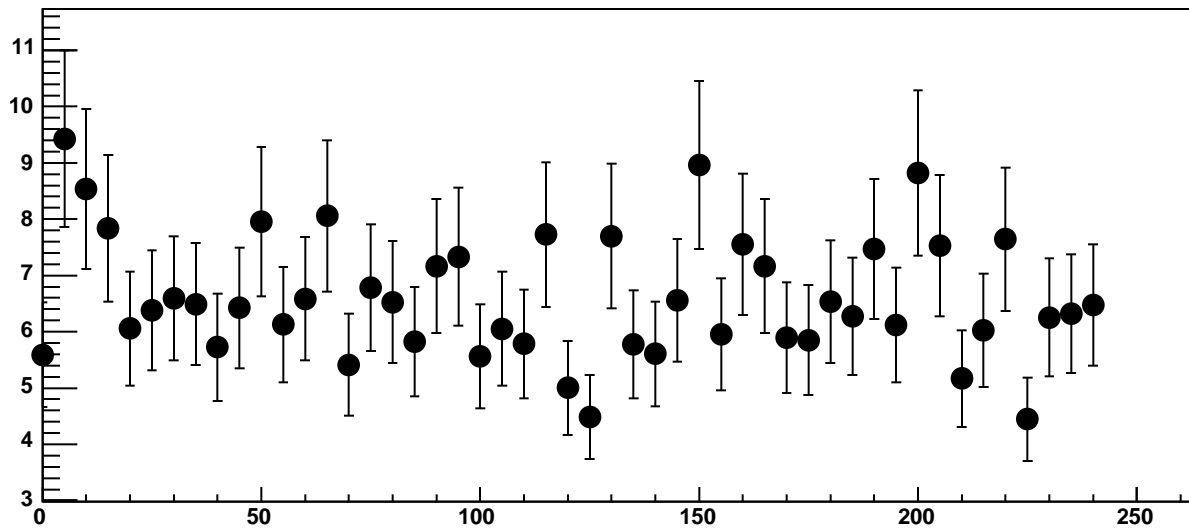


Chip 4, Channel 17, Enable 5, DAC=4000, ADC Mean vs Hold

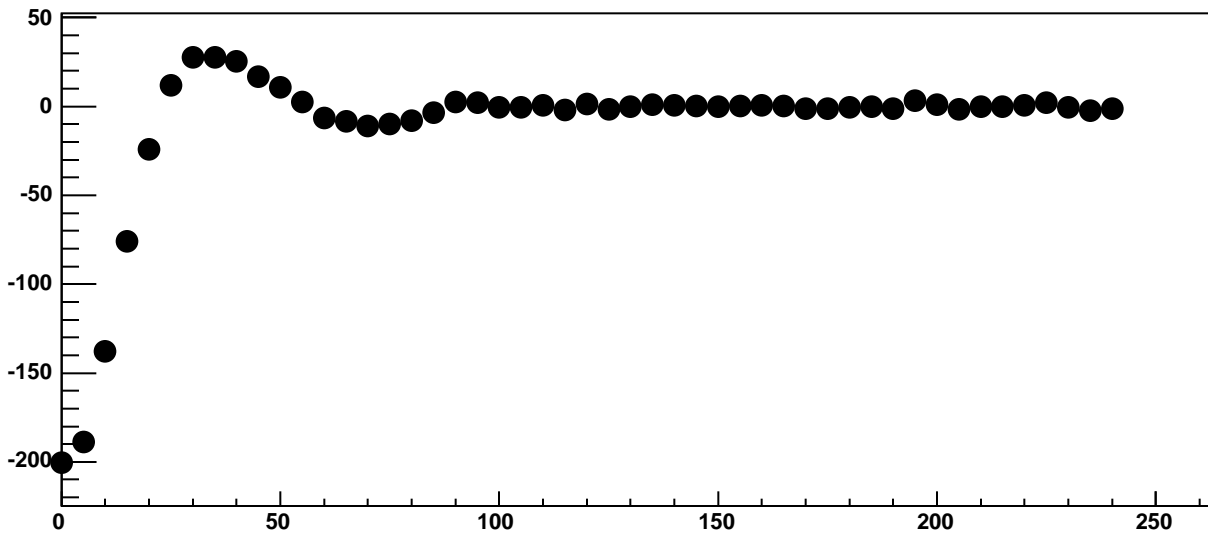


$\chi^2 / \text{ndf}$	3590 / 41
p0	$223.9 \pm 0.7175$
p1	$90.33 \pm 0.6609$
p2	$1509 \pm 258.3$
p3	$897.7 \pm 166.5$
p4	$-1.407 \pm 0.01637$

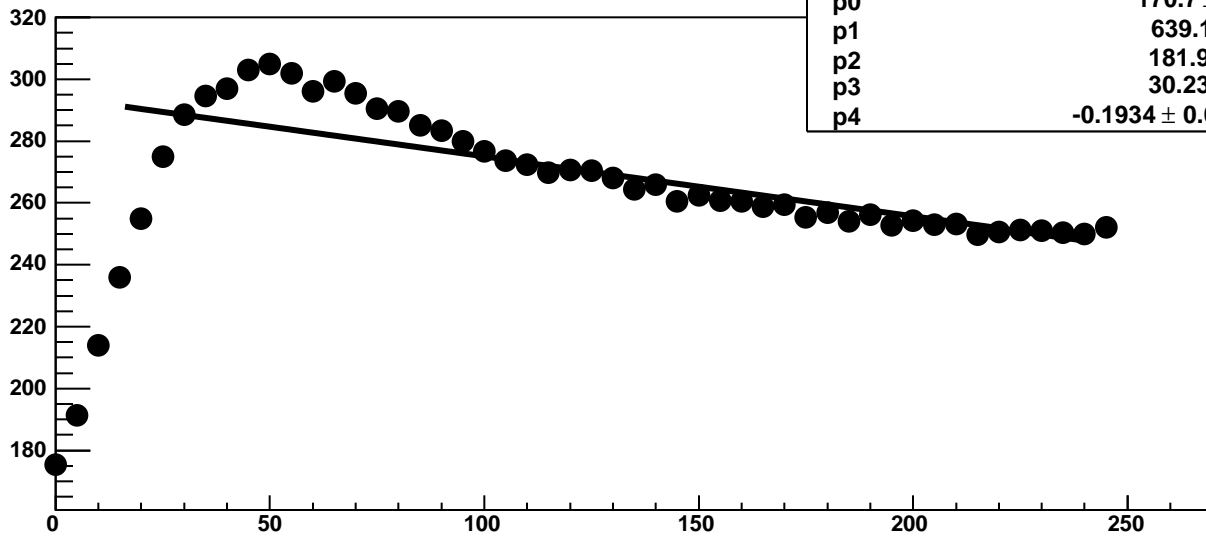
Chip 4, Channel 17, Enable 5, DAC=4000, ADC Noise vs Hold



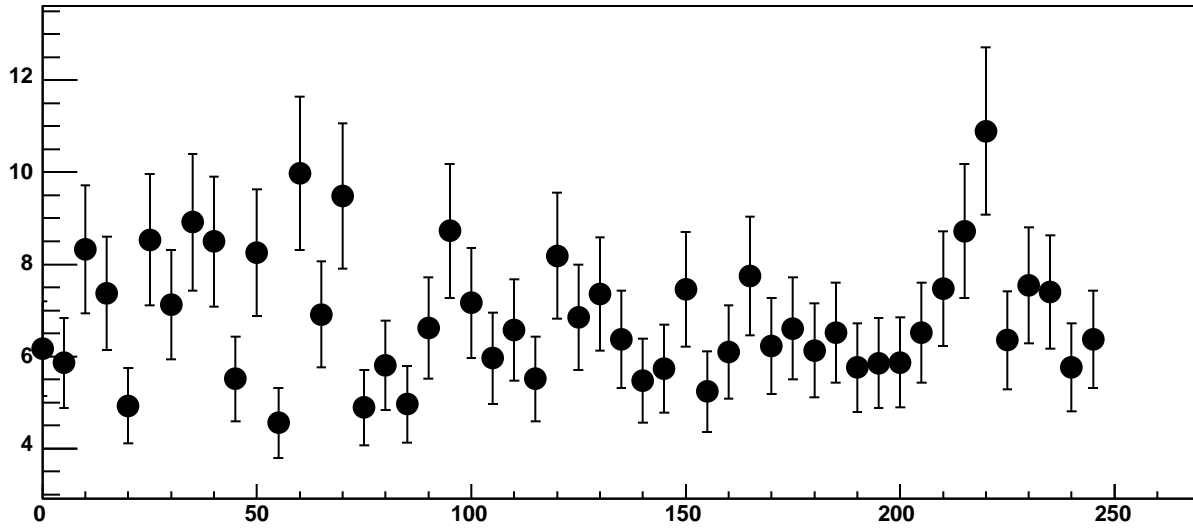
Chip 4, Channel 17, Enable 5, DAC=4000, ADC Residuals vs Hold



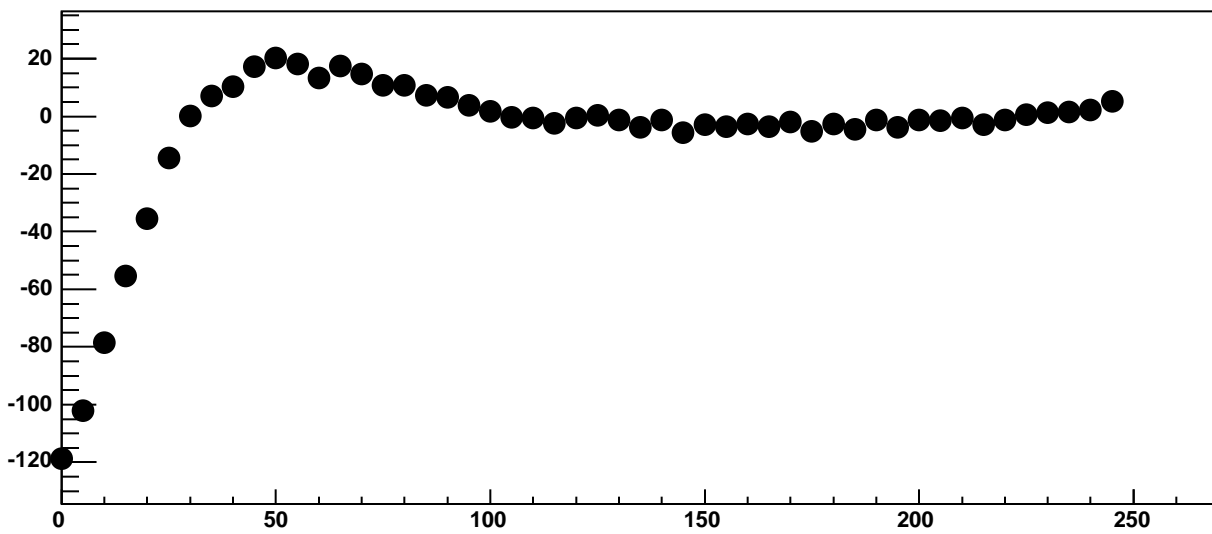
Chip 5, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold



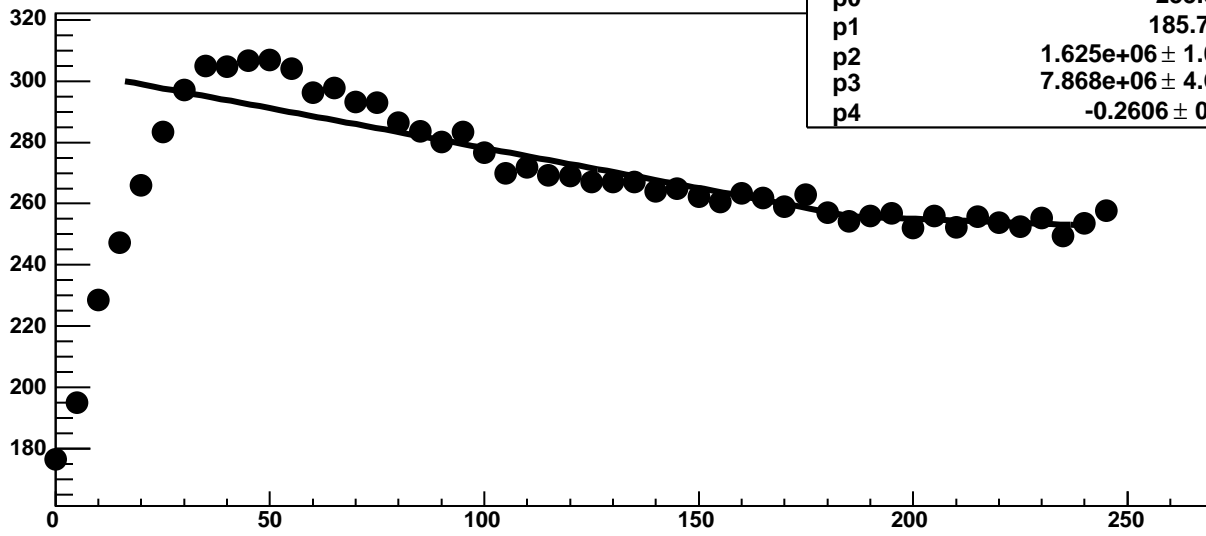
Chip 5, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold

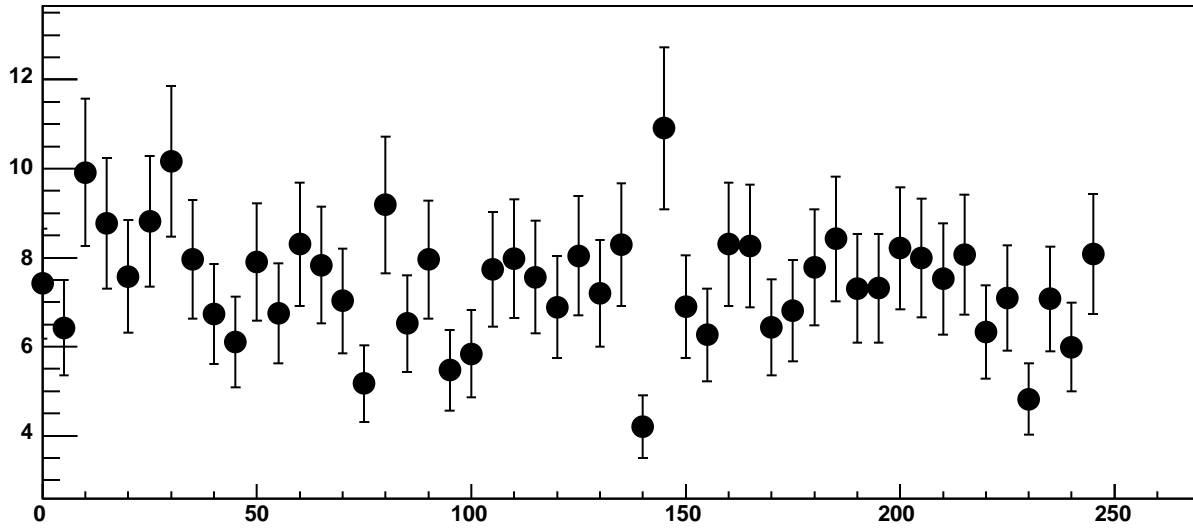


Chip 5, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

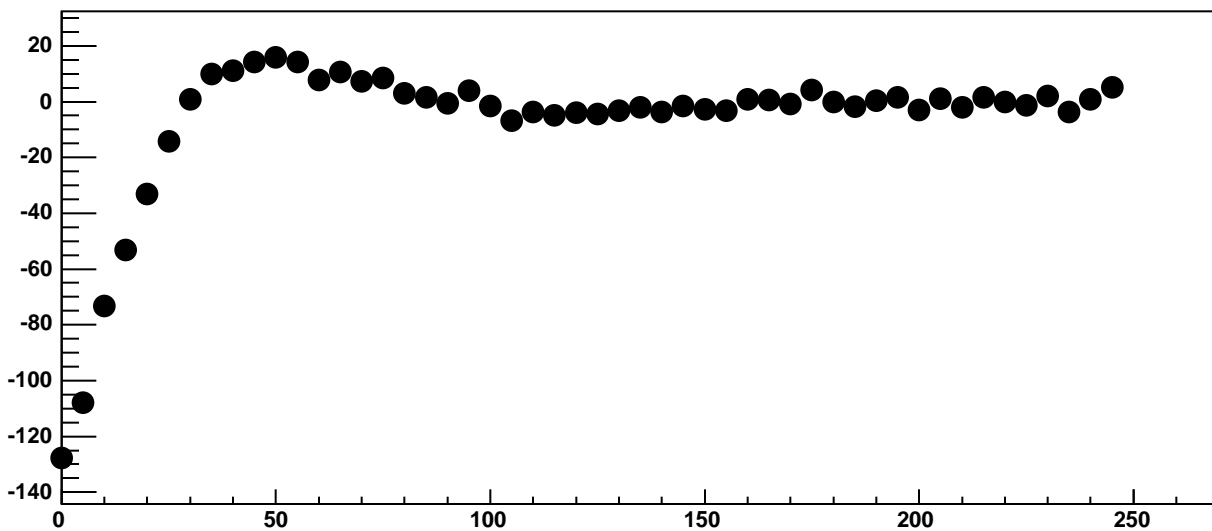


$\chi^2 / \text{ndf}$	1680 / 41
p0	$255.8 \pm 1.36$
p1	$185.7 \pm 5.838$
p2	$1.625\text{e}+06 \pm 1.017\text{e}+06$
p3	$7.868\text{e}+06 \pm 4.655\text{e}+06$
p4	$-0.2606 \pm 0.005767$

Chip 5, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold

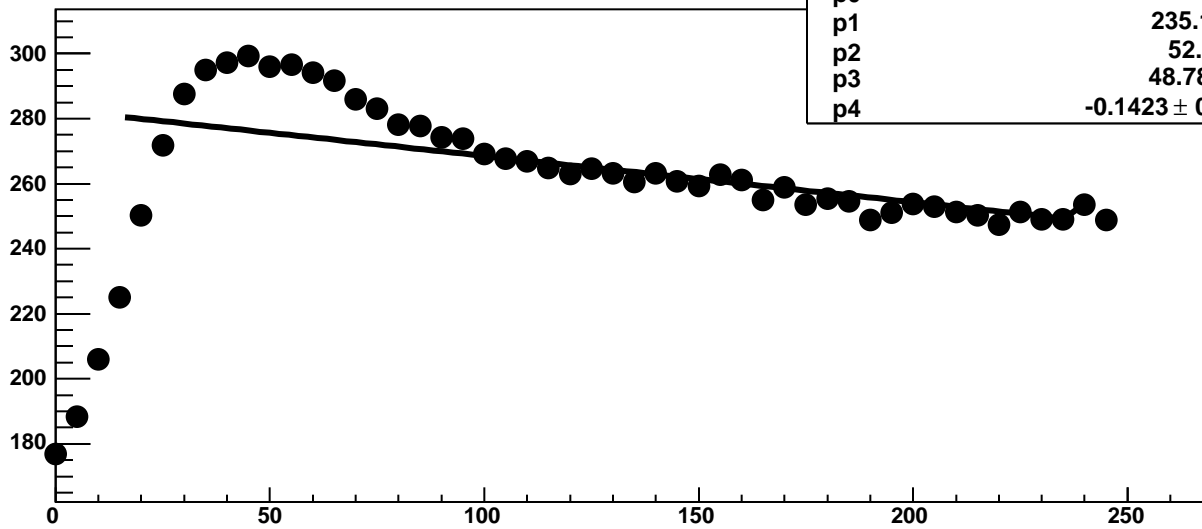


Chip 5, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold



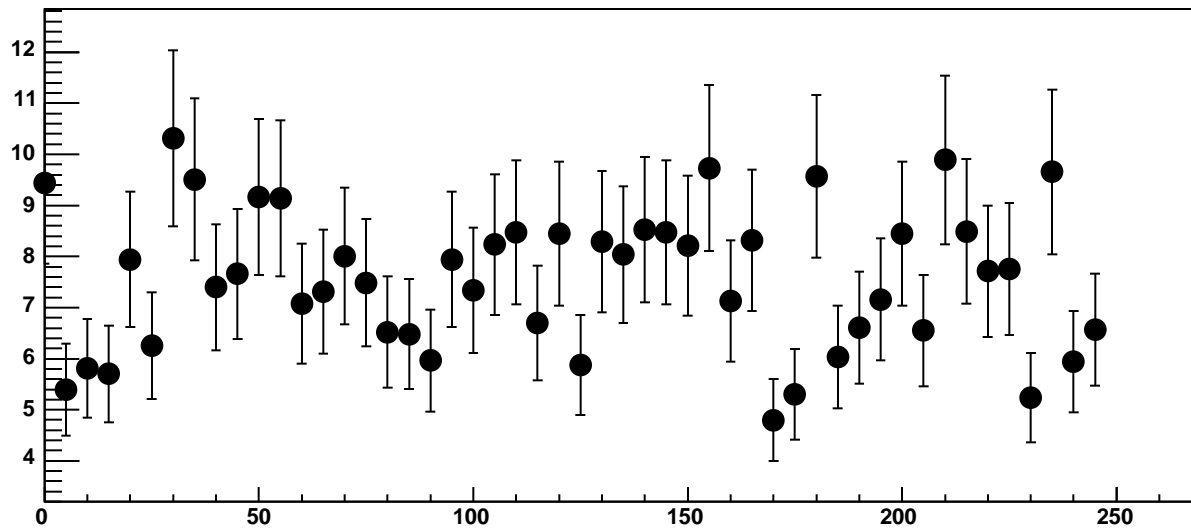


Chip 5, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

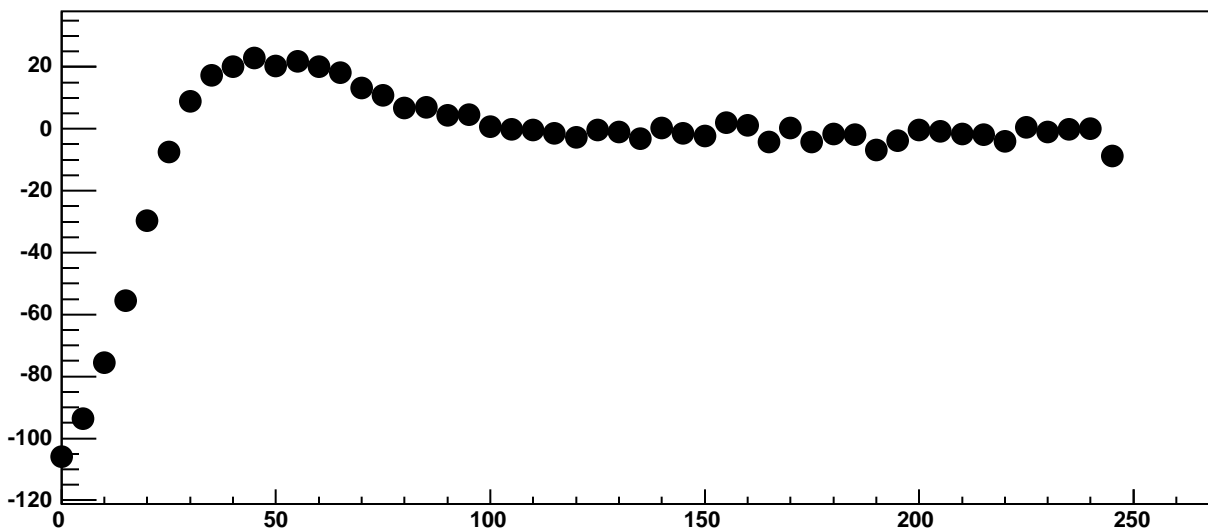


$\chi^2 / \text{ndf}$	3168 / 41
p0	$249.3 \pm 1.895$
p1	$235.1 \pm 13.26$
p2	$52.1 \pm 194.1$
p3	$48.78 \pm 94.47$
p4	$-0.1423 \pm 0.003773$

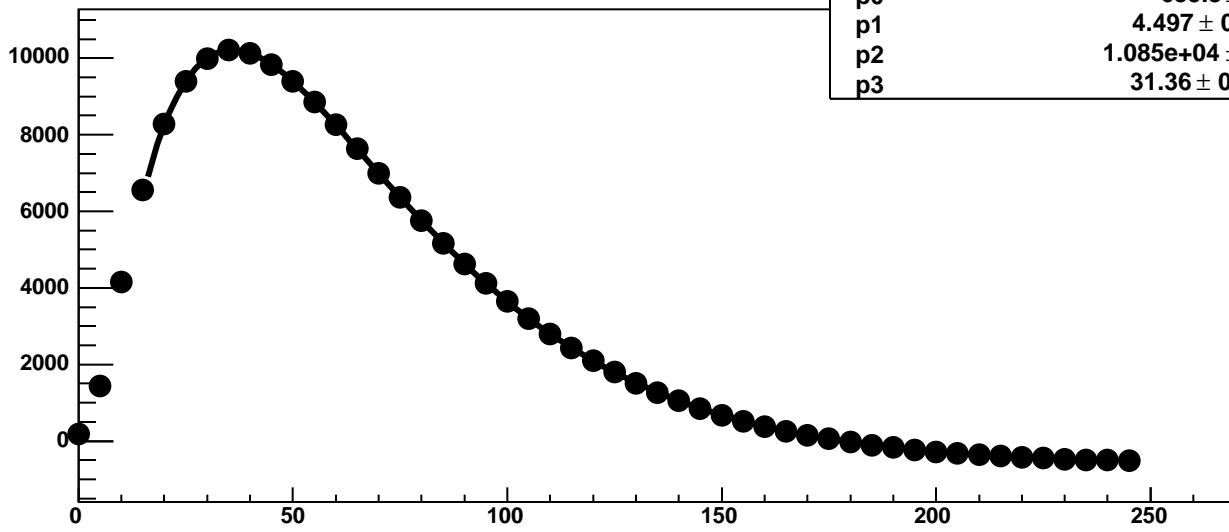
Chip 5, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

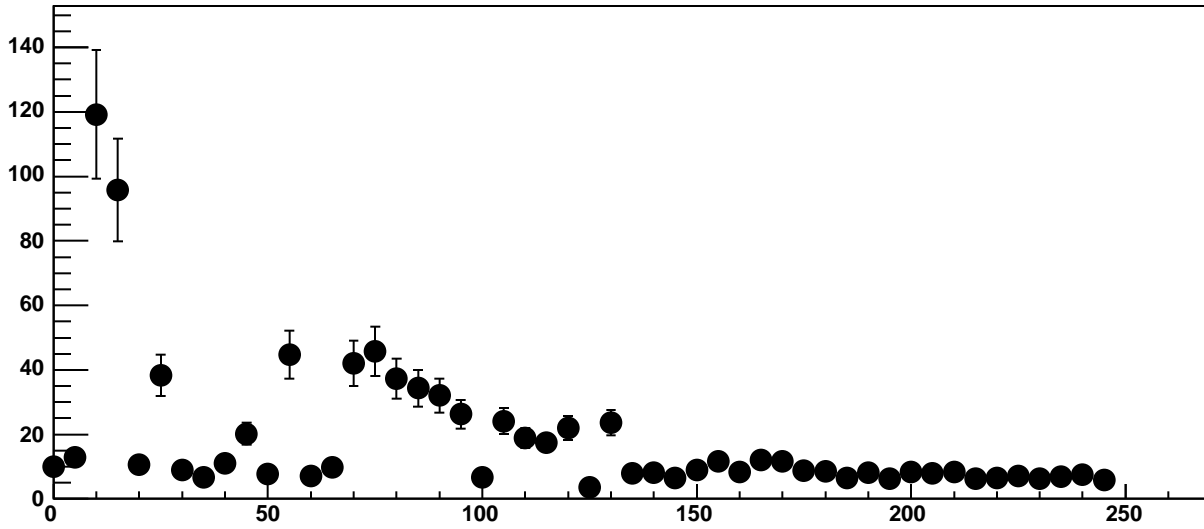


Chip 5, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold

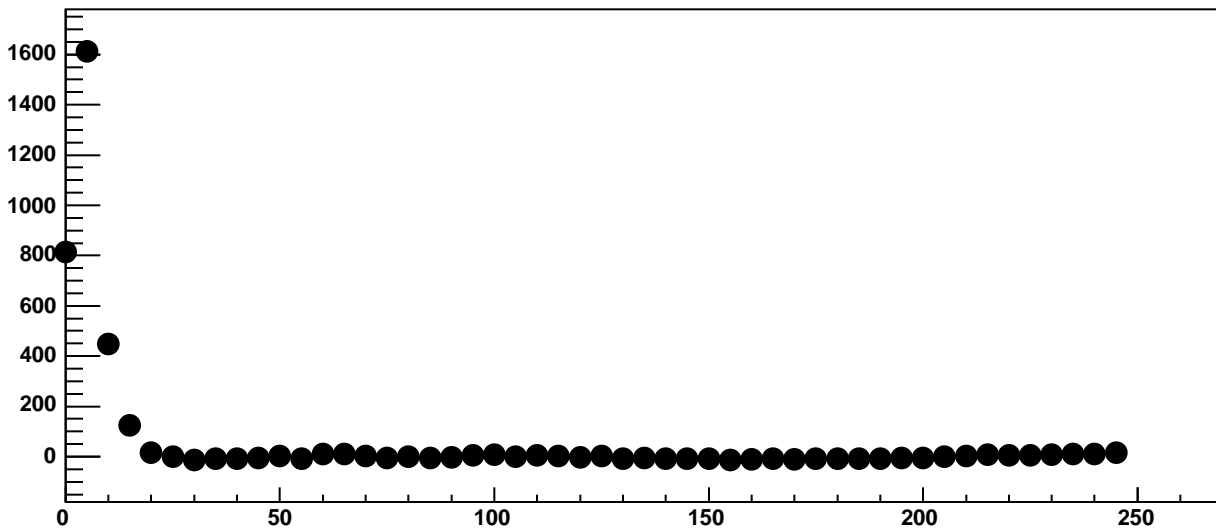


$\chi^2 / \text{ndf}$	646.7 / 42
p0	-635.8 ± 0.5852
p1	4.497 ± 0.009362
p2	1.085e+04 ± 0.9358
p3	31.36 ± 0.005627

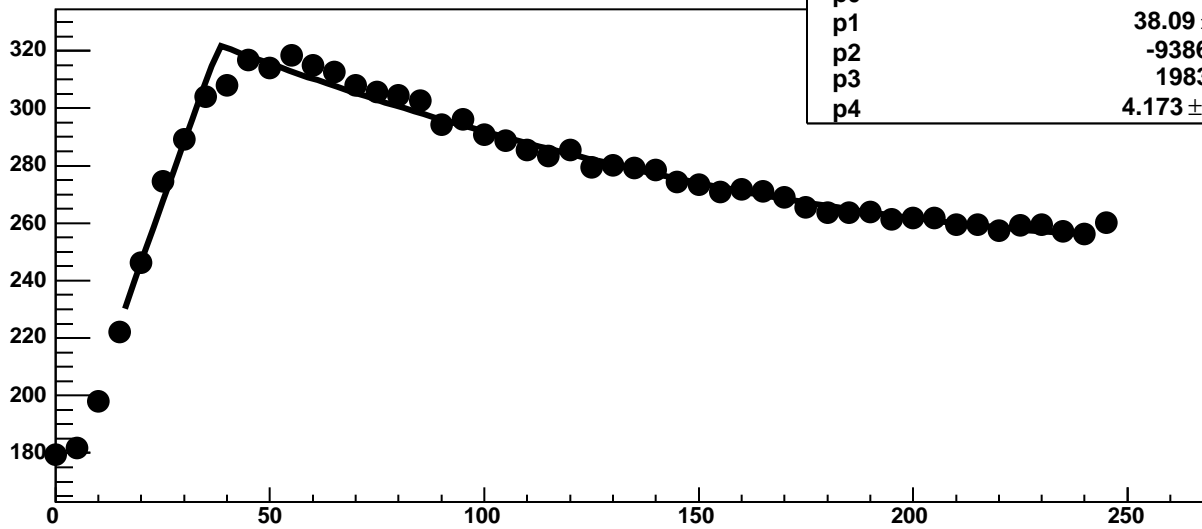
Chip 5, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold

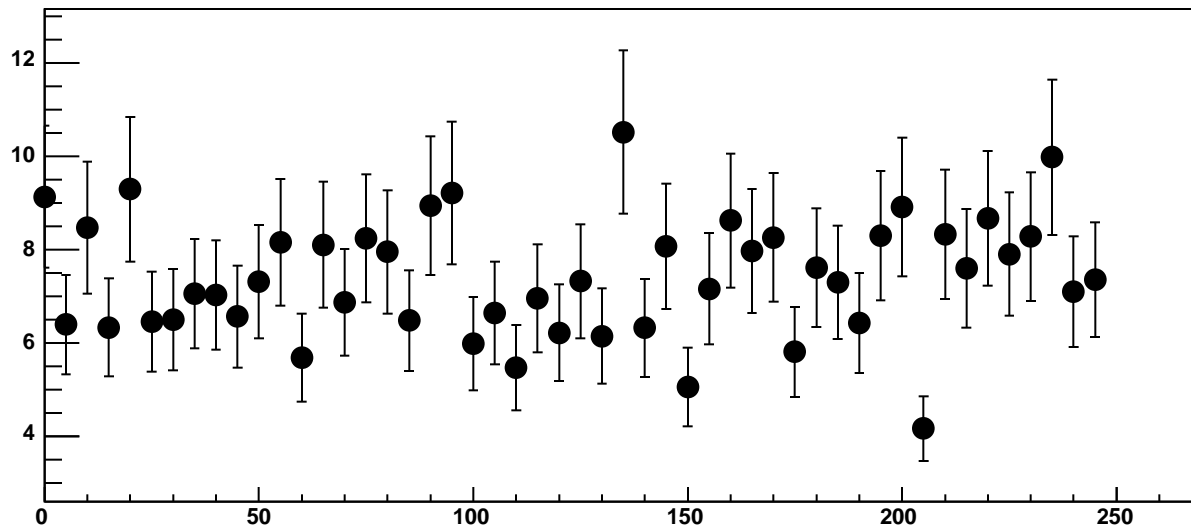


Chip 5, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

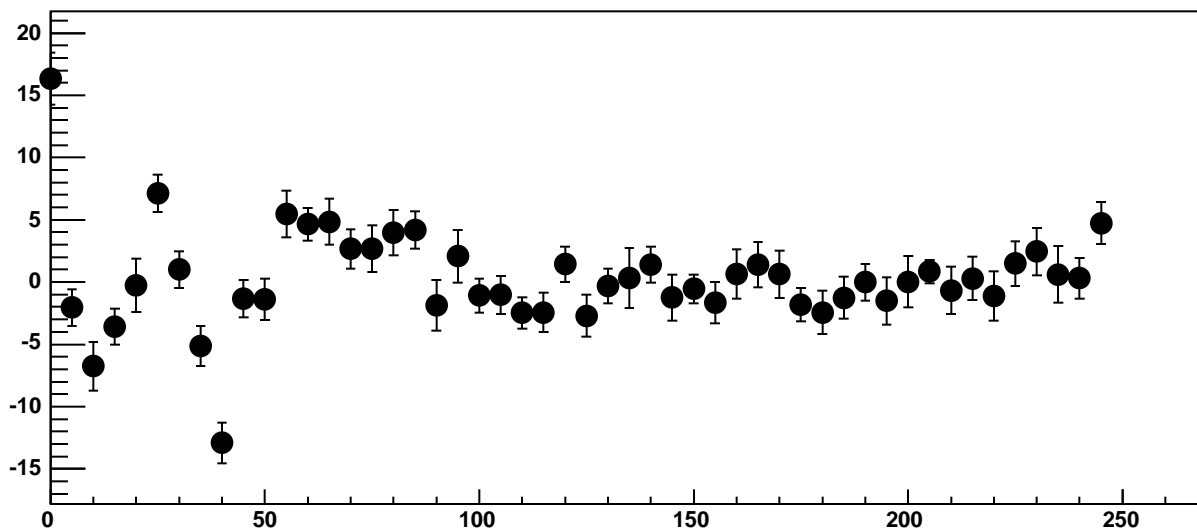


$\chi^2 / \text{ndf}$	176 / 41
p0	$322 \pm 0.6533$
p1	$38.09 \pm 0.3285$
p2	$-9386 \pm 635.6$
p3	$1983 \pm 124.7$
p4	$4.173 \pm 0.09679$

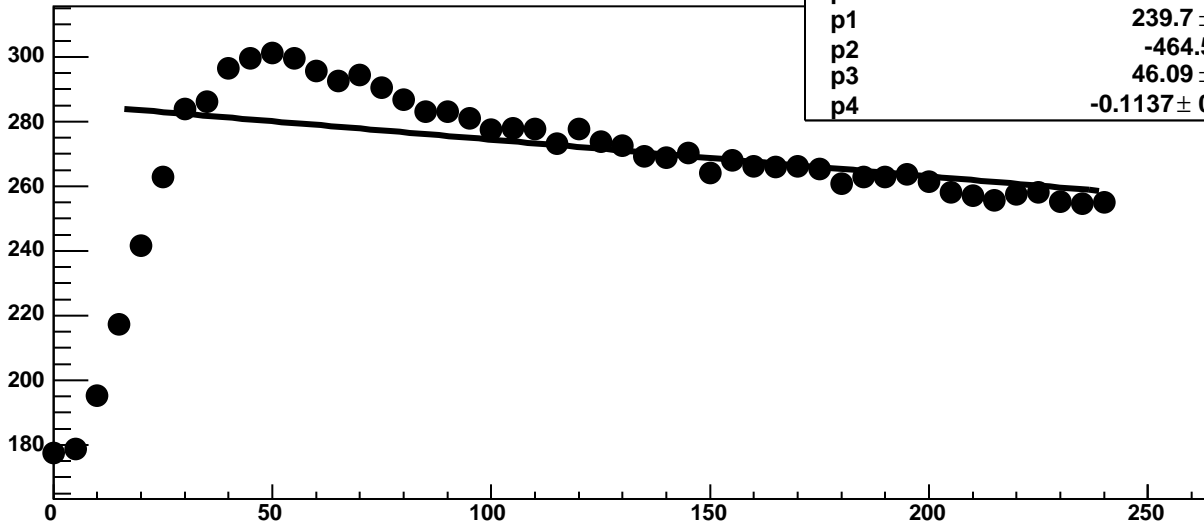
Chip 5, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

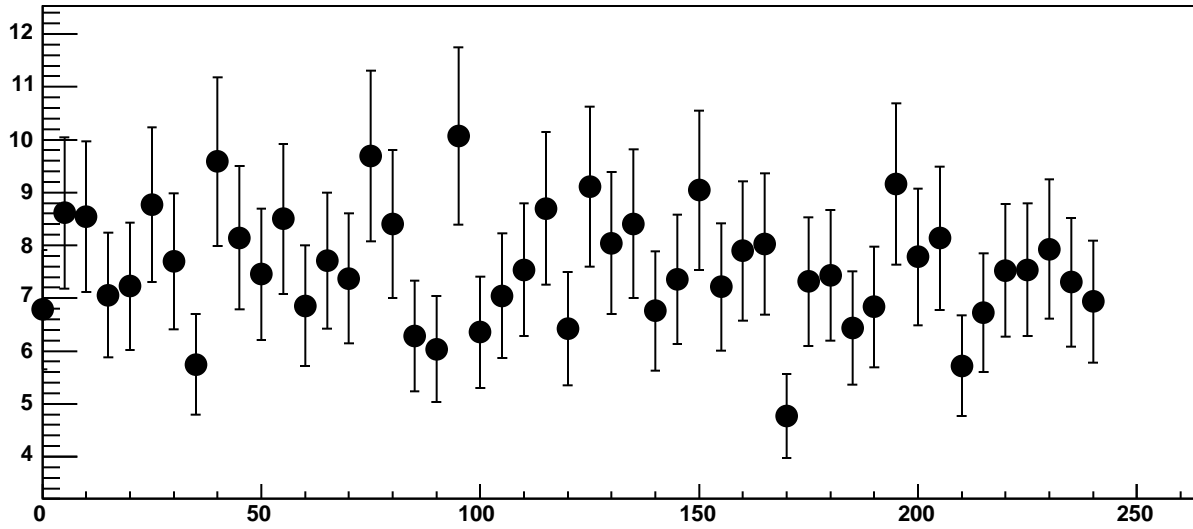


Chip 5, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold

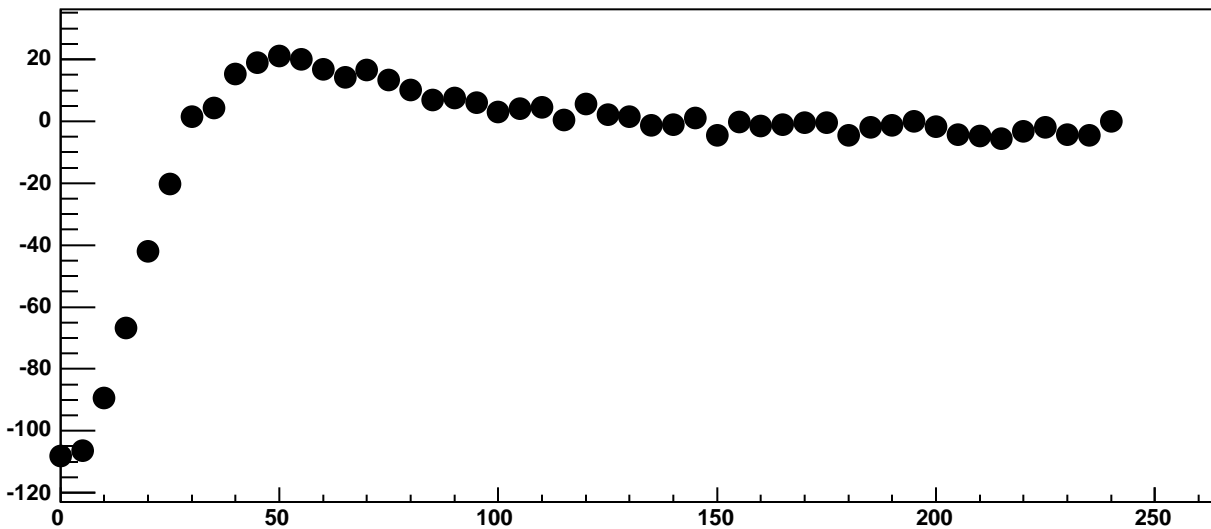


$\chi^2 / \text{ndf}$	3365 / 41
p0	$258.5 \pm 0.5062$
p1	$239.7 \pm 0.1675$
p2	$-464.5 \pm 9.111$
p3	$46.09 \pm 0.4354$
p4	$-0.1137 \pm 0.003861$

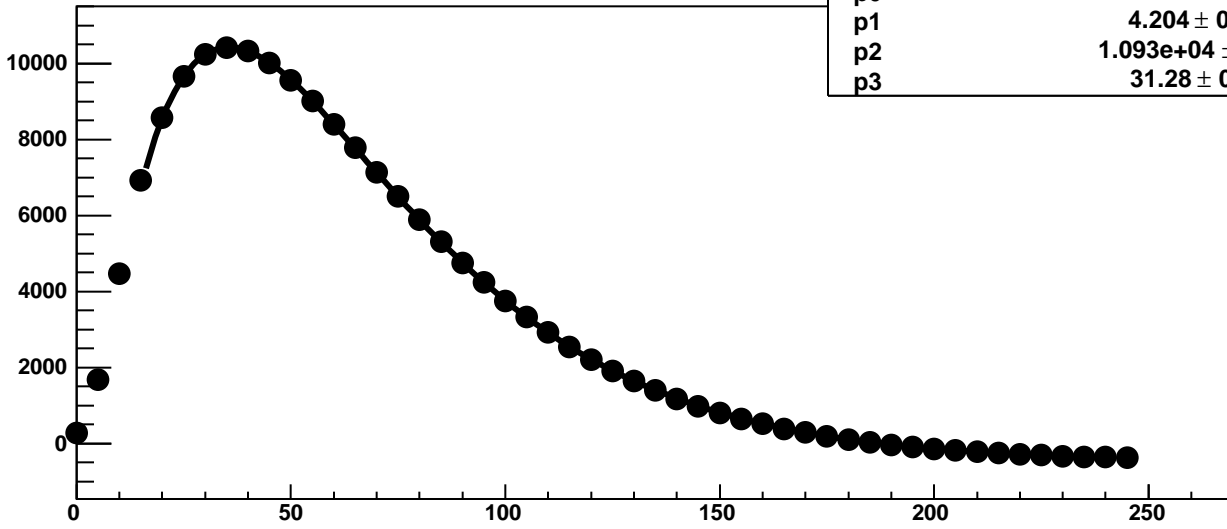
Chip 5, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold

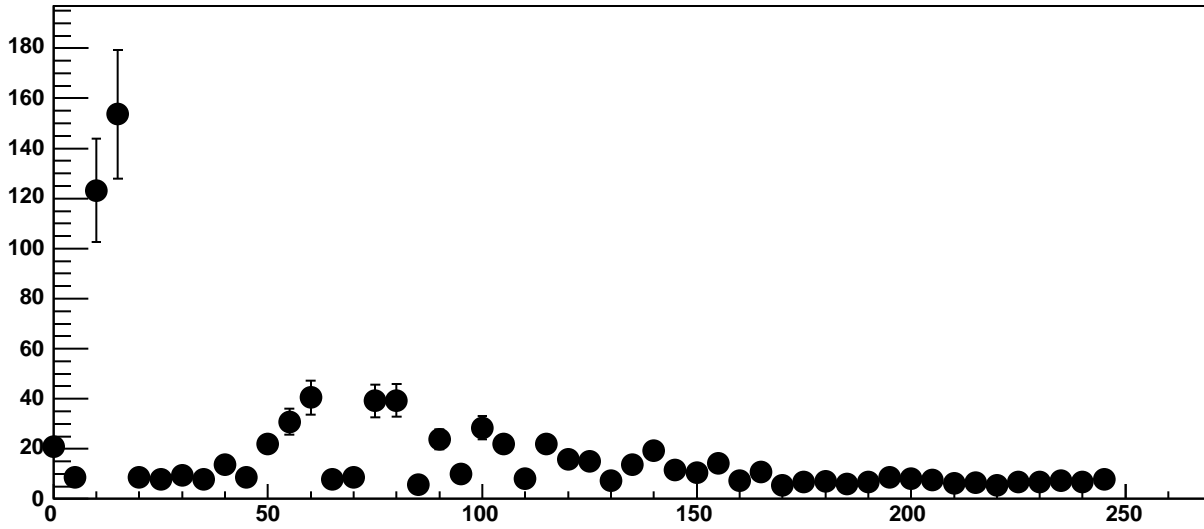


Chip 5, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold

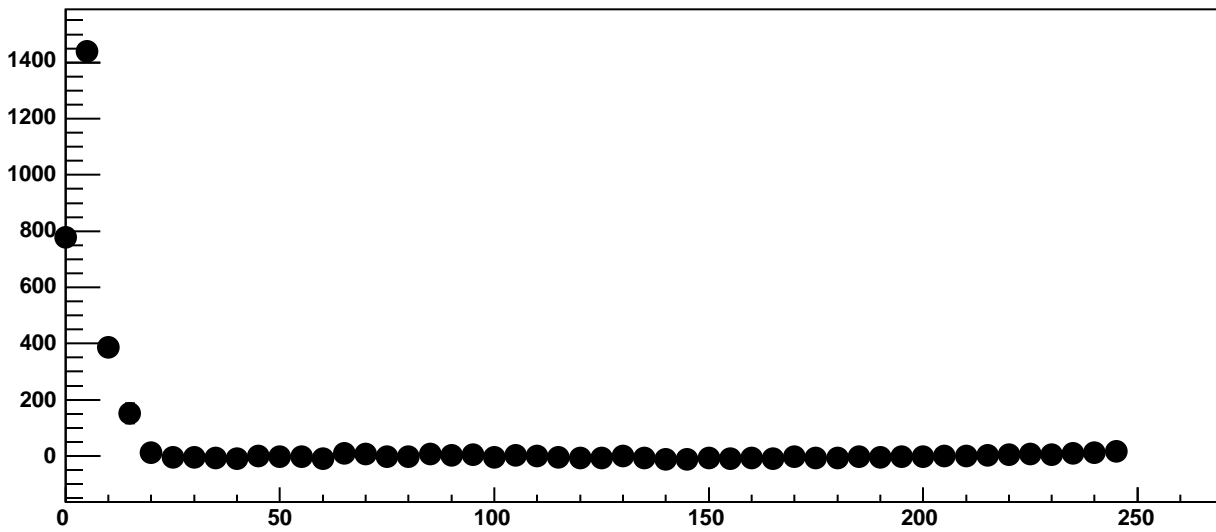


$\chi^2 / \text{ndf}$	414.4 / 42
p0	-498 ± 0.5477
p1	4.204 ± 0.008716
p2	1.093e+04 ± 0.9175
p3	31.28 ± 0.005912

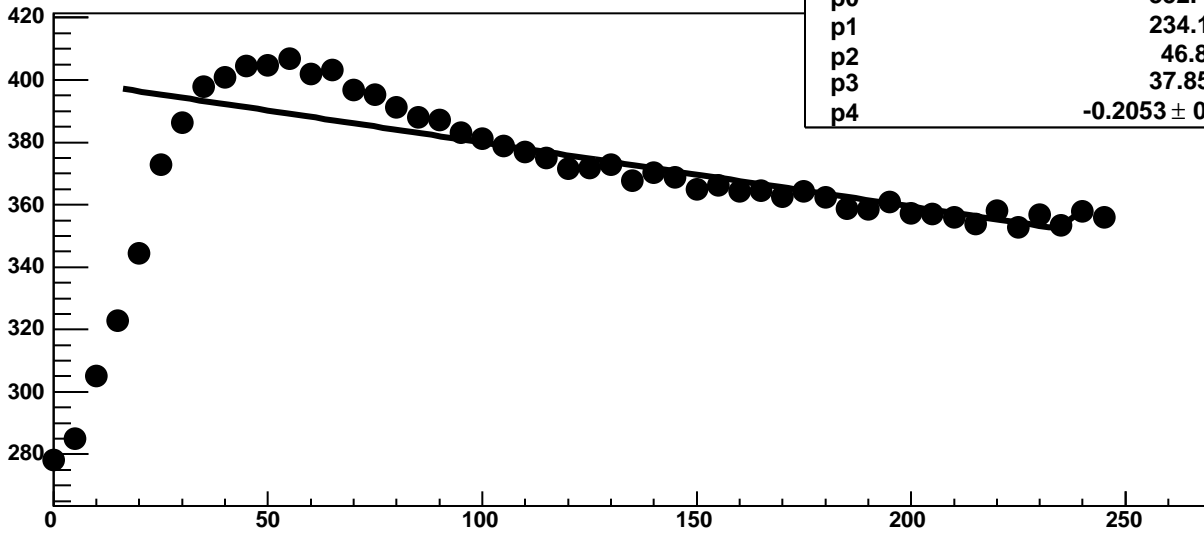
Chip 5, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold

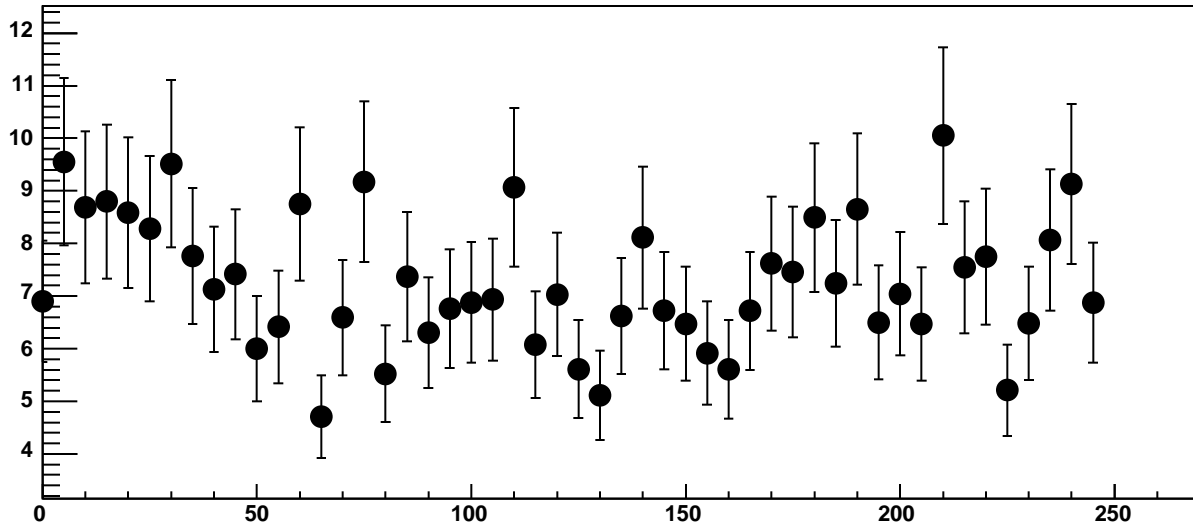


Chip 5, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

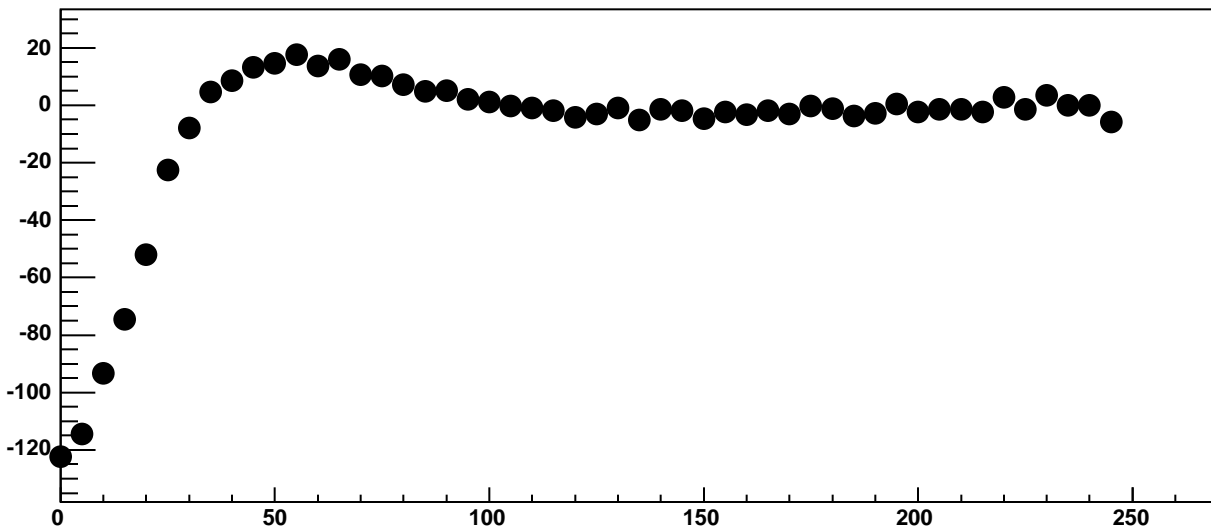


$\chi^2 / \text{ndf}$	3038 / 41
p0	$352.4 \pm 0.551$
p1	$234.1 \pm 1.858$
p2	$46.8 \pm 113.4$
p3	$37.85 \pm 97.41$
p4	$-0.2053 \pm 0.003899$

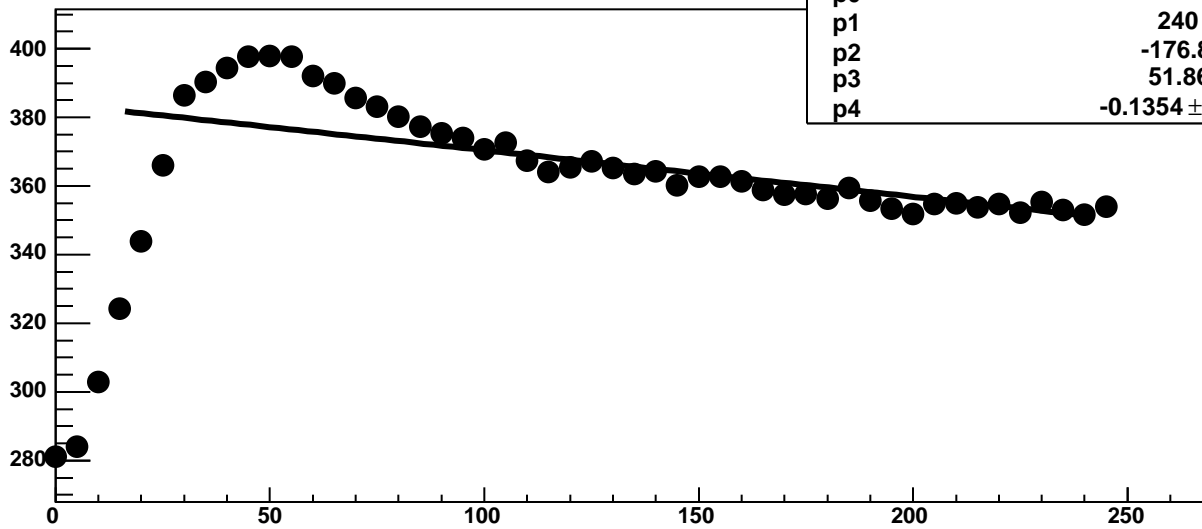
Chip 5, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

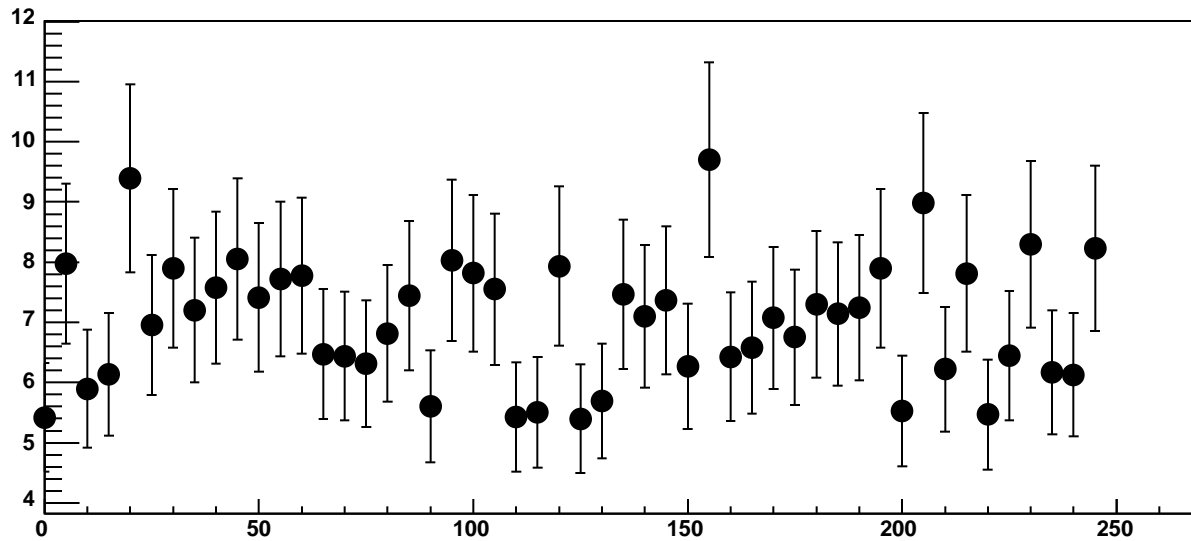


Chip 5, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

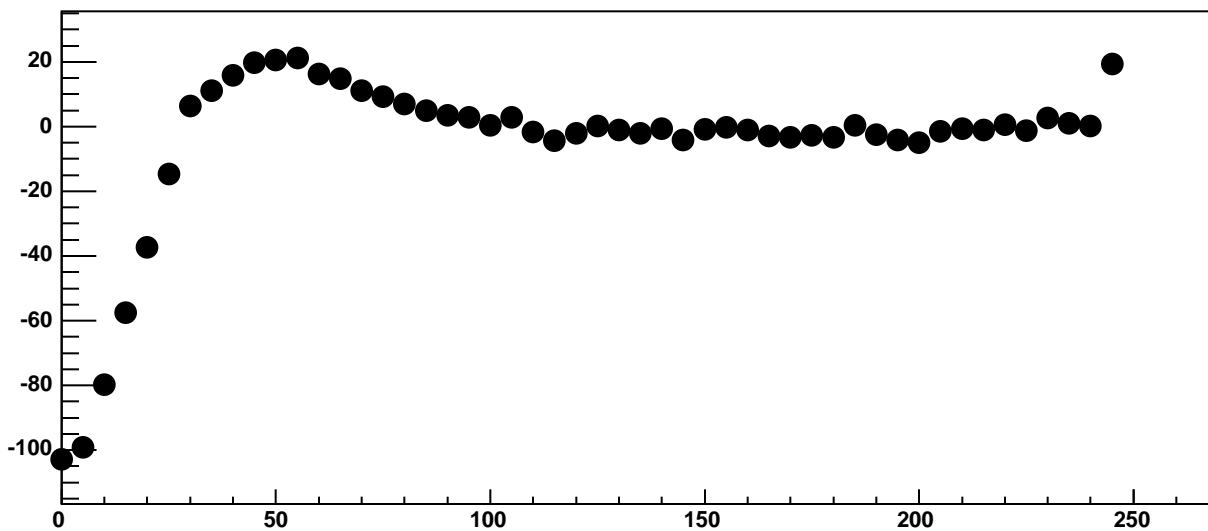


$\chi^2 / \text{ndf}$	2991 / 41
p0	$351.4 \pm 0.4363$
p1	$240 \pm 0.4073$
p2	$-176.8 \pm 12.96$
p3	$51.86 \pm 1.076$
p4	$-0.1354 \pm 0.00338$

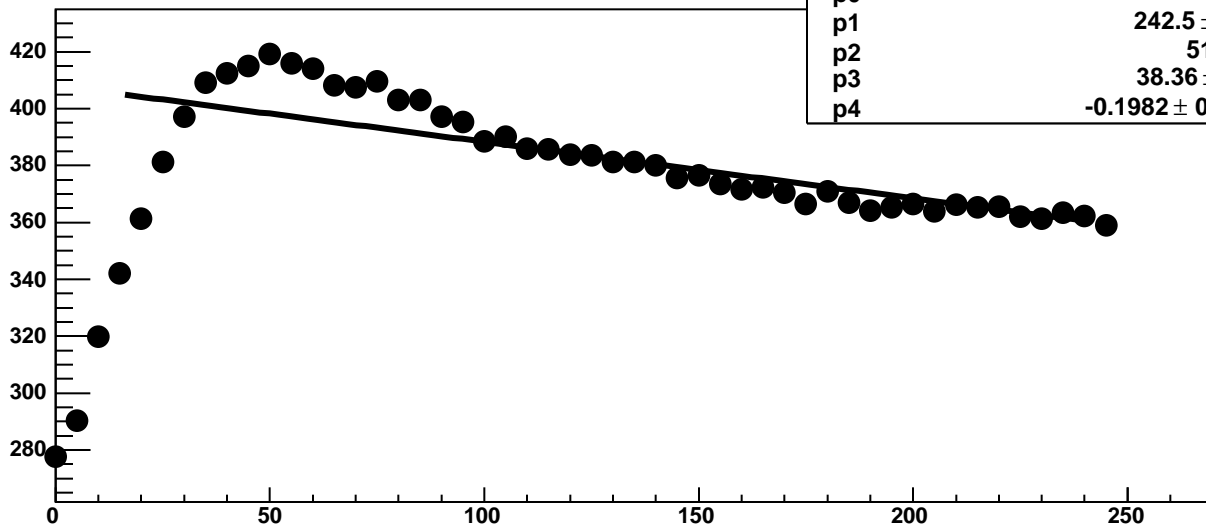
Chip 5, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

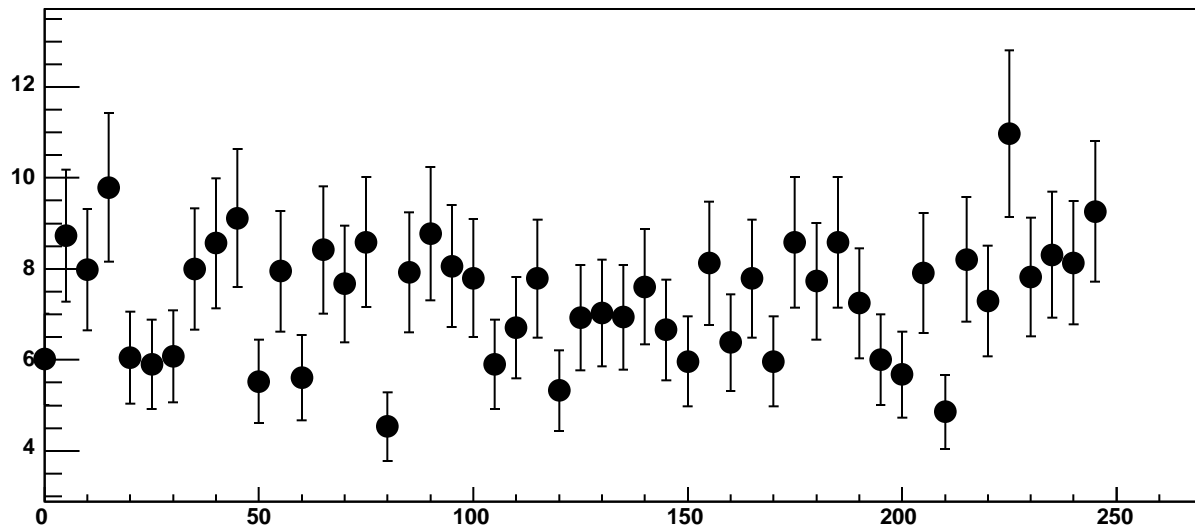


Chip 5, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold

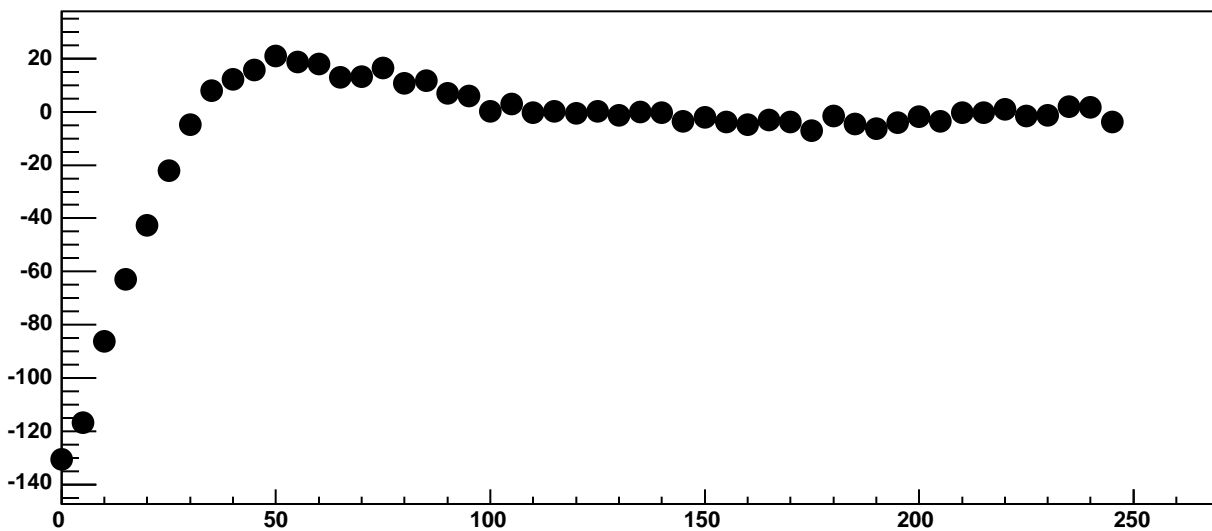


$\chi^2 / \text{ndf}$	3131 / 41
p0	$360.1 \pm 0.463$
p1	$242.5 \pm 0.3706$
p2	$51 \pm 2.114$
p3	$38.36 \pm 0.2353$
p4	$-0.1982 \pm 0.003637$

Chip 5, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold

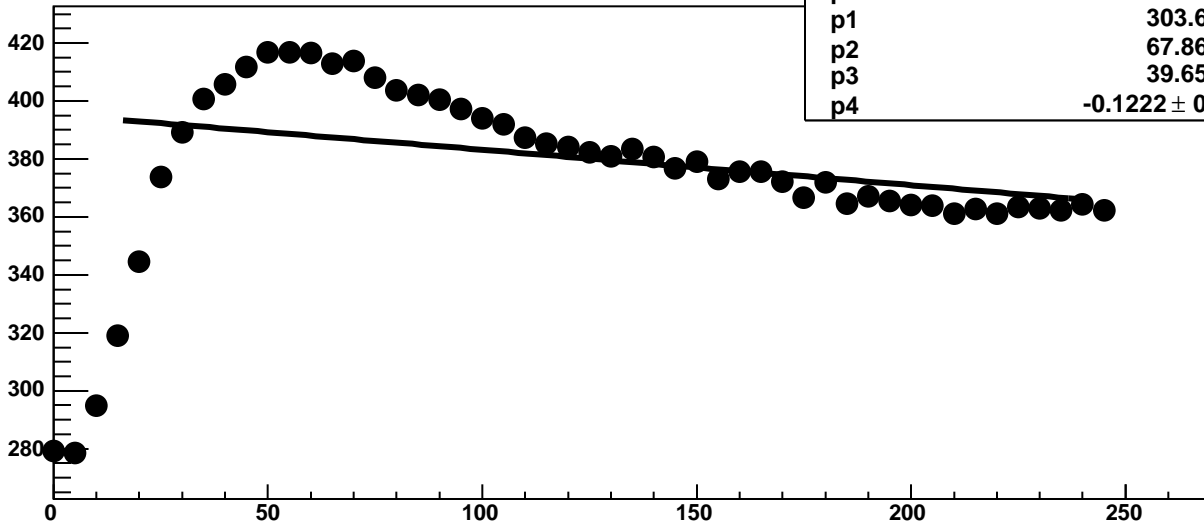


Chip 5, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold



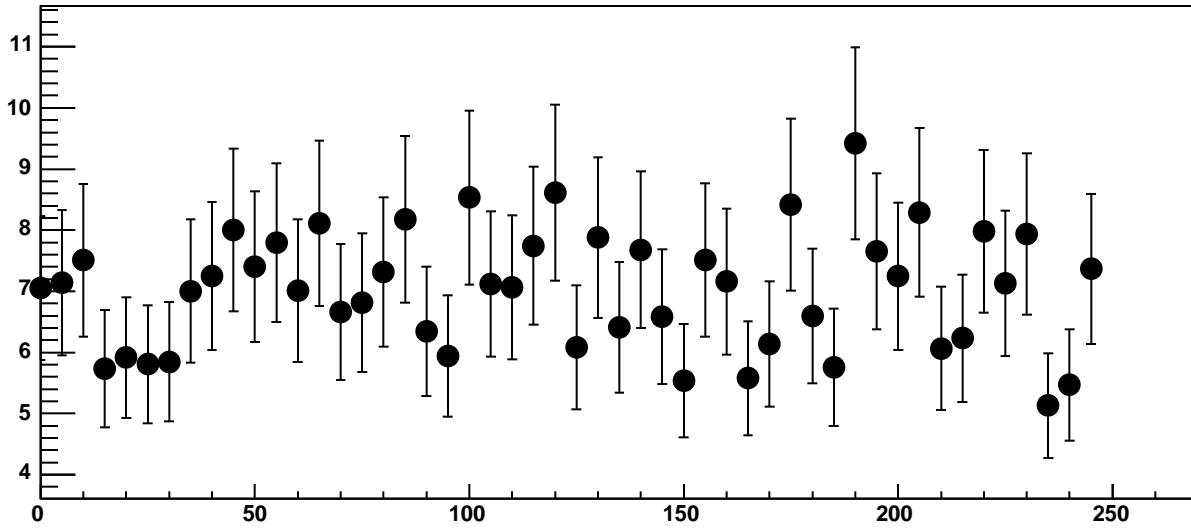


Chip 5, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

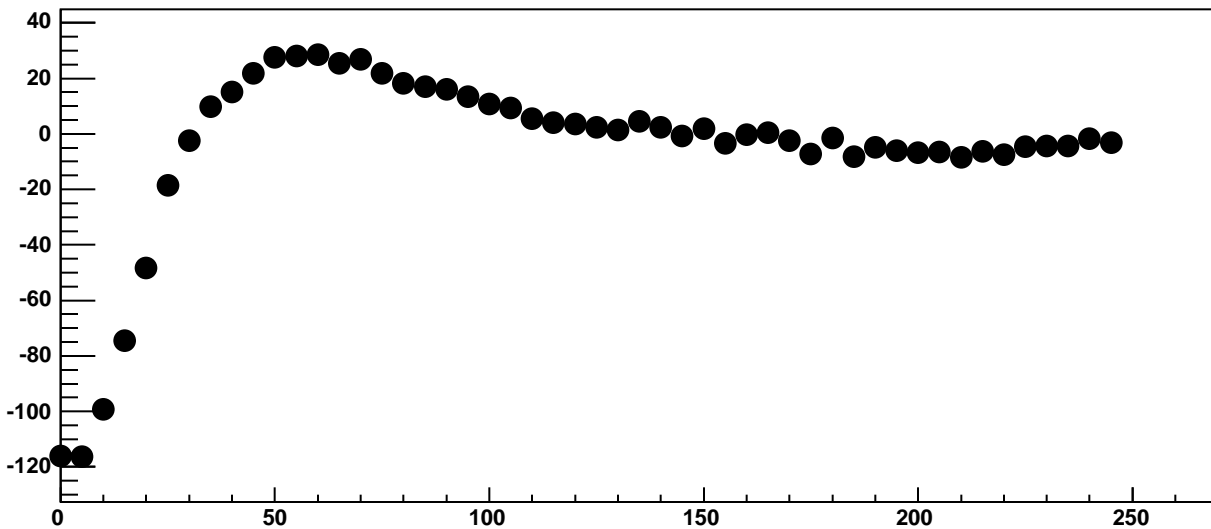


$\chi^2 / \text{ndf}$	7172 / 41
p0	$358.3 \pm 0.2296$
p1	$303.6 \pm 1.879$
p2	$67.86 \pm 84.85$
p3	$39.65 \pm 1.414$
p4	$-0.1222 \pm 0.001219$

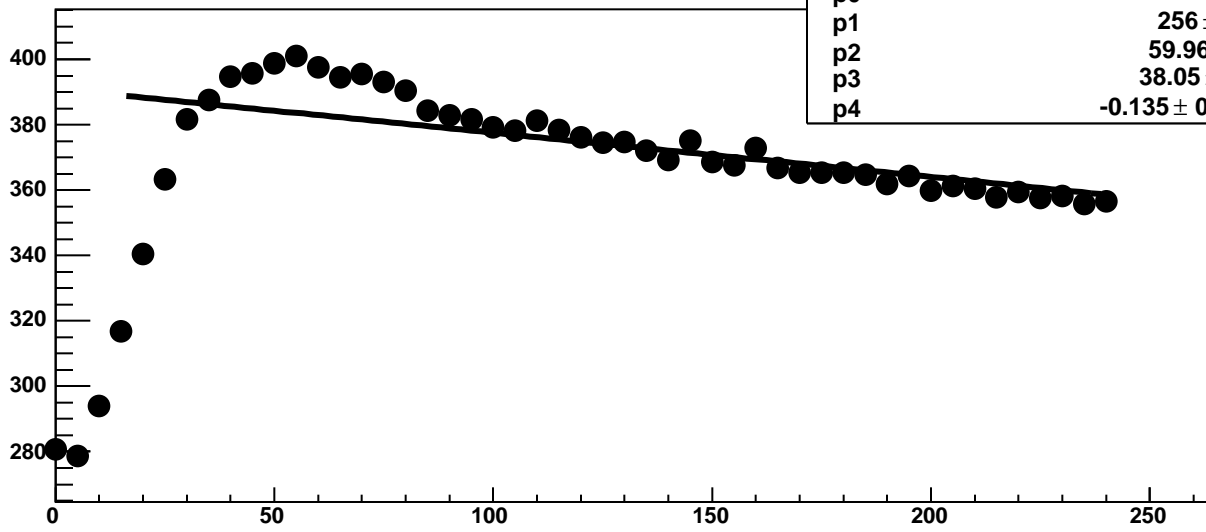
Chip 5, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

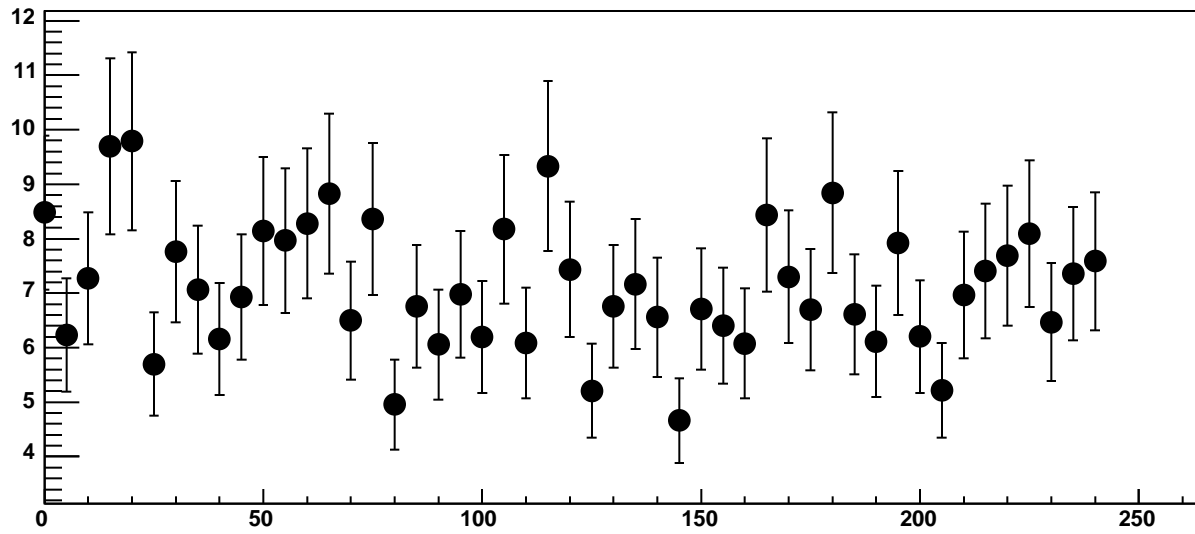


Chip 5, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

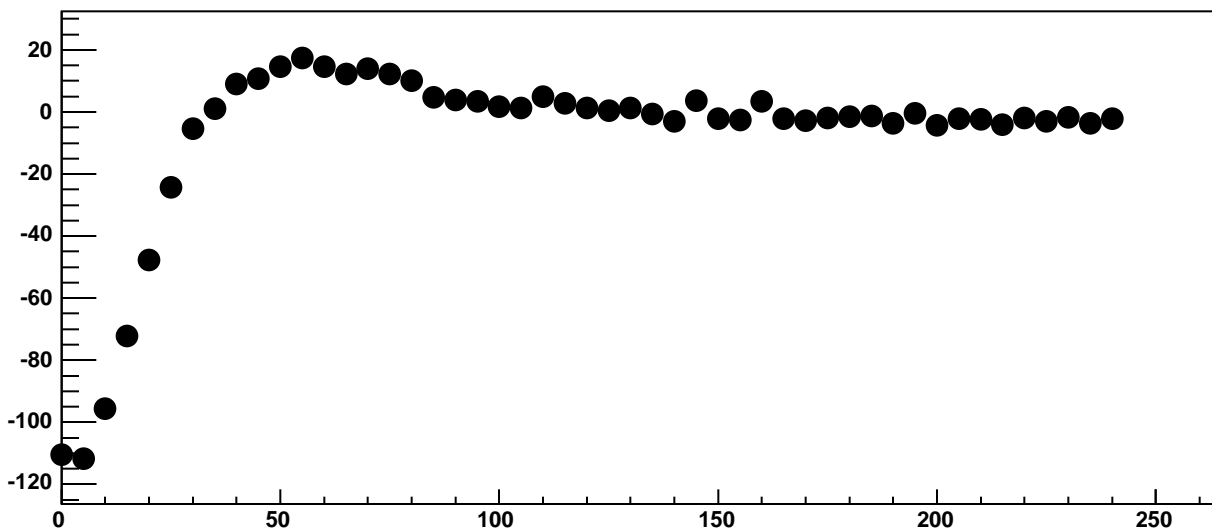


$\chi^2 / \text{ndf}$	2517 / 41
p0	$356.5 \pm 0.4986$
p1	$256 \pm 0.3488$
p2	$59.96 \pm 1.628$
p3	$38.05 \pm 0.1861$
p4	$-0.135 \pm 0.003644$

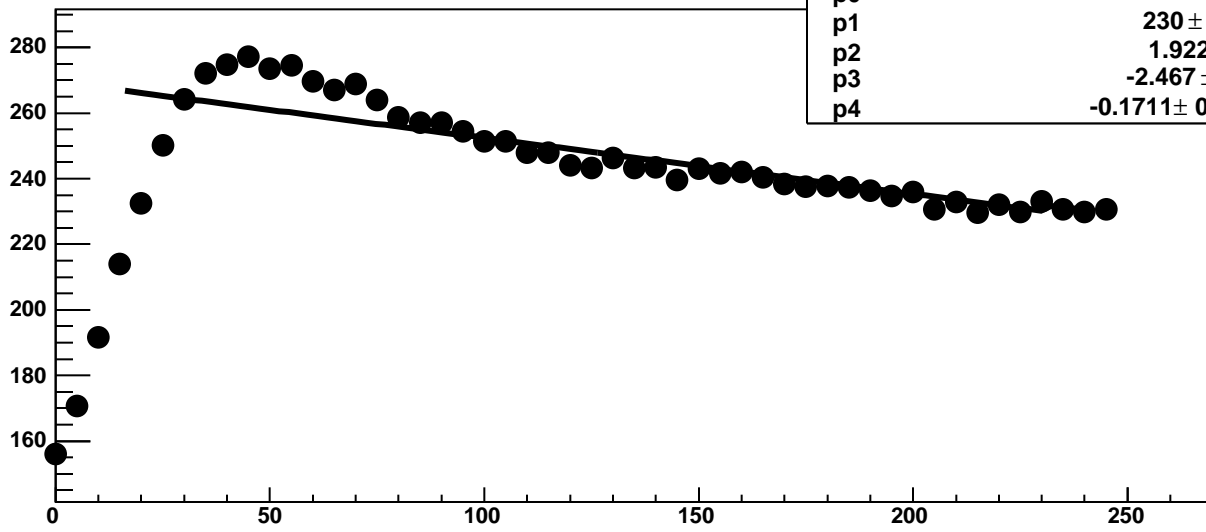
Chip 5, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold

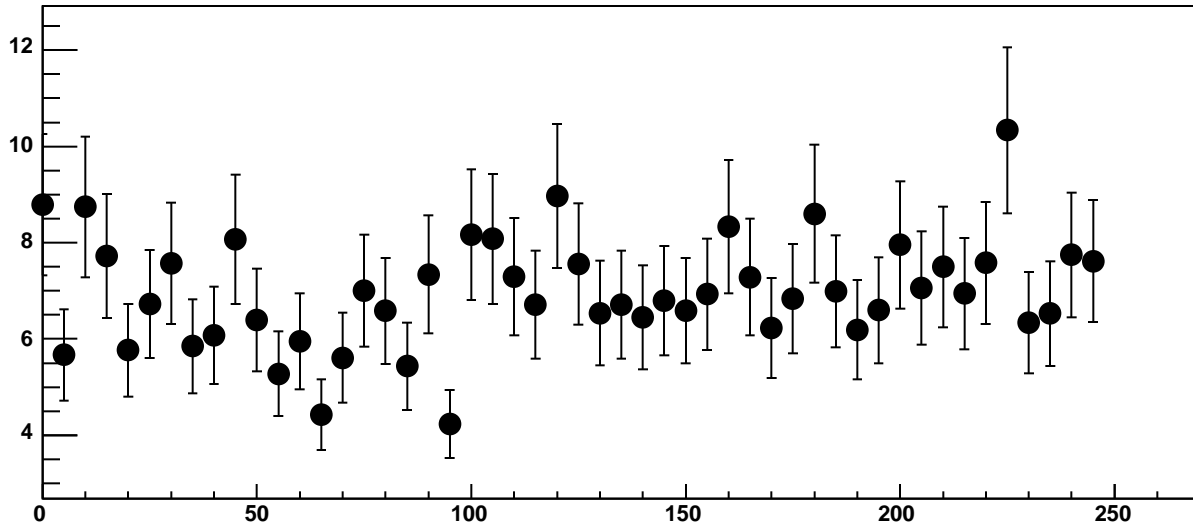


Chip 5, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold

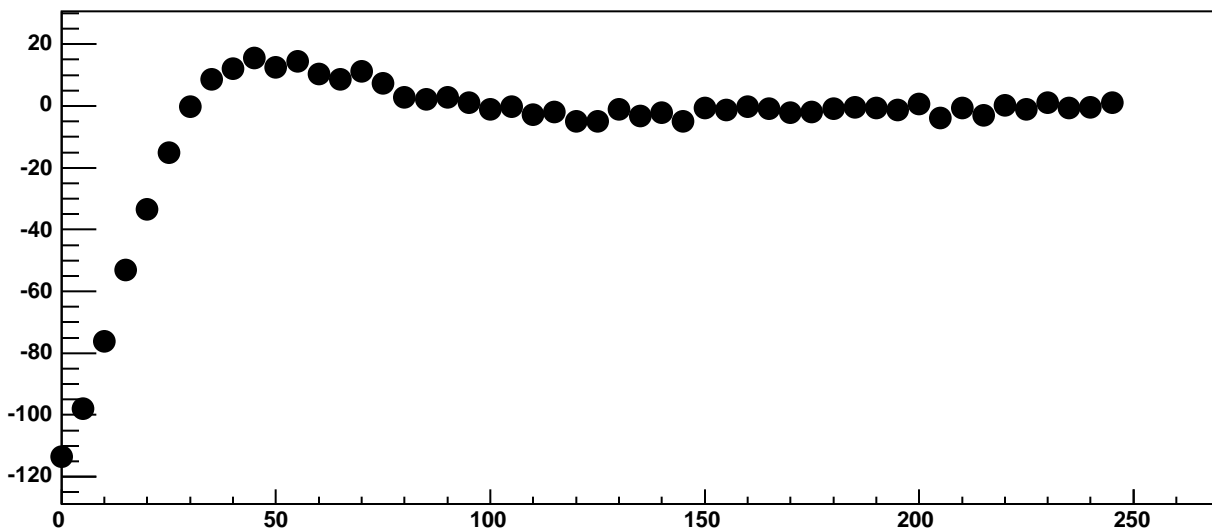


$\chi^2 / \text{ndf}$	2324 / 41
p0	$230.2 \pm 0.7616$
p1	$230 \pm 0.01544$
p2	$1.922 \pm 1.838$
p3	$-2.467 \pm 0.1404$
p4	$-0.1711 \pm 0.005019$

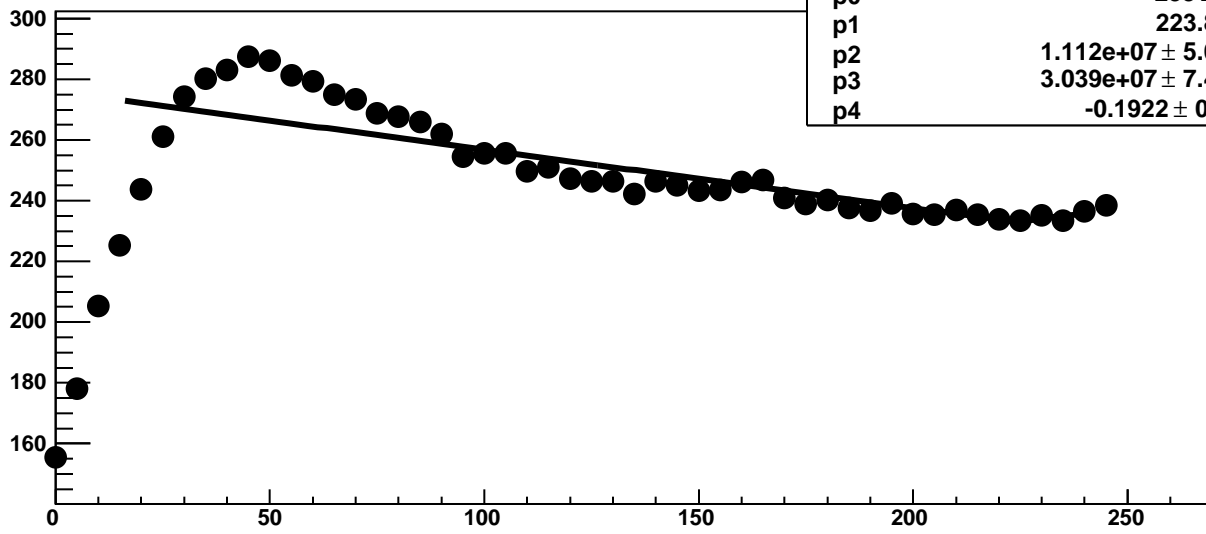
Chip 5, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold

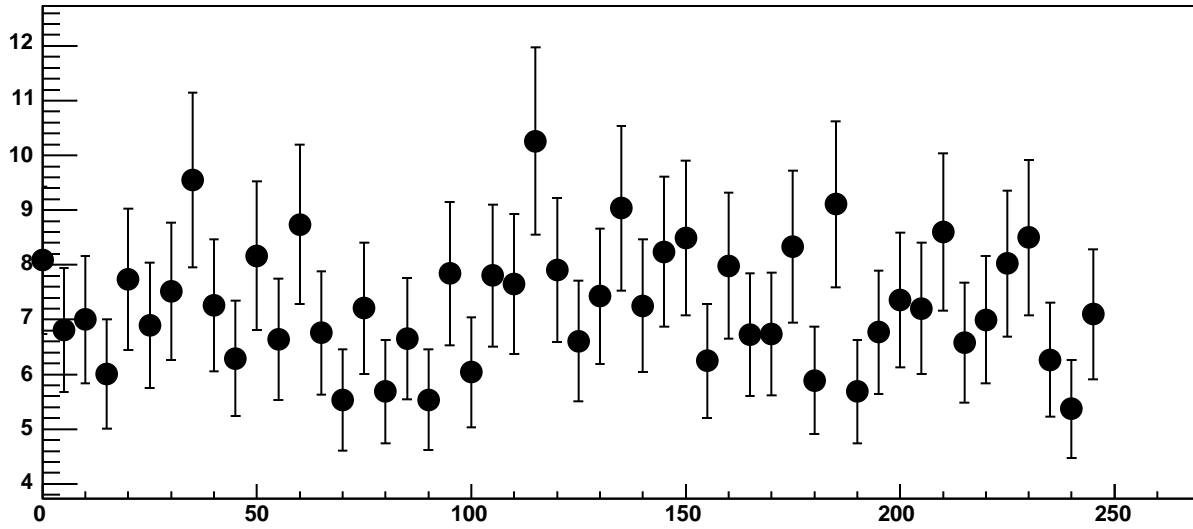


Chip 5, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

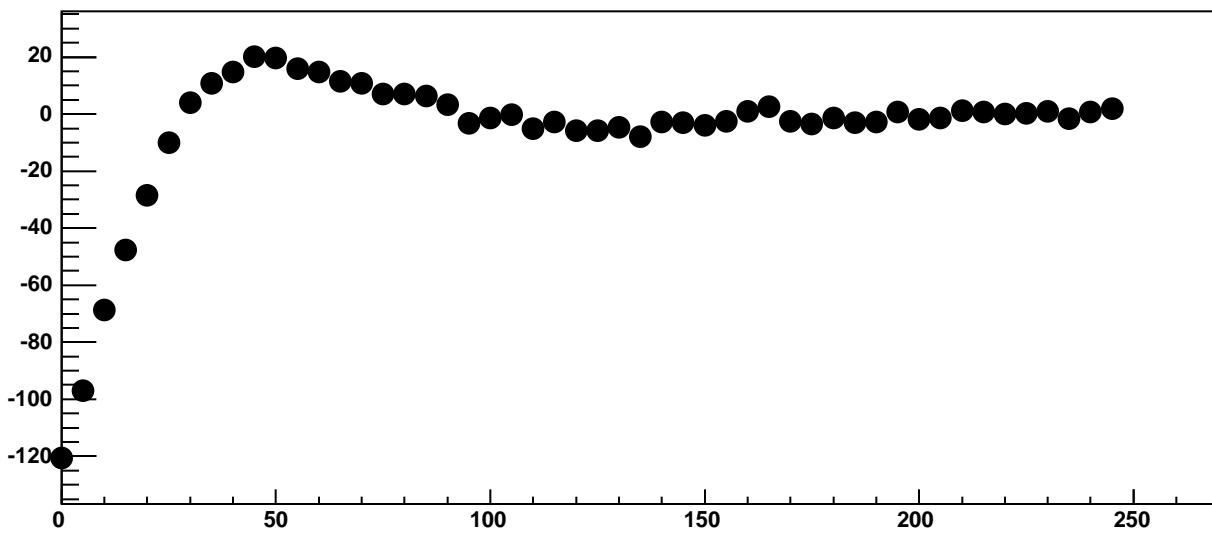


$\chi^2 / \text{ndf}$	2369 / 41
p0	$233 \pm 0.9005$
p1	$223.8 \pm 4.72$
p2	$1.112\text{e}+07 \pm 5.009\text{e}+06$
p3	$3.039\text{e}+07 \pm 7.465\text{e}+06$
p4	$-0.1922 \pm 0.004109$

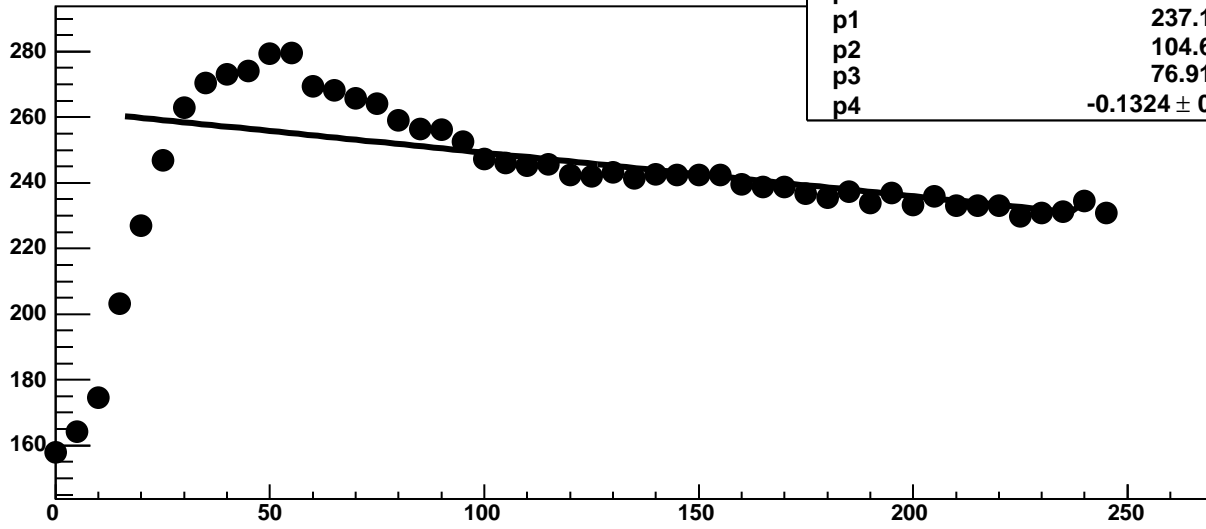
Chip 5, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold

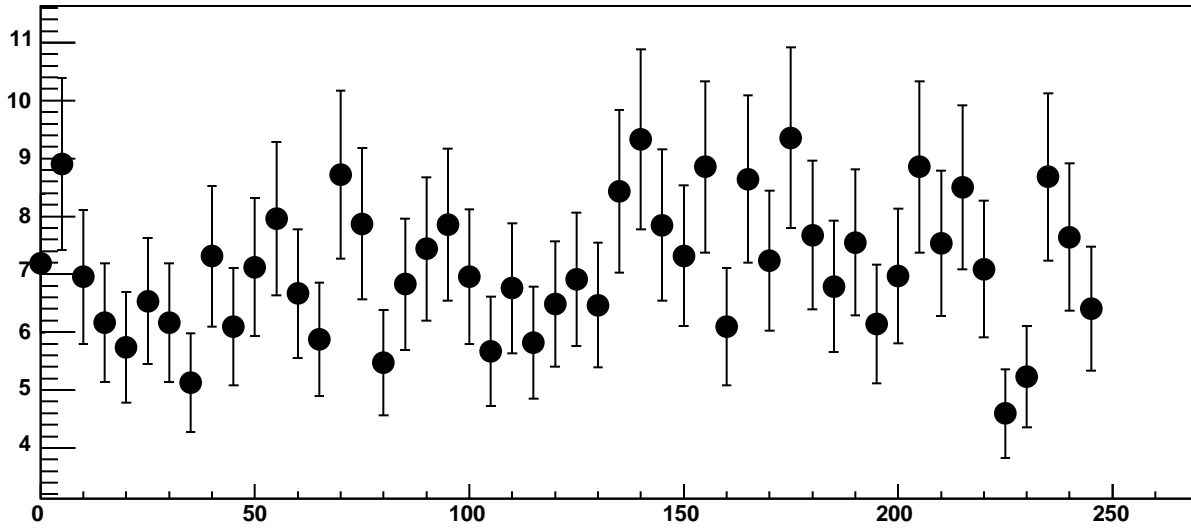


Chip 5, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

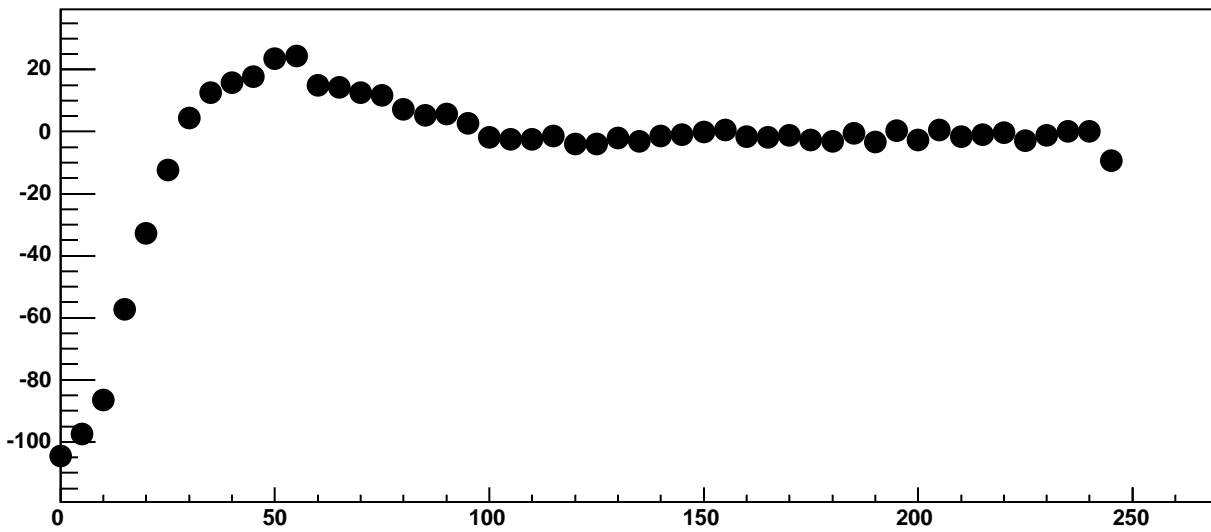


$\chi^2 / \text{ndf}$	3489 / 41
p0	$231 \pm 1.626$
p1	$237.1 \pm 12.27$
p2	$104.6 \pm 651.1$
p3	$76.91 \pm 217.5$
p4	$-0.1324 \pm 0.003342$

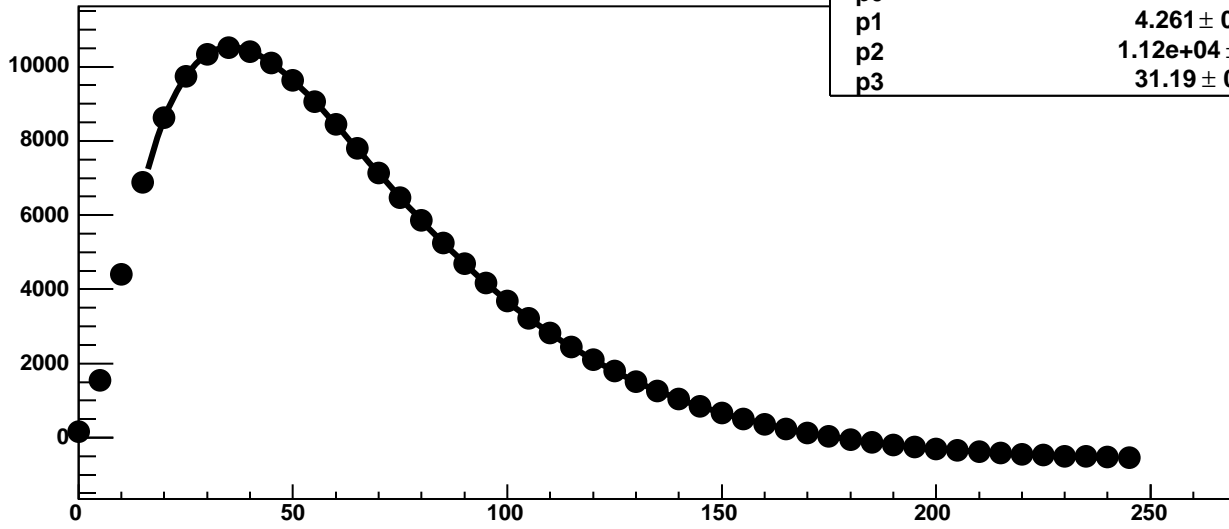
Chip 5, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

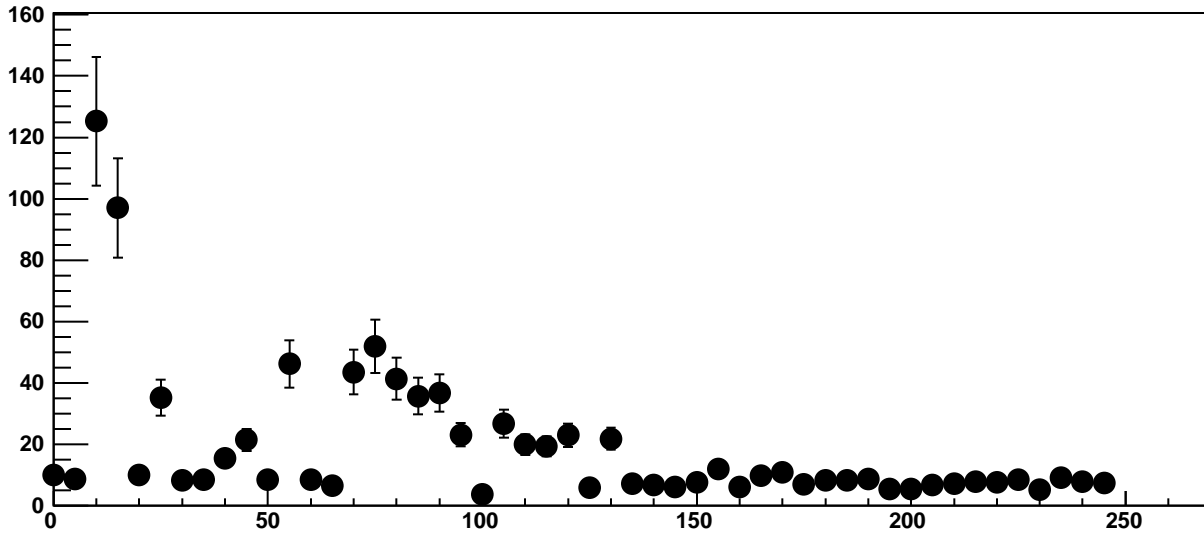


Chip 5, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold

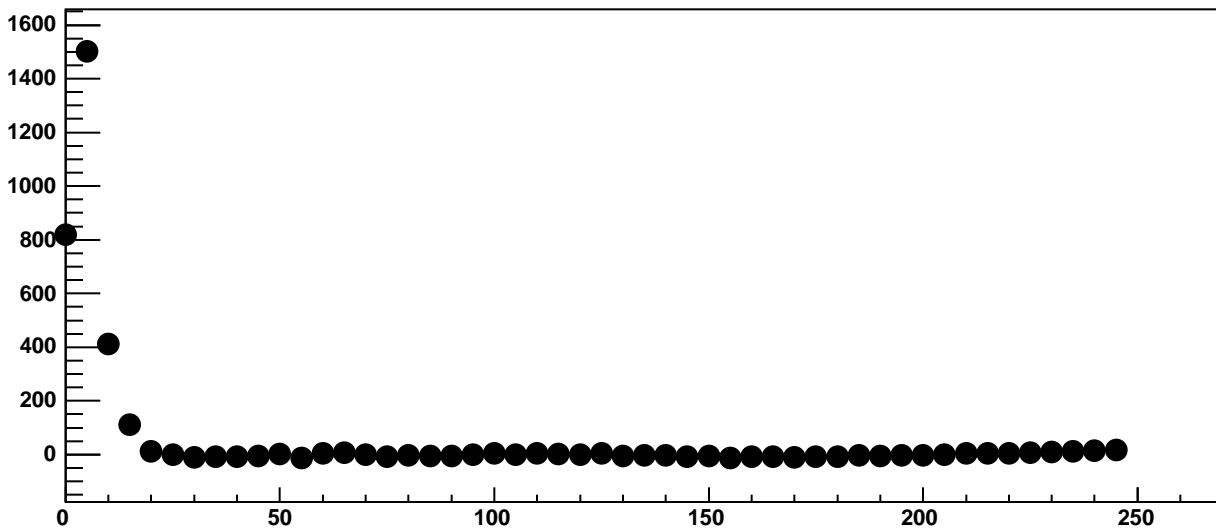


$\chi^2 / \text{ndf}$	550.6 / 42
p0	-663.5 ± 0.5662
p1	4.261 ± 0.009115
p2	1.12e+04 ± 0.9729
p3	31.19 ± 0.005461

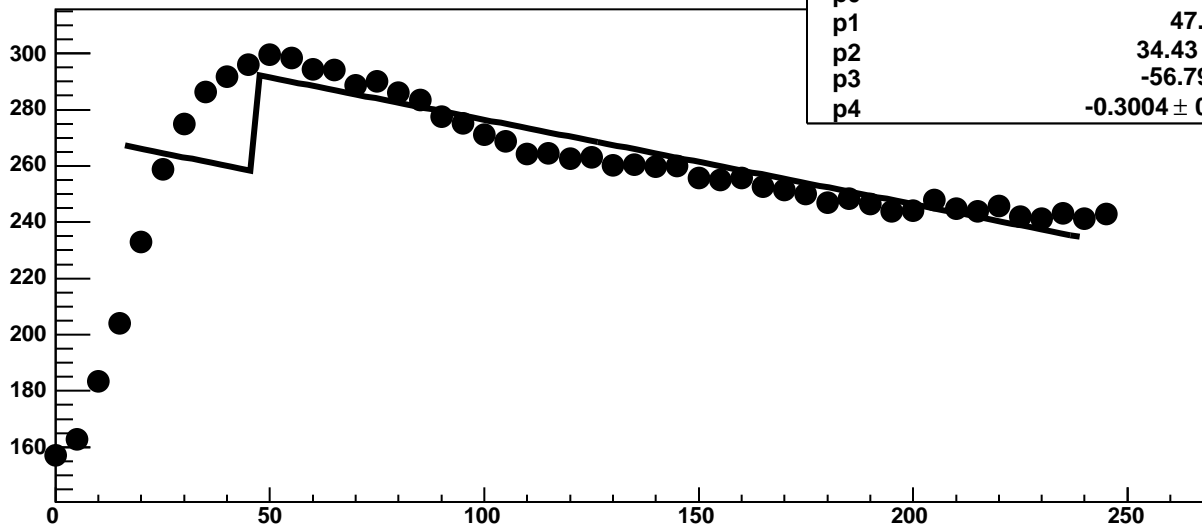
Chip 5, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold



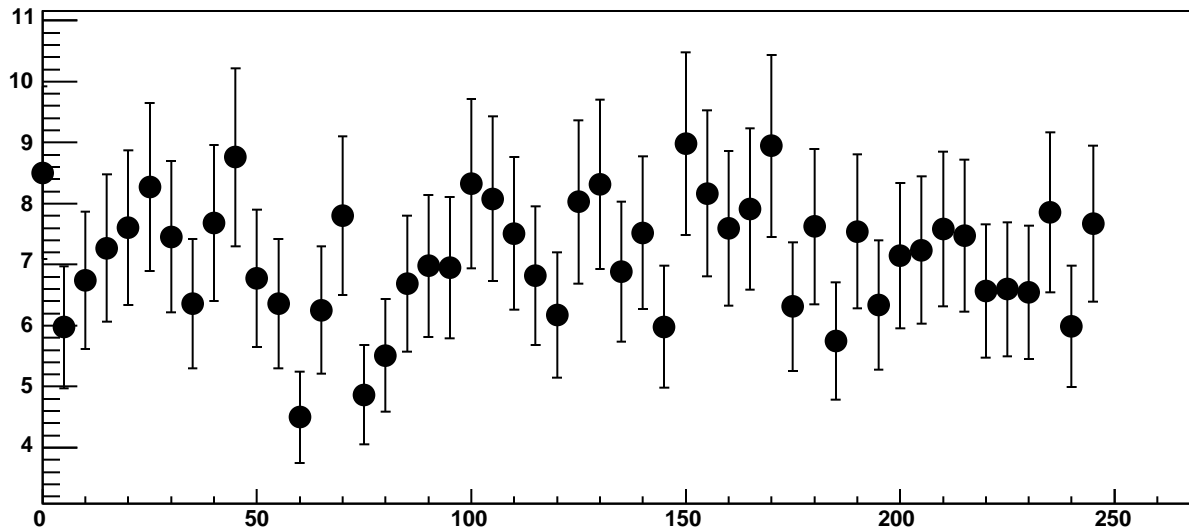
Chip 5, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold



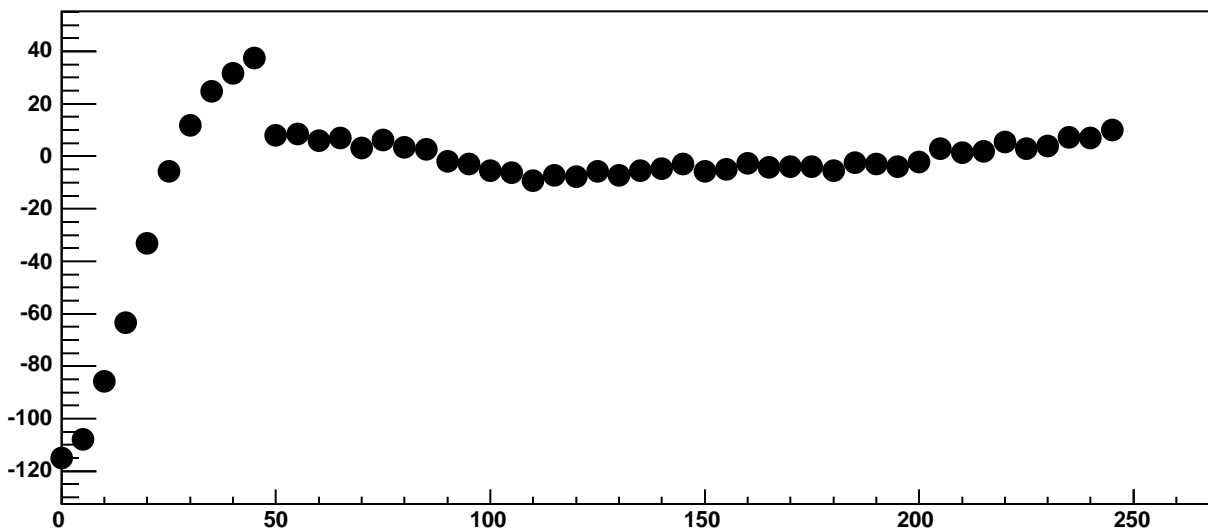
Chip 5, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold



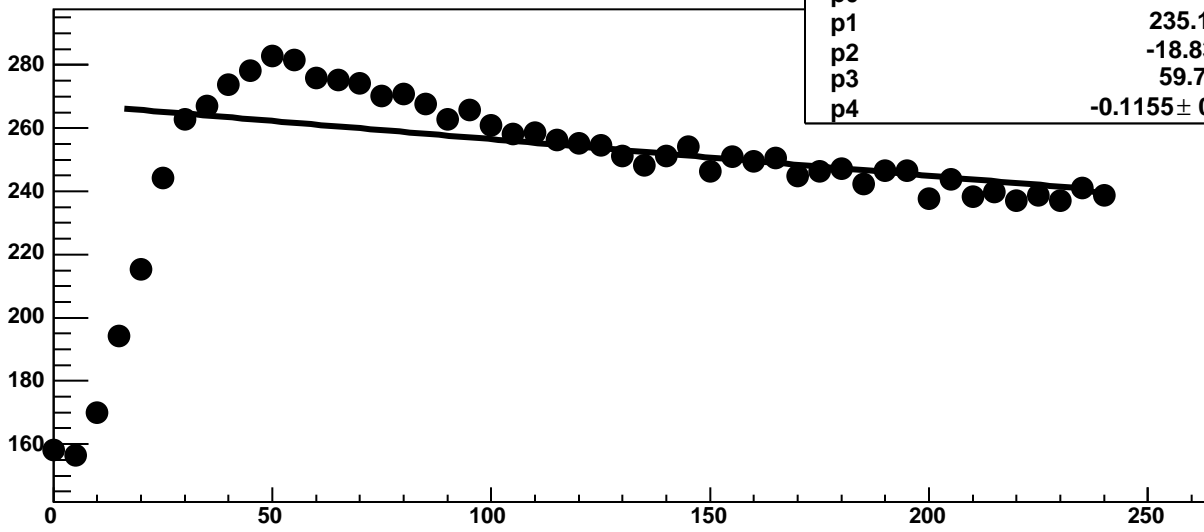
Chip 5, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

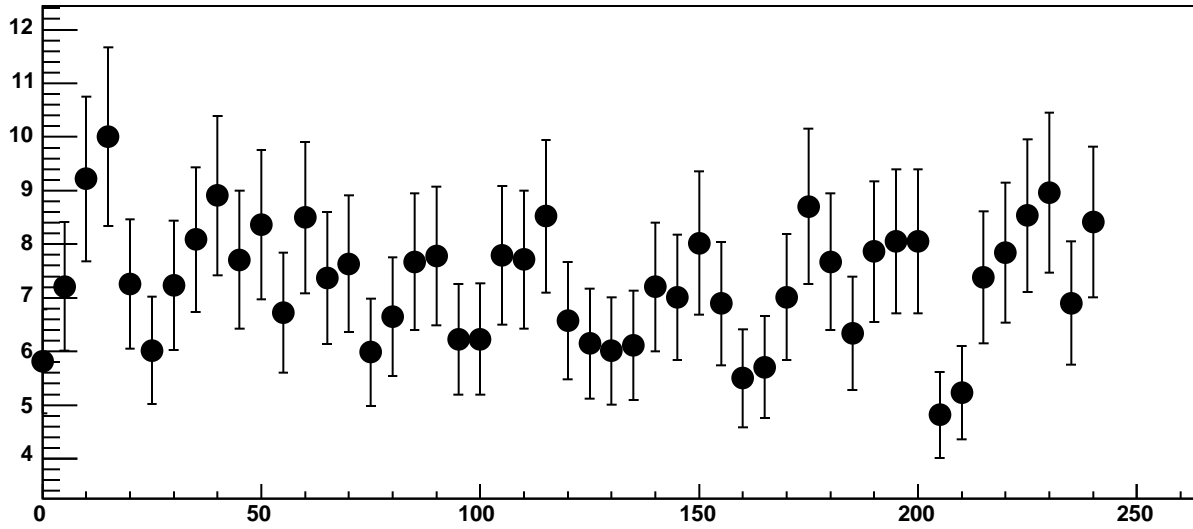


Chip 5, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

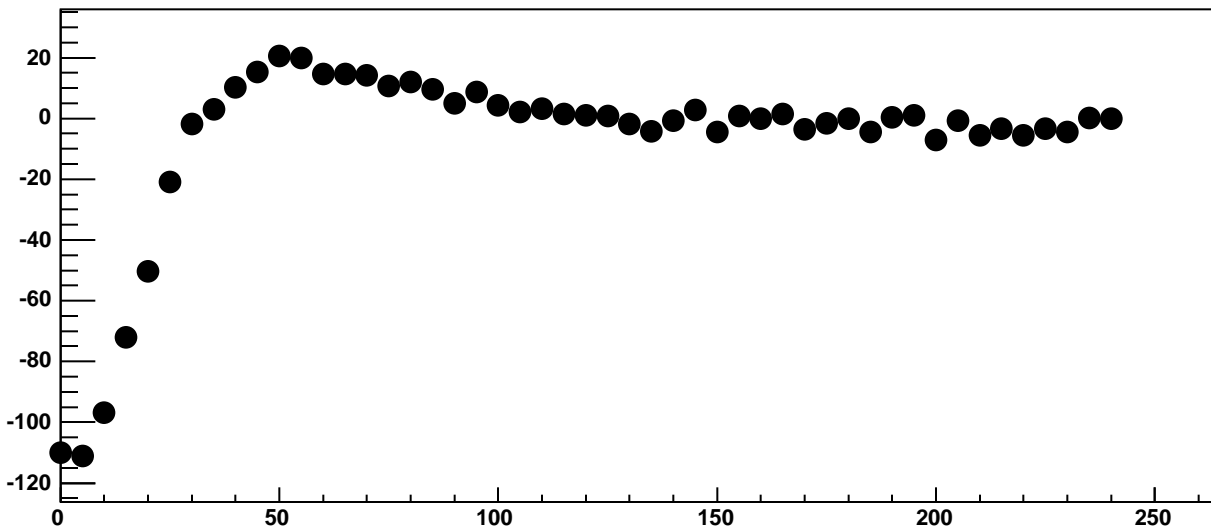


$\chi^2 / \text{ndf}$	3019 / 41
p0	$240.9 \pm 2.93$
p1	$235.1 \pm 24.93$
p2	$-18.83 \pm 81.2$
p3	$59.79 \pm 211$
p4	$-0.1155 \pm 0.003821$

Chip 5, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold

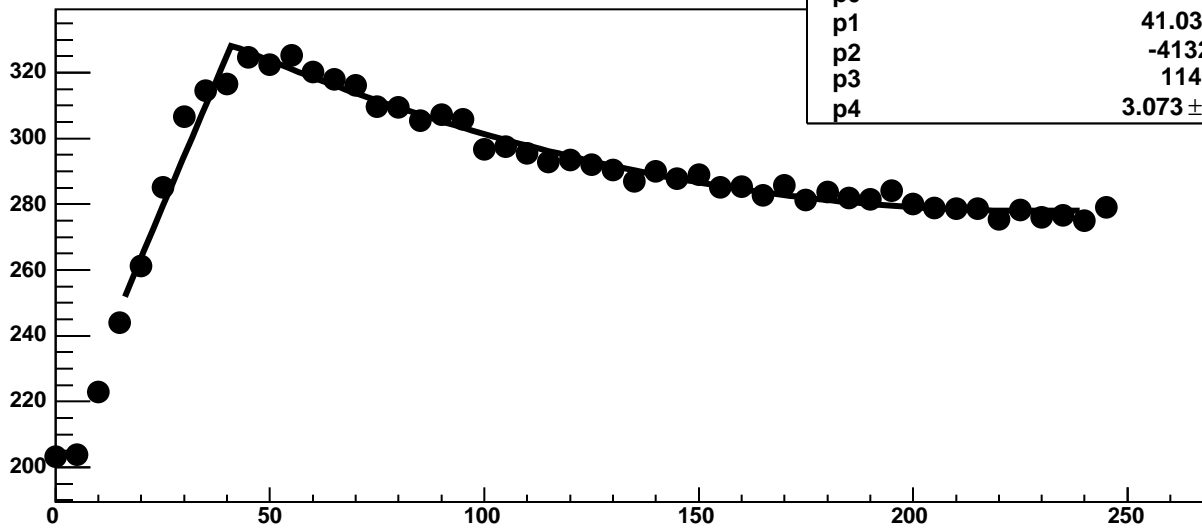


Chip 5, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold



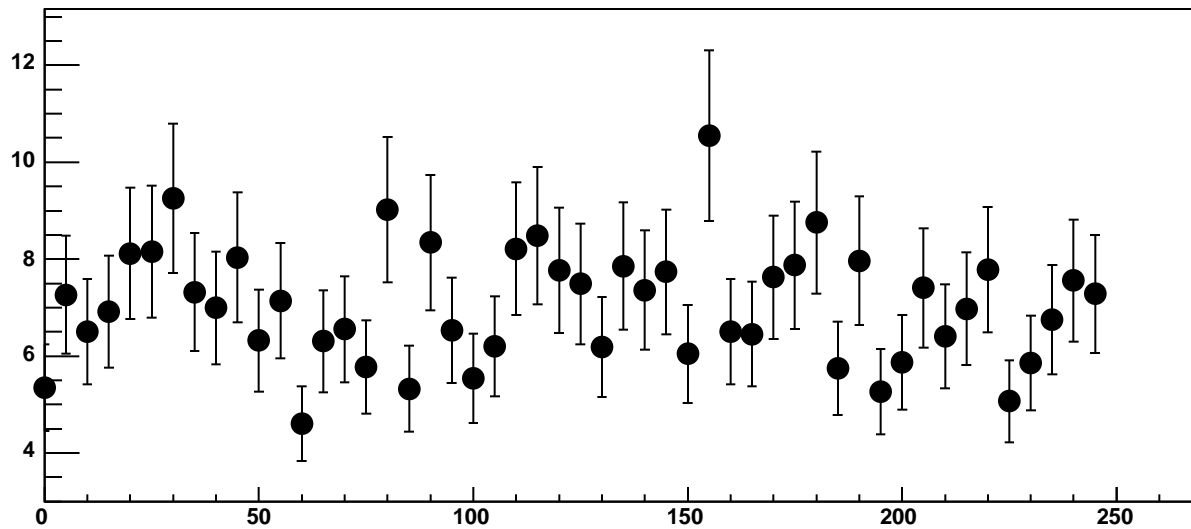


Chip 5, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold

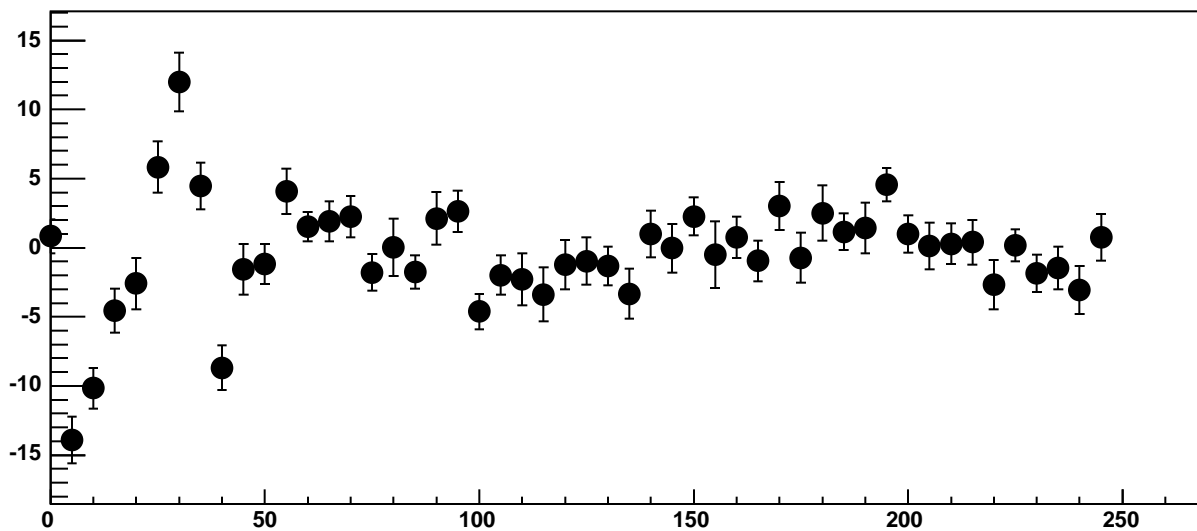


$\chi^2 / \text{ndf}$	167.6 / 41
p0	$328.5 \pm 0.6629$
p1	$41.03 \pm 0.4111$
p2	$-4132 \pm 292.3$
p3	$1140 \pm 71.41$
p4	$3.073 \pm 0.08057$

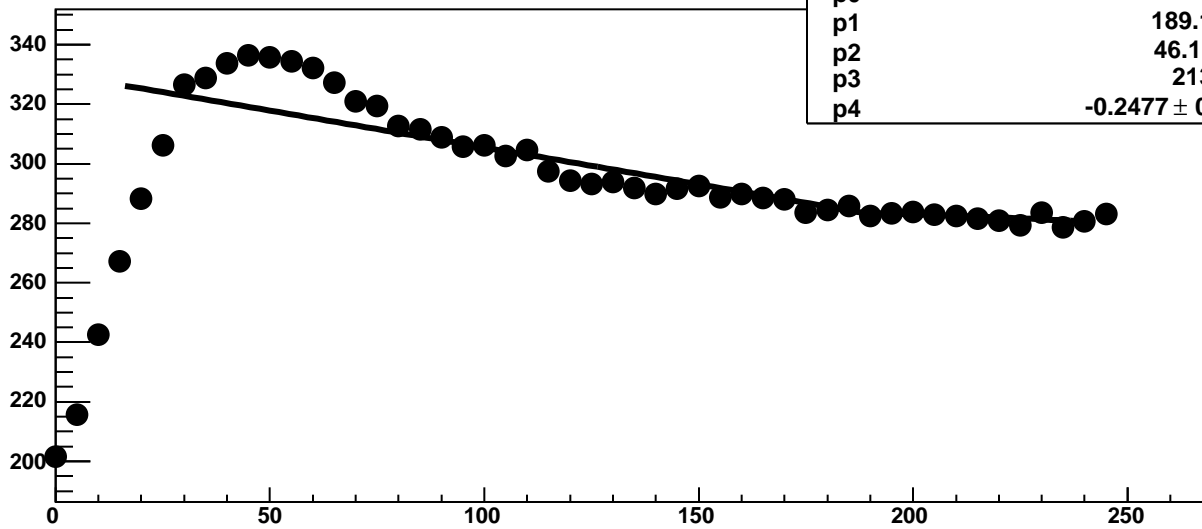
Chip 5, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

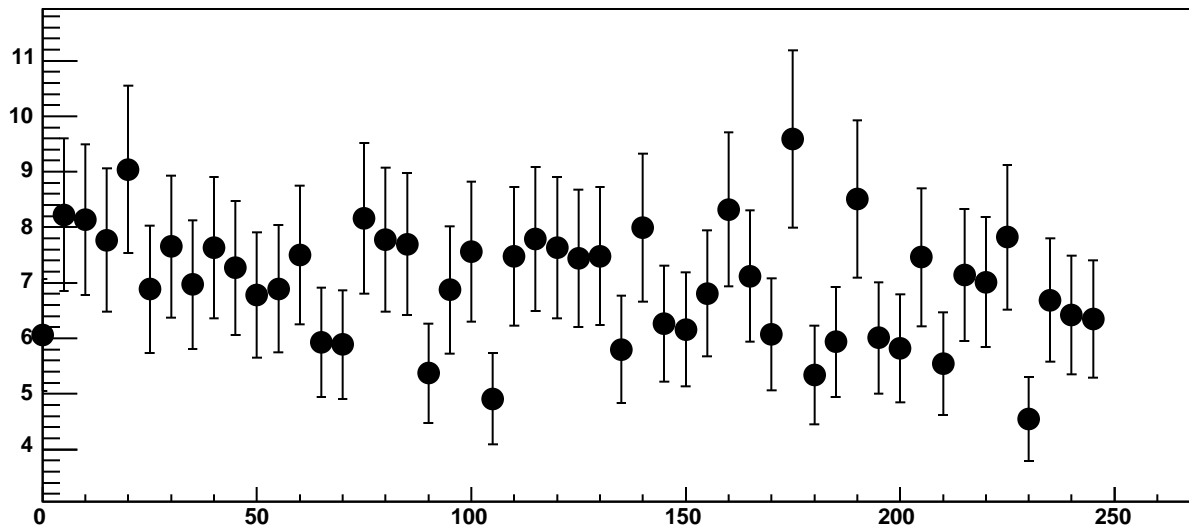


Chip 5, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold

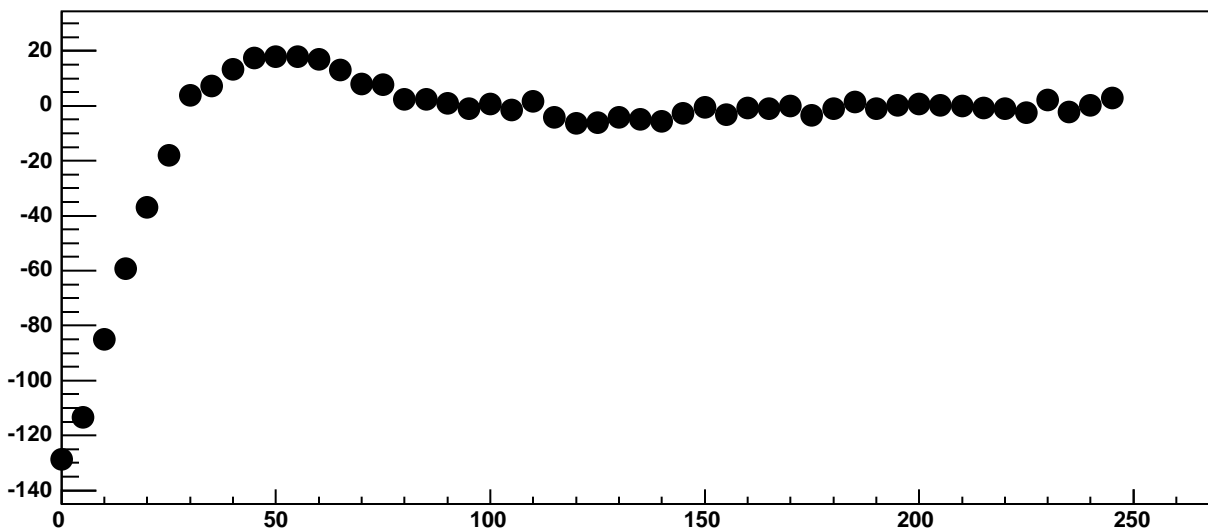


$\chi^2 / \text{ndf}$	2340 / 41
p0	$283.4 \pm 1.513$
p1	$189.1 \pm 6.704$
p2	$46.11 \pm 224.8$
p3	$213 \pm 1169$
p4	$-0.2477 \pm 0.005049$

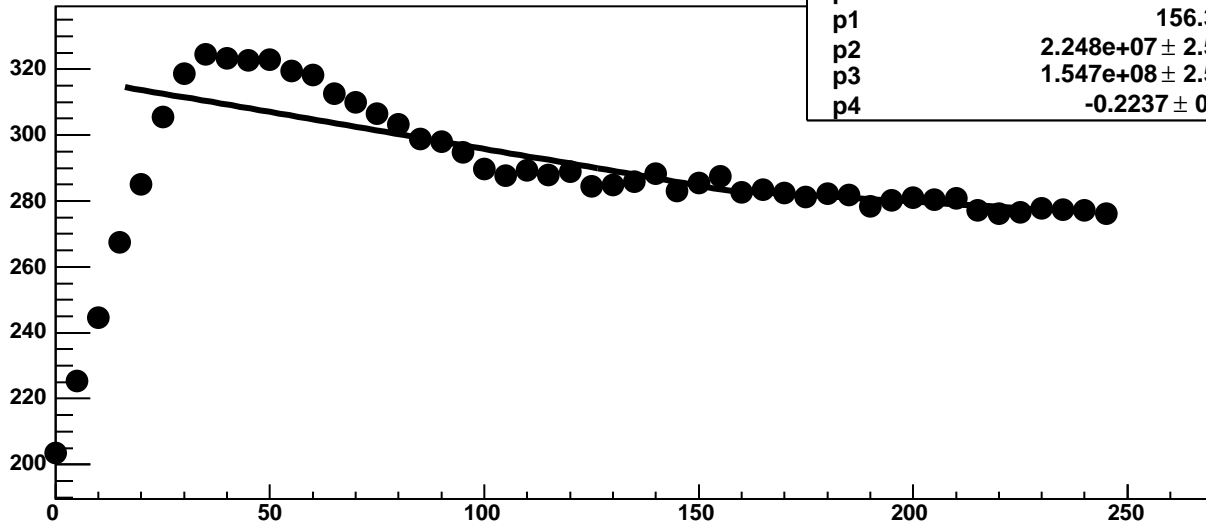
Chip 5, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold

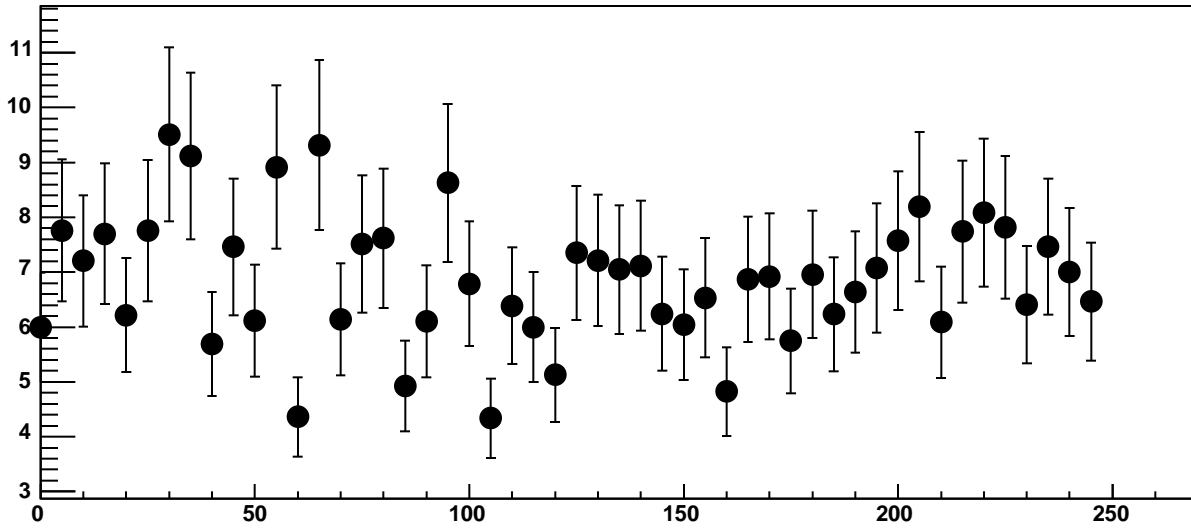


Chip 5, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

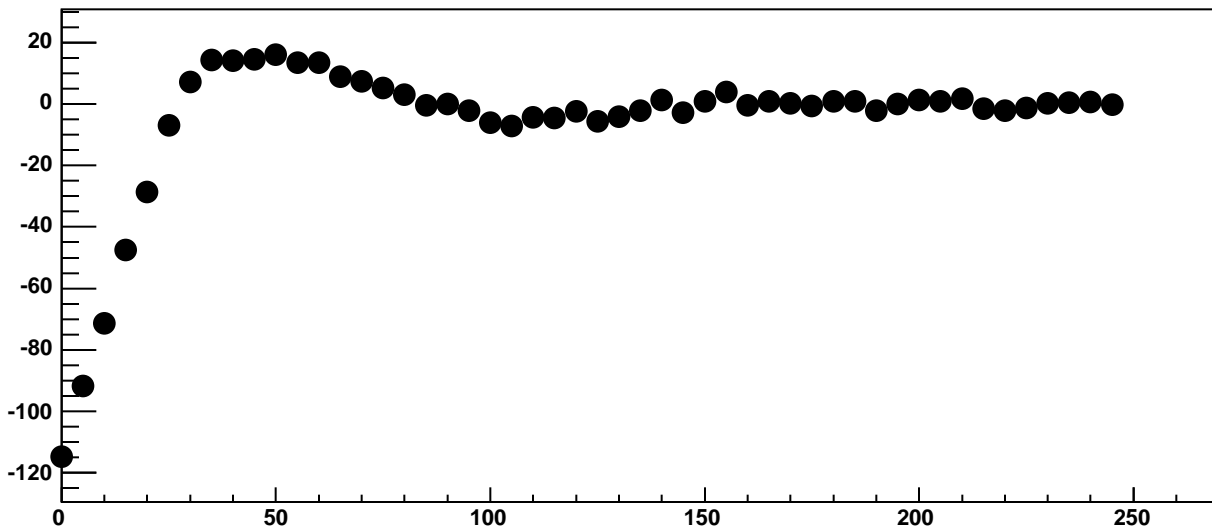


$\chi^2 / \text{ndf}$	1930 / 41
p0	$283.2 \pm 1.111$
p1	$156.3 \pm 6.08$
p2	$2.248\text{e}+07 \pm 2.547\text{e}+06$
p3	$1.547\text{e}+08 \pm 2.569\text{e}+06$
p4	$-0.2237 \pm 0.006907$

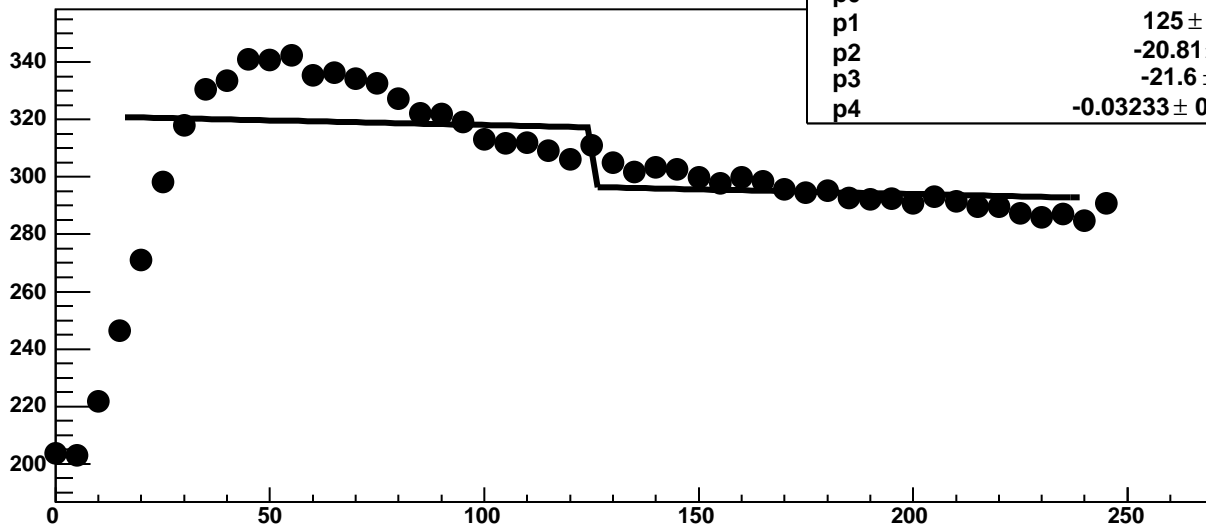
Chip 5, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

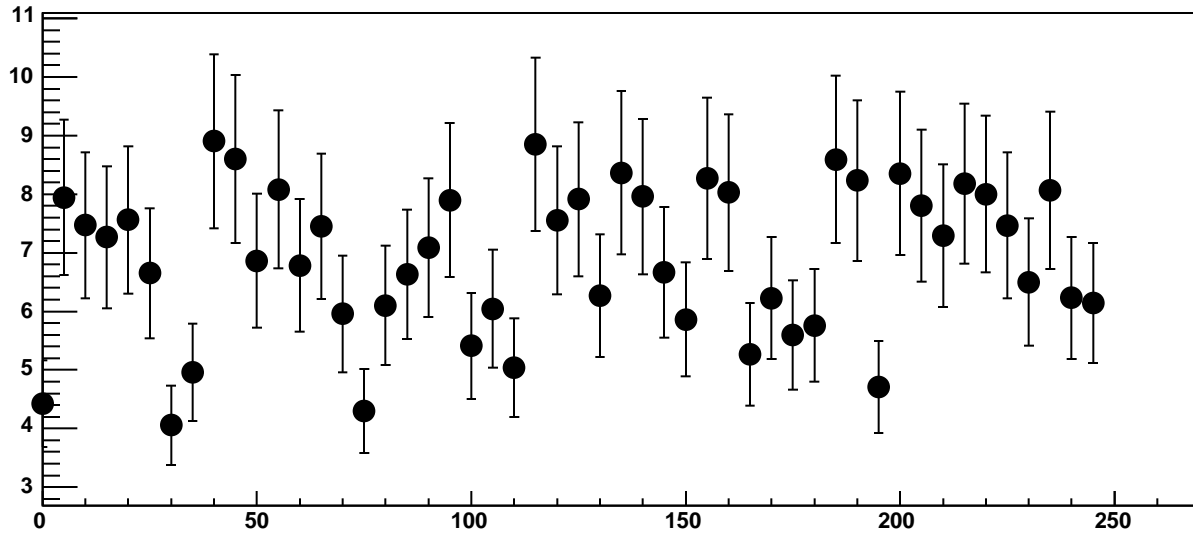


Chip 5, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

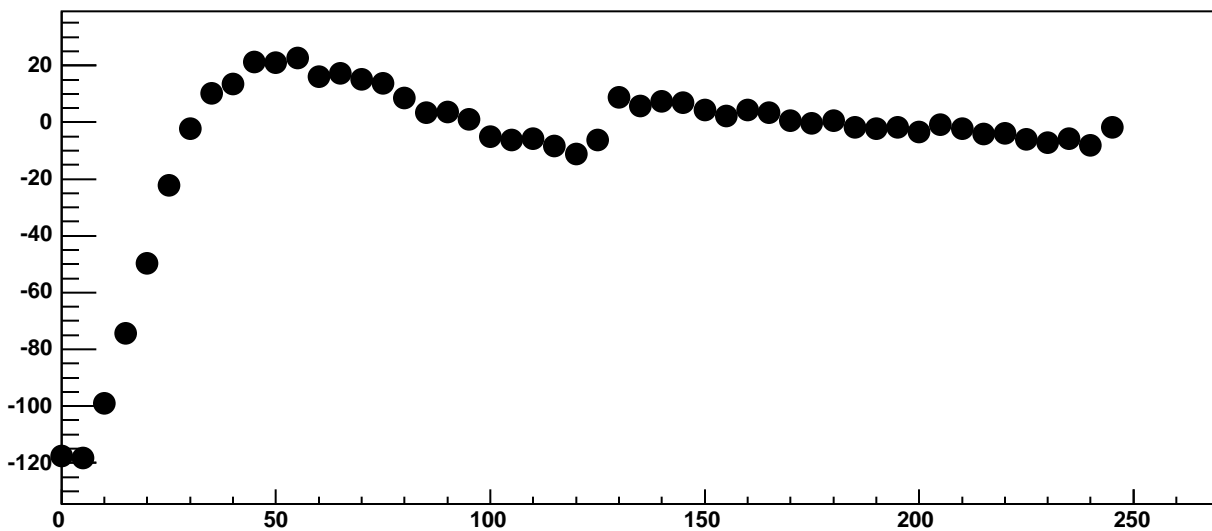


$\chi^2 / \text{ndf}$	4504 / 41
p0	$317.3 \pm 0.5555$
p1	$125 \pm 0.02896$
p2	$-20.81 \pm 0.9881$
p3	$-21.6 \pm 0.9057$
p4	$-0.03233 \pm 0.006937$

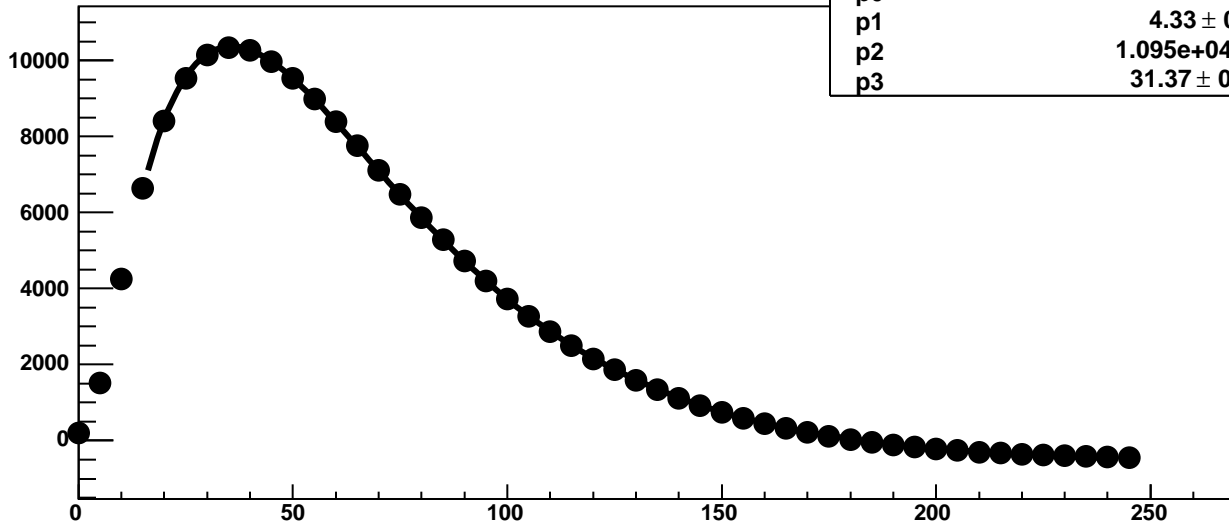
Chip 5, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

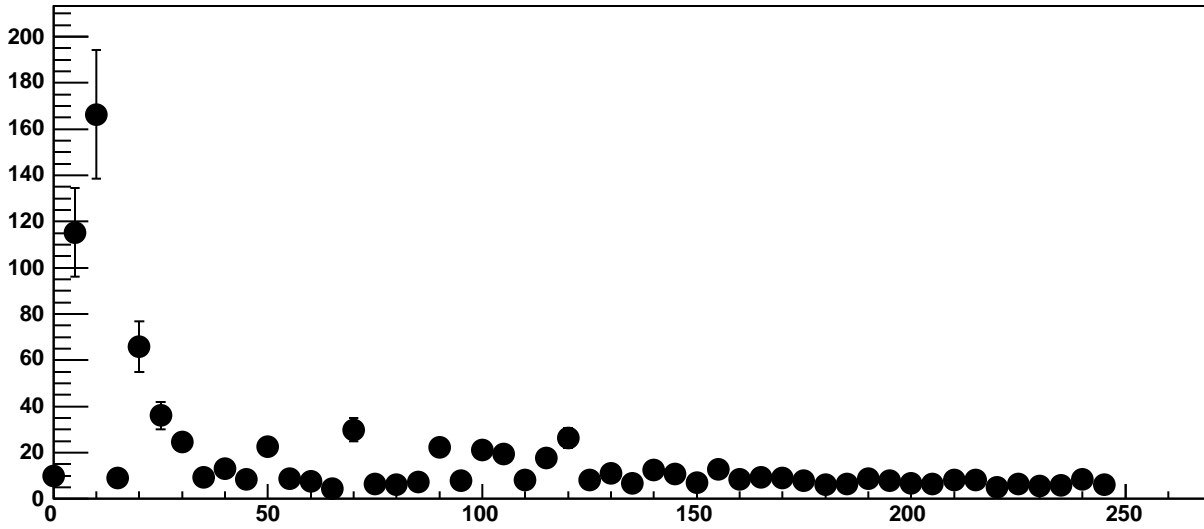


Chip 5, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold

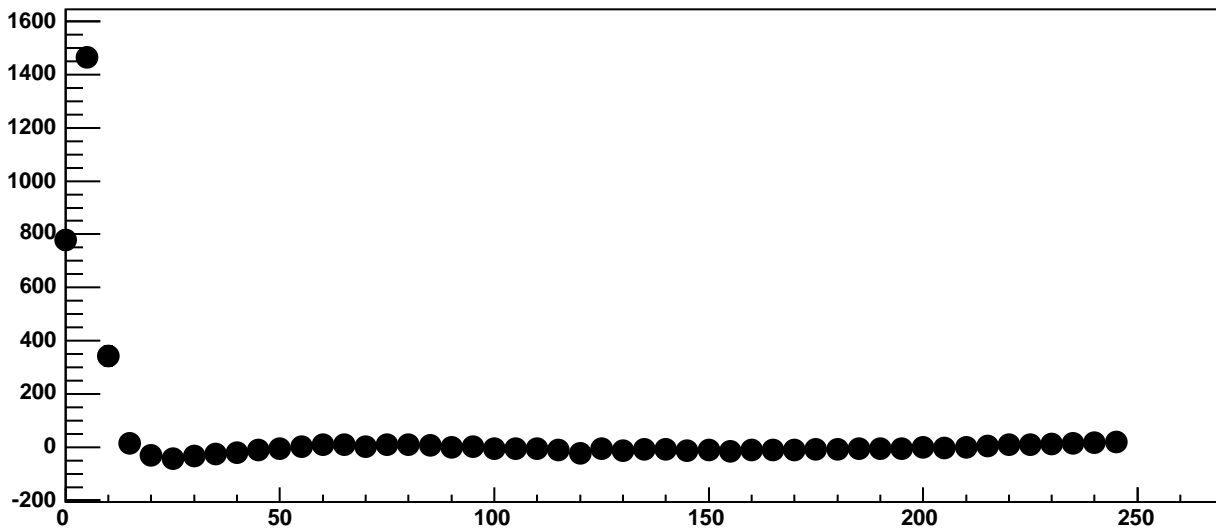


$\chi^2 / \text{ndf}$	1272 / 42
p0	$-575.2 \pm 0.5216$
p1	$4.33 \pm 0.005811$
p2	$1.095\text{e}+04 \pm 0.985$
p3	$31.37 \pm 0.004887$

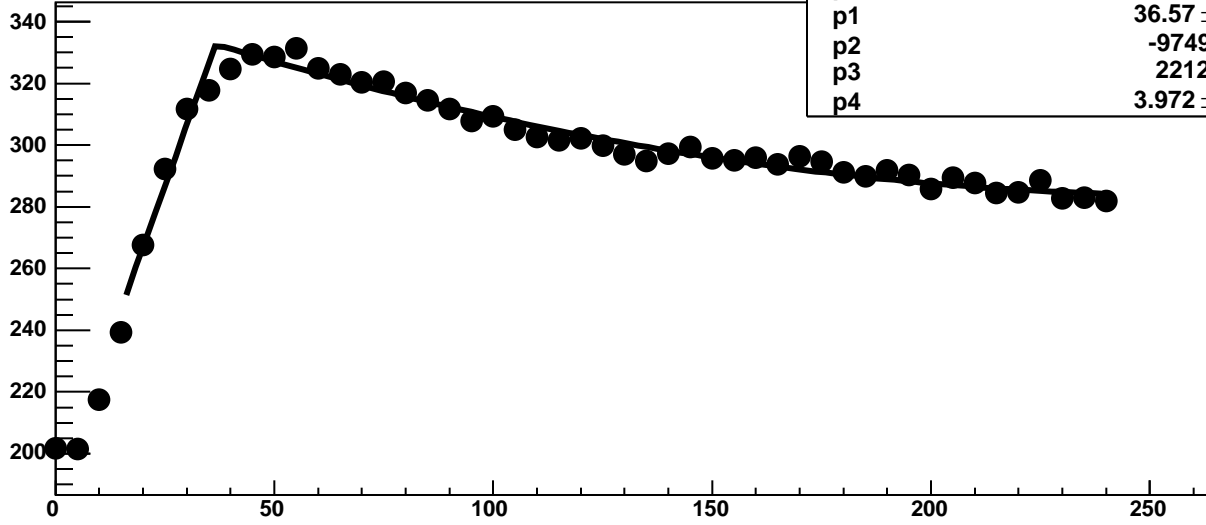
Chip 5, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold



Chip 5, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold



$\chi^2 / \text{ndf}$

166 / 41

p0

$332.8 \pm 0.7005$

p1

$36.57 \pm 0.4006$

p2

$-9749 \pm 909.4$

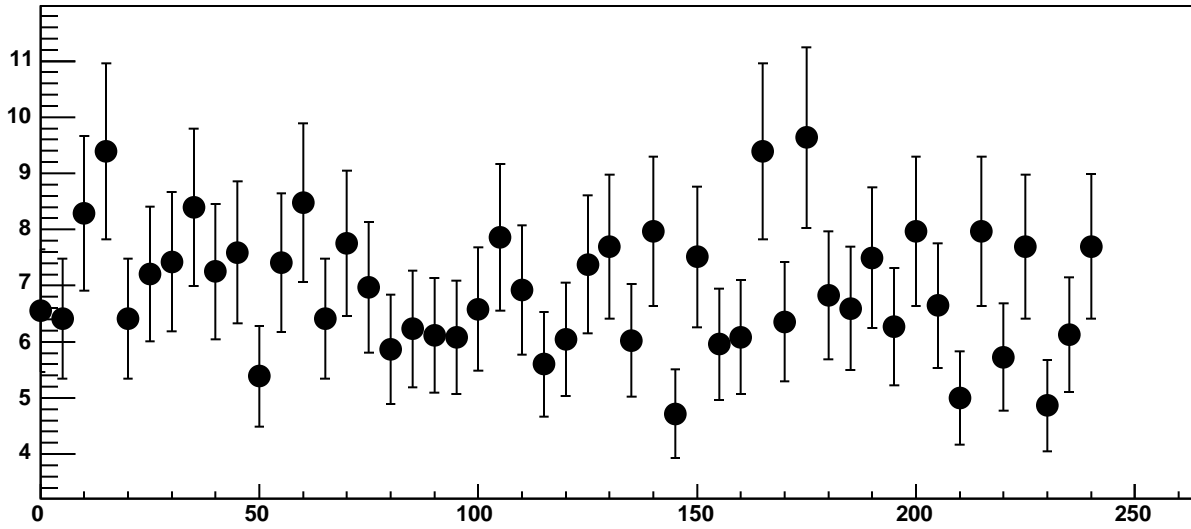
p3

$2212 \pm 185.4$

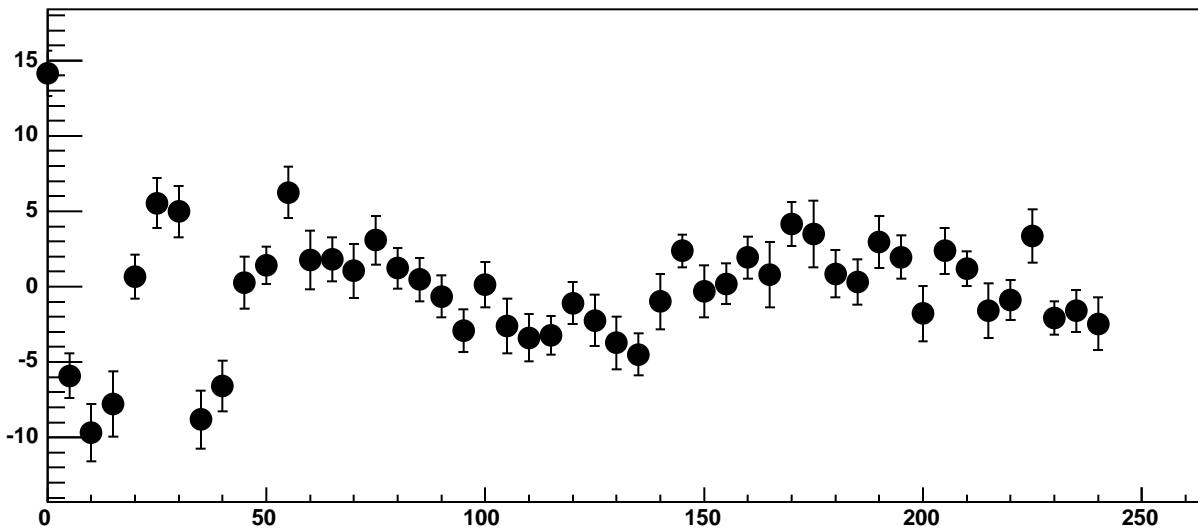
p4

$3.972 \pm 0.1205$

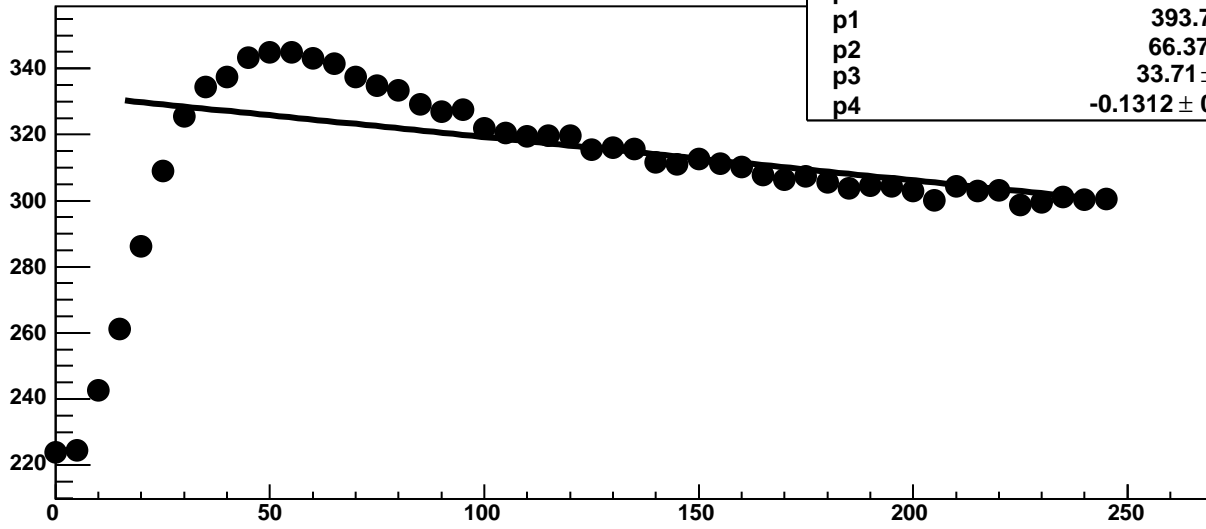
Chip 5, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold

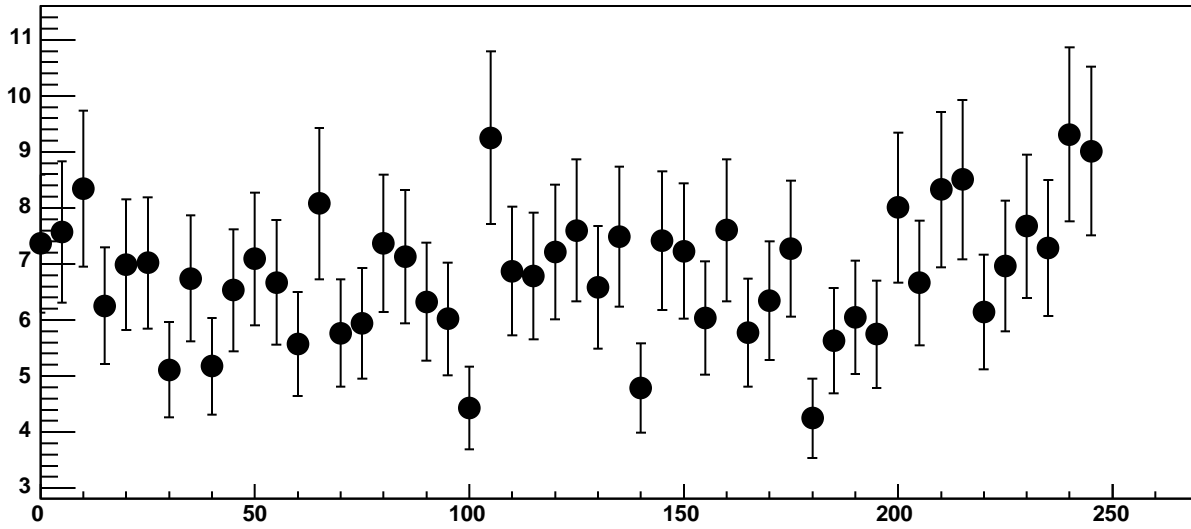


Chip 5, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

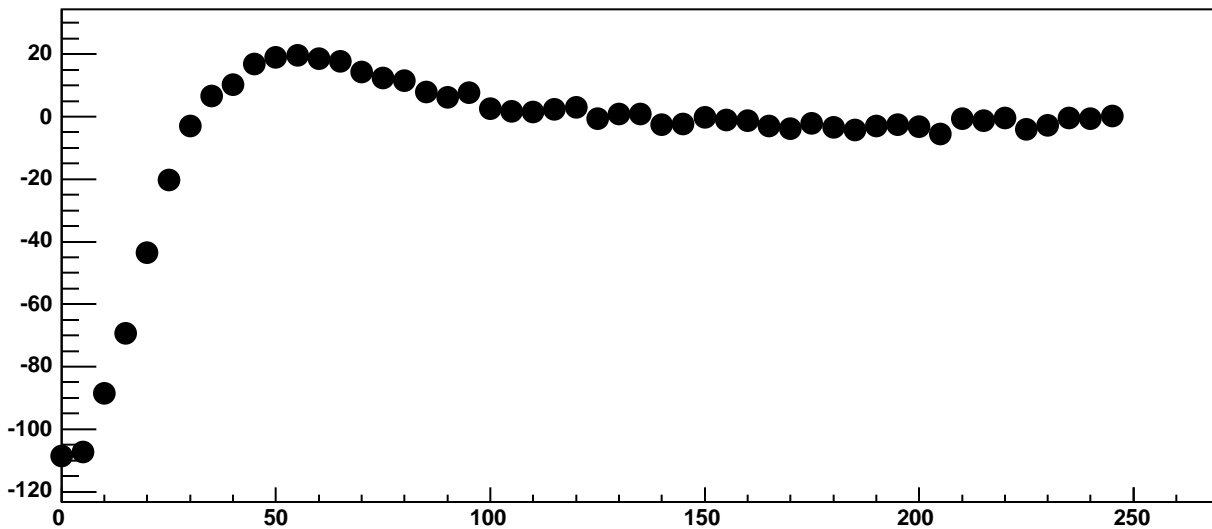


$\chi^2 / \text{ndf}$	4463 / 41
p0	$280.7 \pm 1.044$
p1	$393.7 \pm 1.381$
p2	$66.37 \pm 4.435$
p3	$33.71 \pm 0.2934$
p4	$-0.1312 \pm 0.00361$

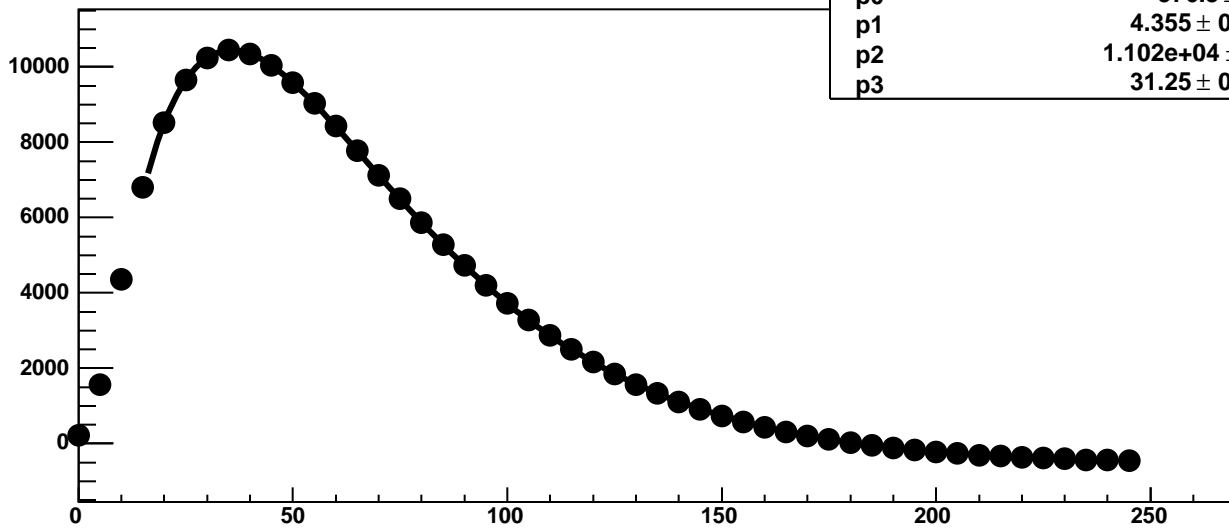
Chip 5, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

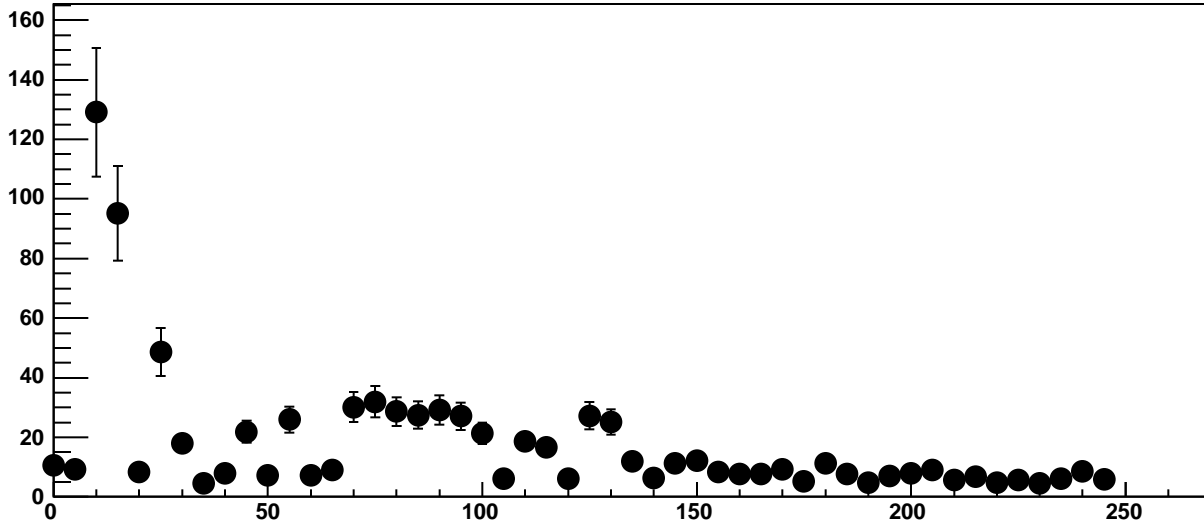


Chip 5, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold

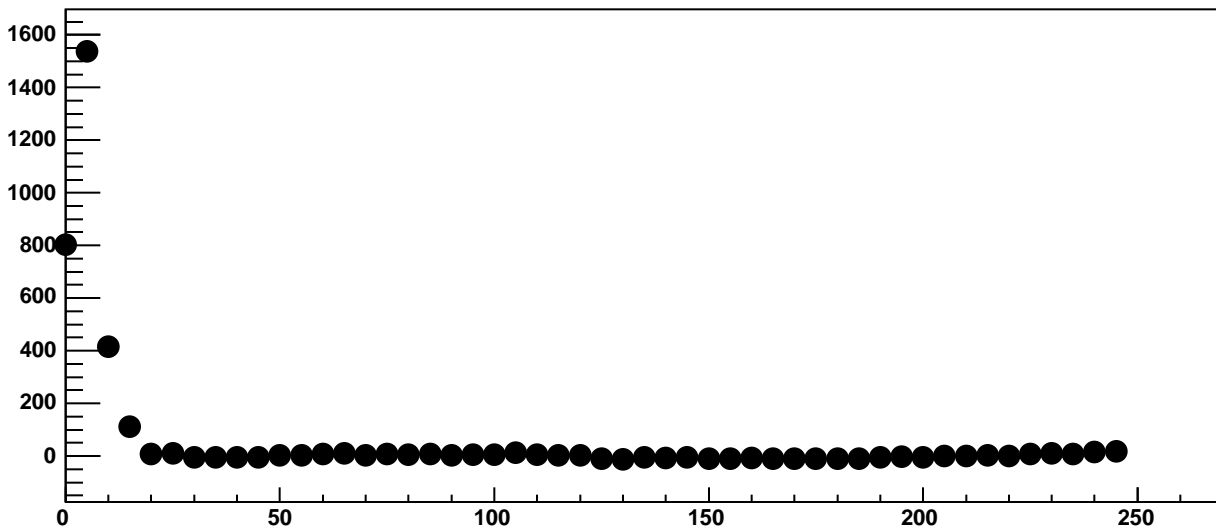


$\chi^2 / \text{ndf}$	800.2 / 42
p0	-576.3 $\pm$ 0.5046
p1	4.355 $\pm$ 0.008059
p2	1.102e+04 $\pm$ 0.7969
p3	31.25 $\pm$ 0.005319

Chip 5, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold

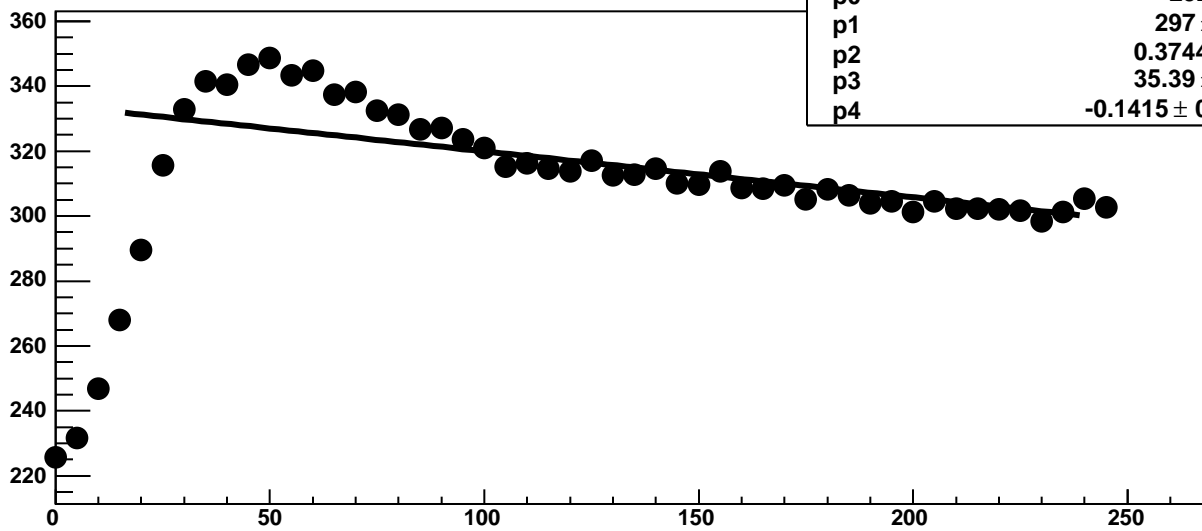


Chip 5, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold



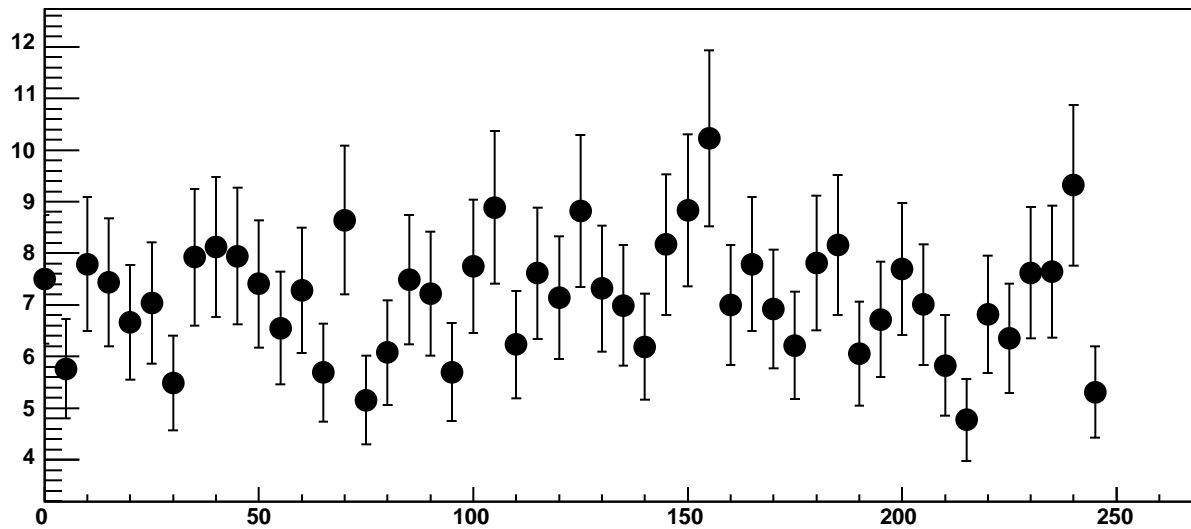


Chip 5, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

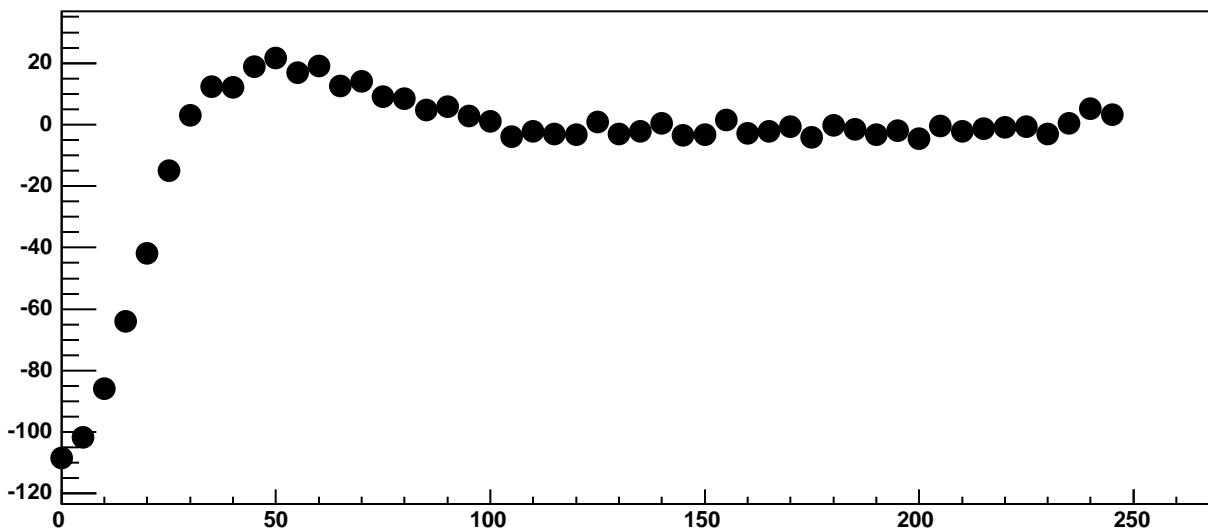


$\chi^2 / \text{ndf}$	3191 / 41
p0	$292 \pm 0.667$
p1	$297 \pm 0.7394$
p2	$0.3744 \pm 4.463$
p3	$35.39 \pm 0.3104$
p4	$-0.1415 \pm 0.003497$

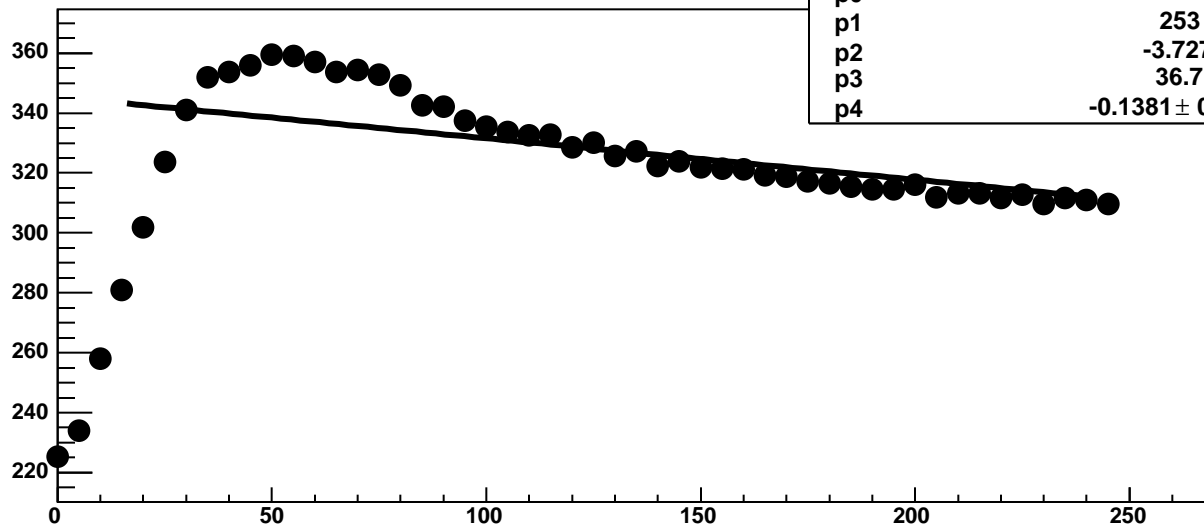
Chip 5, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

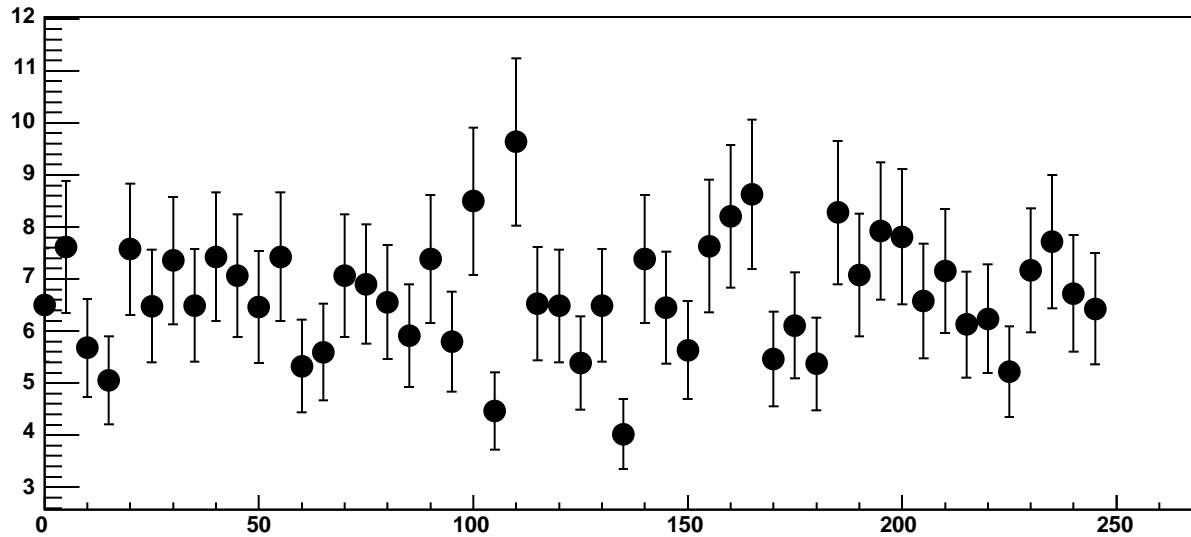


Chip 5, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

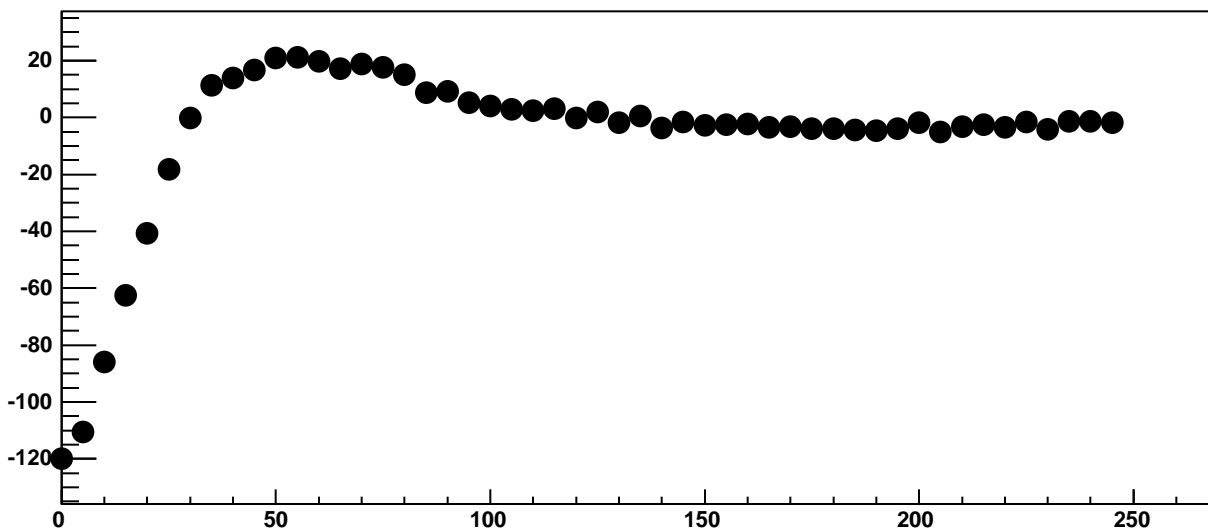


$\chi^2 / \text{ndf}$	5188 / 41
p0	$310.4 \pm 0.4786$
p1	$253 \pm 0.2769$
p2	$-3.727 \pm 3.003$
p3	$36.7 \pm 0.2381$
p4	$-0.1381 \pm 0.003328$

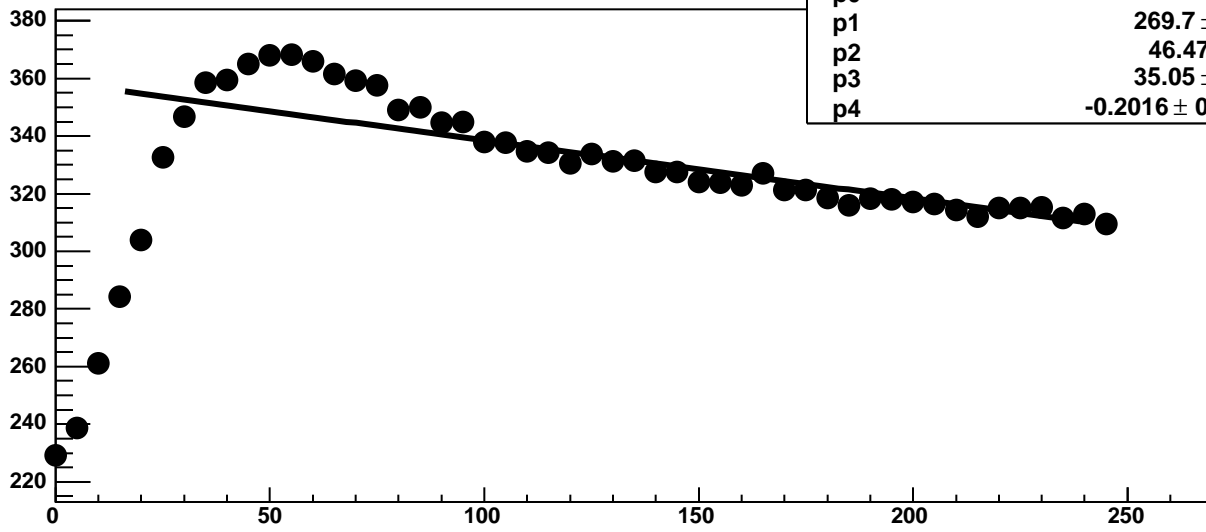
Chip 5, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

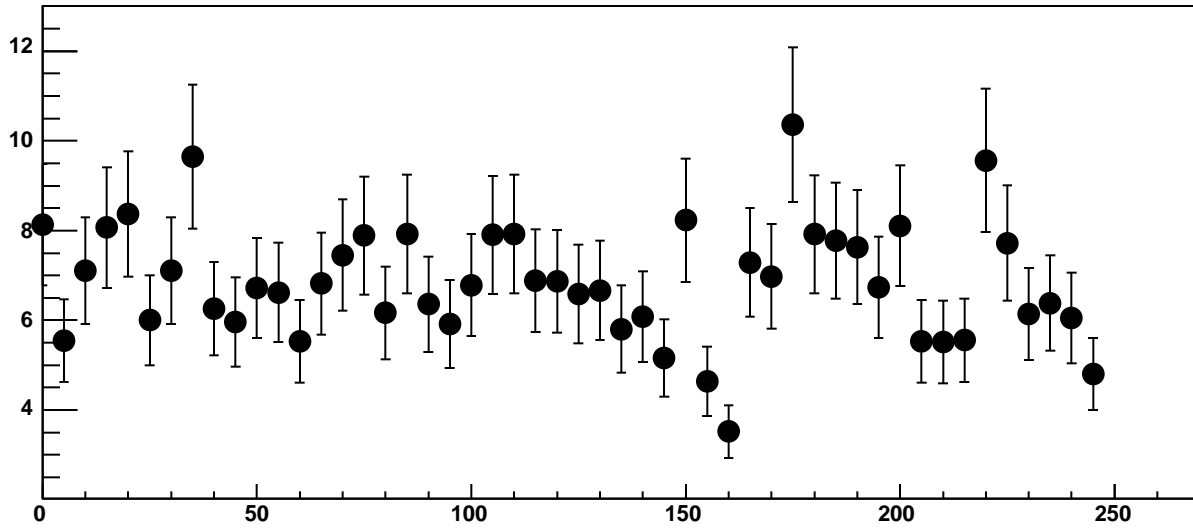


Chip 5, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold

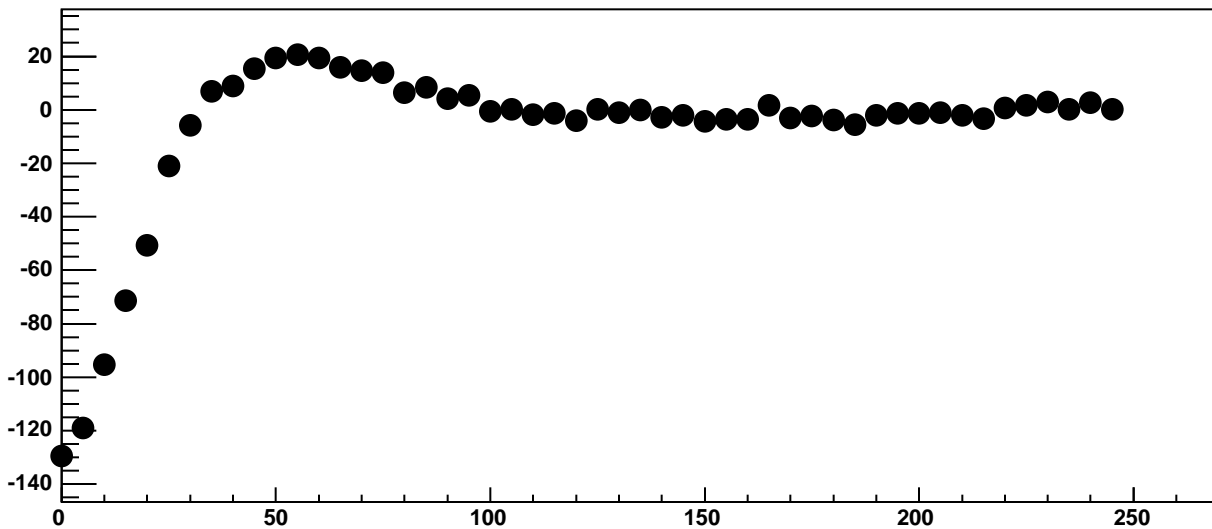


$\chi^2 / \text{ndf}$	3581 / 41
p0	$304.3 \pm 0.5225$
p1	$269.7 \pm 0.3465$
p2	$46.47 \pm 2.706$
p3	$35.05 \pm 0.2146$
p4	$-0.2016 \pm 0.003427$

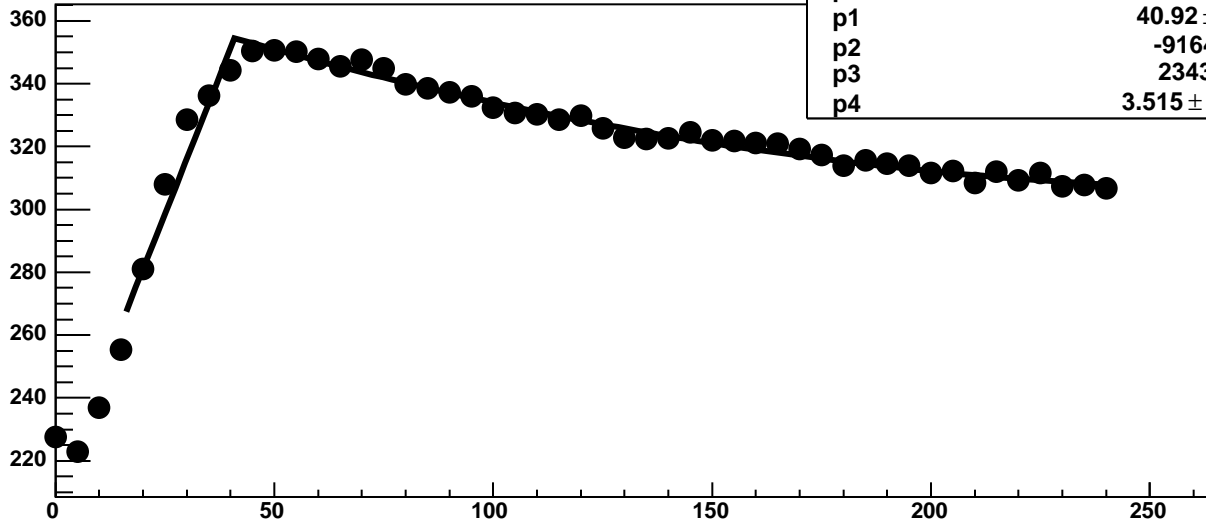
Chip 5, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold

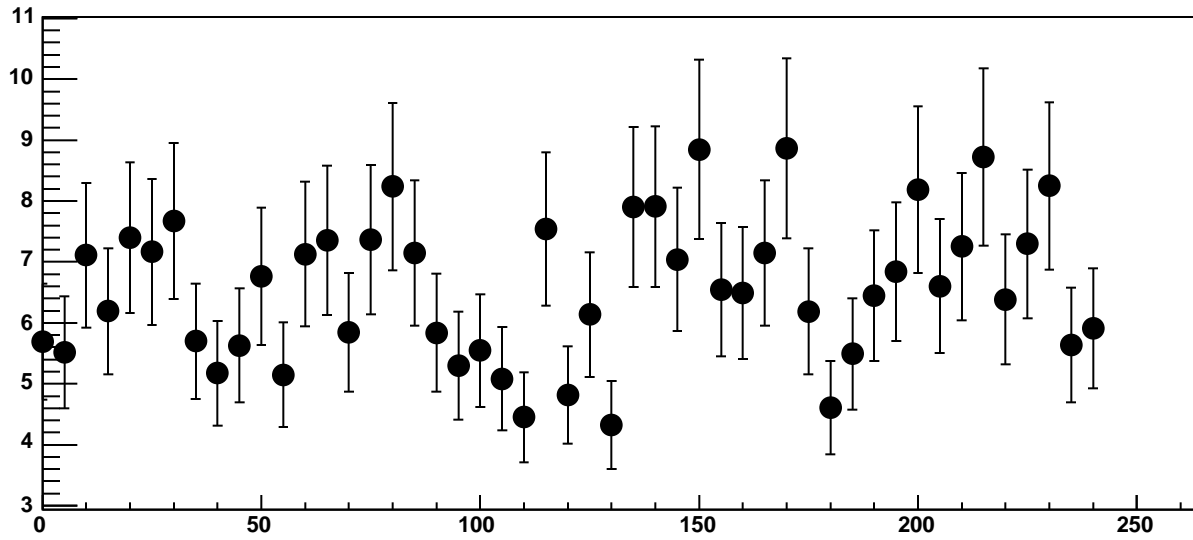


Chip 5, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold

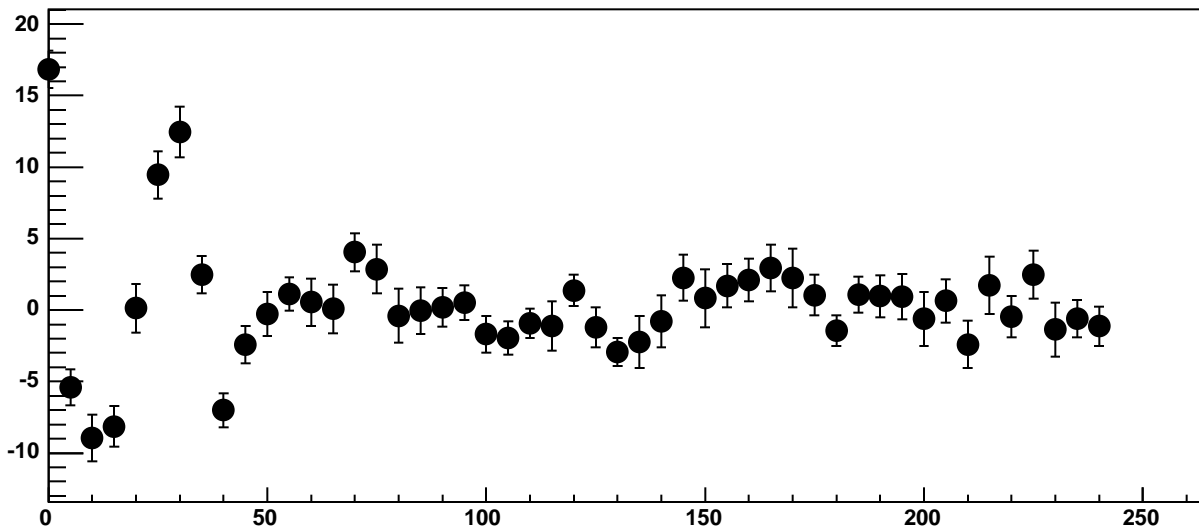


$\chi^2 / \text{ndf}$	210.4 / 41
p0	$354.5 \pm 0.5719$
p1	$40.92 \pm 0.2847$
p2	$-9164 \pm 680$
p3	$2343 \pm 172.1$
p4	$3.515 \pm 0.06407$

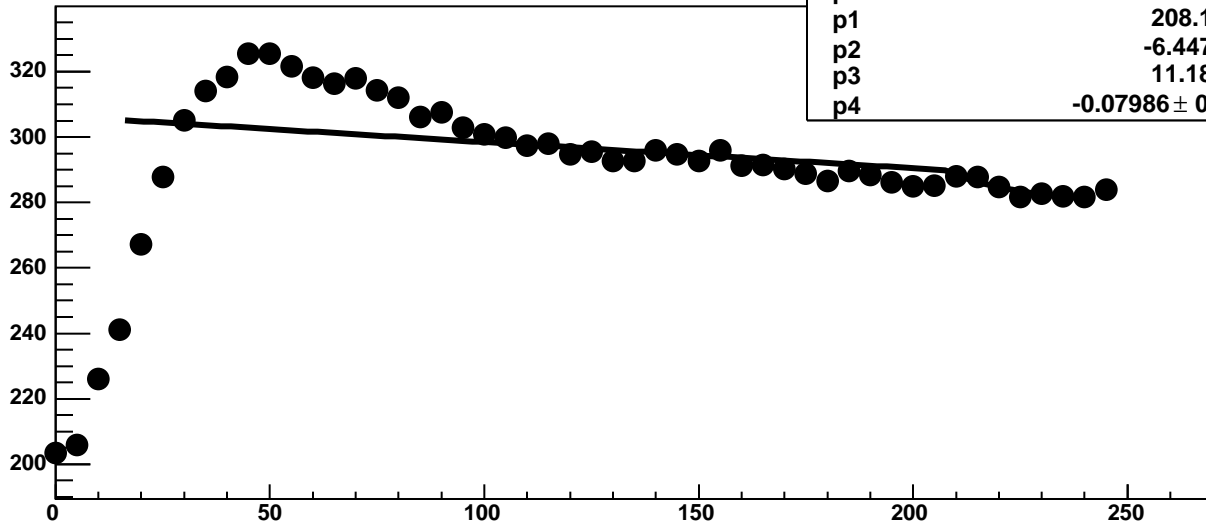
Chip 5, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold

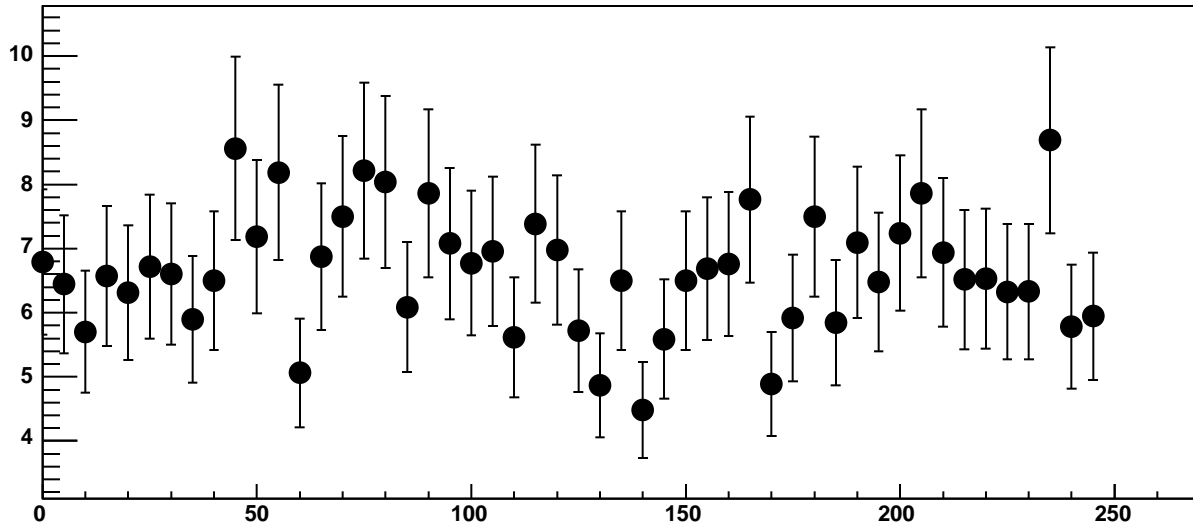


Chip 5, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold

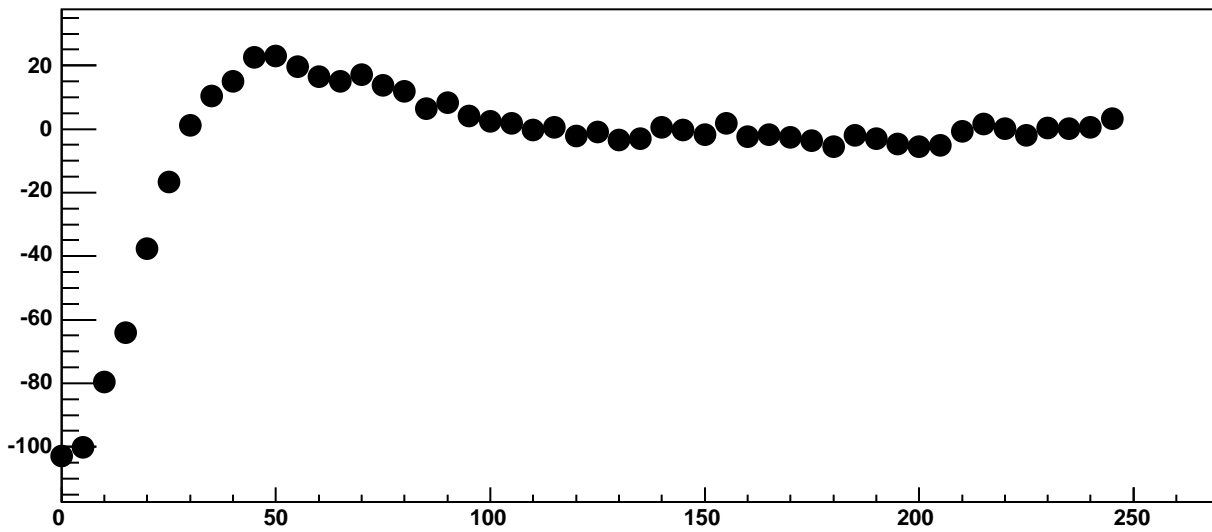


$\chi^2 / \text{ndf}$	3819 / 41
p0	$289.8 \pm 0.608$
p1	$208.1 \pm 3.612$
p2	$-6.447 \pm 1.731$
p3	$11.18 \pm 8.614$
p4	$-0.07986 \pm 0.004316$

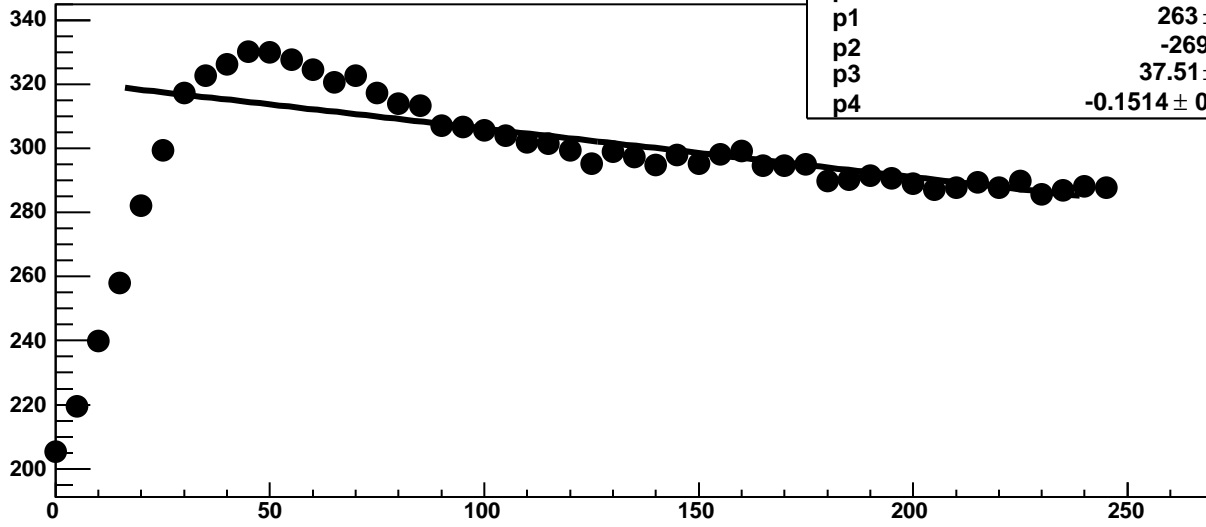
Chip 5, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

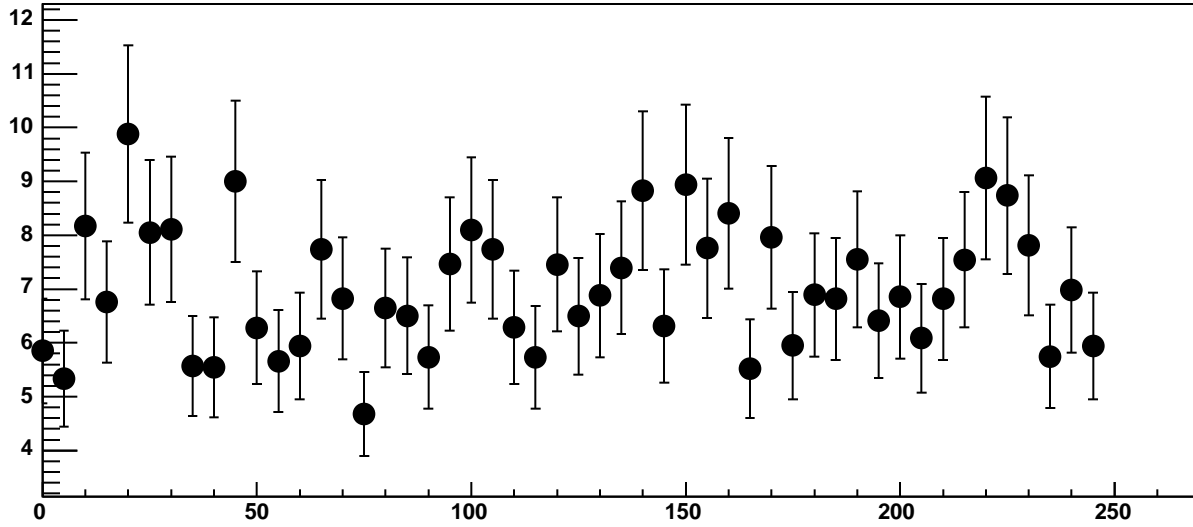


Chip 5, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold

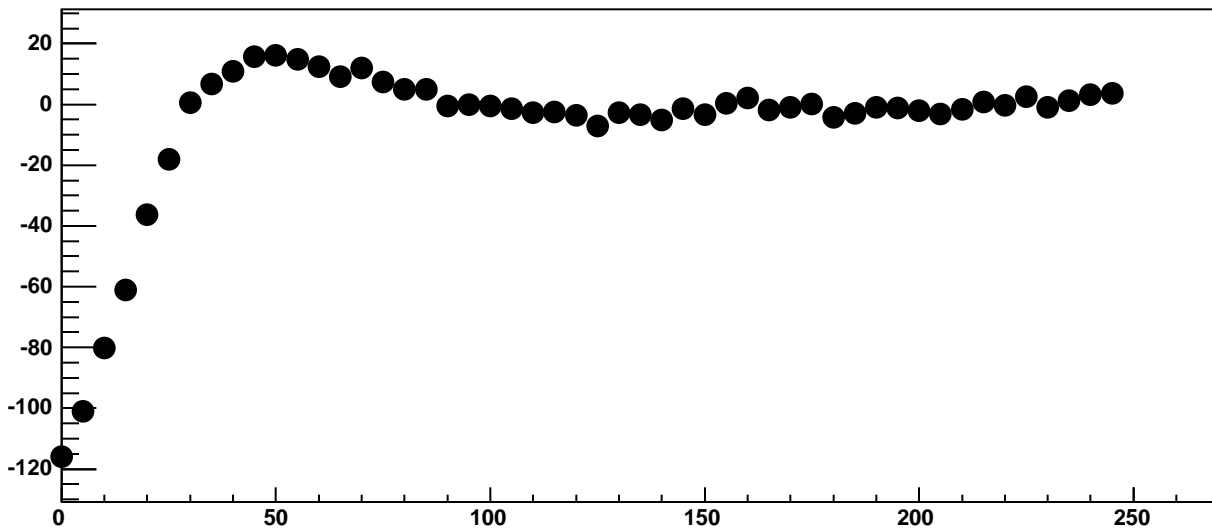


$\chi^2 / \text{ndf}$	2638 / 41
p0	$281.5 \pm 0.561$
p1	$263 \pm 0.3467$
p2	$-269 \pm 10.37$
p3	$37.51 \pm 0.5323$
p4	$-0.1514 \pm 0.003517$

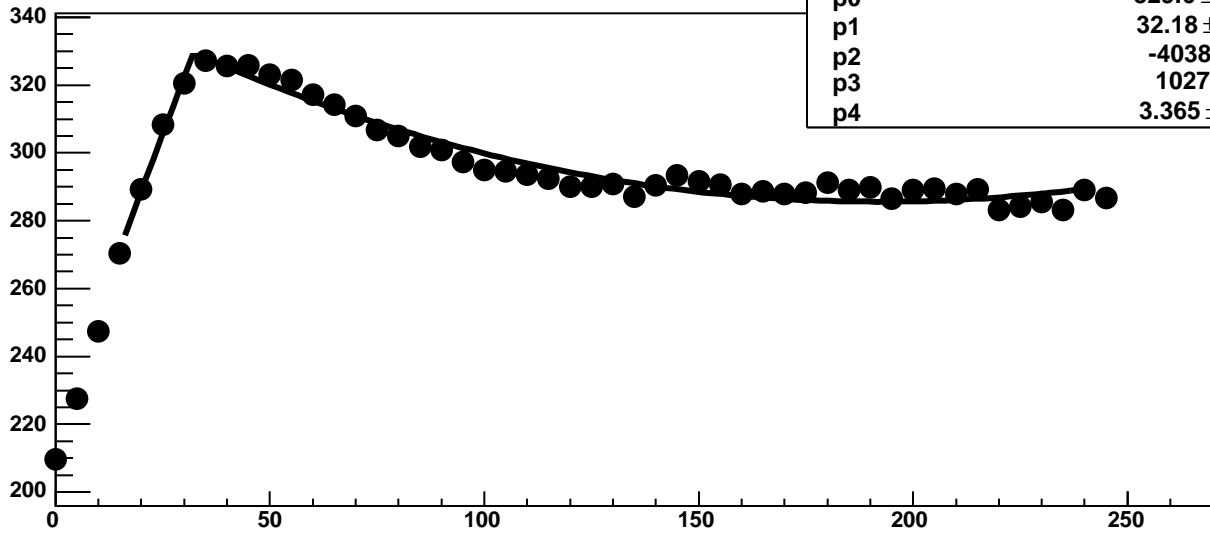
Chip 5, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold



Chip 5, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold



$\chi^2 / \text{ndf}$

164.9 / 41

p0

$329.6 \pm 0.6974$

p1

$32.18 \pm 0.4359$

p2

$-4038 \pm 338.7$

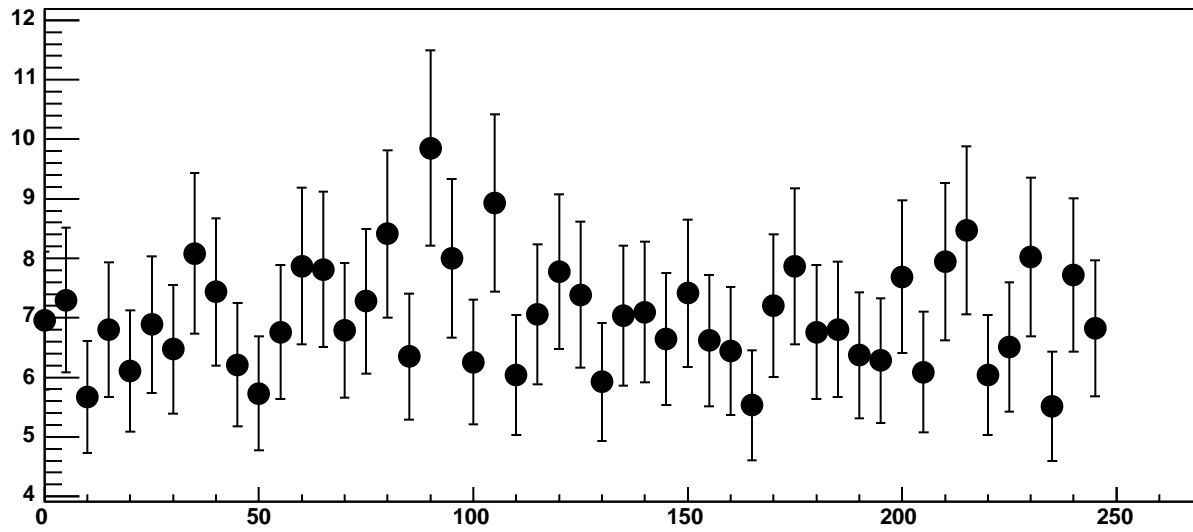
p3

$1027 \pm 60.19$

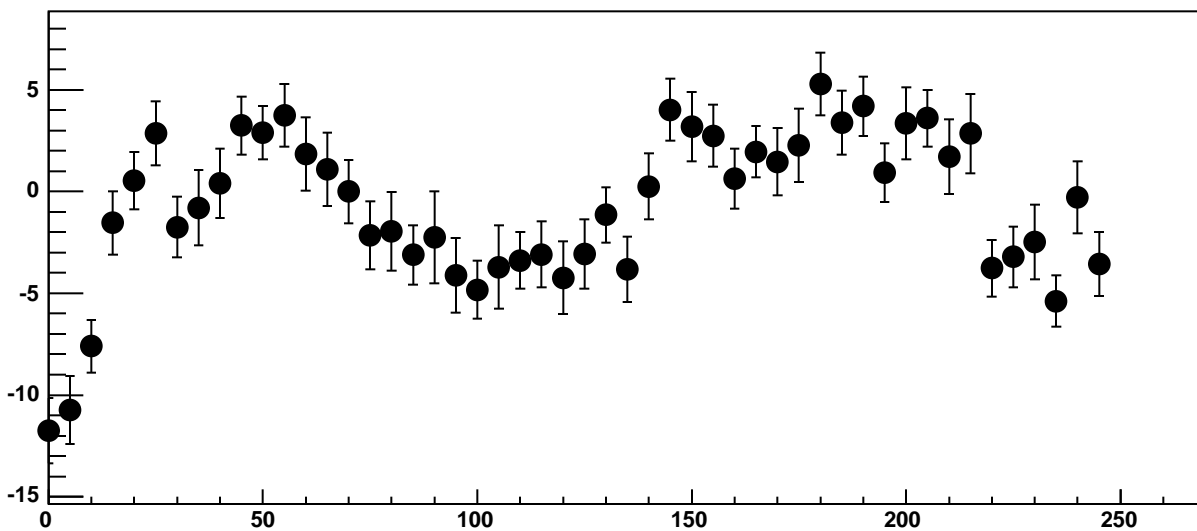
p4

$3.365 \pm 0.1371$

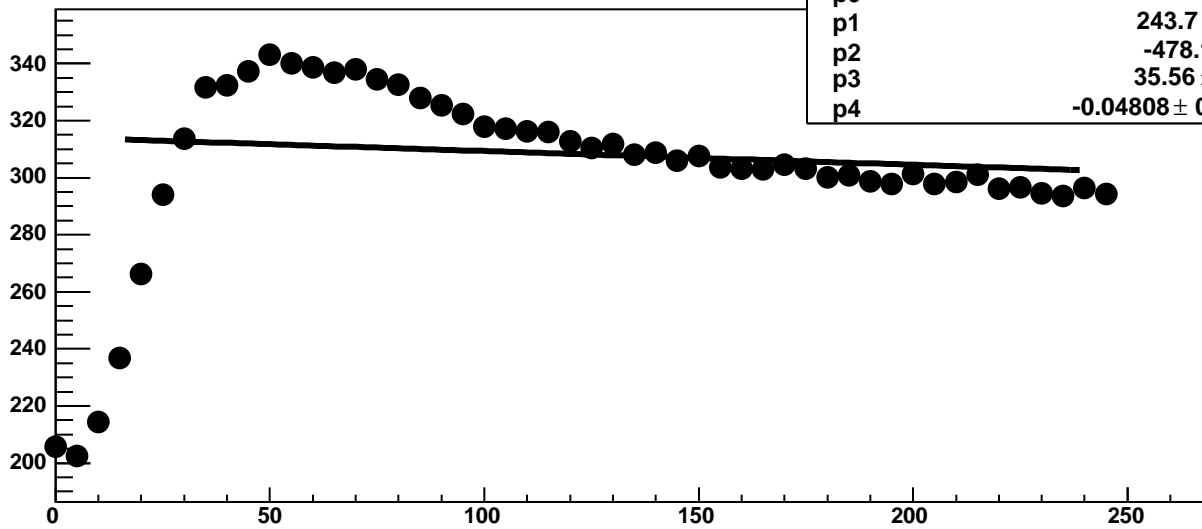
Chip 5, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

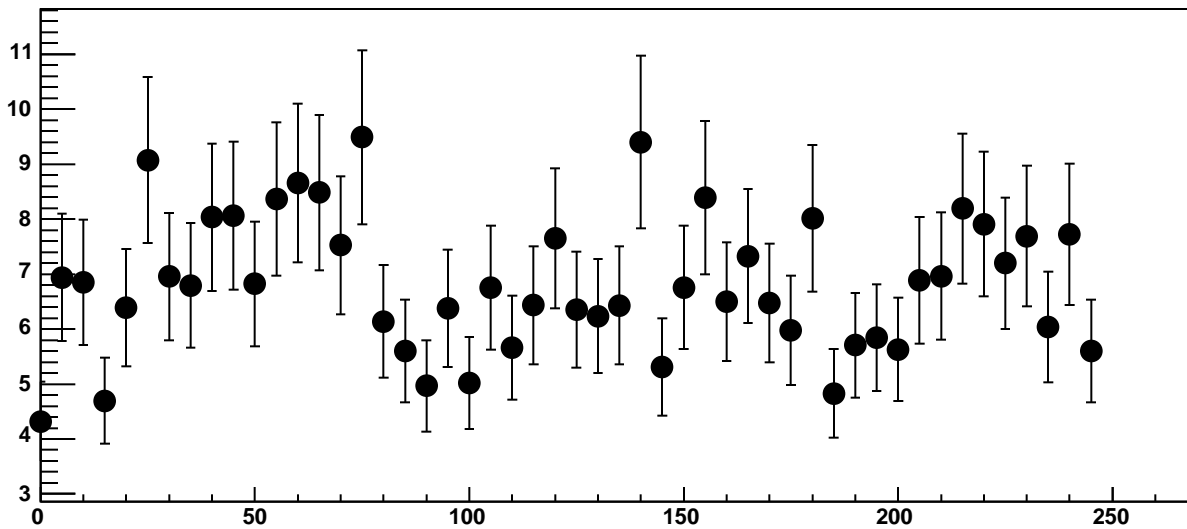


Chip 5, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

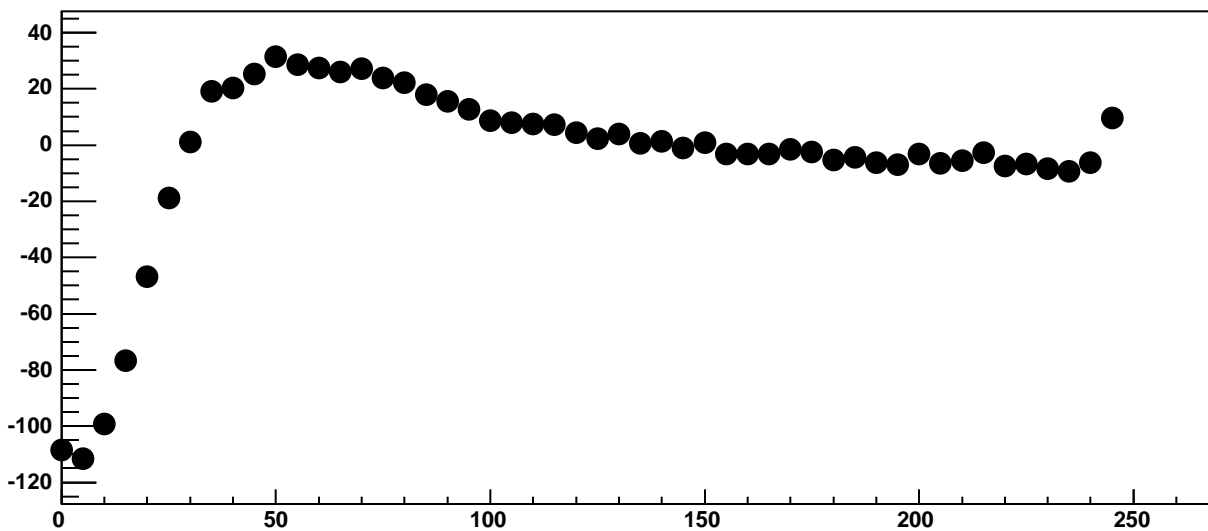


$\chi^2 / \text{ndf}$	9082 / 41
p0	$302.4 \pm 0.4592$
p1	$243.7 \pm 0.2233$
p2	$-478.1 \pm 10.98$
p3	$35.56 \pm 0.5605$
p4	$-0.04808 \pm 0.003436$

Chip 5, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold

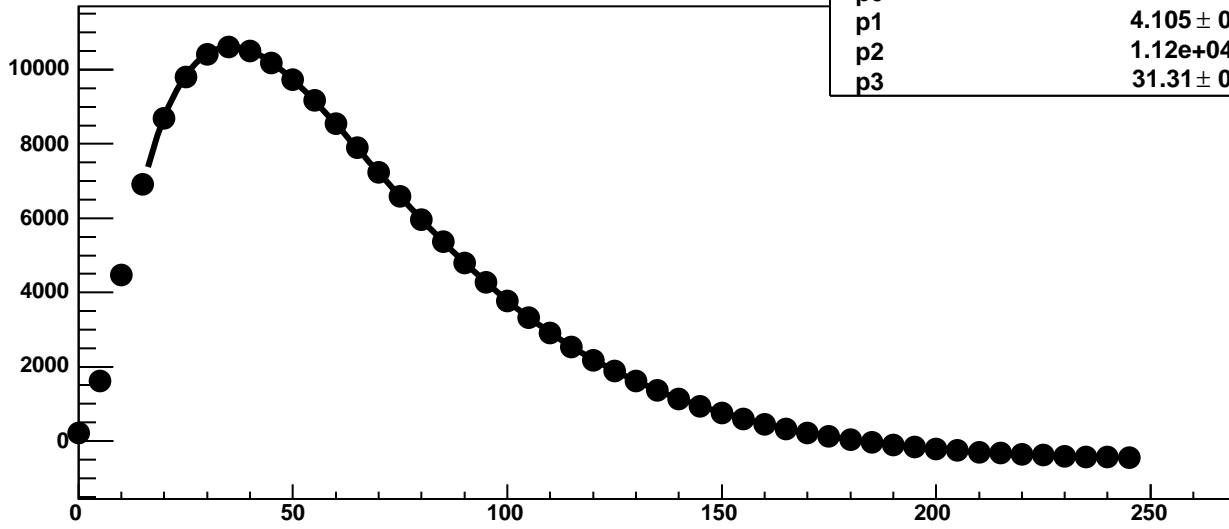


Chip 5, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold



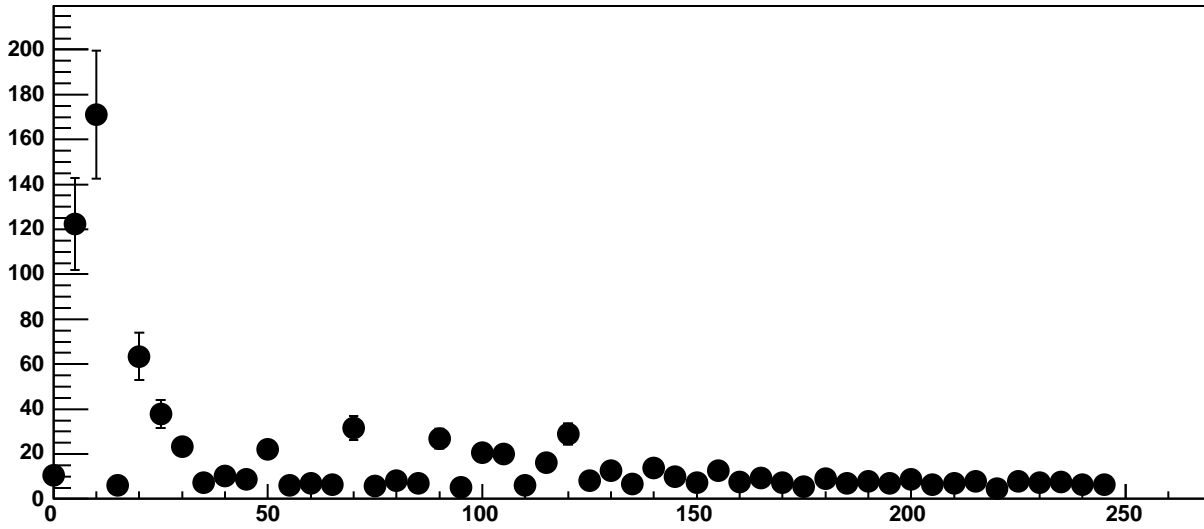


Chip 5, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold

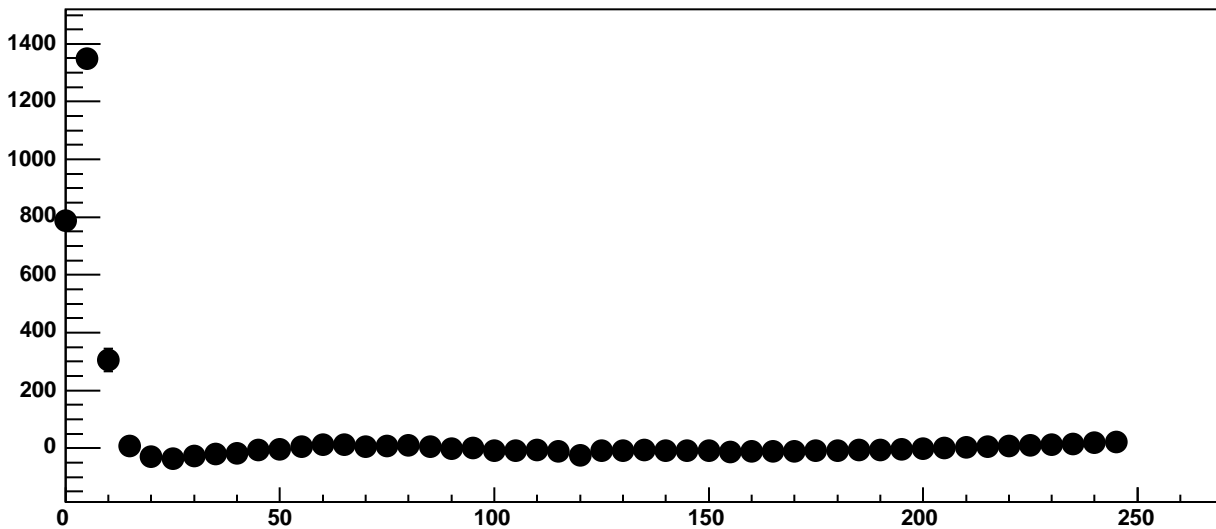


$\chi^2 / \text{ndf}$	1211 / 42
p0	-578.2 ± 0.5095
p1	4.105 ± 0.004363
p2	1.12e+04 ± 0.876
p3	31.31 ± 0.004188

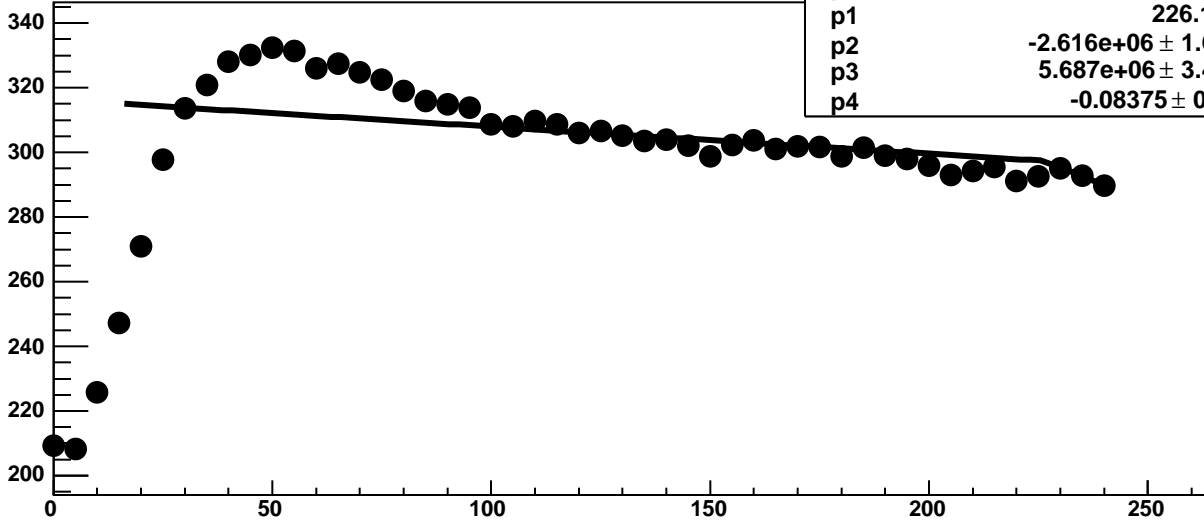
Chip 5, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold

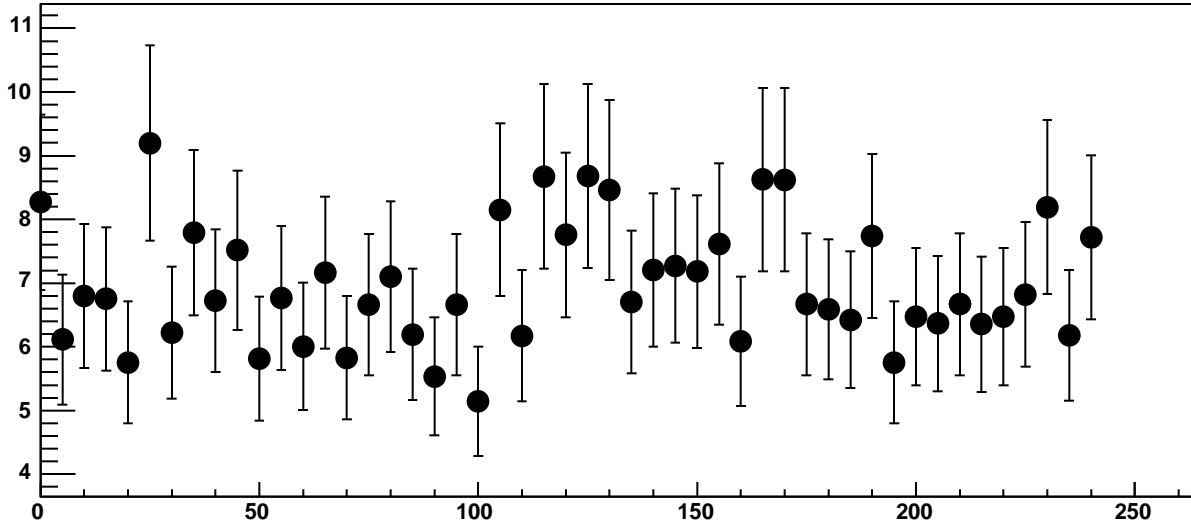


Chip 5, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

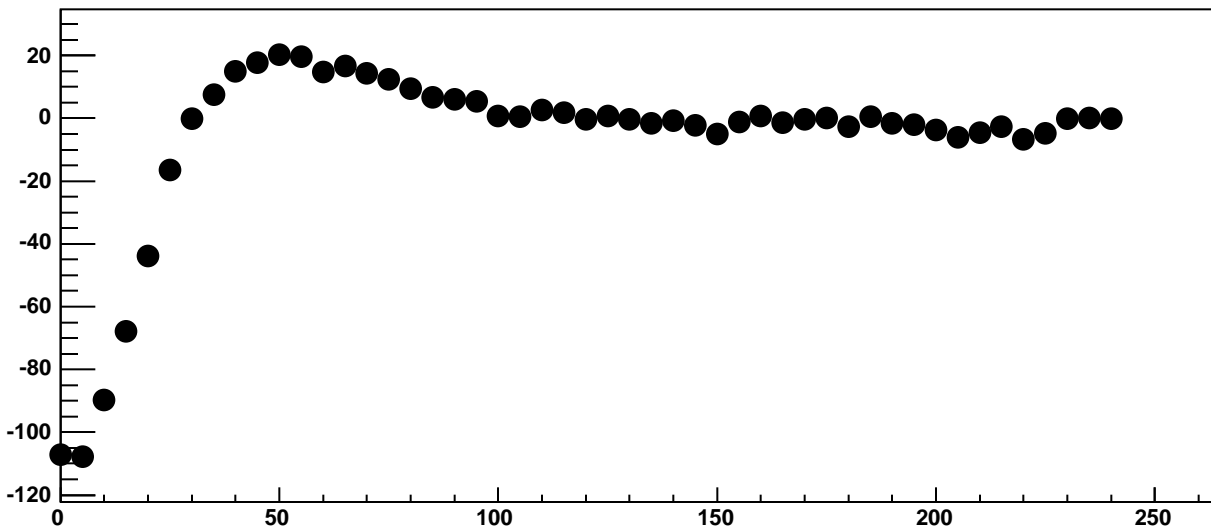


$\chi^2 / \text{ndf}$	4263 / 41
p0	$297.4 \pm 0.7204$
p1	$226.1 \pm 5.631$
p2	$-2.616\text{e}+06 \pm 1.631\text{e}+07$
p3	$5.687\text{e}+06 \pm 3.439\text{e}+07$
p4	$-0.08375 \pm 0.003748$

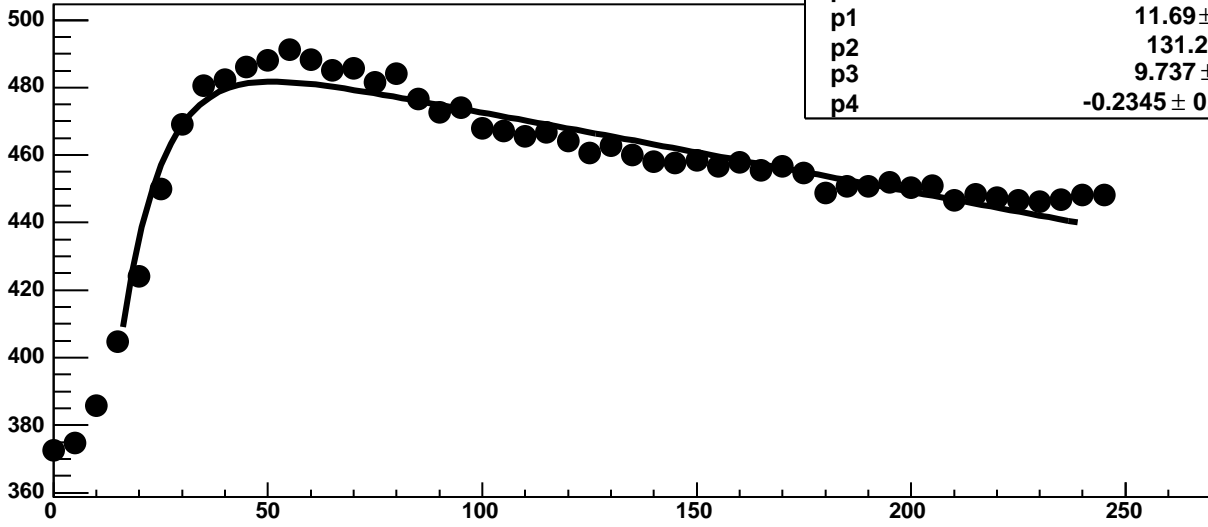
Chip 5, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

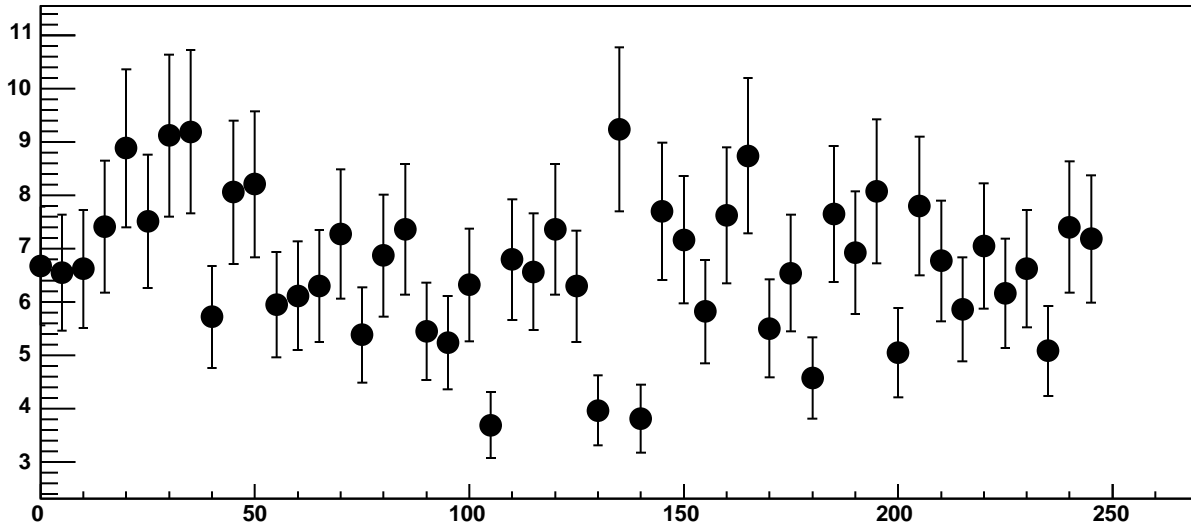


Chip 5, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

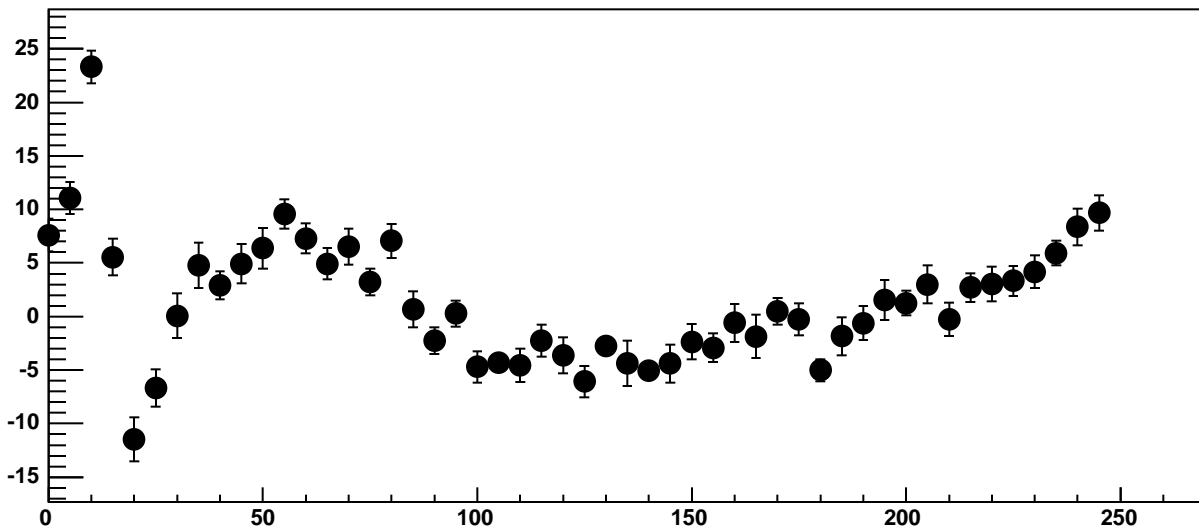


$\chi^2 / \text{ndf}$	450.5 / 41
p0	$362.1 \pm 2.75$
p1	$11.69 \pm 0.2975$
p2	$131.2 \pm 2.799$
p3	$9.737 \pm 0.2868$
p4	$-0.2345 \pm 0.004018$

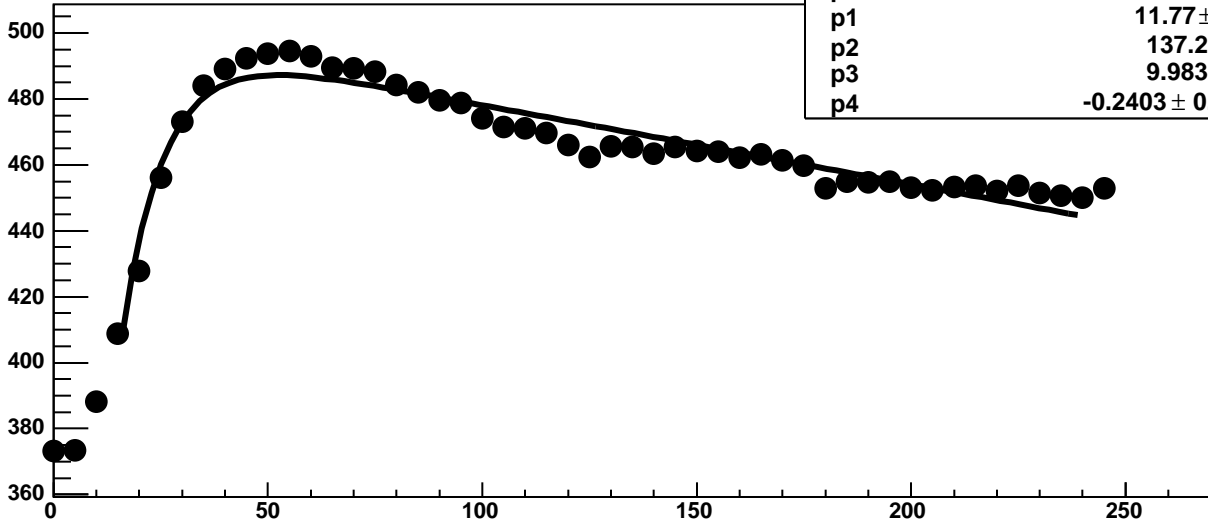
Chip 5, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold

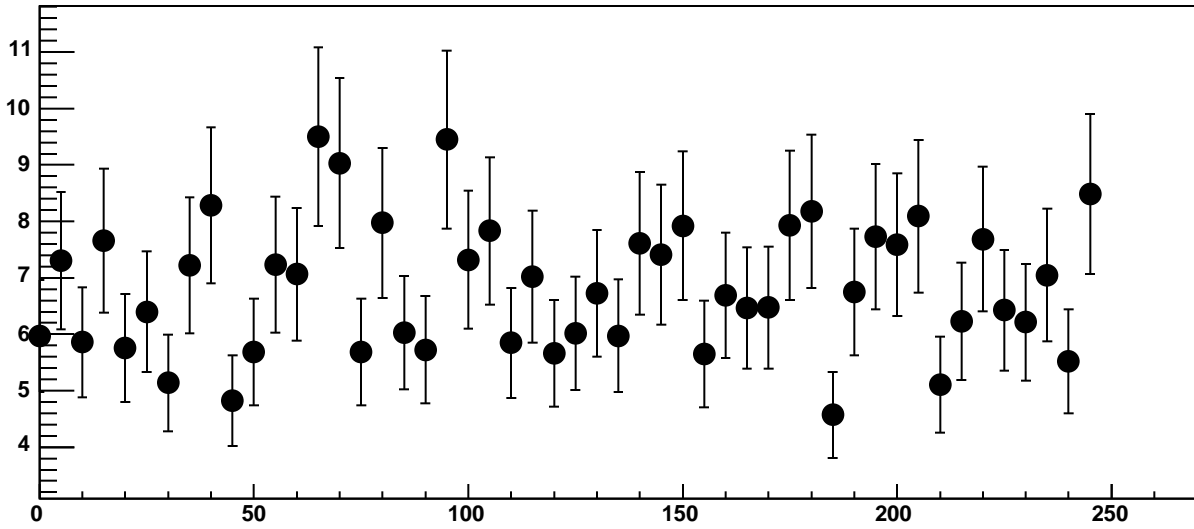


Chip 5, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold

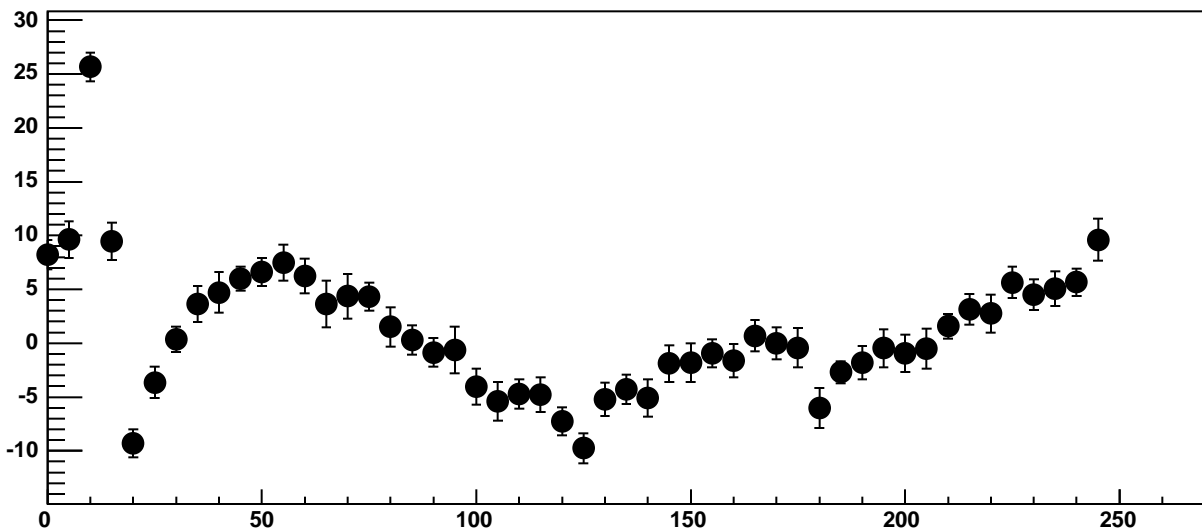


$\chi^2 / \text{ndf}$	439.3 / 41
p0	$362.2 \pm 2.89$
p1	$11.77 \pm 0.2894$
p2	$137.2 \pm 2.942$
p3	$9.983 \pm 0.247$
p4	$-0.2403 \pm 0.004193$

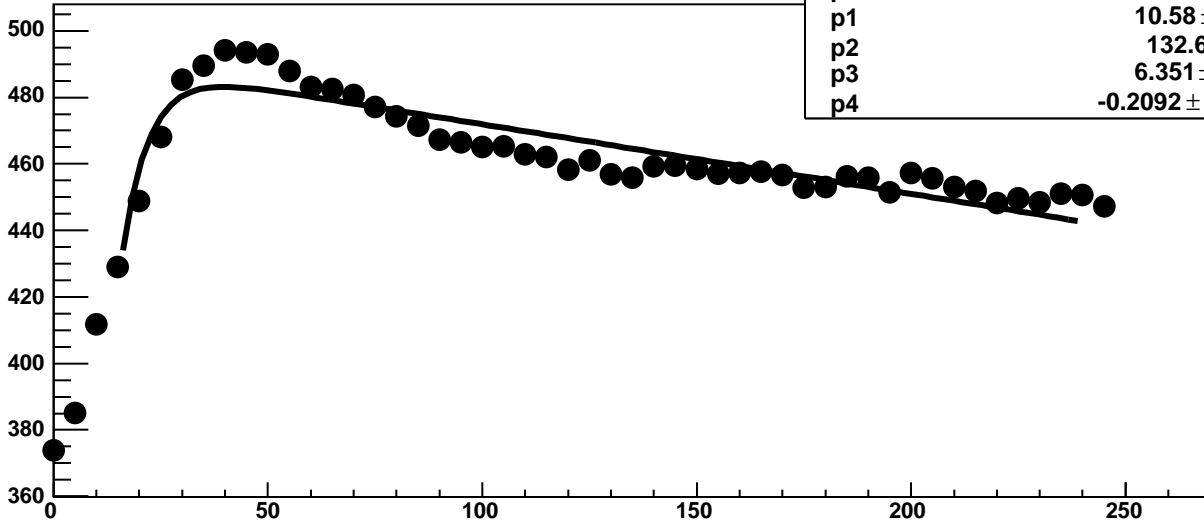
Chip 5, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold

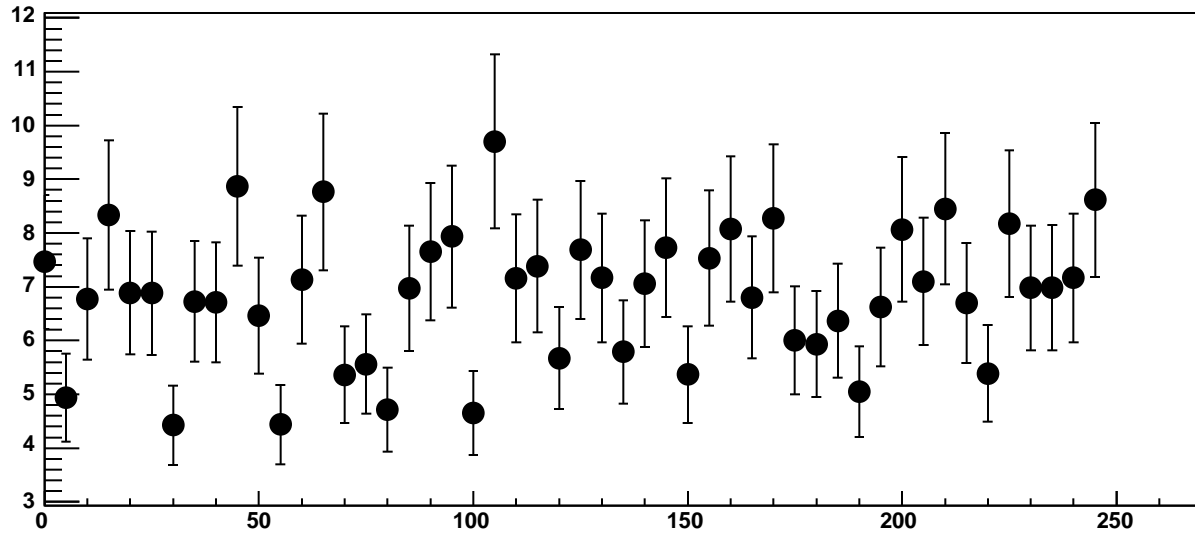


Chip 5, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold

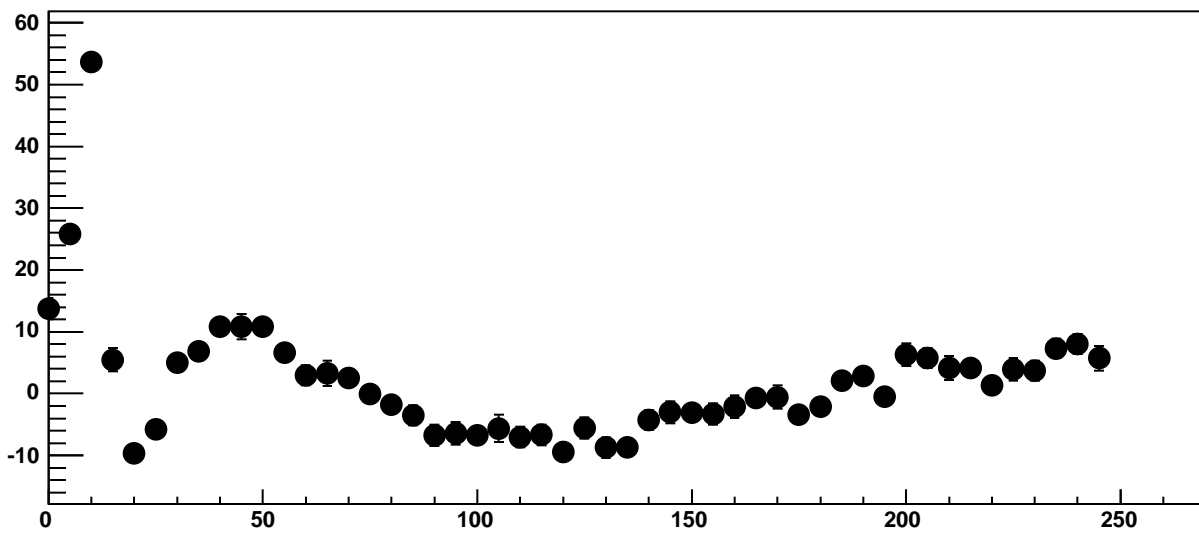


$\chi^2 / \text{ndf}$	665.6 / 41
p0	$358 \pm 2.89$
p1	$10.58 \pm 0.3043$
p2	$132.6 \pm 2.919$
p3	$6.351 \pm 0.2395$
p4	$-0.2092 \pm 0.00378$

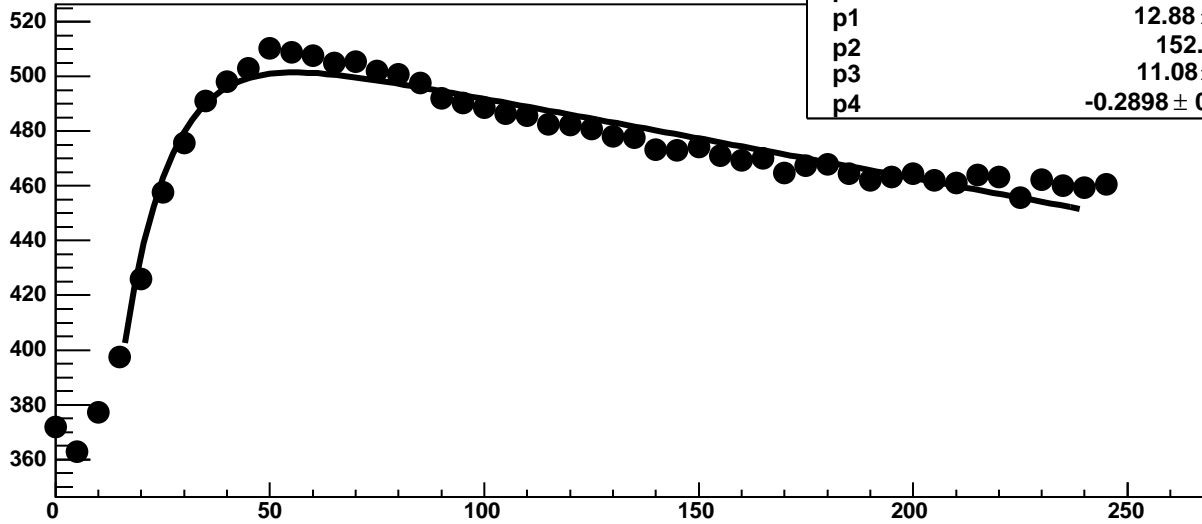
Chip 5, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold

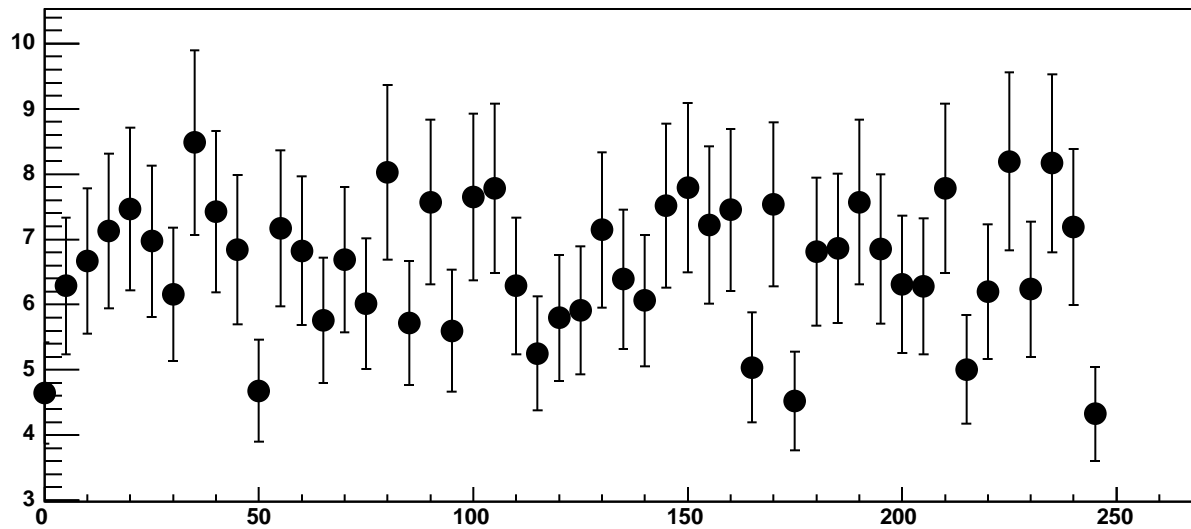


Chip 5, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

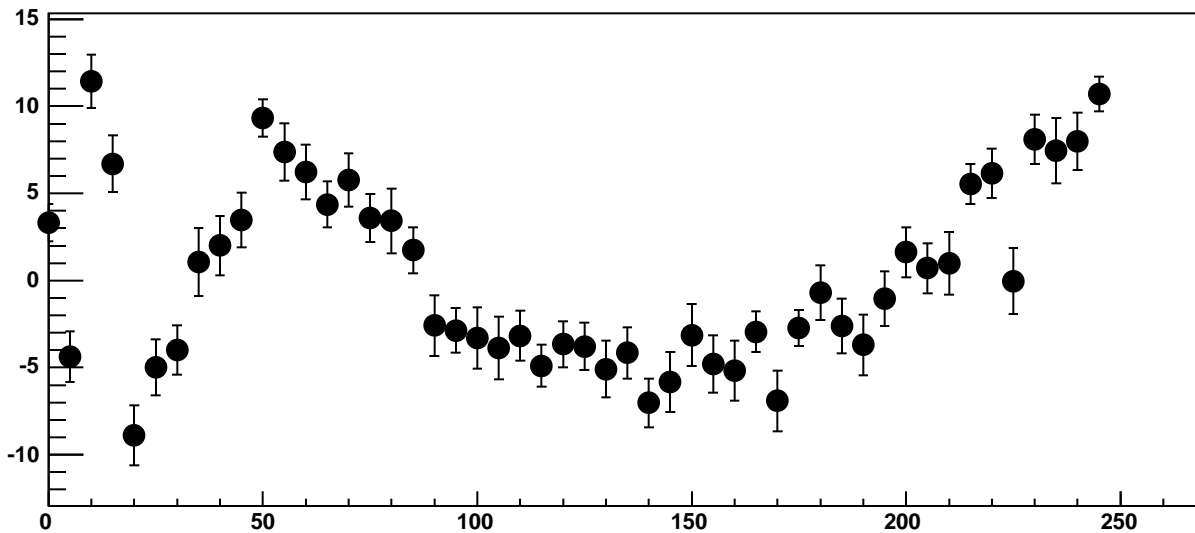


$\chi^2 / \text{ndf}$	496.6 / 41
p0	$364.9 \pm 2.867$
p1	$12.88 \pm 0.2594$
p2	$152.2 \pm 2.93$
p3	$11.08 \pm 0.2305$
p4	$-0.2898 \pm 0.004346$

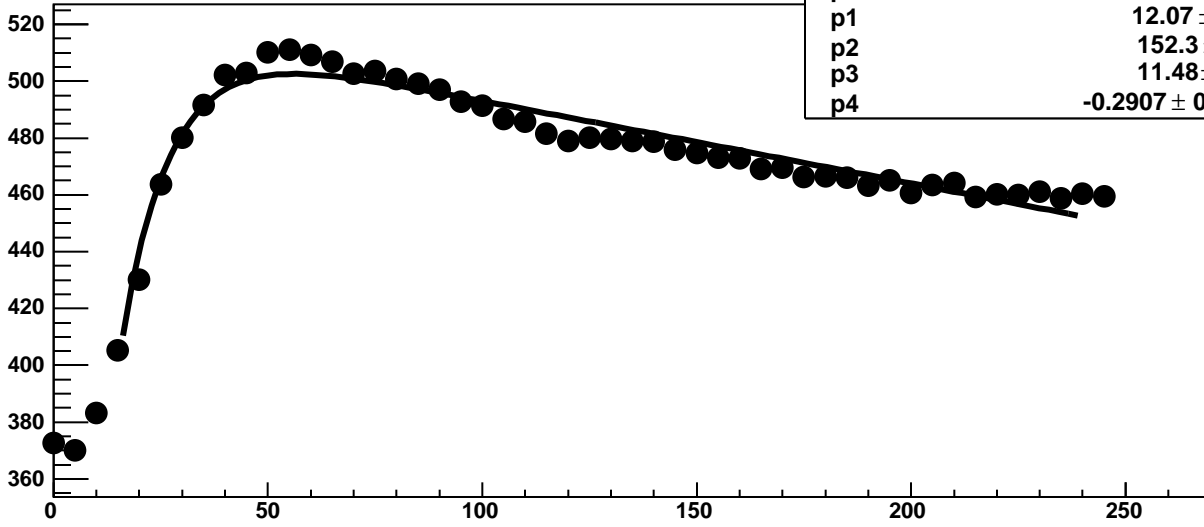
Chip 5, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

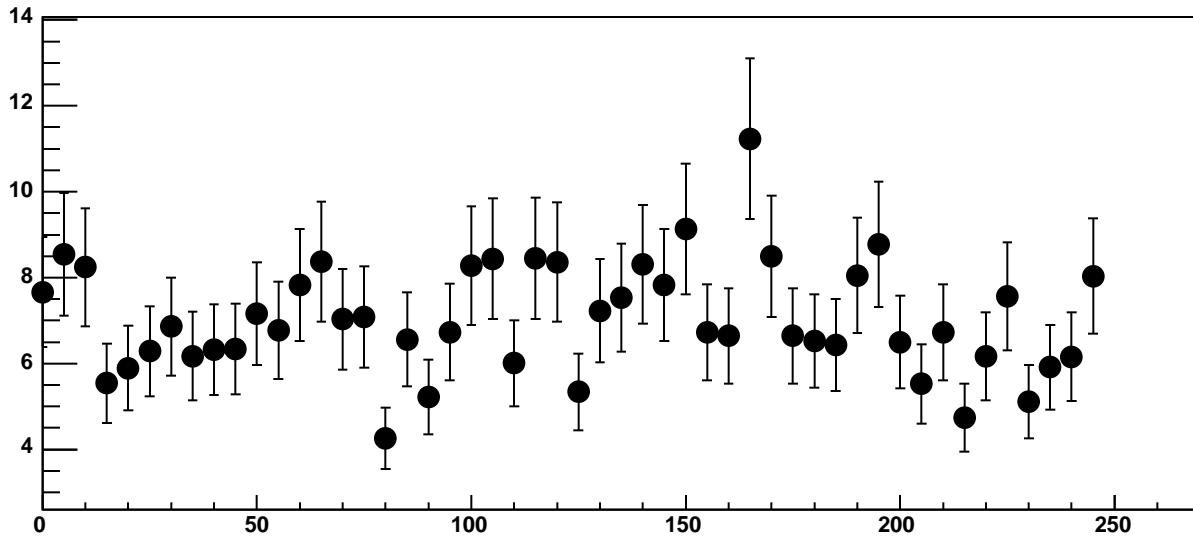


Chip 5, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

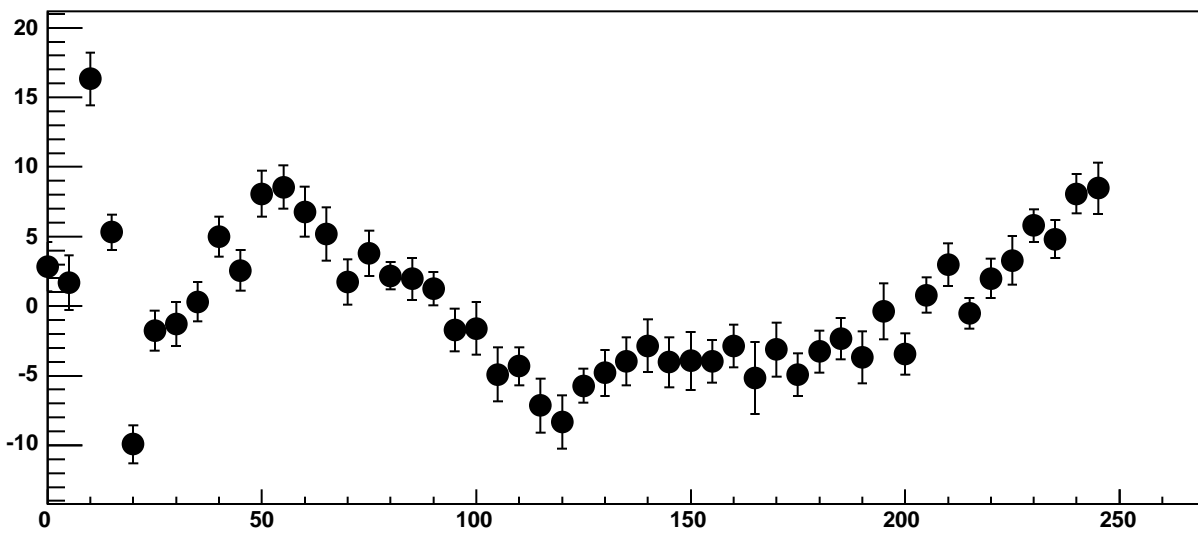


$\chi^2 / \text{ndf}$	398.9 / 41
p0	$366.4 \pm 0.3173$
p1	$12.07 \pm 0.1466$
p2	$152.3 \pm 0.4071$
p3	$11.48 \pm 0.2392$
p4	$-0.2907 \pm 0.004297$

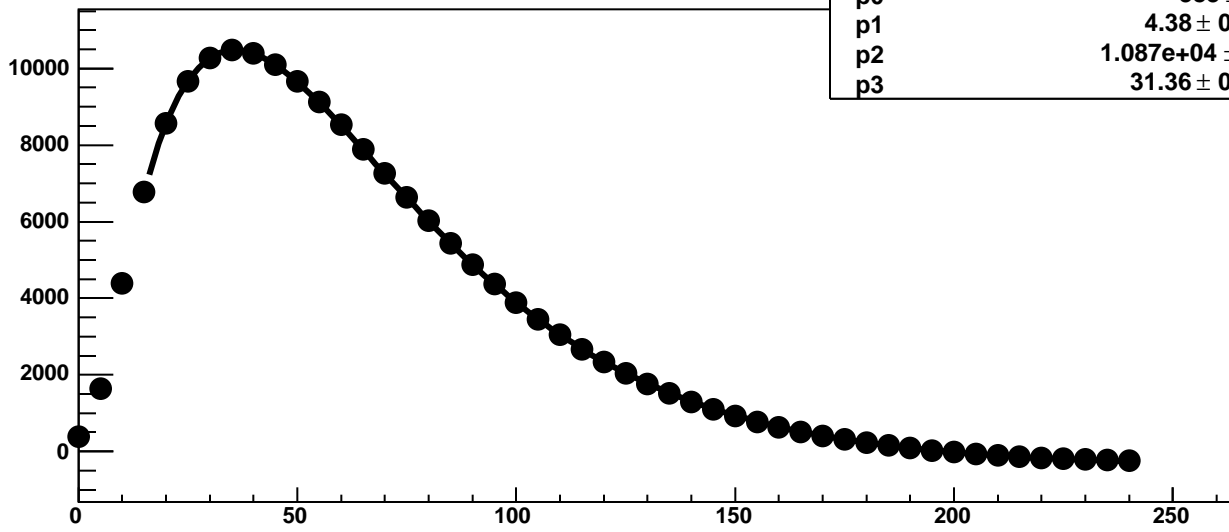
Chip 5, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

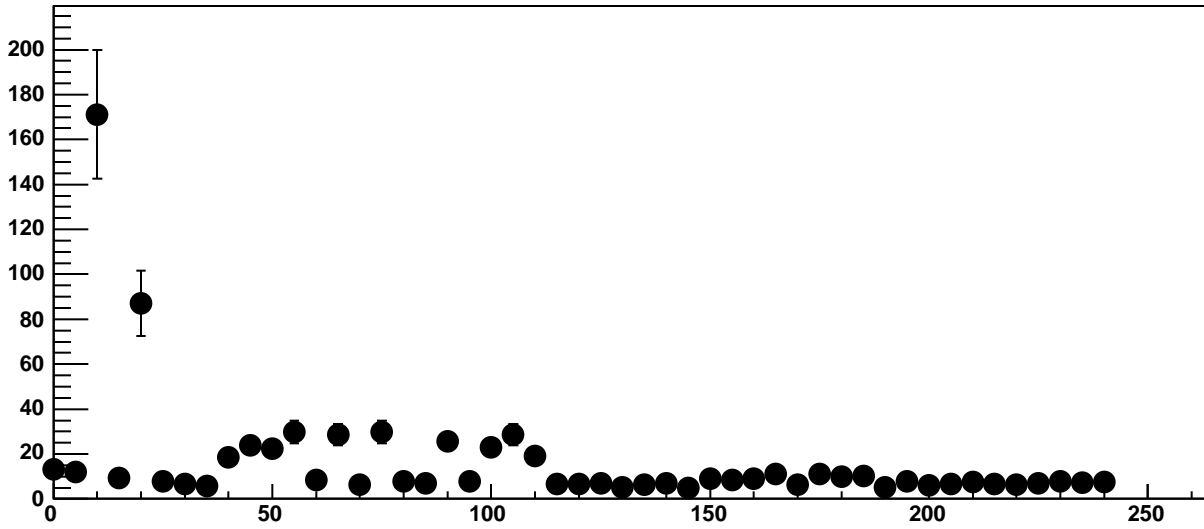


Chip 5, Channel 6, Enable 5!, DAC=4000, ADC Mean vs Hold

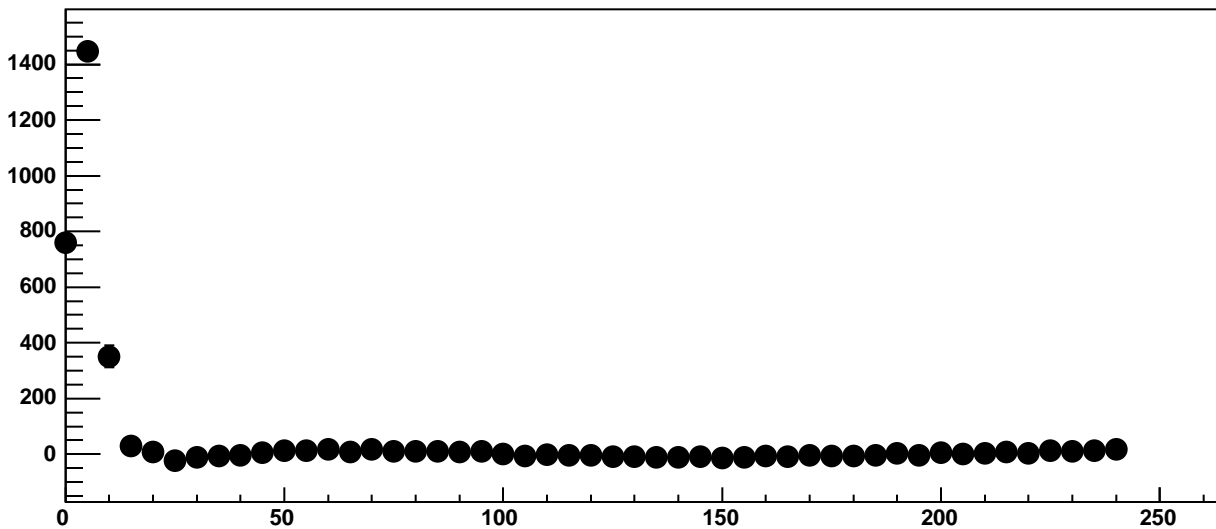


$\chi^2 / \text{ndf}$	1504 / 42
p0	-383 ± 0.5369
p1	4.38 ± 0.005869
p2	1.087e+04 ± 0.8666
p3	31.36 ± 0.004333

Chip 5, Channel 6, Enable 5!, DAC=4000, ADC Noise vs Hold

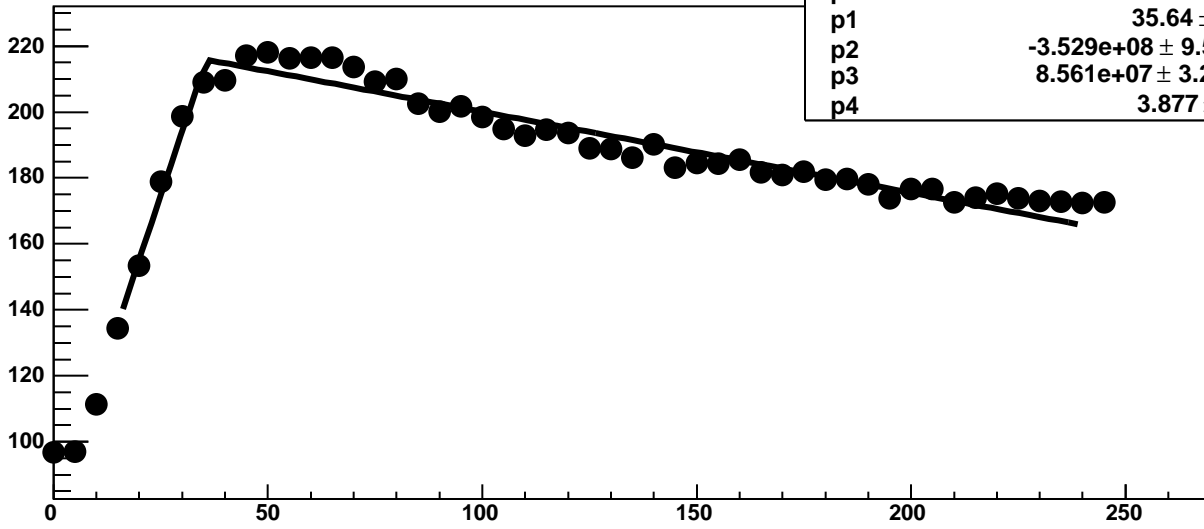


Chip 5, Channel 6, Enable 5!, DAC=4000, ADC Residuals vs Hold



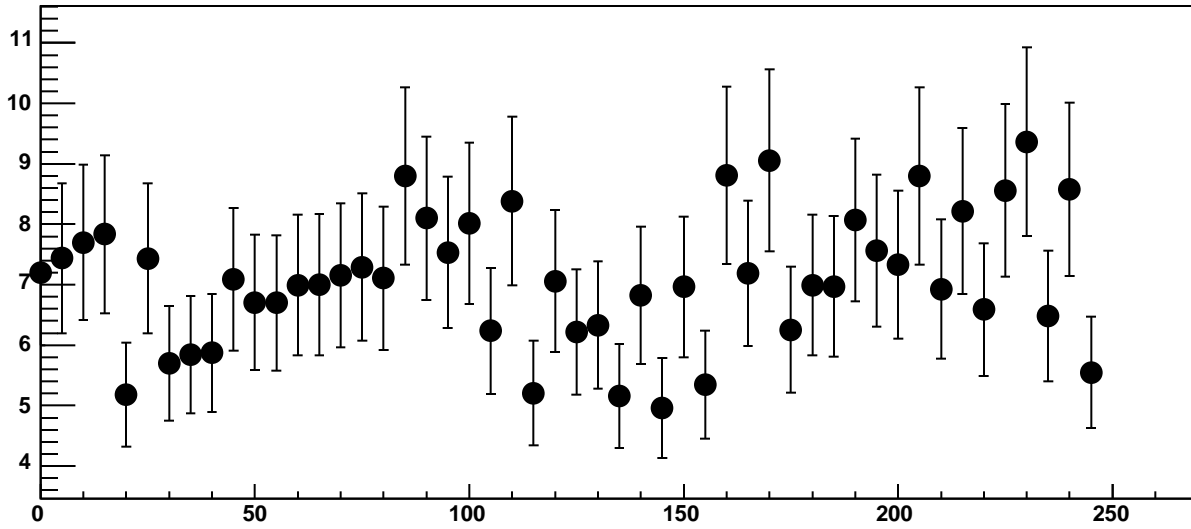


Chip 5, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

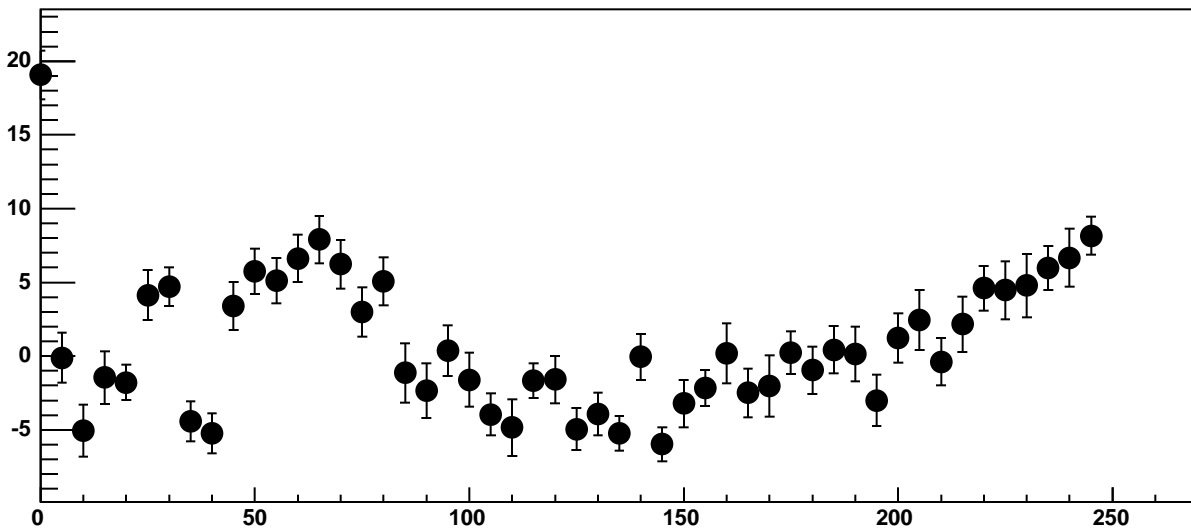


$\chi^2 / \text{ndf}$	295.9 / 41
p0	$215.9 \pm 0.4795$
p1	$35.64 \pm 0.3466$
p2	$-3.529\text{e}+08 \pm 9.595\text{e}+06$
p3	$8.561\text{e}+07 \pm 3.276\text{e}+04$
p4	$3.877 \pm 0.1118$

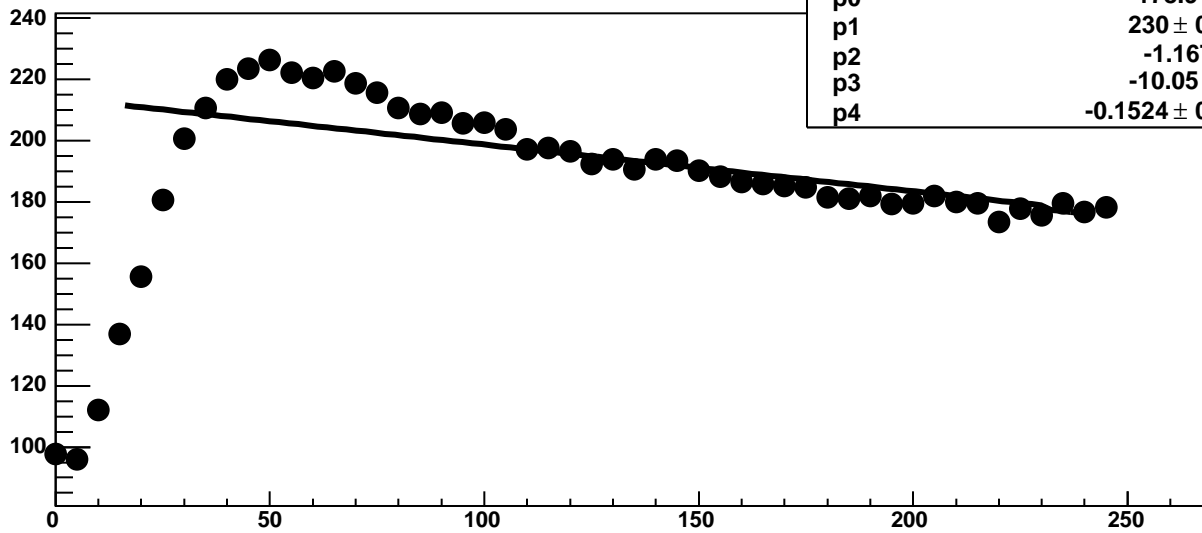
Chip 5, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

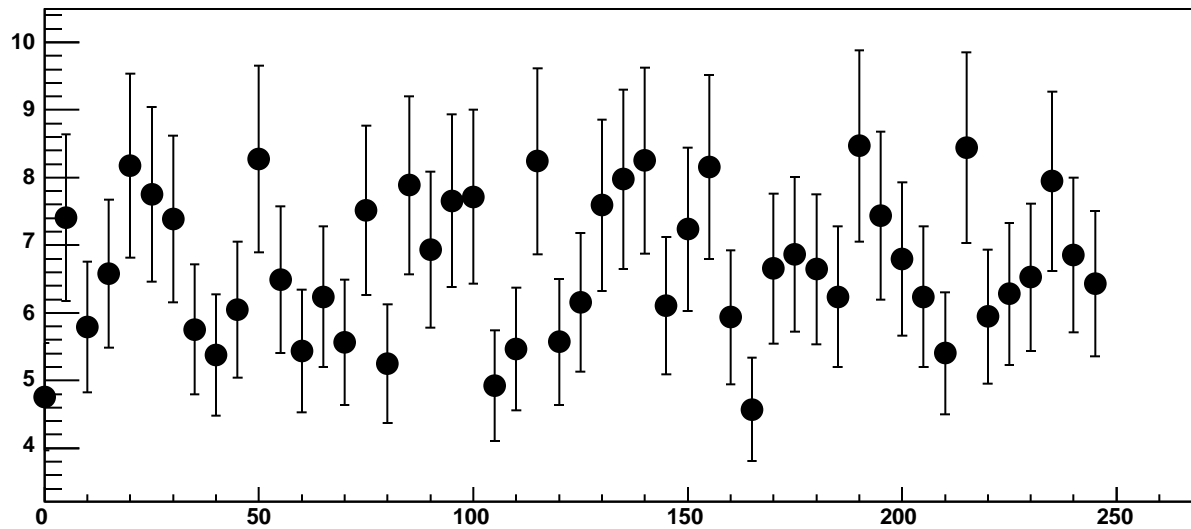


Chip 5, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold

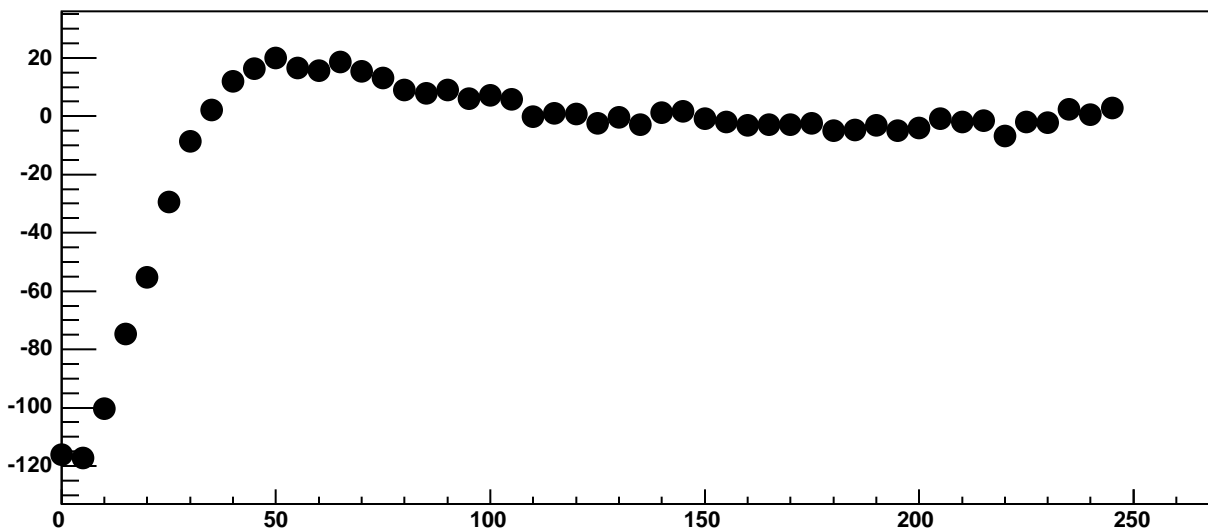


$\chi^2 / \text{ndf}$	4879 / 41
p0	$178.9 \pm 0.4638$
p1	$230 \pm 0.001433$
p2	$-1.167 \pm 1.051$
p3	$-10.05 \pm 0.8917$
p4	$-0.1524 \pm 0.006589$

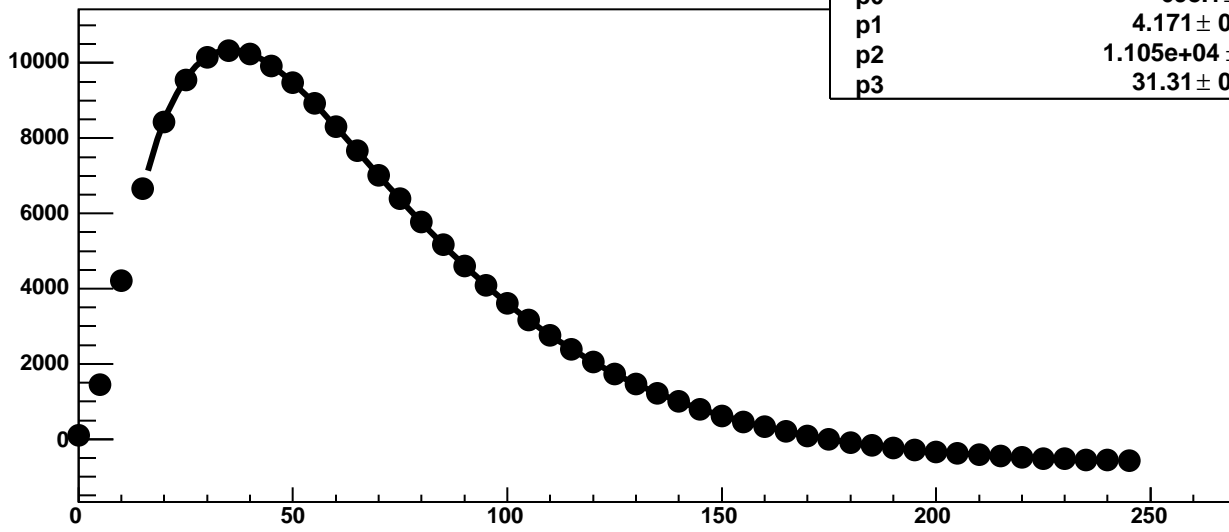
Chip 5, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

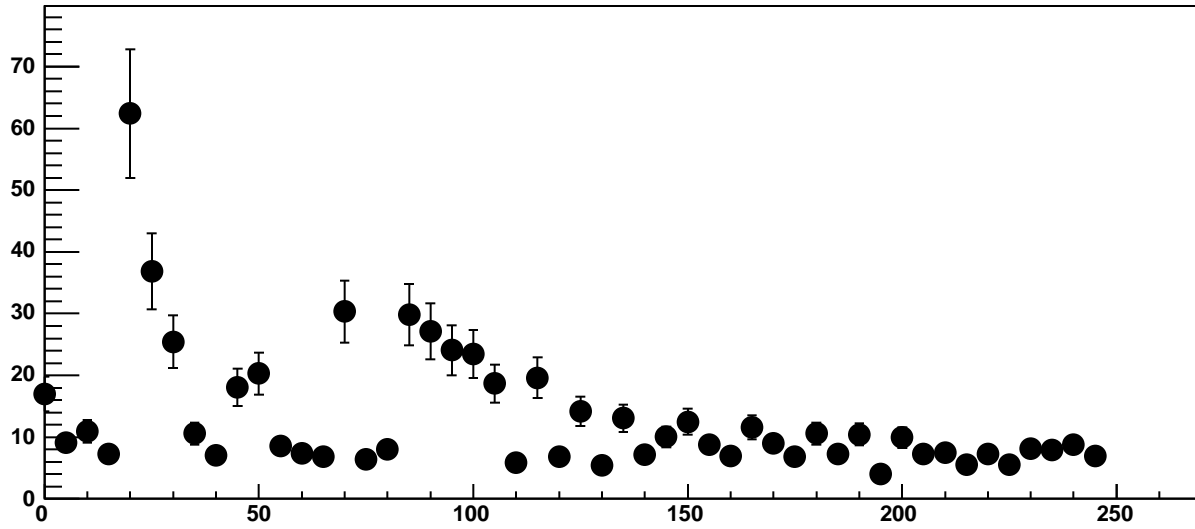


Chip 5, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold

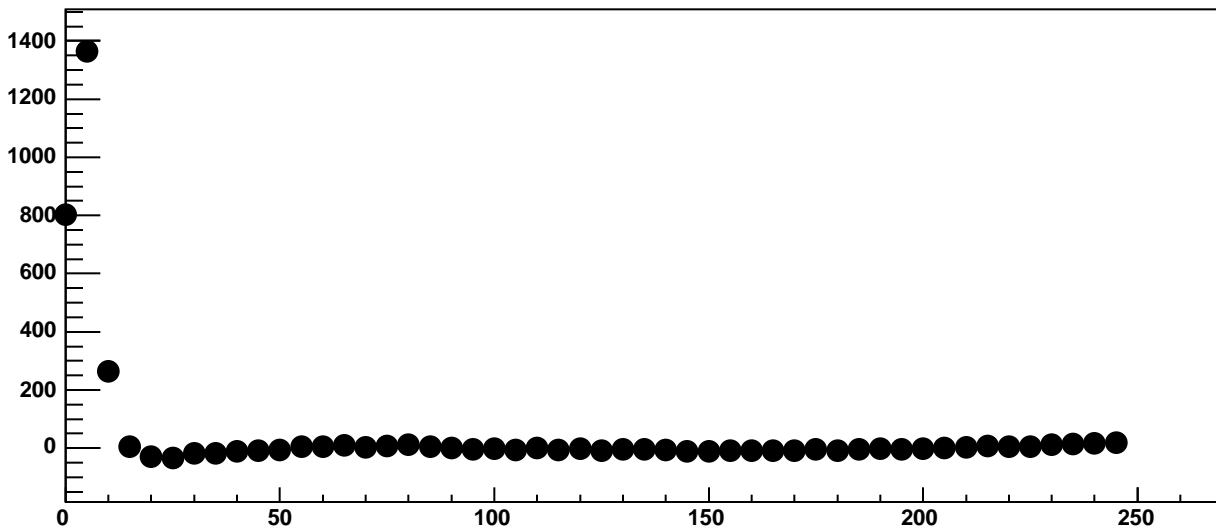


$\chi^2 / \text{ndf}$	680.6 / 42
p0	-698.1 ± 0.5386
p1	4.171 ± 0.004998
p2	1.105e+04 ± 0.9739
p3	31.31 ± 0.004703

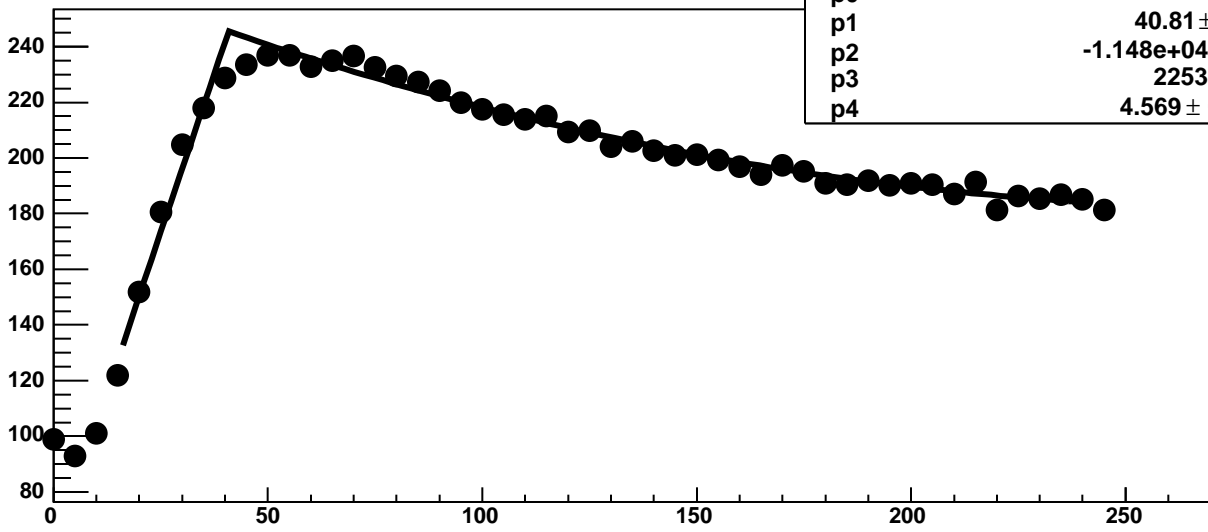
Chip 5, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold

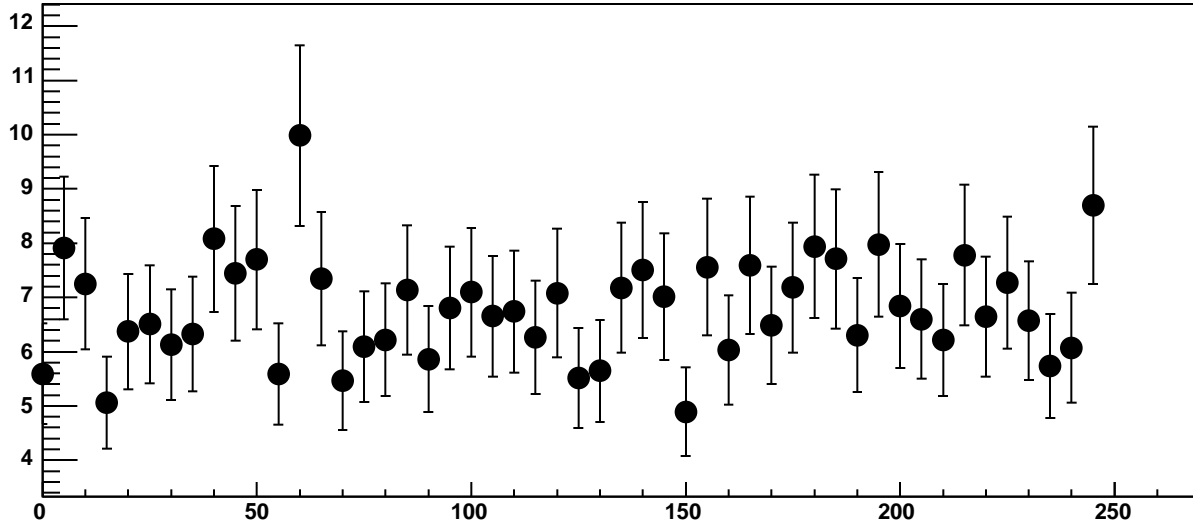


Chip 5, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

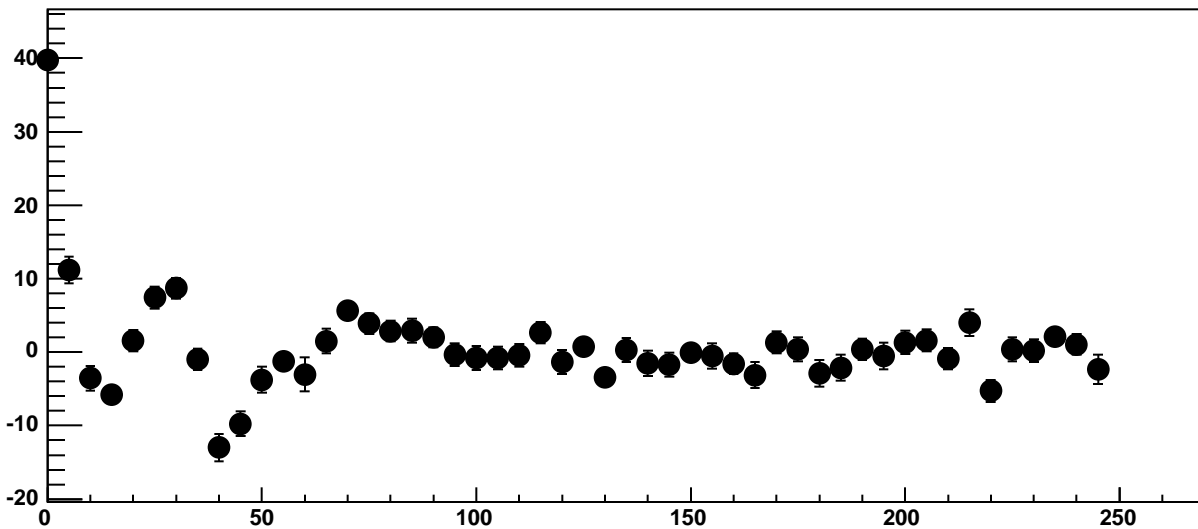


$\chi^2 / \text{ndf}$	262.4 / 41
p0	$245.4 \pm 0.6388$
p1	$40.81 \pm 0.2614$
p2	$-1.148e+04 \pm 688.6$
p3	$2253 \pm 133.6$
p4	$4.569 \pm 0.06824$

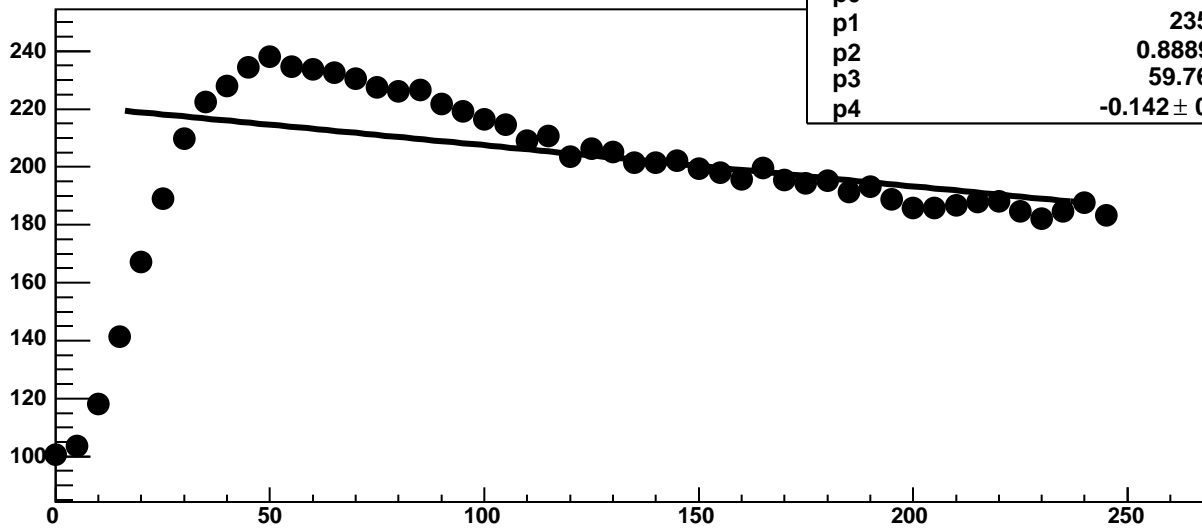
Chip 5, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

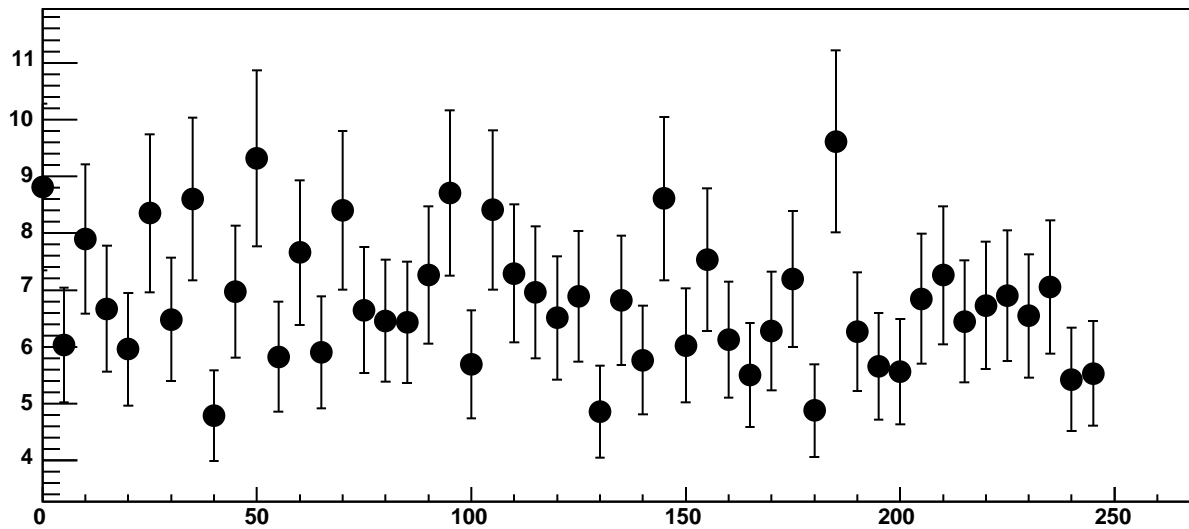


Chip 5, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

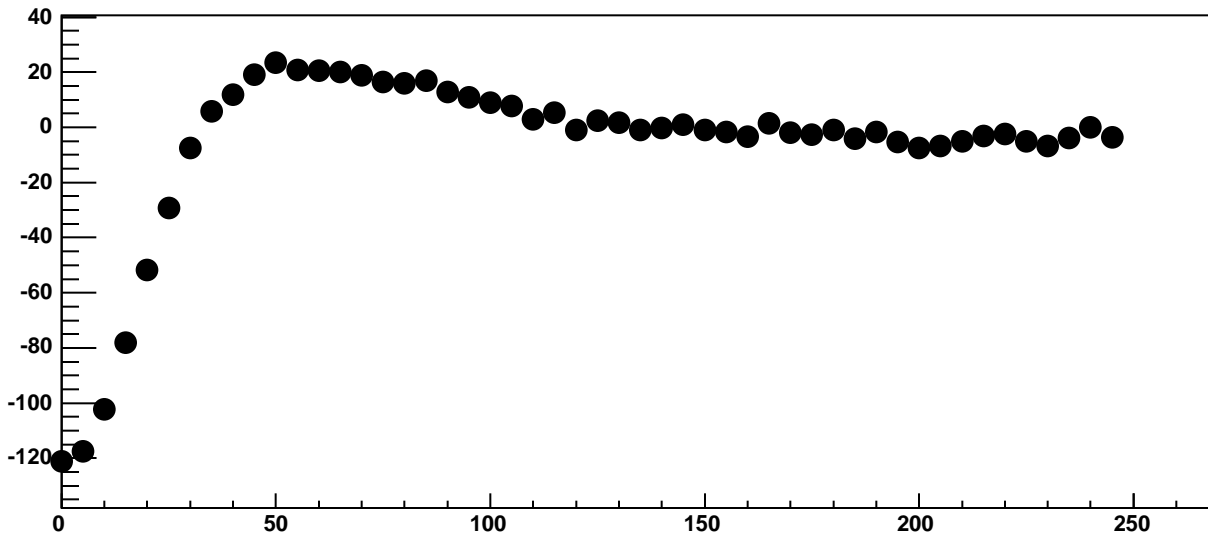


$\chi^2 / \text{ndf}$	6056 / 41
p0	$188.3 \pm 0.4974$
p1	$235 \pm 1.554$
p2	$0.8889 \pm 22.78$
p3	$59.76 \pm 1216$
p4	$-0.142 \pm 0.003489$

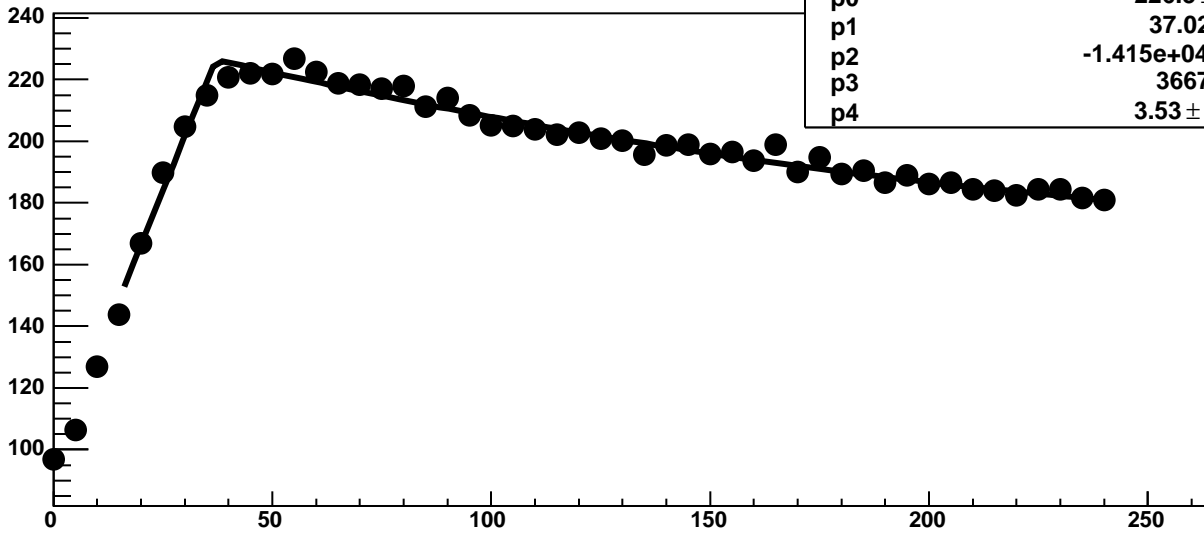
Chip 5, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



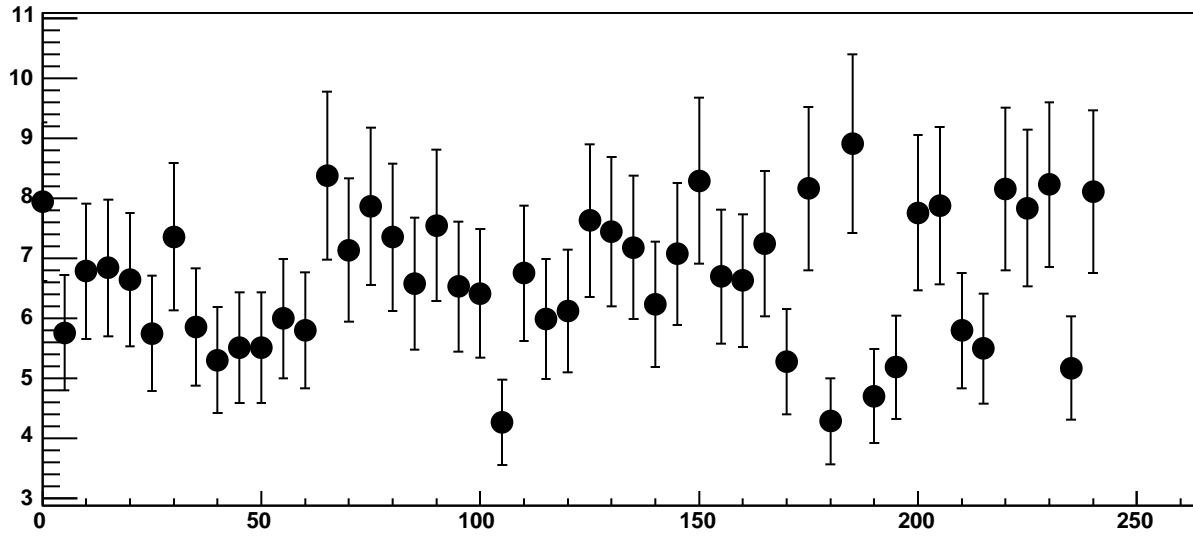
Chip 5, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold



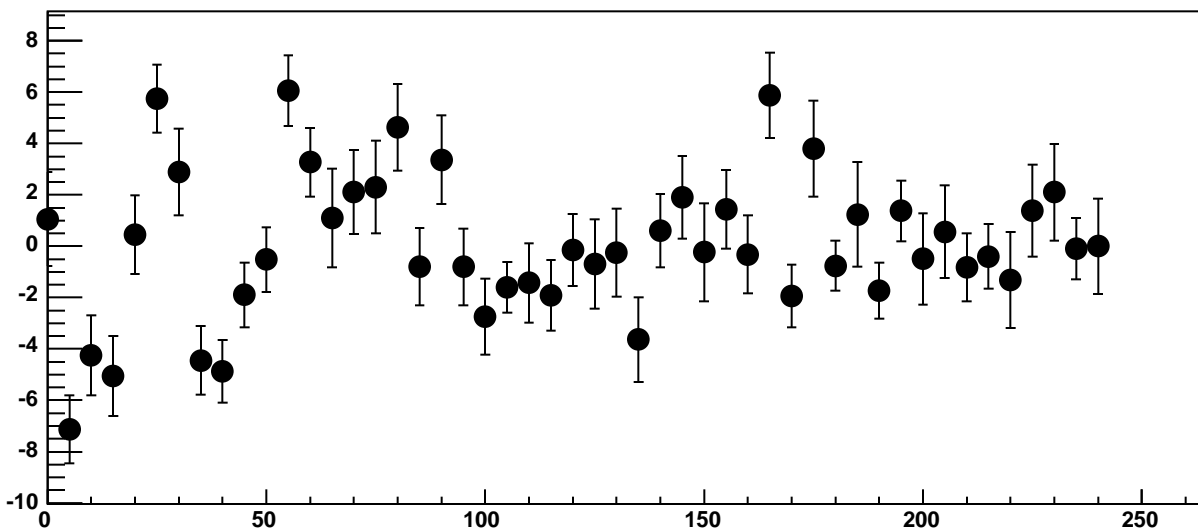
Chip 5, Channel 7, Enable 5, DAC=4000, ADC Mean vs Hold



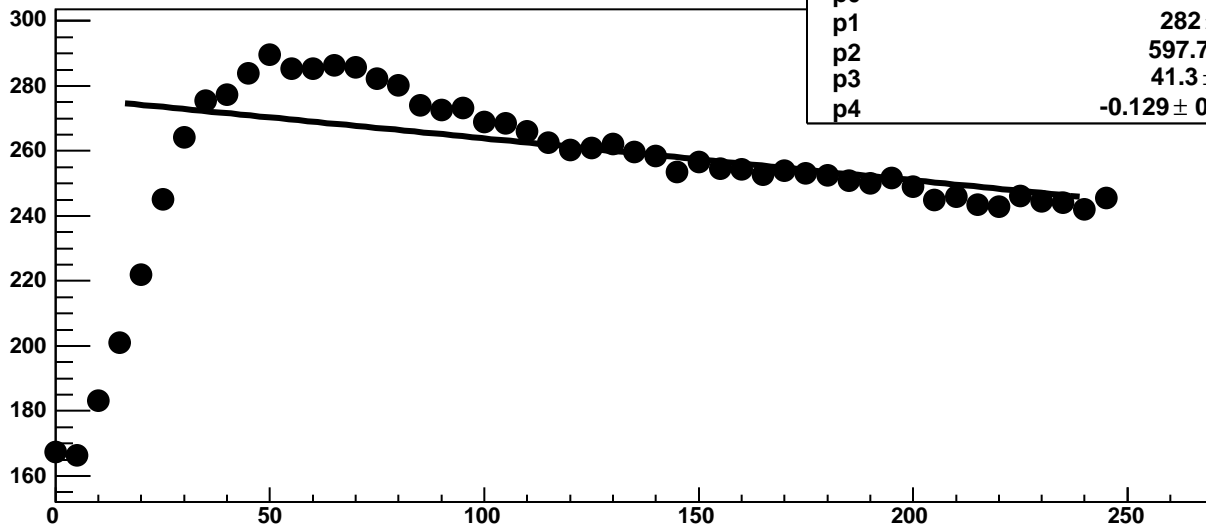
Chip 5, Channel 7, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 7, Enable 5, DAC=4000, ADC Residuals vs Hold

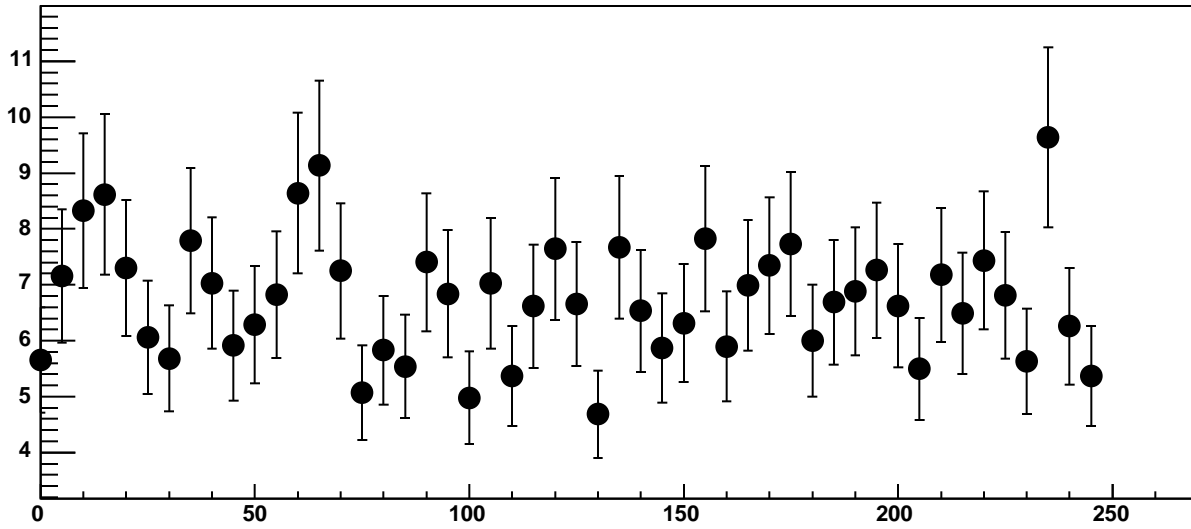


Chip 5, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

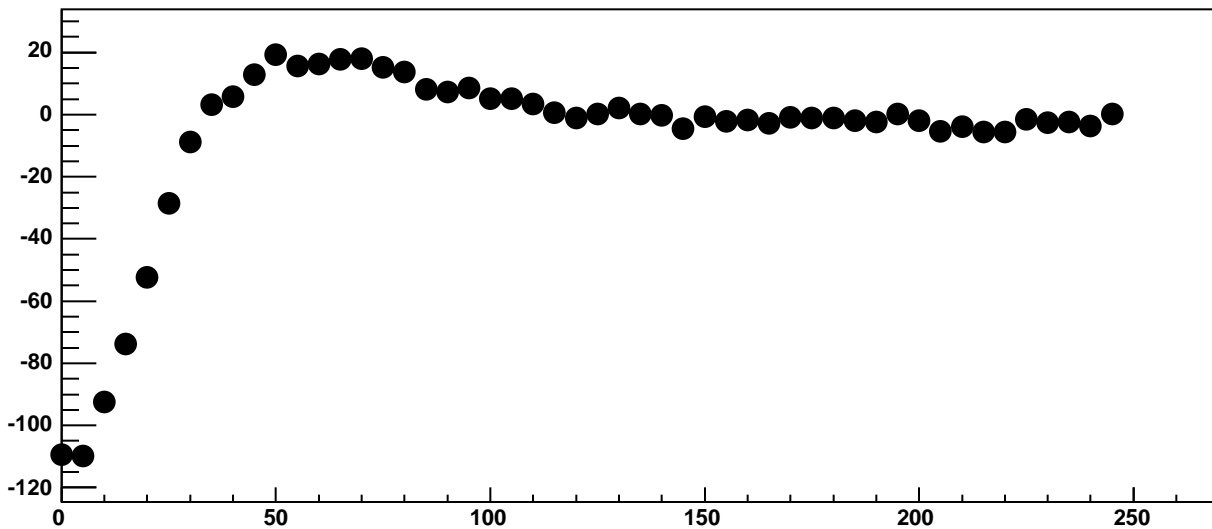


$\chi^2 / \text{ndf}$	3970 / 41
p0	$240.4 \pm 0.6024$
p1	$282 \pm 0.6251$
p2	$597.7 \pm 8.817$
p3	$41.3 \pm 0.4008$
p4	$-0.129 \pm 0.003424$

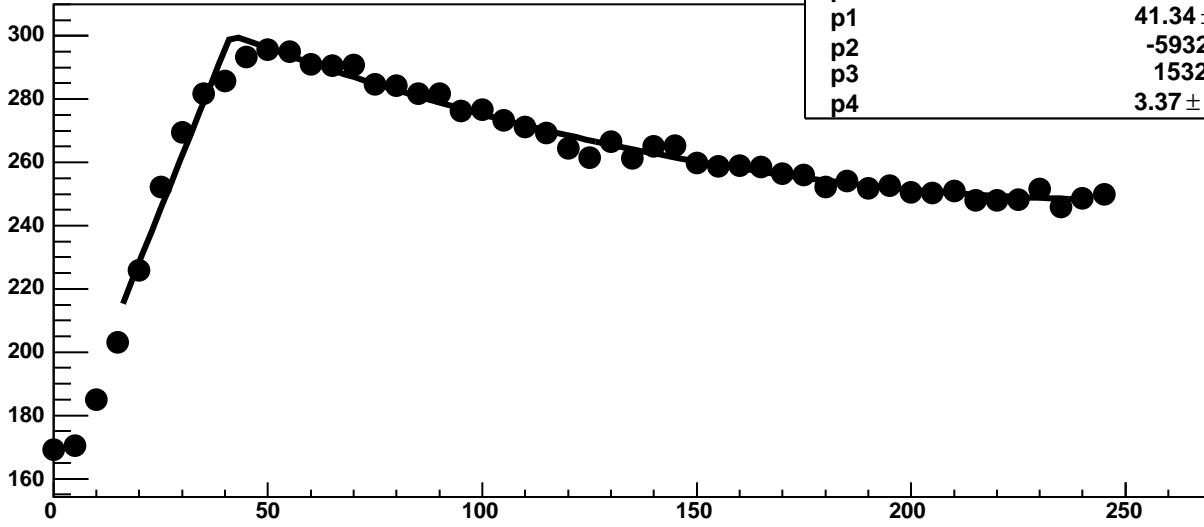
Chip 5, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

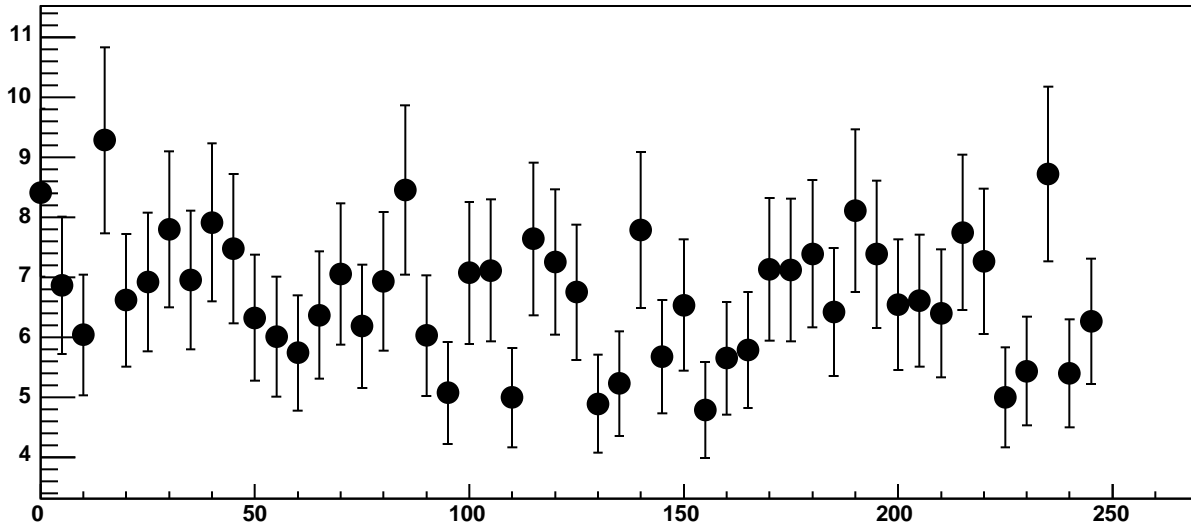


Chip 5, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

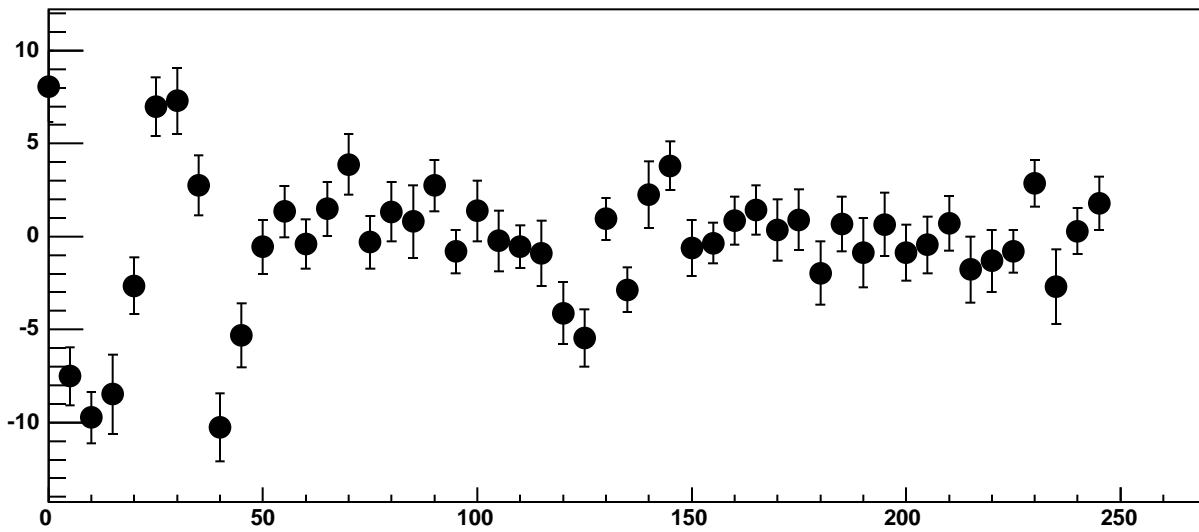


$\chi^2 / \text{ndf}$	162.7 / 41
p0	$300.4 \pm 0.6578$
p1	$41.34 \pm 0.4009$
p2	$-5932 \pm 446.3$
p3	$1532 \pm 103.9$
p4	$3.37 \pm 0.08743$

Chip 5, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold

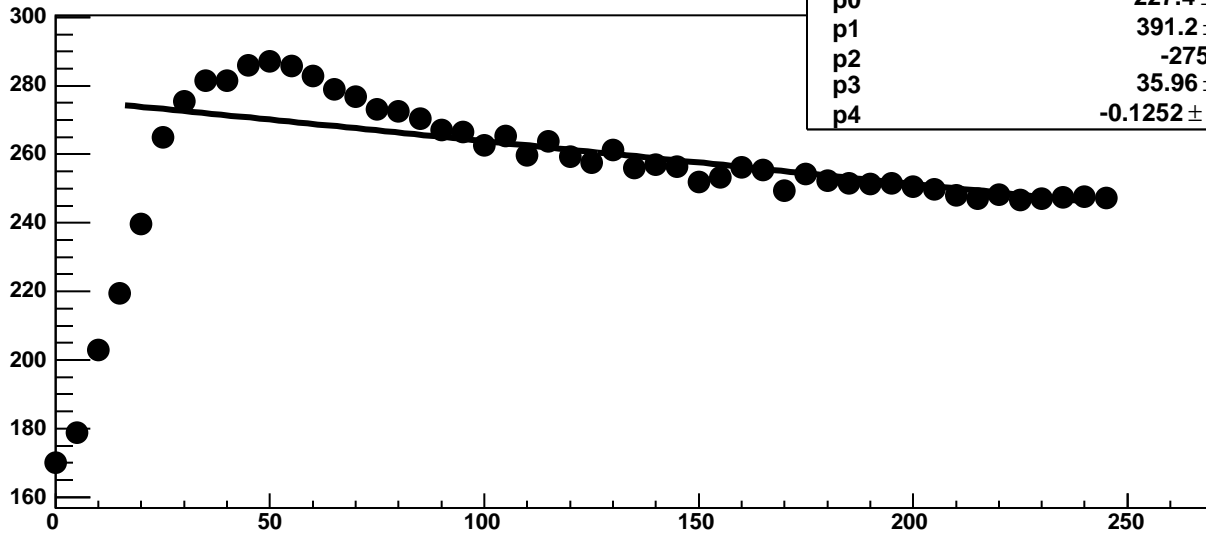


Chip 5, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

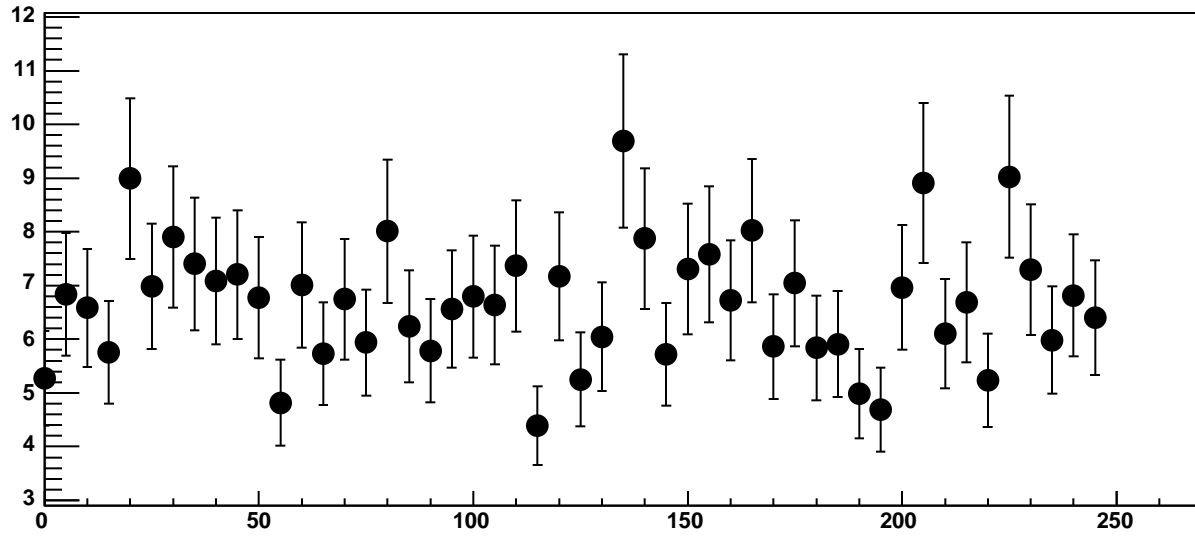




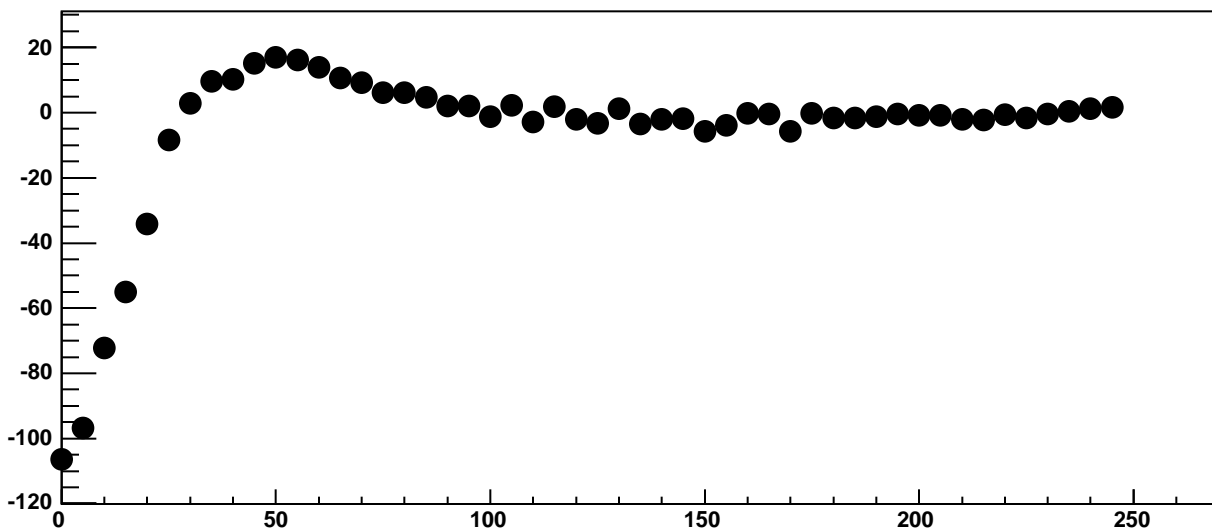
Chip 5, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold



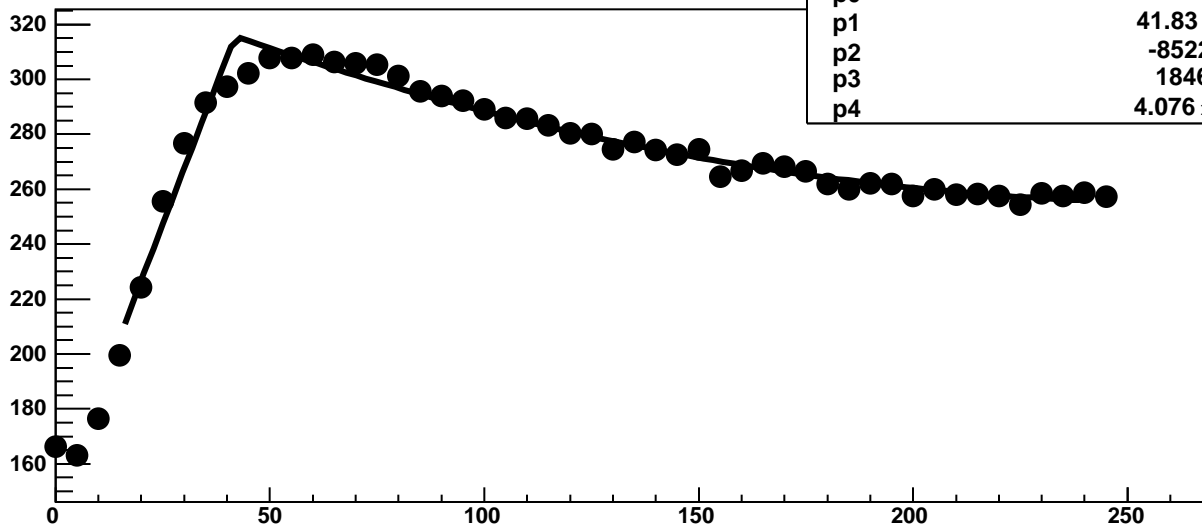
Chip 5, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold

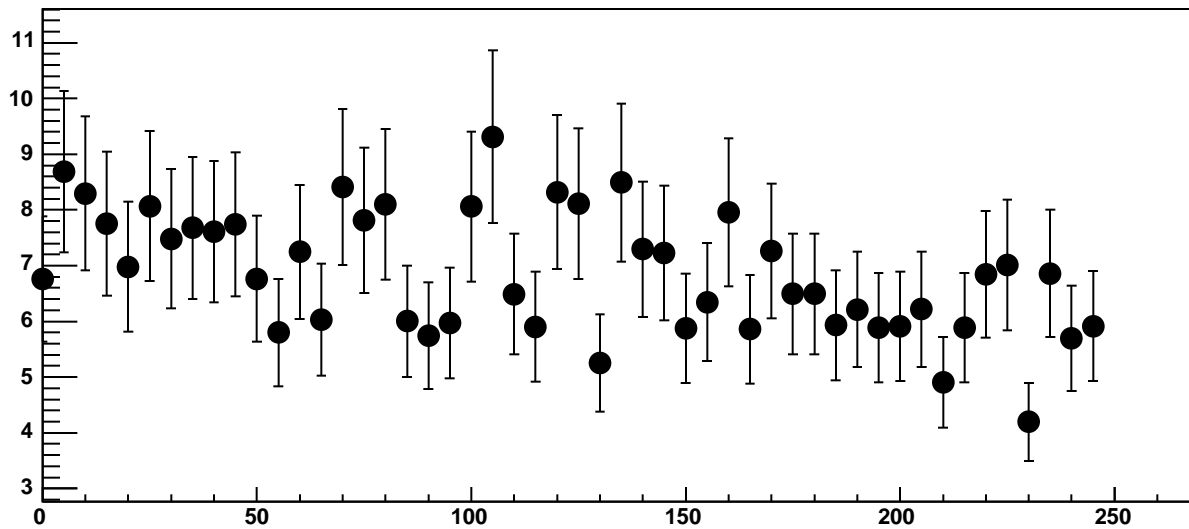


Chip 5, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

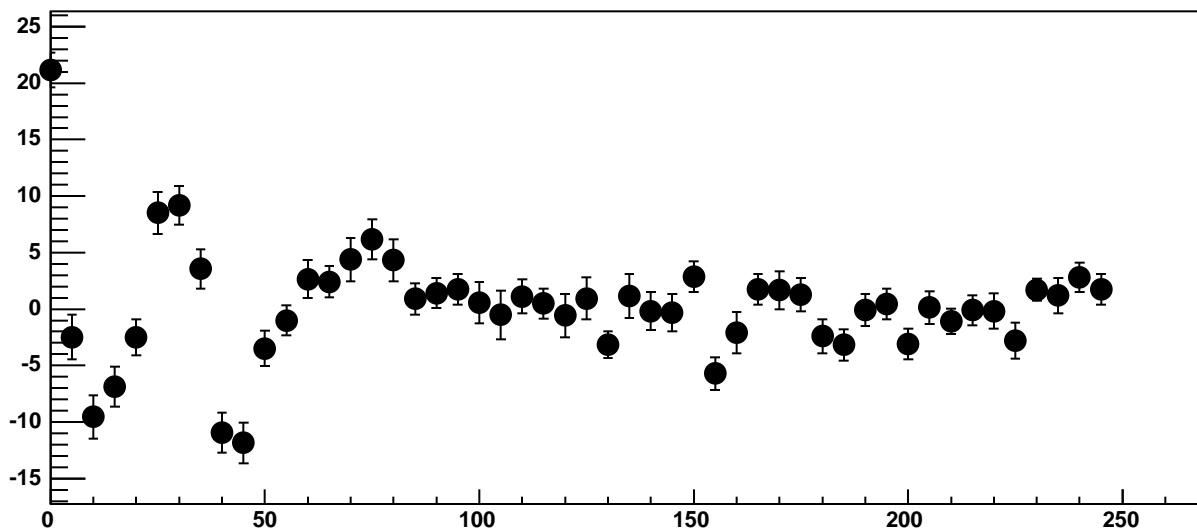


$\chi^2 / \text{ndf}$	250.7 / 41
p0	$315.7 \pm 0.6429$
p1	$41.83 \pm 0.3223$
p2	$-8522 \pm 509.3$
p3	$1846 \pm 103.8$
p4	$4.076 \pm 0.0815$

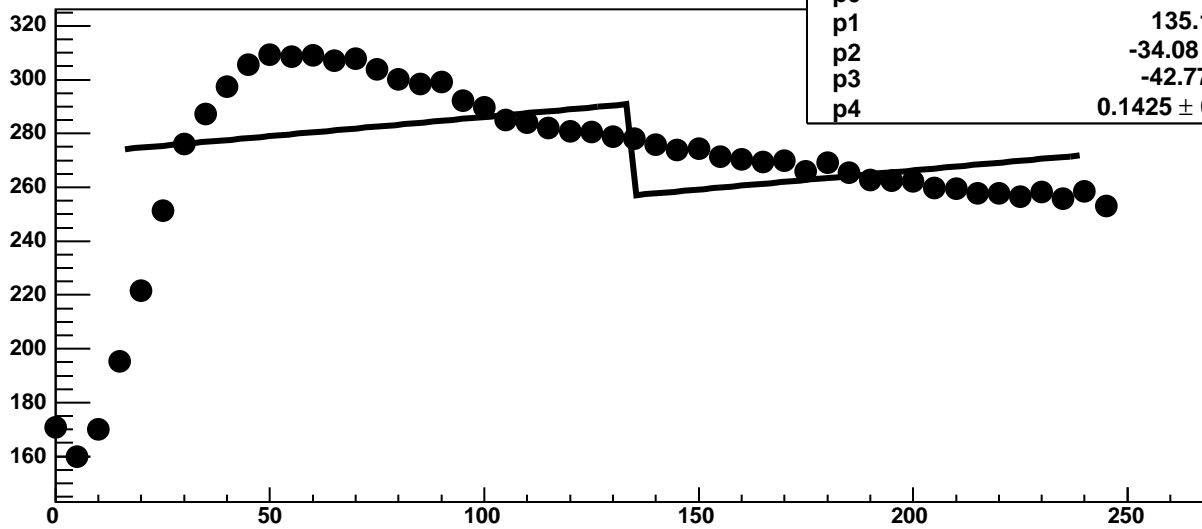
Chip 5, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



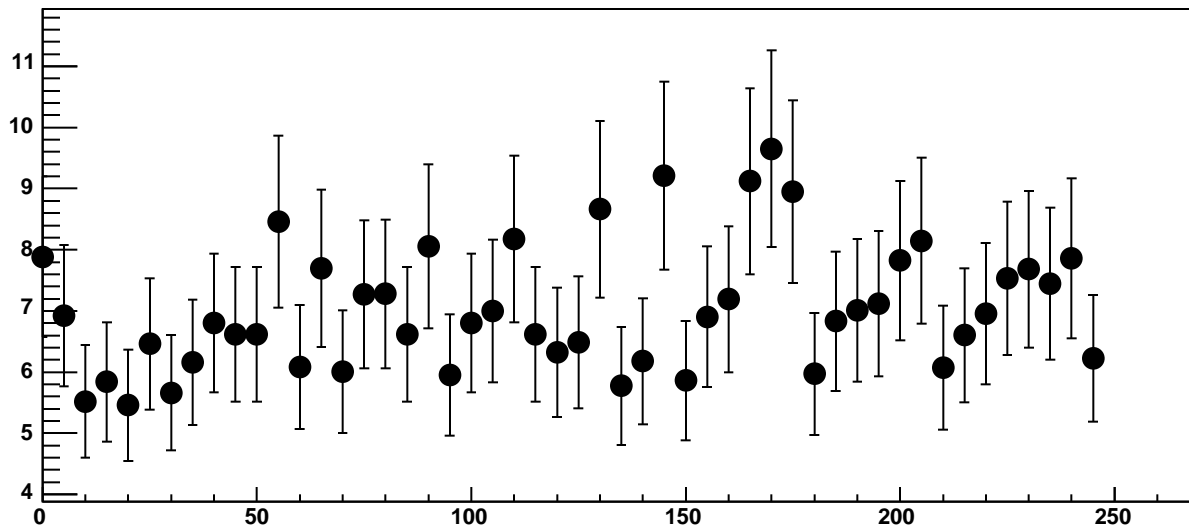
Chip 5, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold



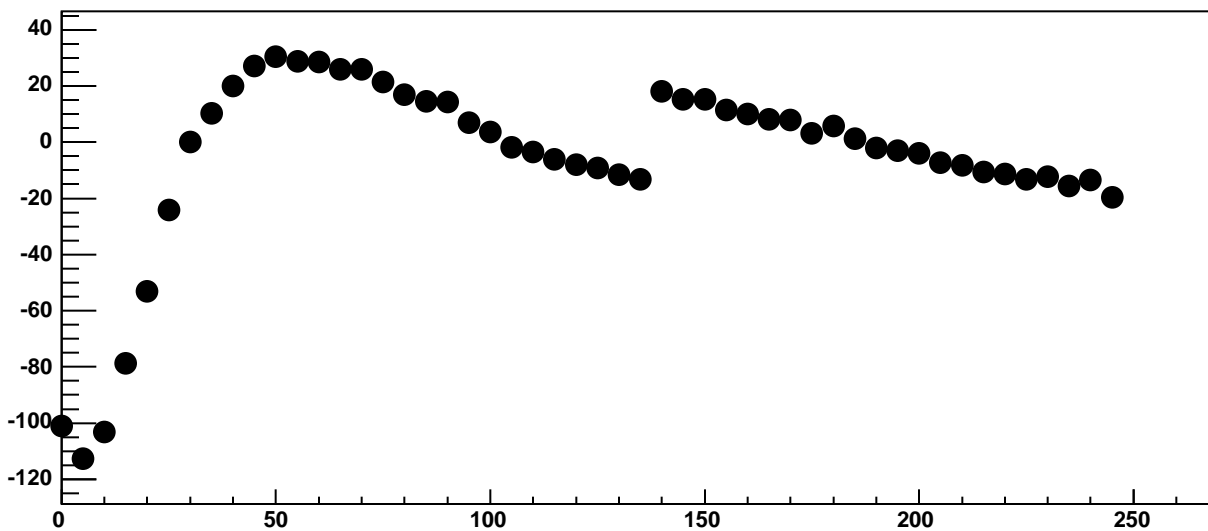
Chip 5, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold



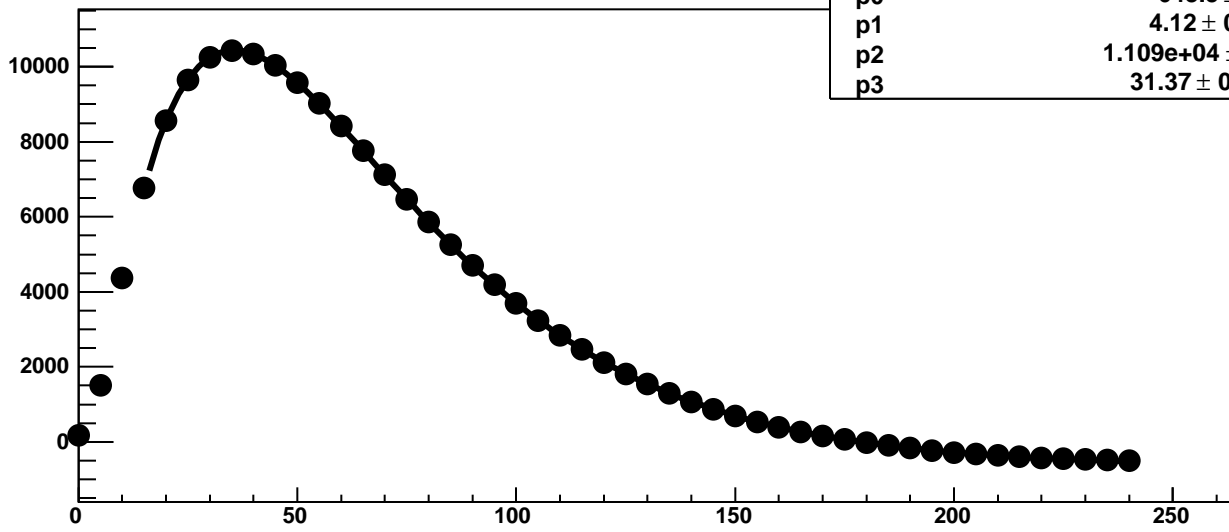
Chip 5, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

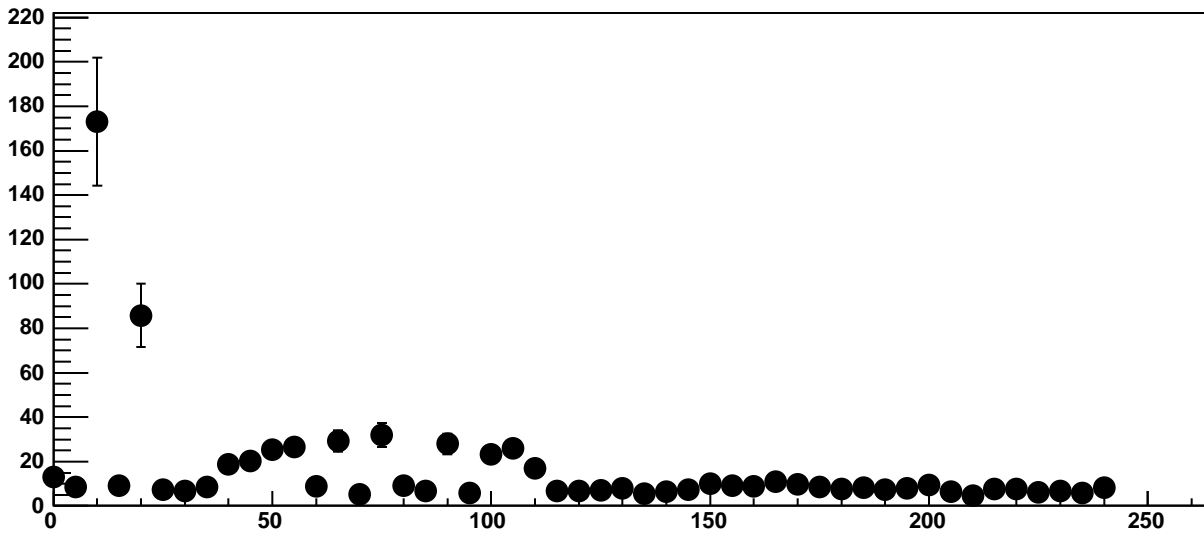


Chip 5, Channel 8, Enable 5!, DAC=4000, ADC Mean vs Hold

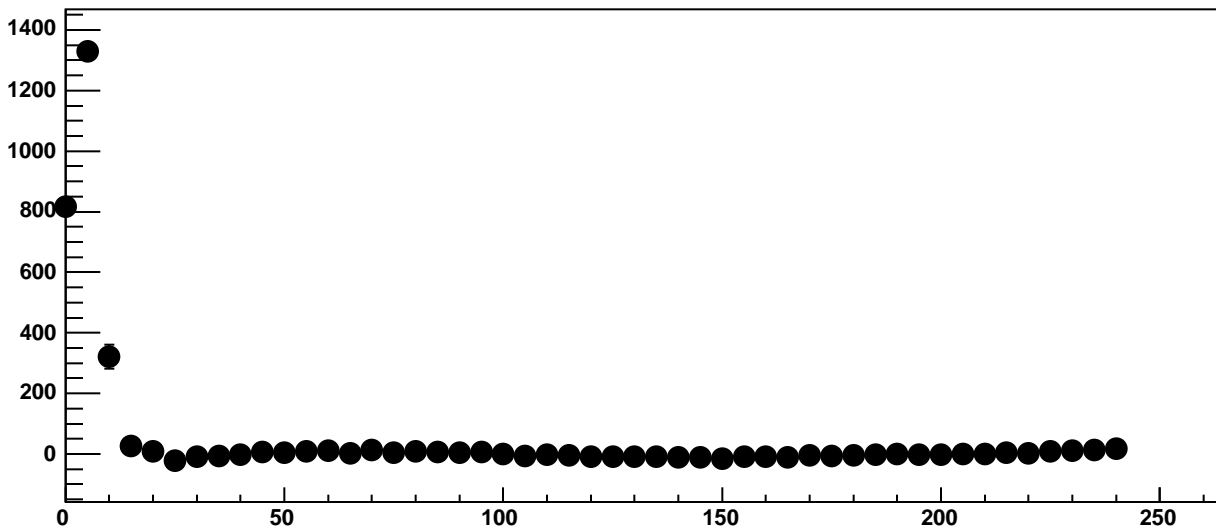


$\chi^2 / \text{ndf}$	1196 / 42
p0	-643.5 ± 0.5174
p1	4.12 ± 0.005901
p2	1.109e+04 ± 0.9093
p3	31.37 ± 0.004367

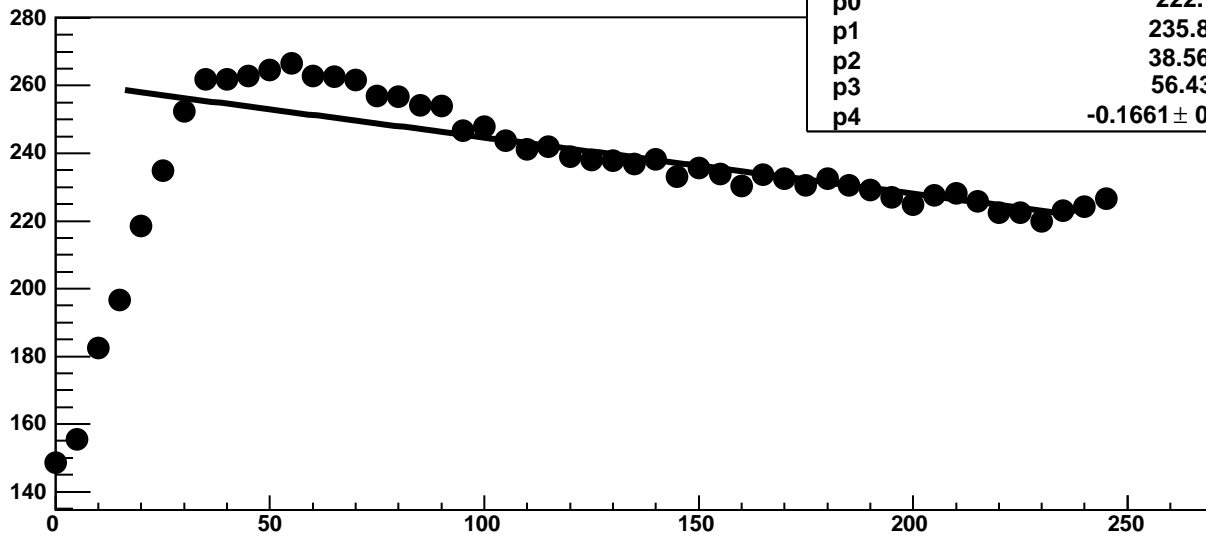
Chip 5, Channel 8, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 8, Enable 5!, DAC=4000, ADC Residuals vs Hold



Chip 5, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold



$\chi^2 / \text{ndf}$

2019 / 41

p0  $222.1 \pm 2.411$

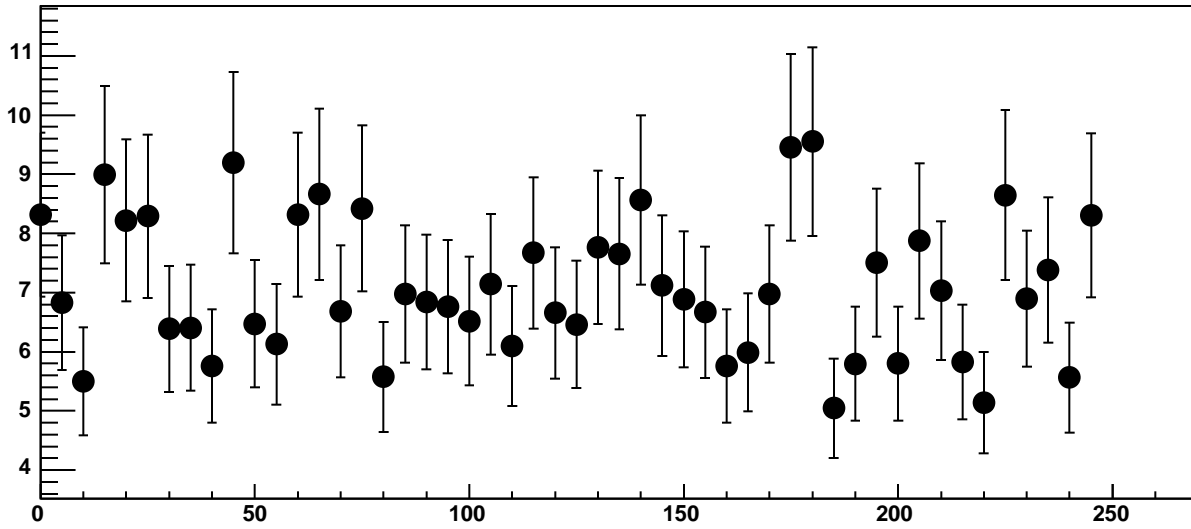
p1  $235.8 \pm 14.63$

p2  $38.56 \pm 204.3$

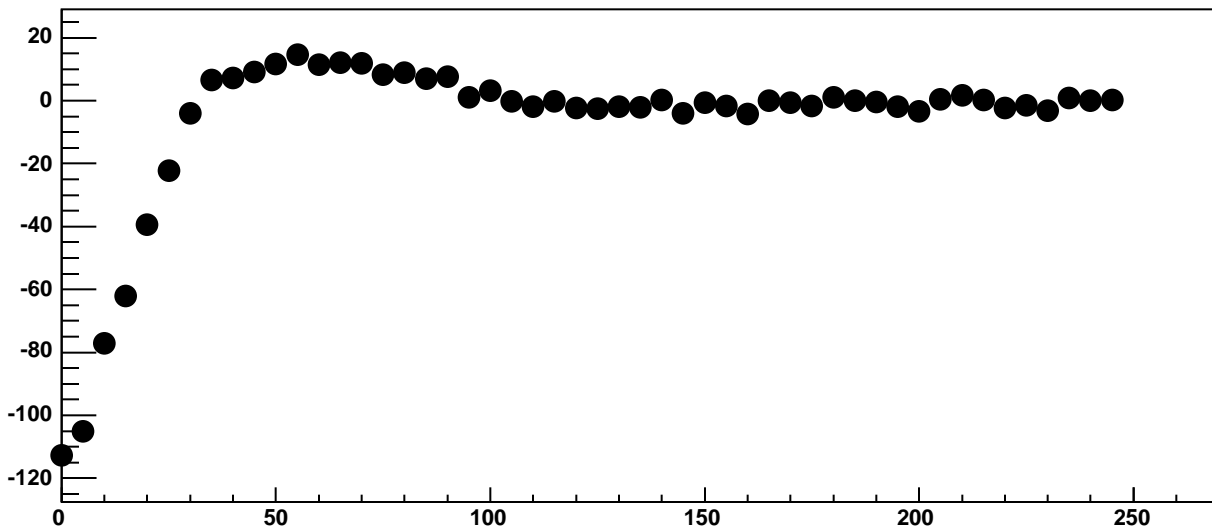
p3  $56.43 \pm 169.1$

p4  $-0.1661 \pm 0.003588$

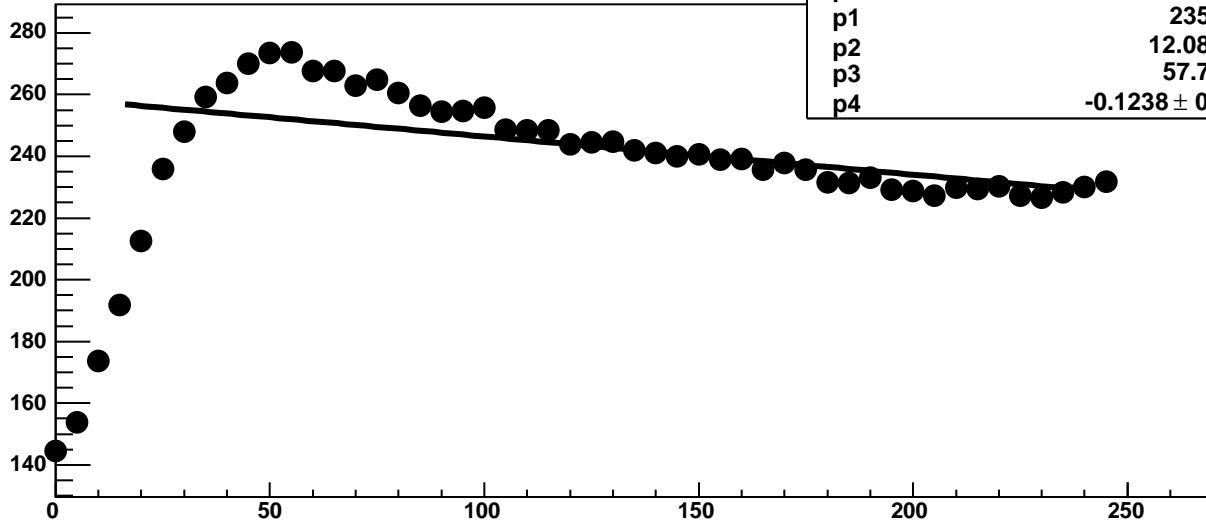
Chip 5, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold

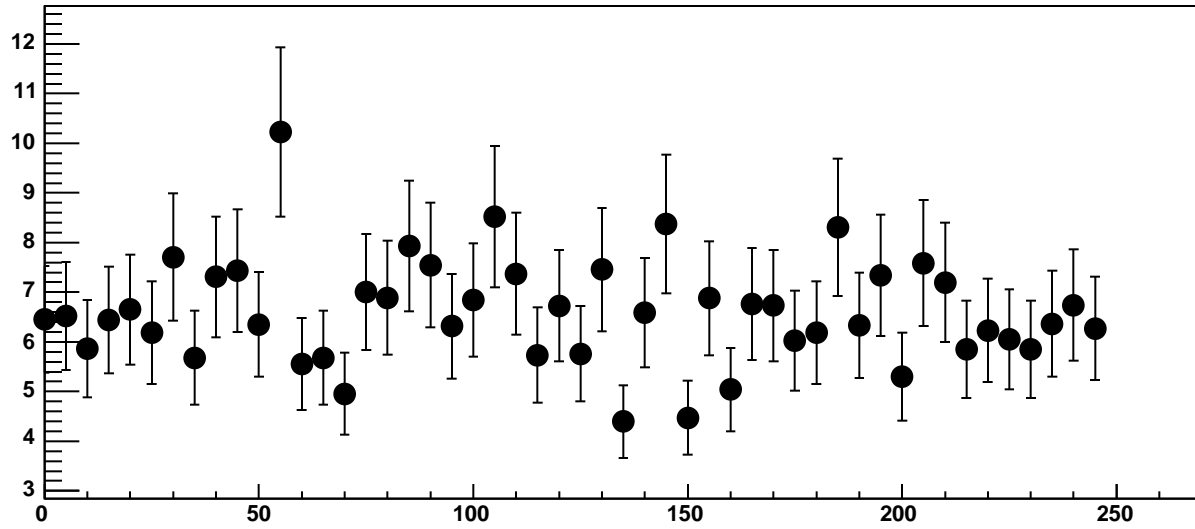


Chip 5, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

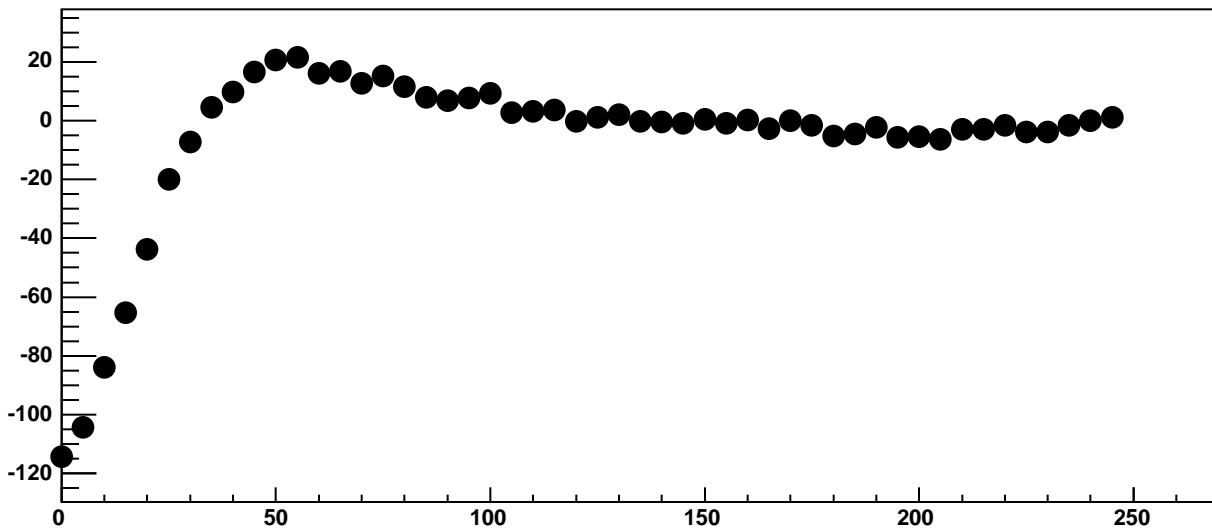


$\chi^2 / \text{ndf}$	4224 / 41
p0	$229.7 \pm 1.721$
p1	$235 \pm 14.28$
p2	$12.08 \pm 230.2$
p3	$57.7 \pm 1028$
p4	$-0.1238 \pm 0.003258$

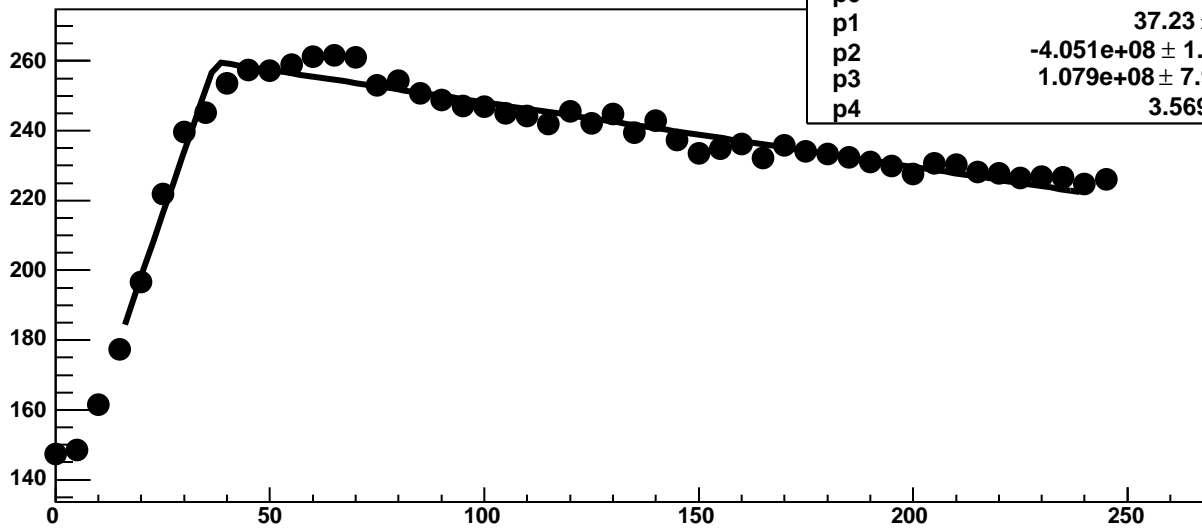
Chip 5, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

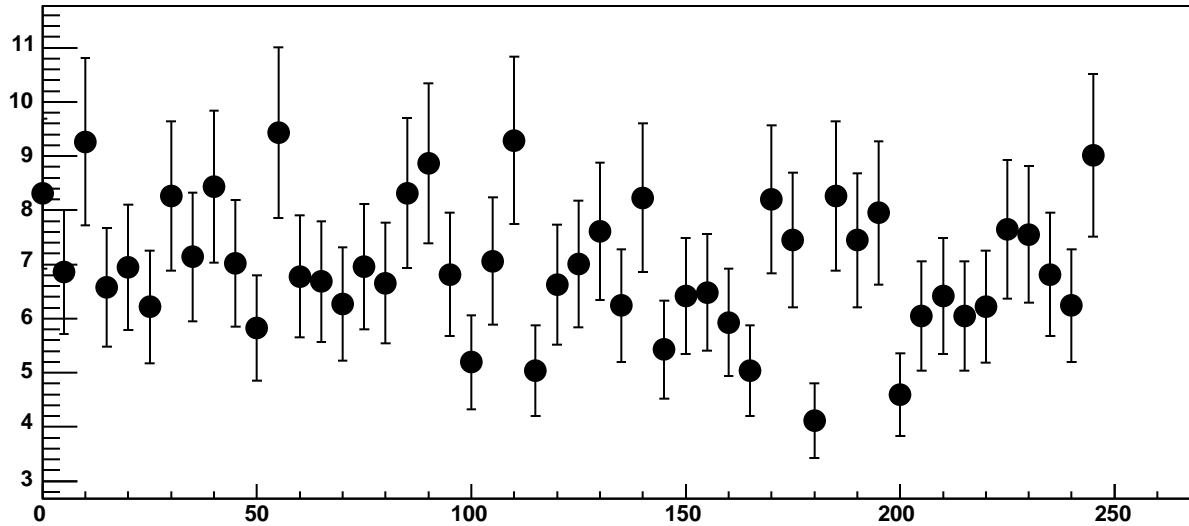


Chip 5, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

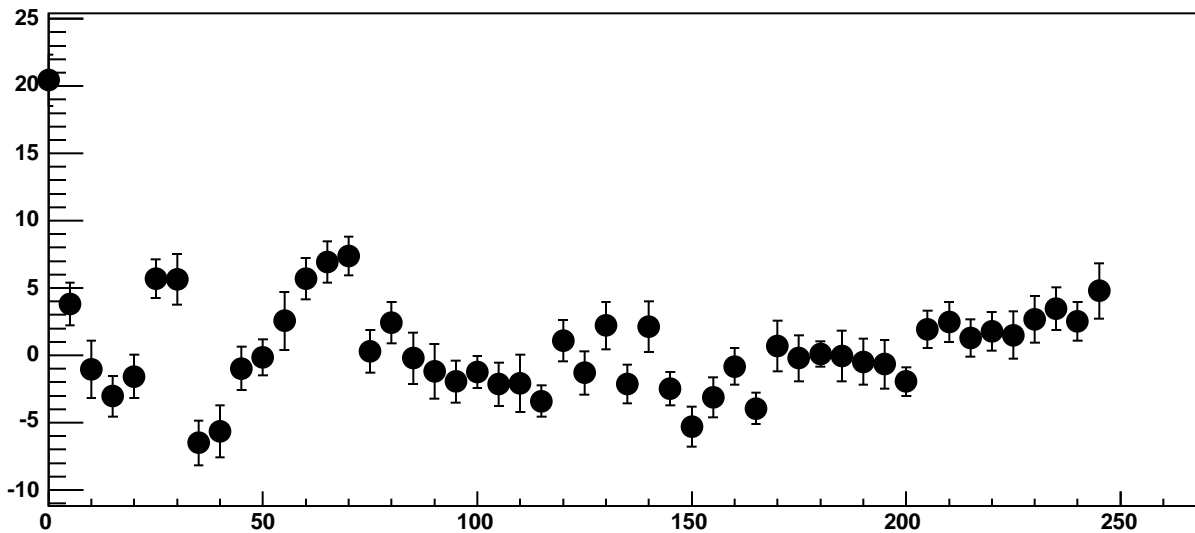


$\chi^2 / \text{ndf}$	194.9 / 41
p0	$259.7 \pm 0.4768$
p1	$37.23 \pm 0.4204$
p2	$-4.051\text{e}+08 \pm 1.097\text{e}+07$
p3	$1.079\text{e}+08 \pm 7.919\text{e}+04$
p4	$3.569 \pm 0.102$

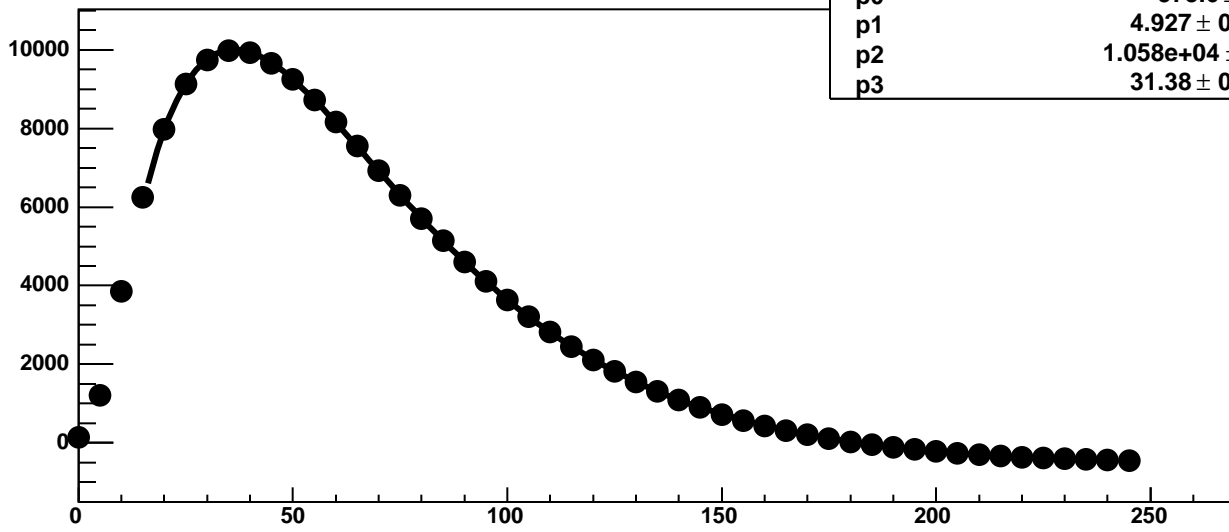
Chip 5, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold

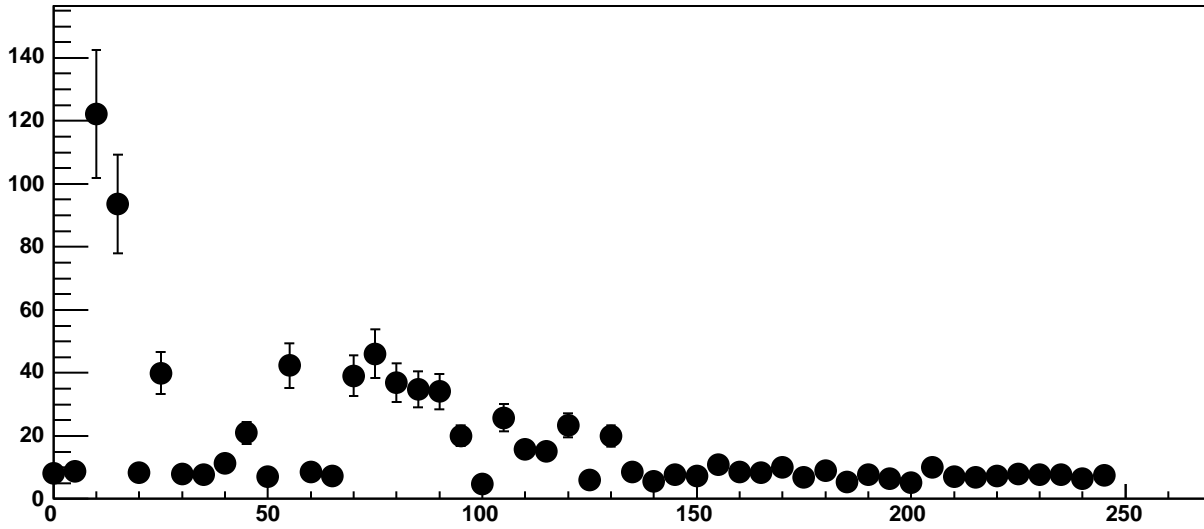


Chip 5, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold

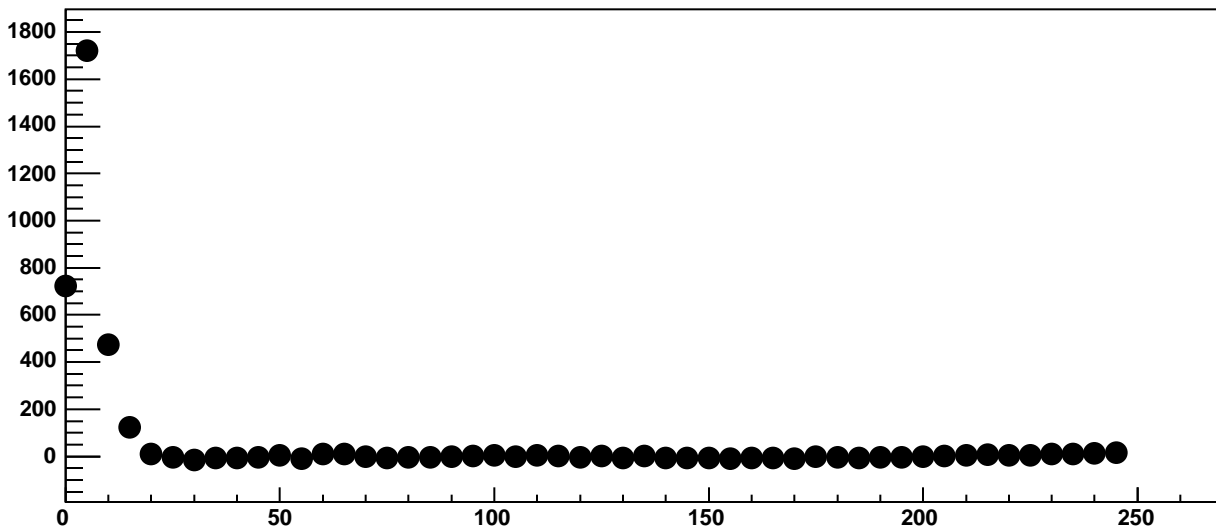


$\chi^2 / \text{ndf}$	584.9 / 42
p0	$-575.6 \pm 0.5666$
p1	$4.927 \pm 0.008155$
p2	$1.058e+04 \pm 0.9068$
p3	$31.38 \pm 0.005557$

Chip 5, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold

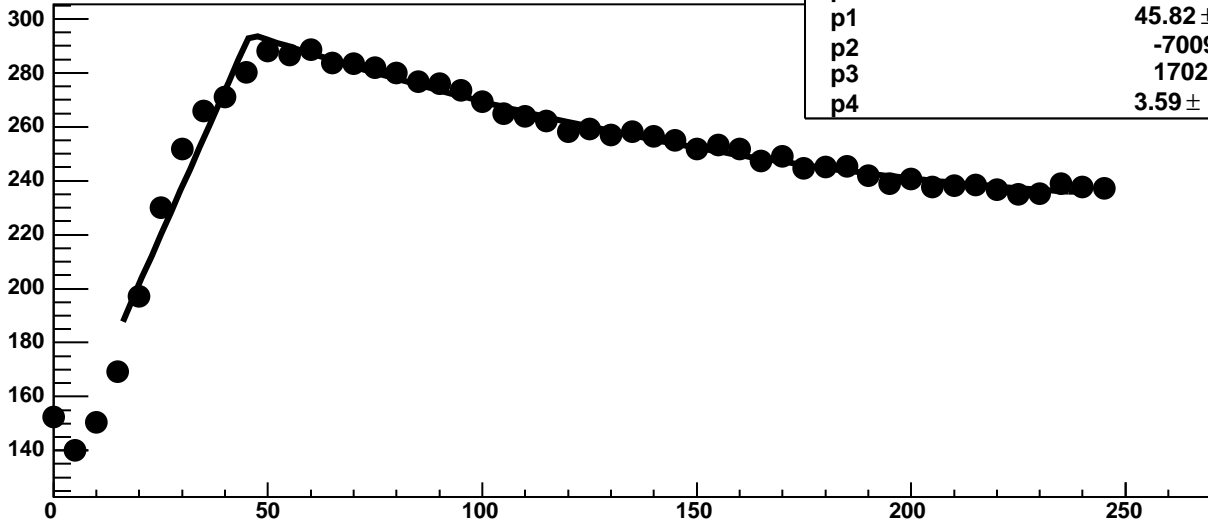


Chip 5, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold



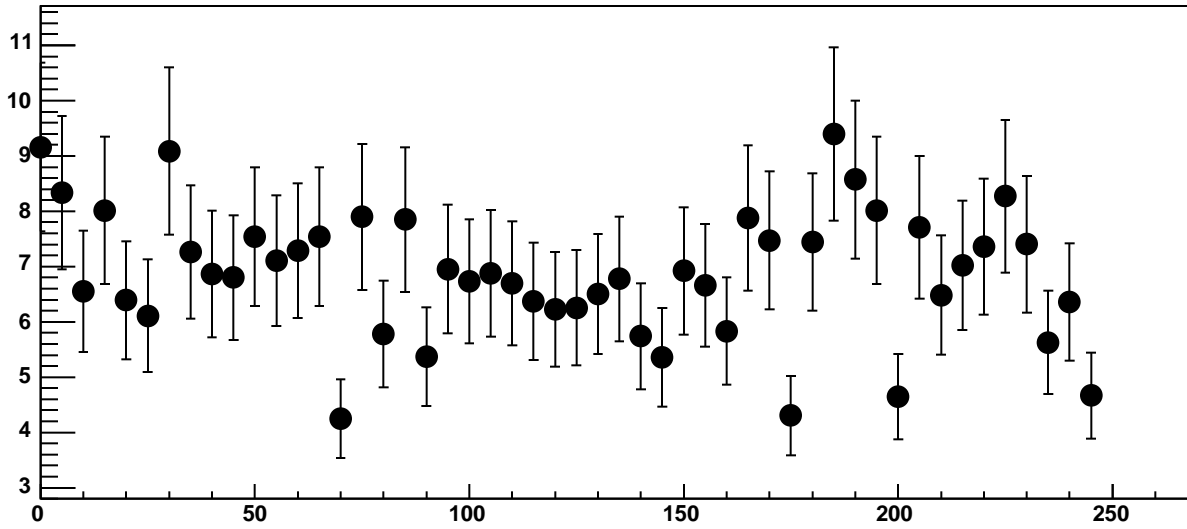


Chip 5, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

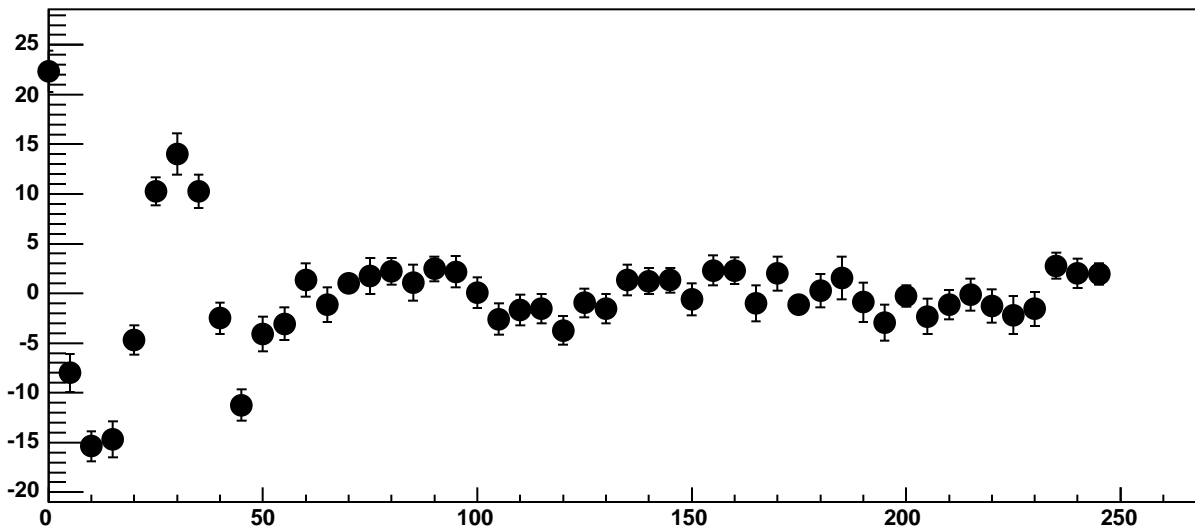


$\chi^2 / \text{ndf}$	325.8 / 41
p0	$294.5 \pm 0.6271$
p1	$45.82 \pm 0.3113$
p2	$-7009 \pm 435$
p3	$1702 \pm 104.4$
p4	$3.59 \pm 0.06042$

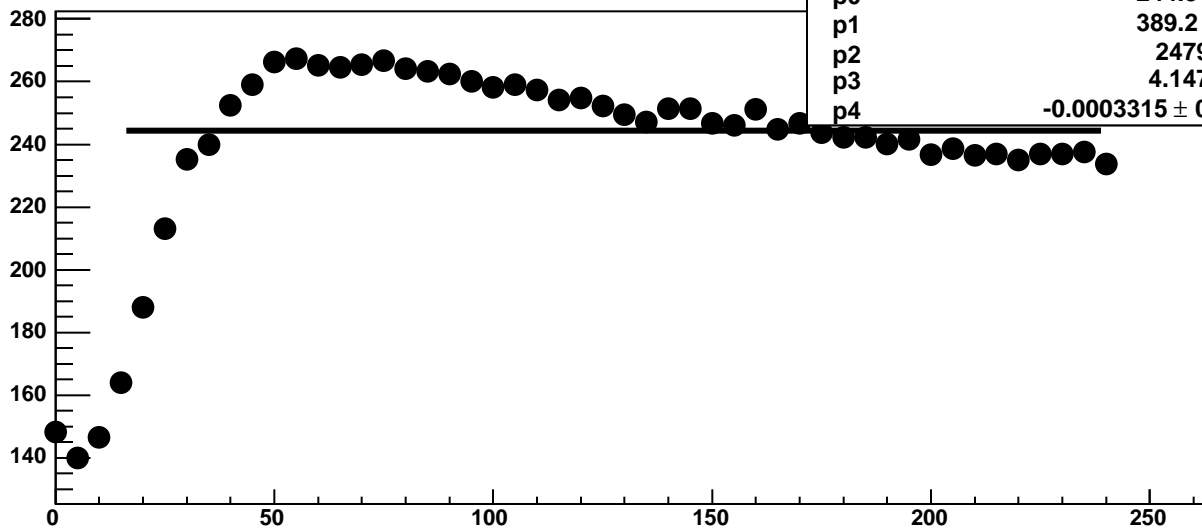
Chip 5, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold



Chip 5, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold



$\chi^2 / \text{ndf}$

7033 / 41

p0  $244.3 \pm 0.8469$

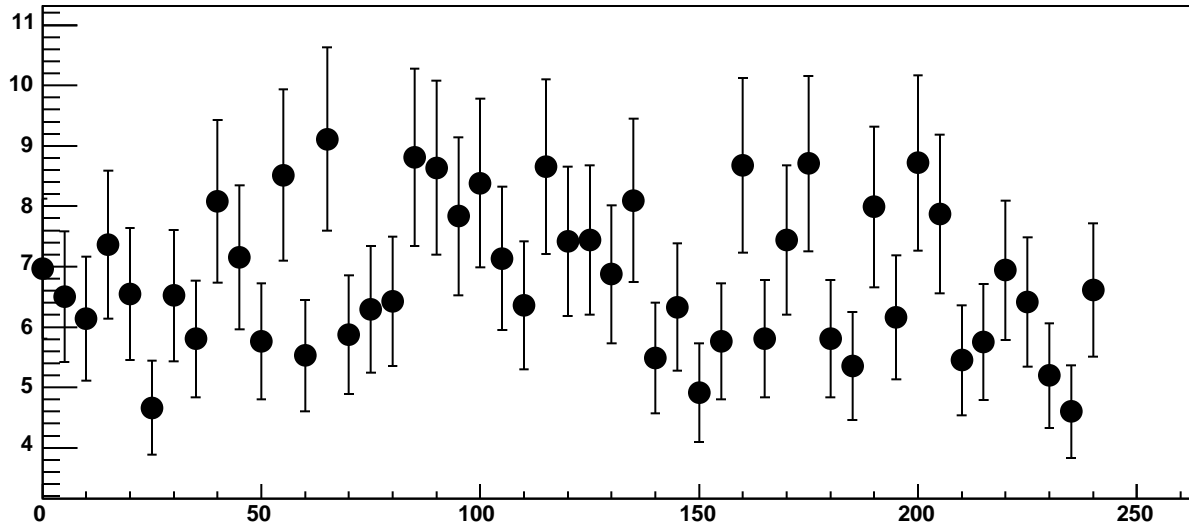
p1  $389.2 \pm 0.5408$

p2  $2479 \pm 22.42$

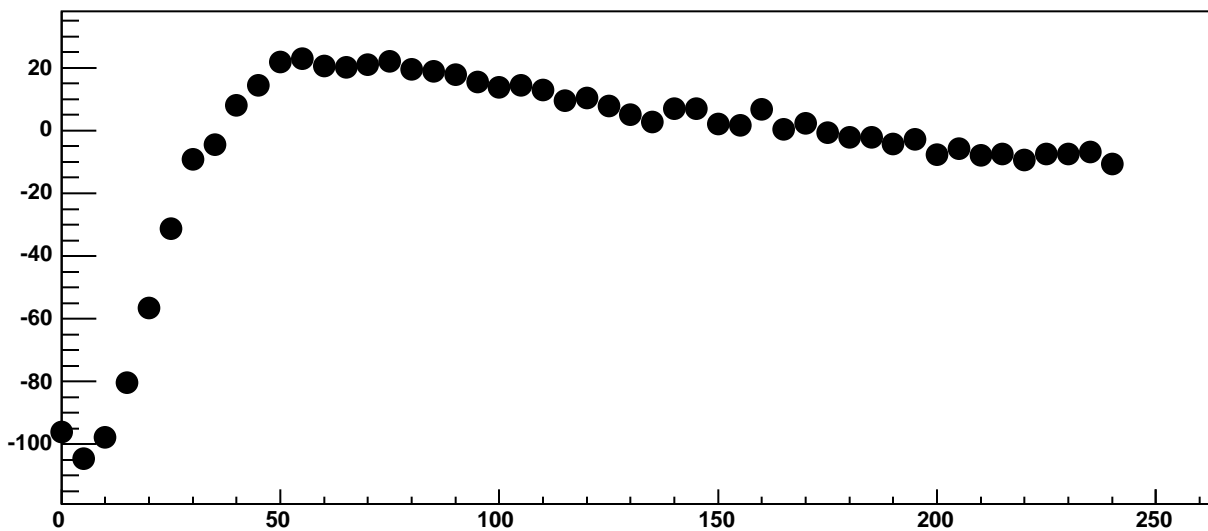
p3  $4.147 \pm 0.462$

p4  $-0.0003315 \pm 0.003174$

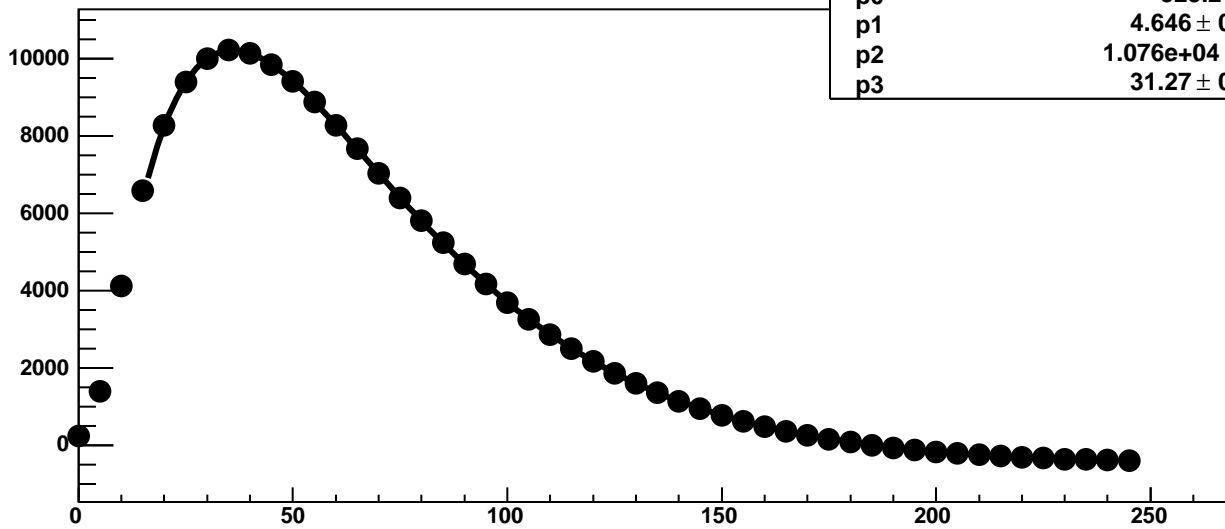
Chip 5, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold

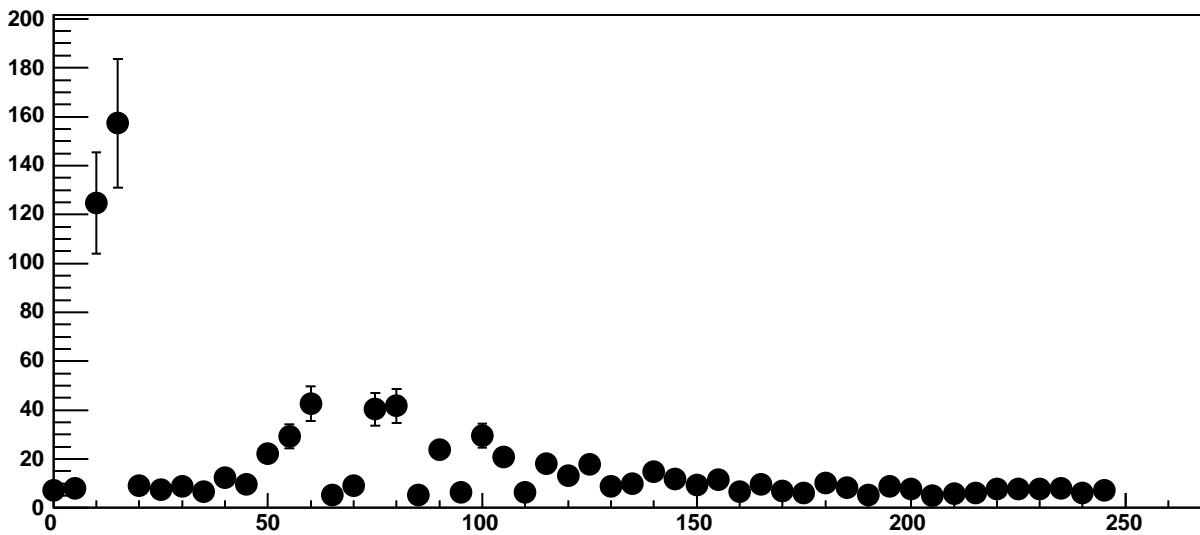


Chip 5, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold

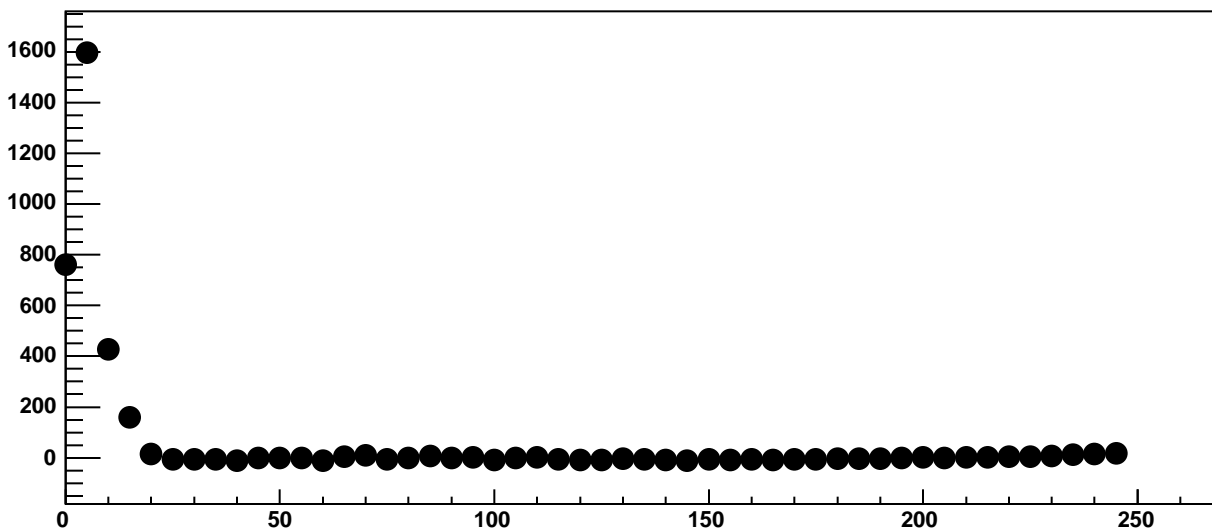


$\chi^2 / \text{ndf}$	573.1 / 42
p0	-523.2 ± 0.5319
p1	4.646 ± 0.008587
p2	1.076e+04 ± 0.8597
p3	31.27 ± 0.005606

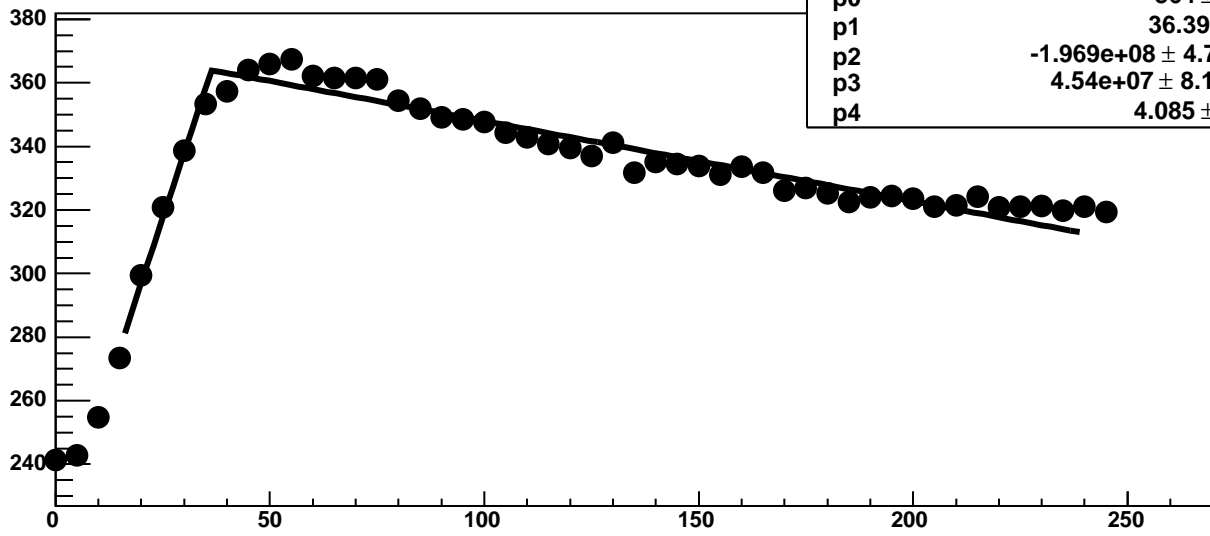
Chip 5, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold

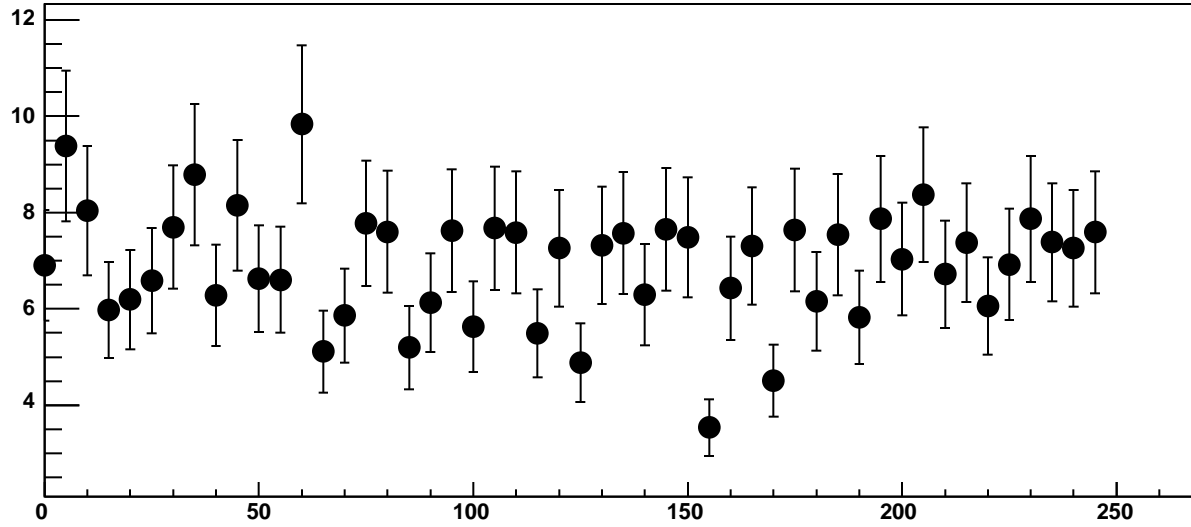


Chip 5, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

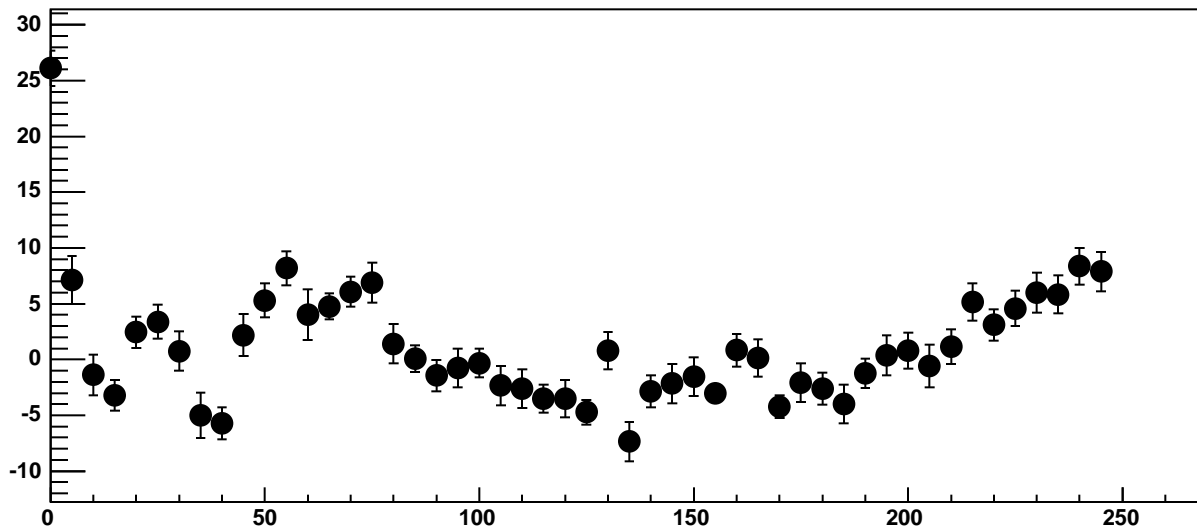


$\chi^2 / \text{ndf}$	306.7 / 41
p0	$364 \pm 0.4606$
p1	$36.39 \pm 0.373$
p2	$-1.969\text{e}+08 \pm 4.732\text{e}+06$
p3	$4.54\text{e}+07 \pm 8.187\text{e}+04$
p4	$4.085 \pm 0.1044$

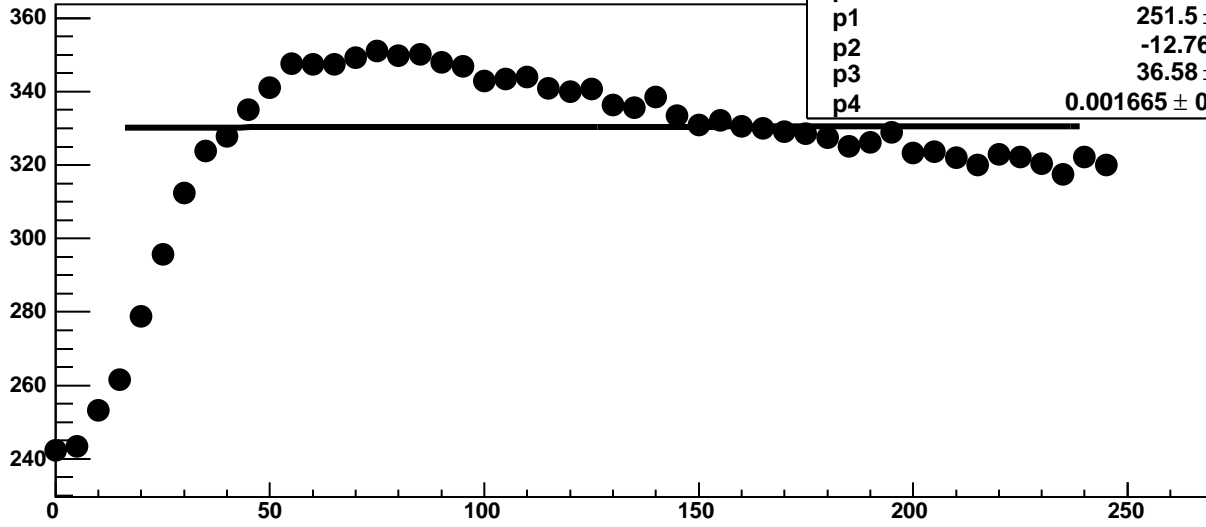
Chip 5, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

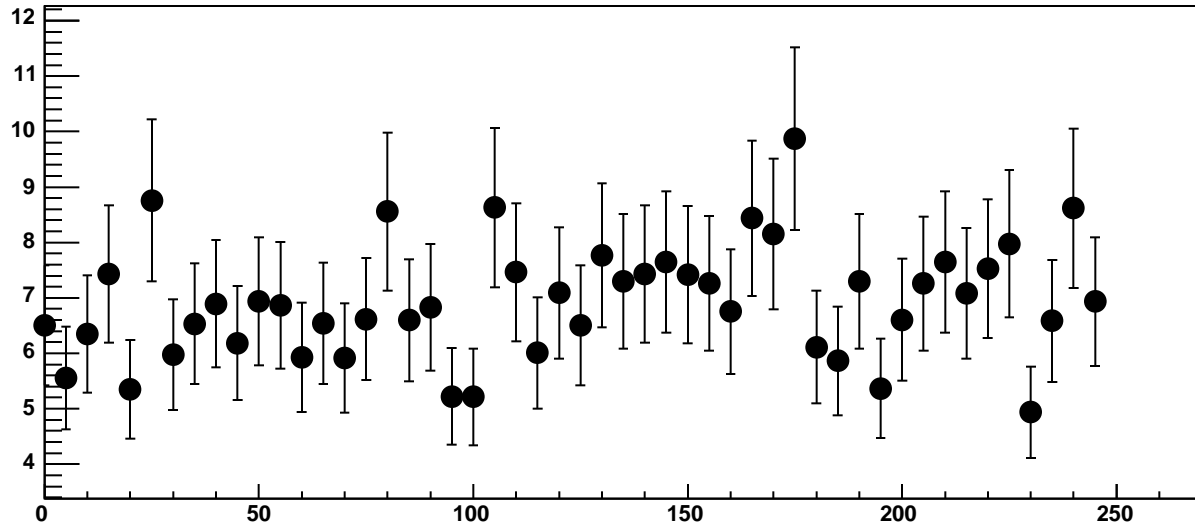


Chip 5, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

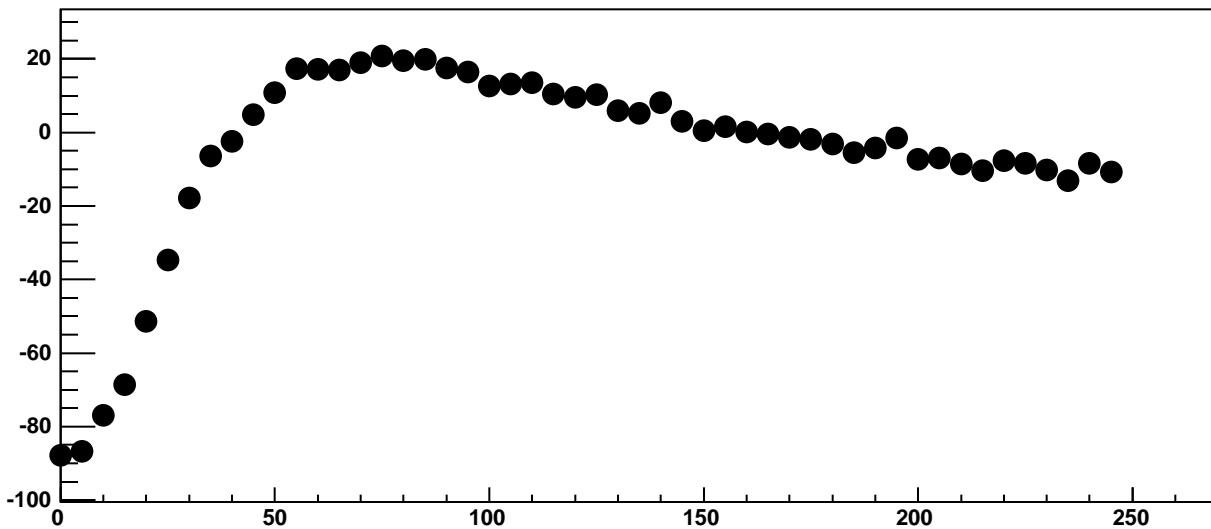


$\chi^2 / \text{ndf}$	6055 / 41
p0	$330.6 \pm 0.4873$
p1	$251.5 \pm 0.2873$
p2	$-12.76 \pm 3.162$
p3	$36.58 \pm 0.2388$
p4	$0.001665 \pm 0.003394$

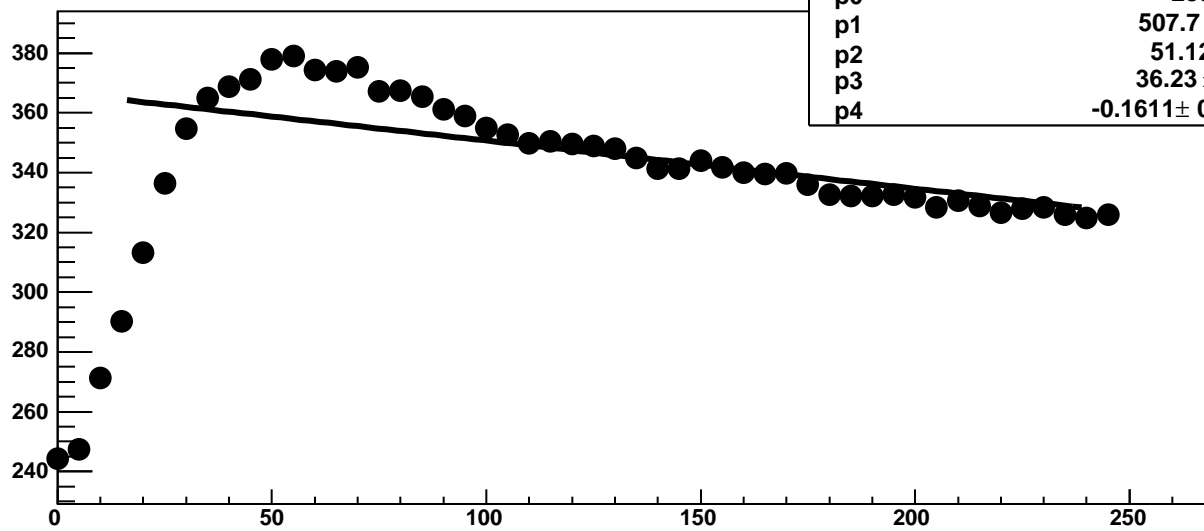
Chip 5, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

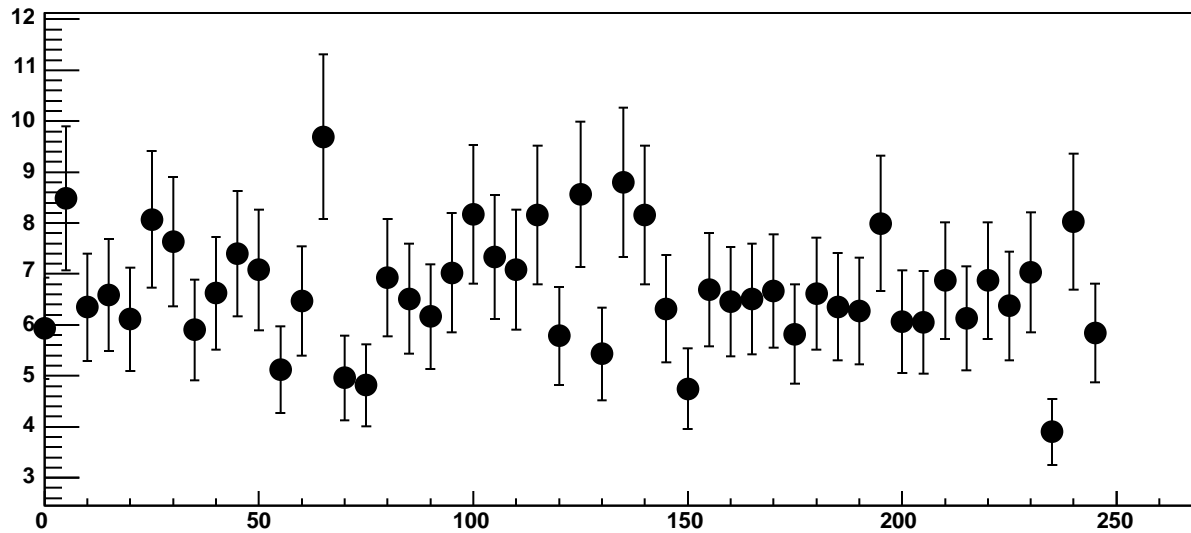


Chip 5, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold

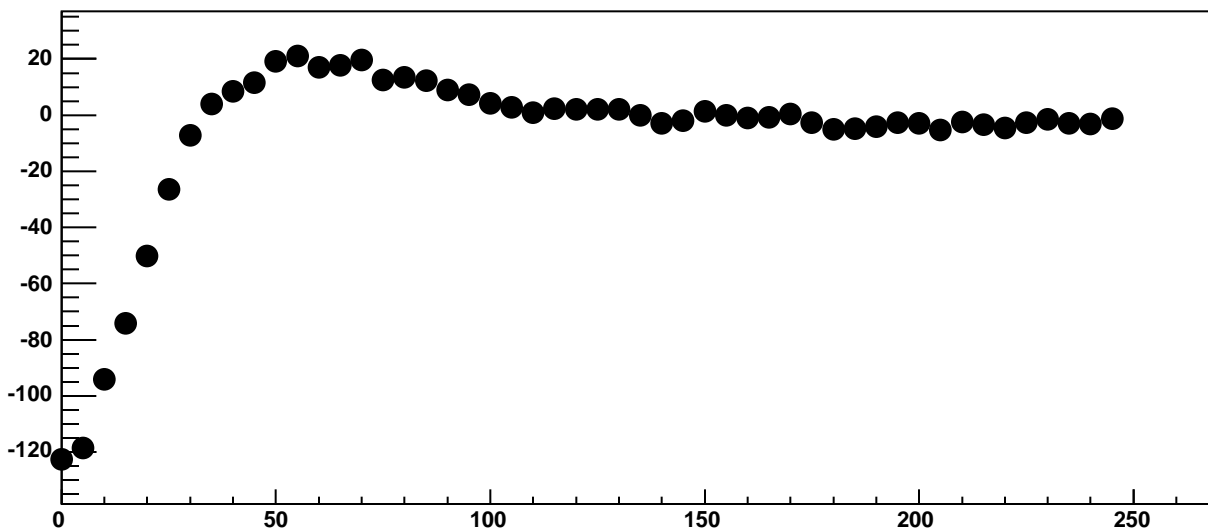


$\chi^2 / \text{ndf}$	5400 / 41
p0	$285 \pm 1.167$
p1	$507.7 \pm 0.6533$
p2	$51.12 \pm 1.748$
p3	$36.23 \pm 0.1556$
p4	$-0.1611 \pm 0.003203$

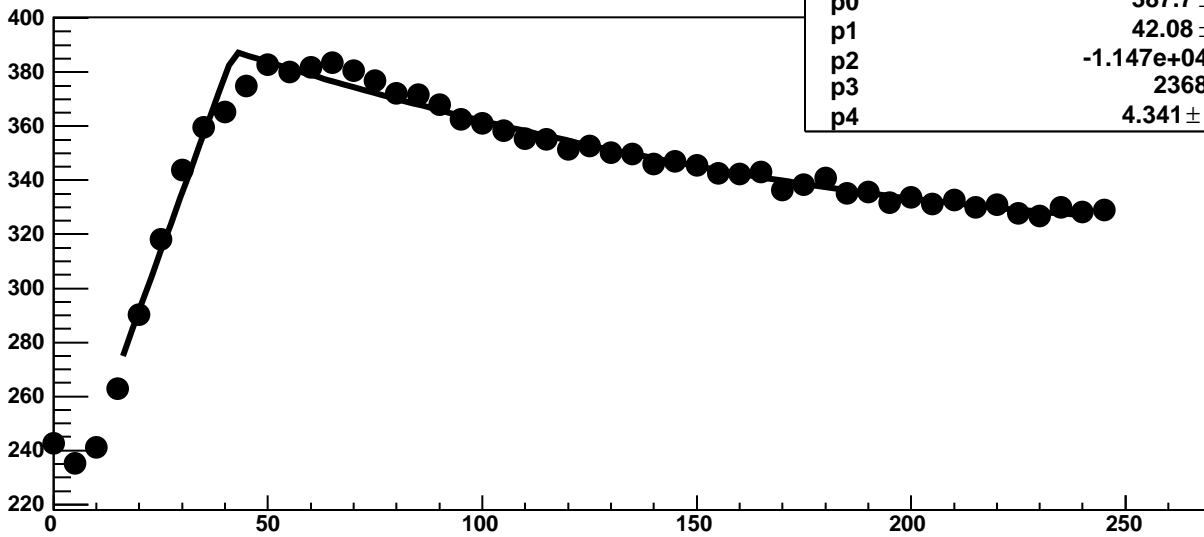
Chip 5, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold

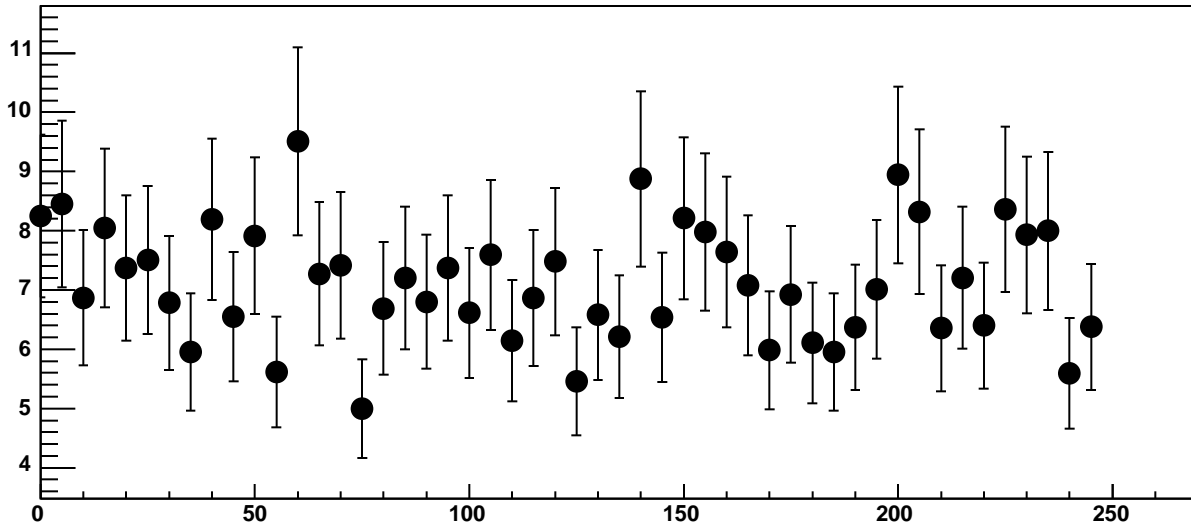


Chip 5, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

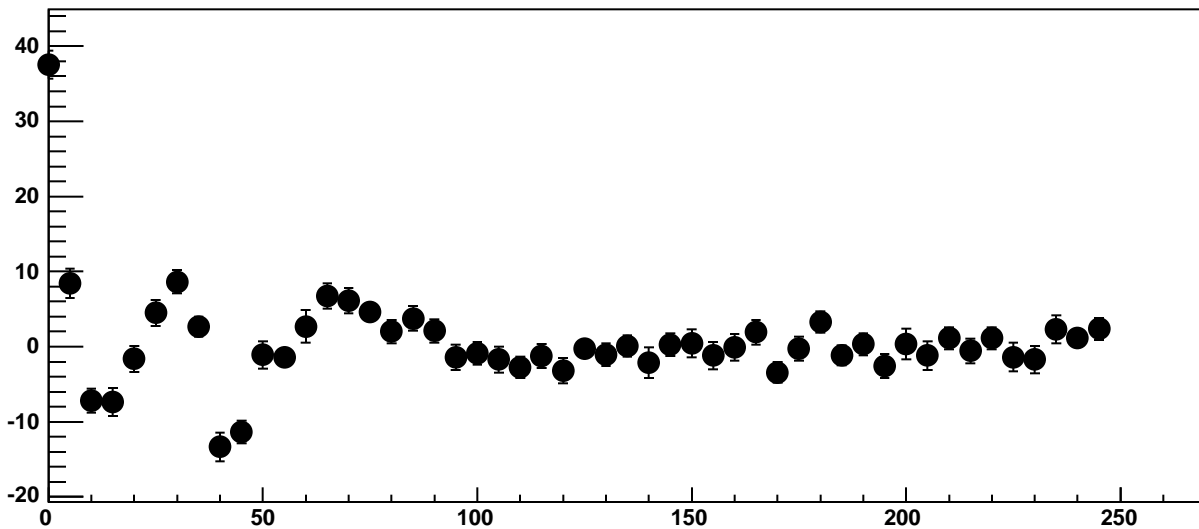


$\chi^2 / \text{ndf}$	258.1 / 41
p0	$387.7 \pm 0.6236$
p1	$42.08 \pm 0.2964$
p2	$-1.147e+04 \pm 774.6$
p3	$2368 \pm 155.6$
p4	$4.341 \pm 0.08225$

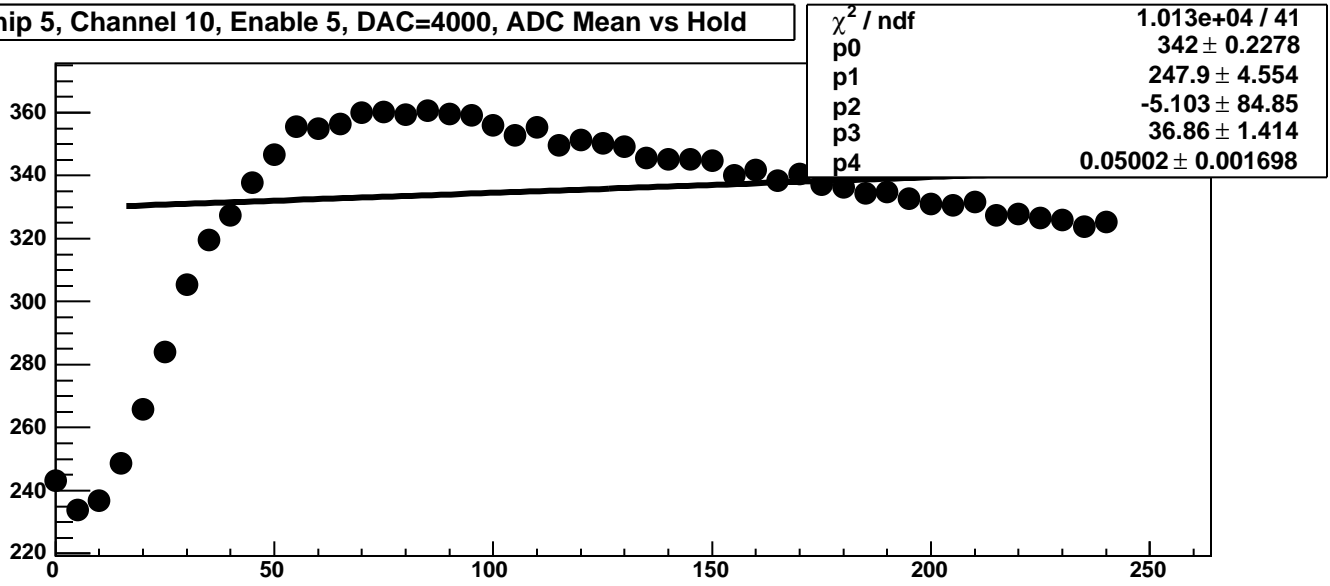
Chip 5, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



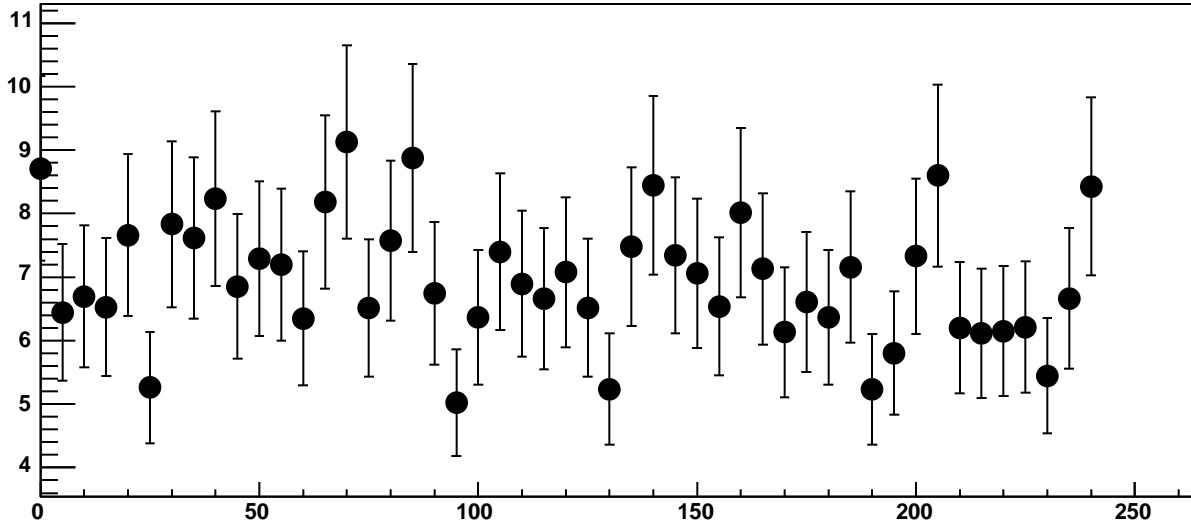
Chip 5, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold



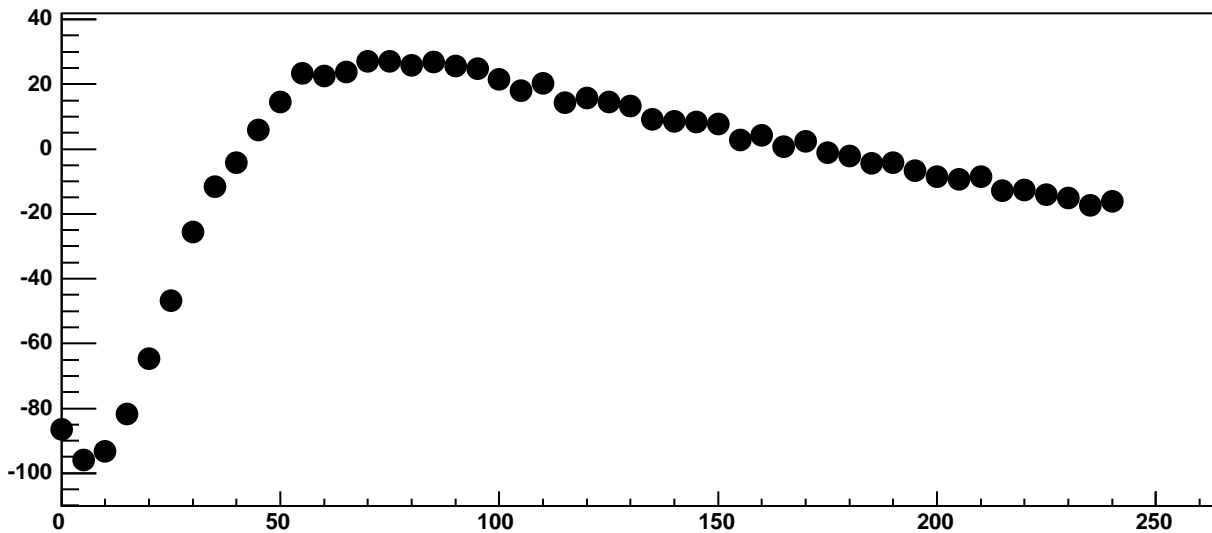
Chip 5, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold

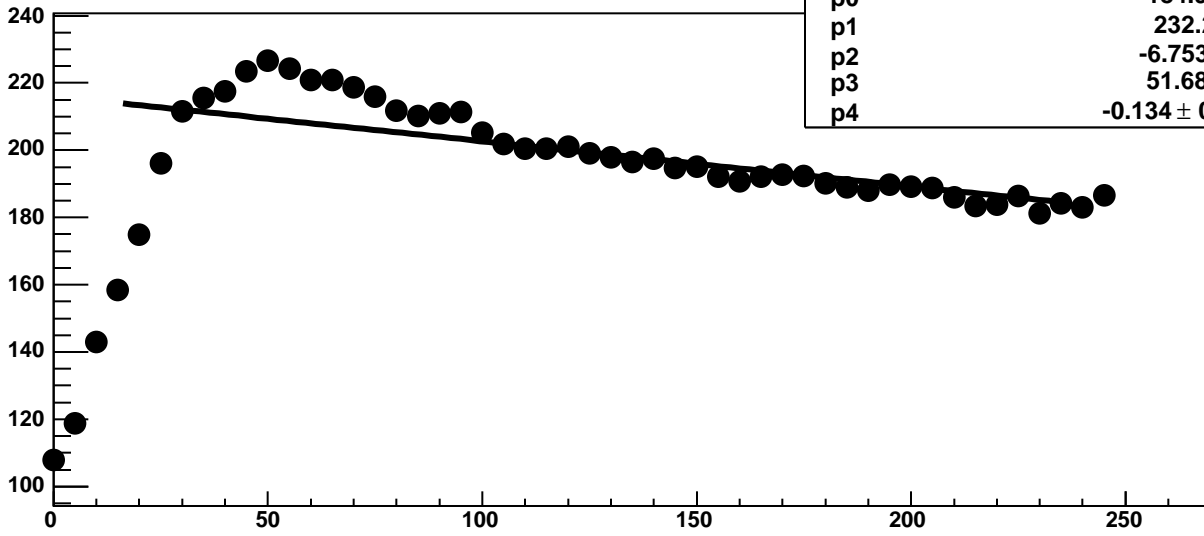


Chip 5, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold



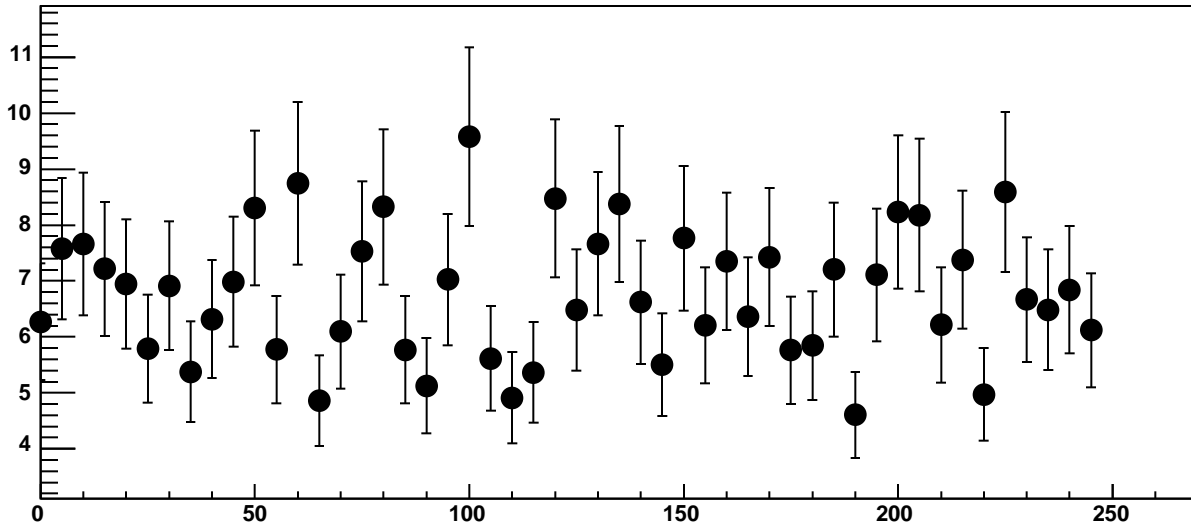


Chip 5, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold

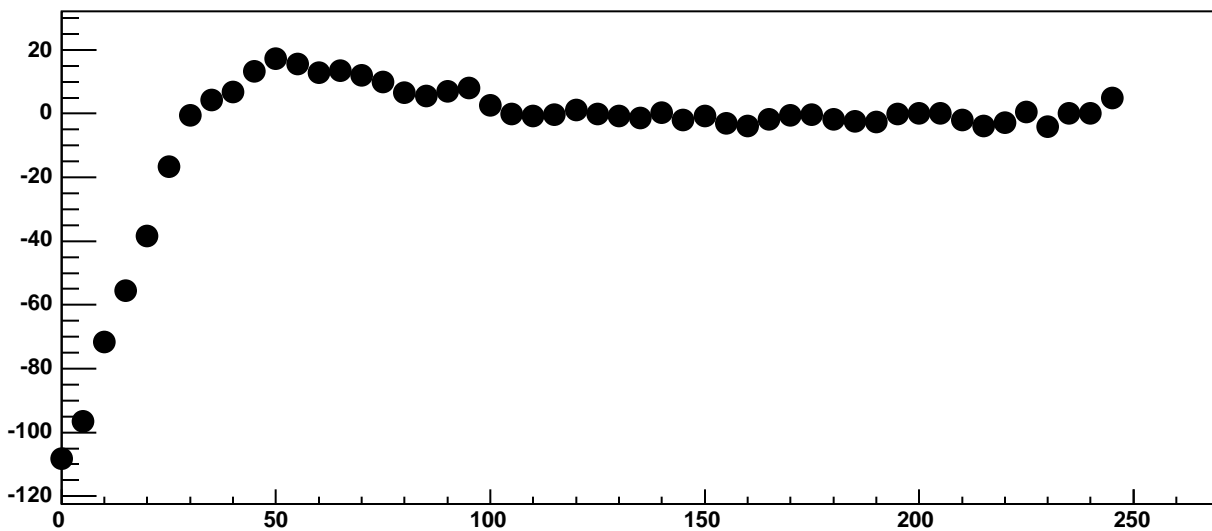


$\chi^2 / \text{ndf}$	2620 / 41
p0	$184.9 \pm 3.039$
p1	$232.2 \pm 21.9$
p2	$-6.753 \pm 21.13$
p3	$51.68 \pm 217.4$
p4	$-0.134 \pm 0.003561$

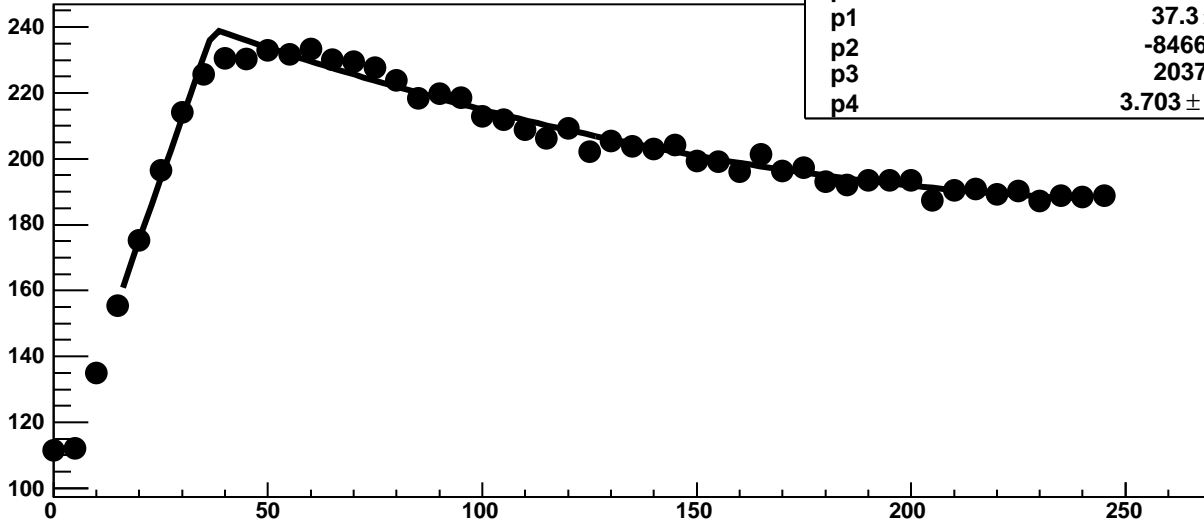
Chip 5, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold

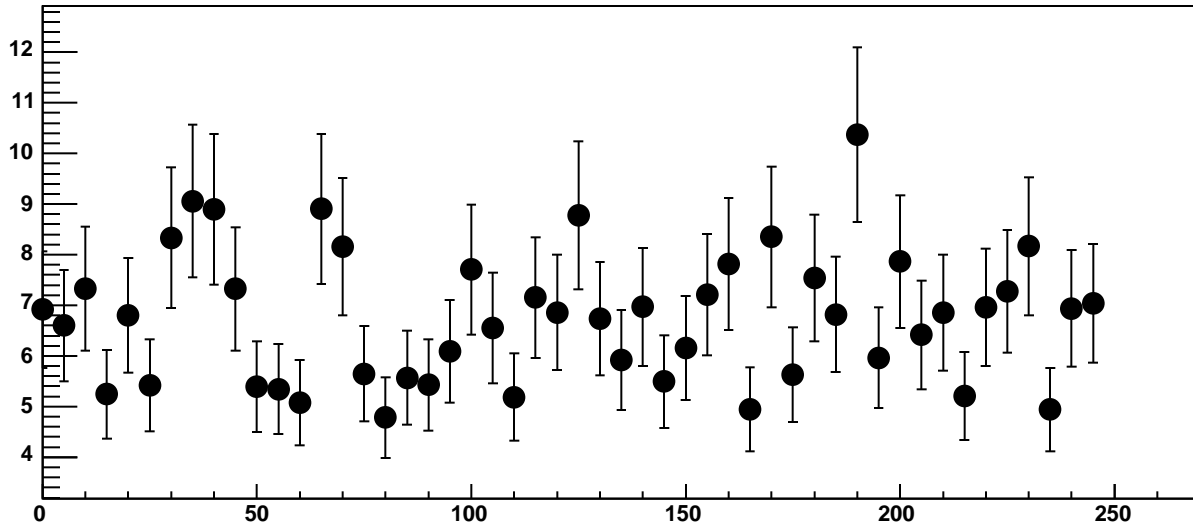


Chip 5, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

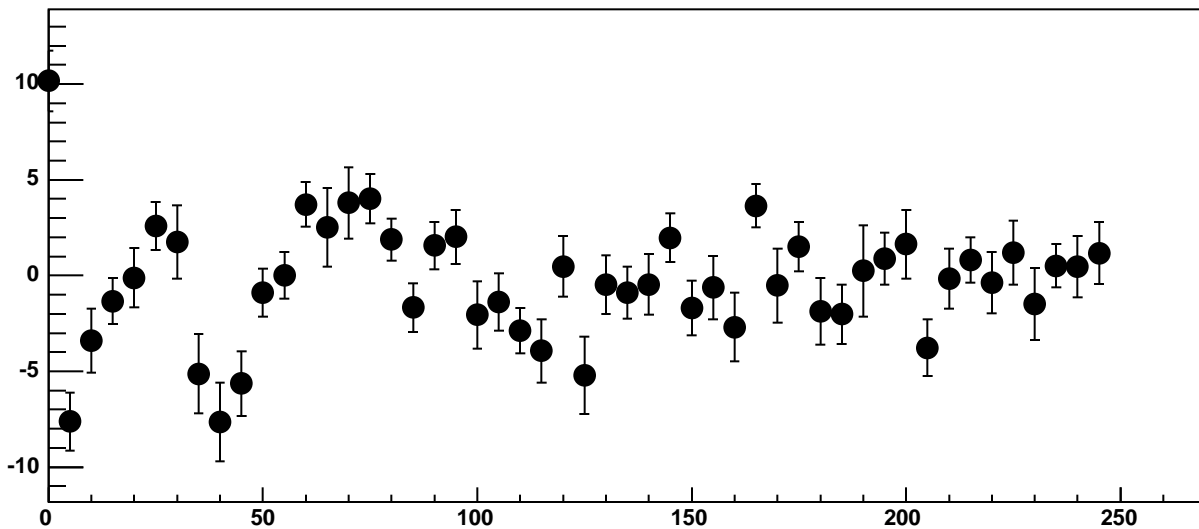


$\chi^2 / \text{ndf}$	123.9 / 41
p0	$239.4 \pm 0.6177$
p1	$37.3 \pm 0.4021$
p2	$-8466 \pm 629.6$
p3	$2037 \pm 137.5$
p4	$3.703 \pm 0.09914$

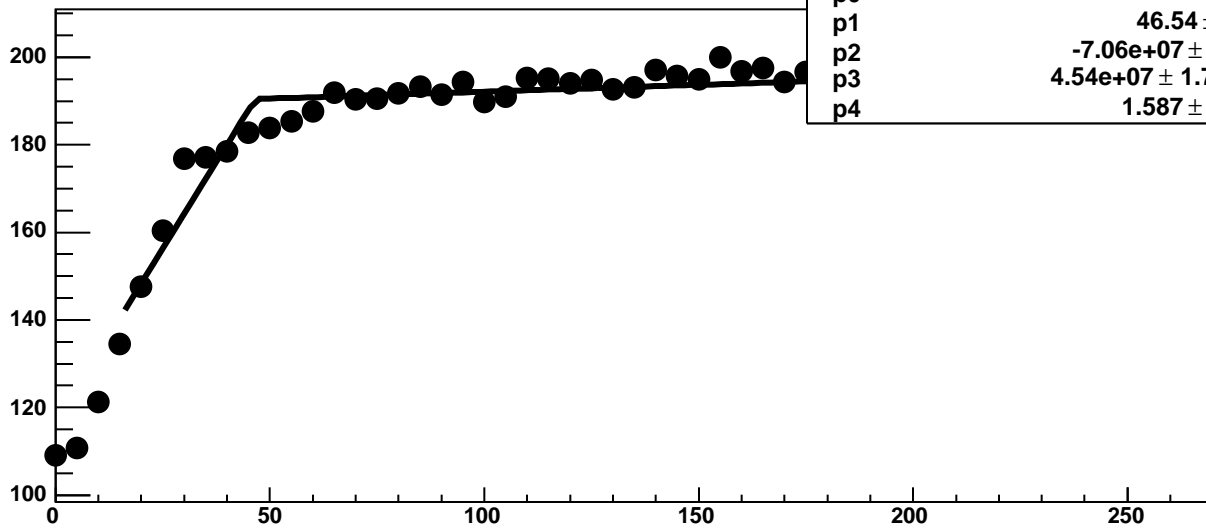
Chip 5, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold

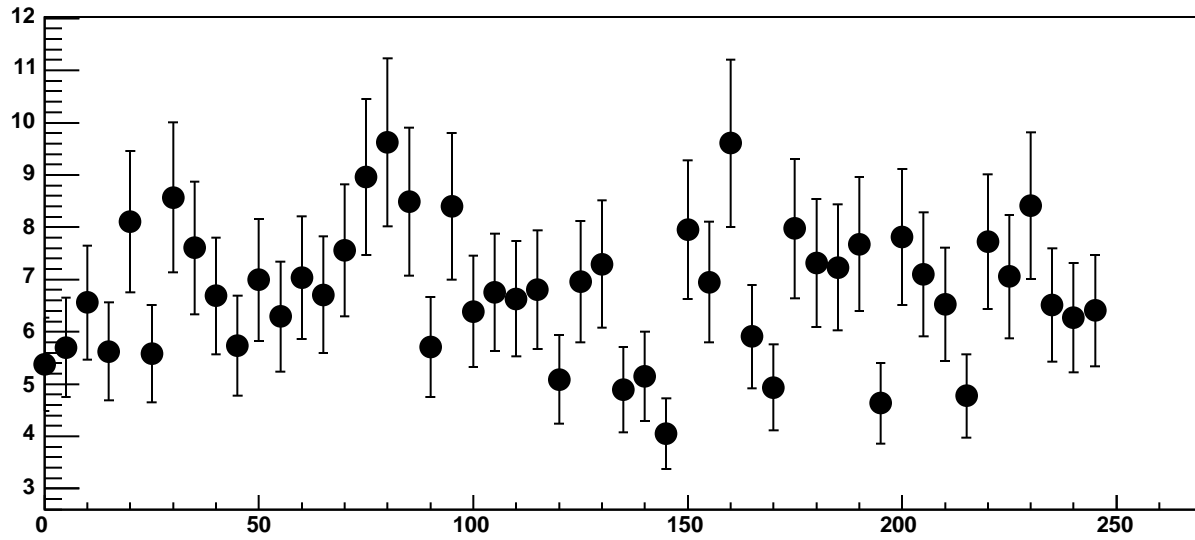


Chip 5, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

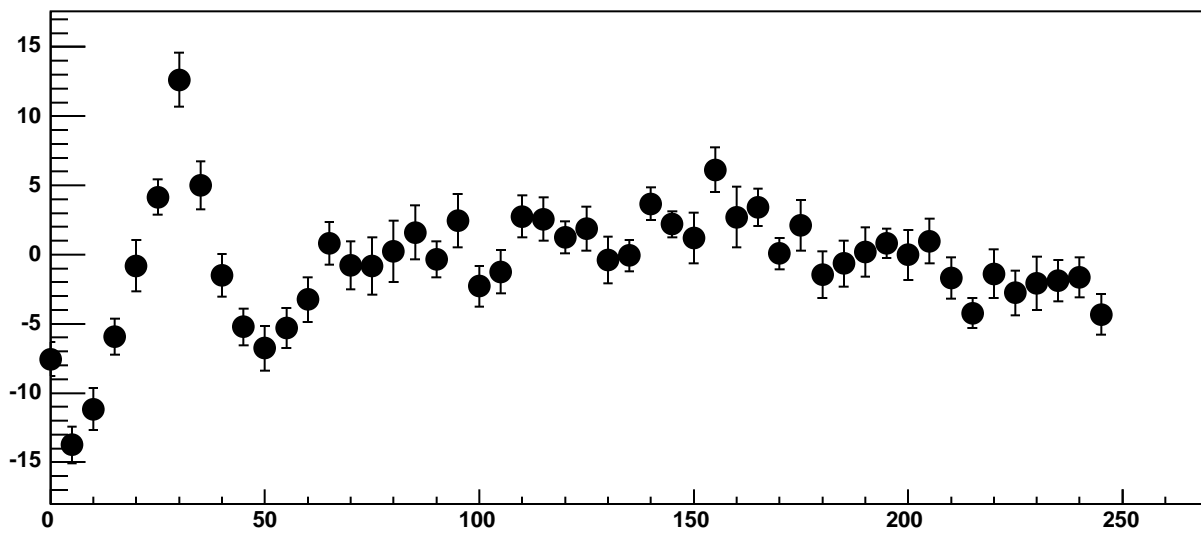


$\chi^2 / \text{ndf}$	213 / 41
p0	$190.4 \pm 0.5205$
p1	$46.54 \pm 0.7503$
p2	$-7.06e+07 \pm 2.4e+06$
p3	$4.54e+07 \pm 1.735e+05$
p4	$1.587 \pm 0.05278$

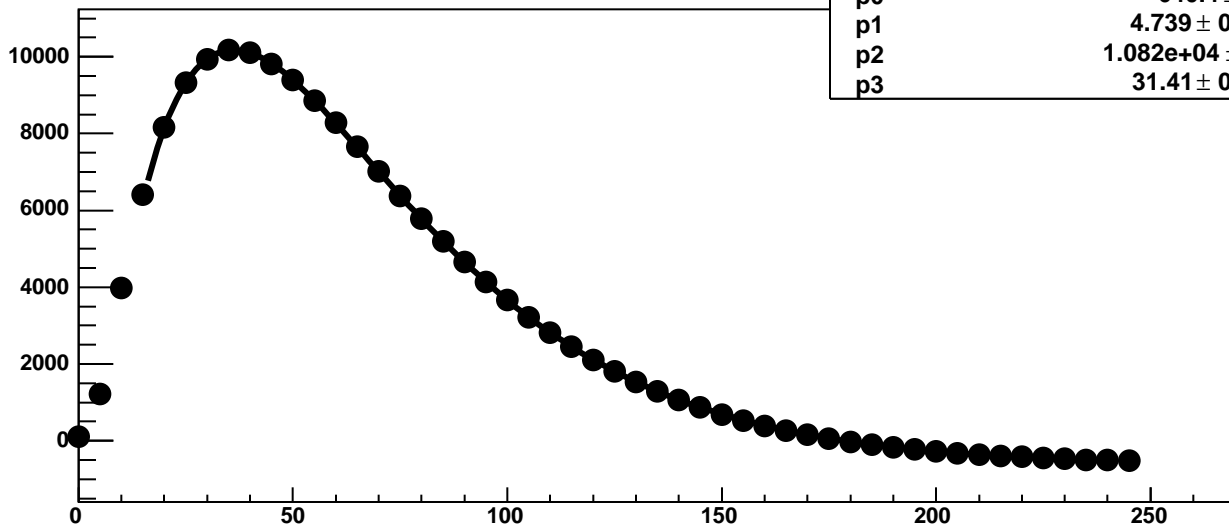
Chip 5, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

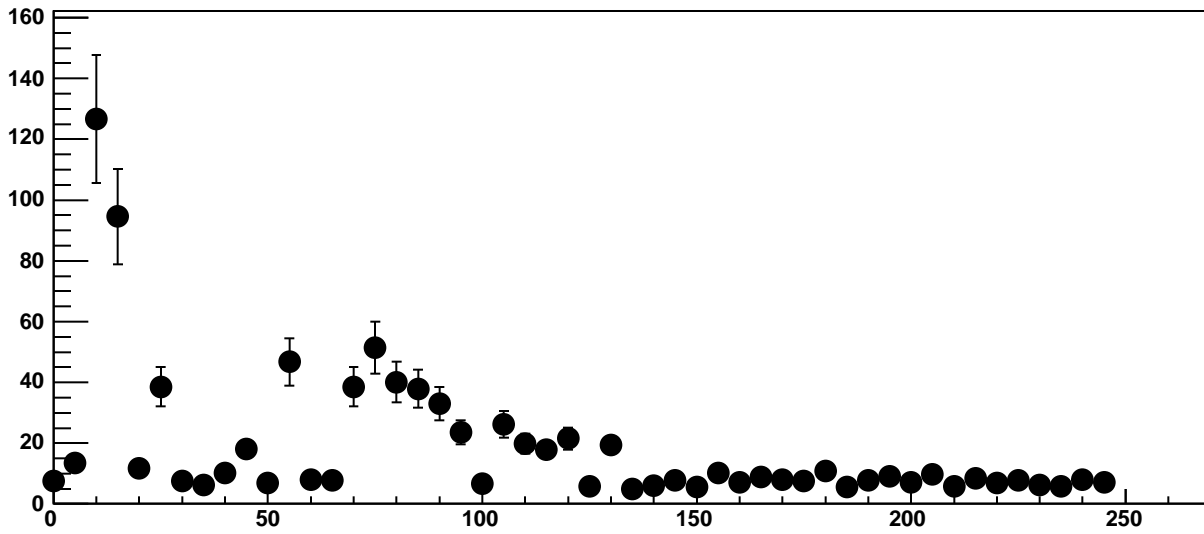


Chip 5, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold

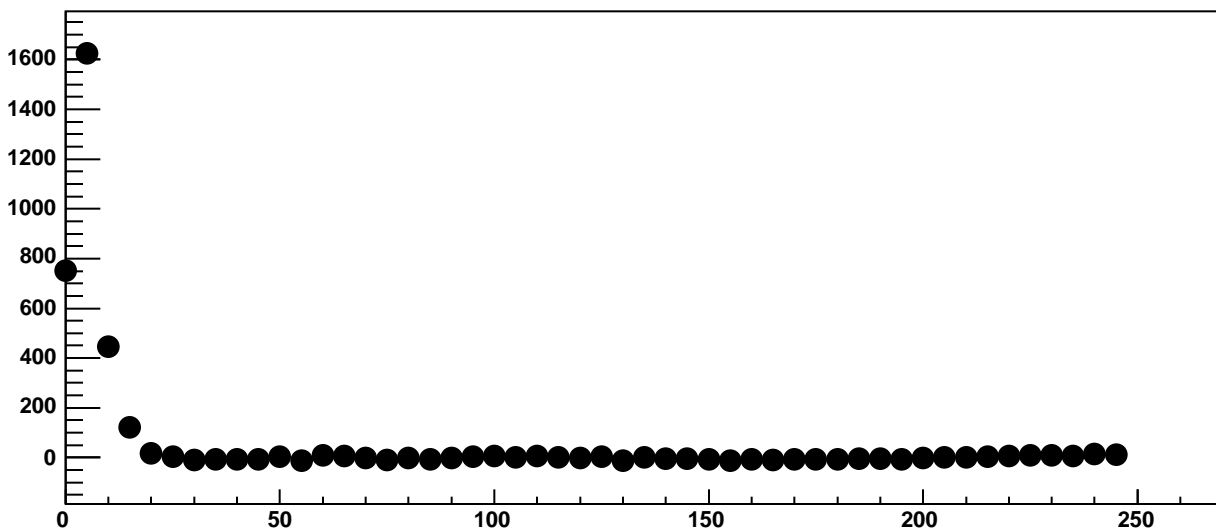


$\chi^2 / \text{ndf}$	557.3 / 42
p0	-640.4 ± 0.5904
p1	4.739 ± 0.009564
p2	1.082e+04 ± 0.8803
p3	31.41 ± 0.005786

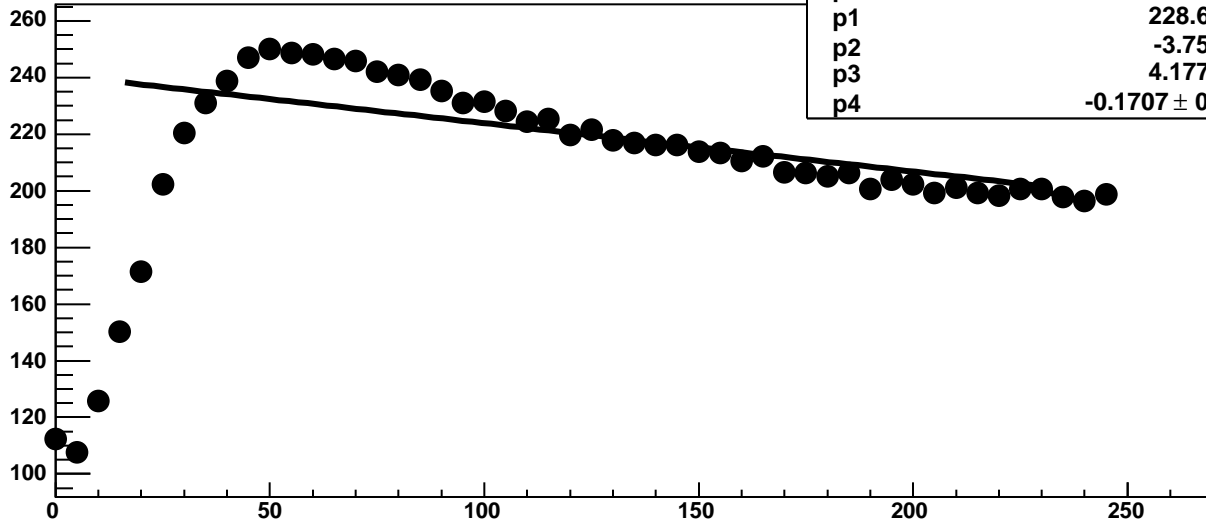
Chip 5, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold

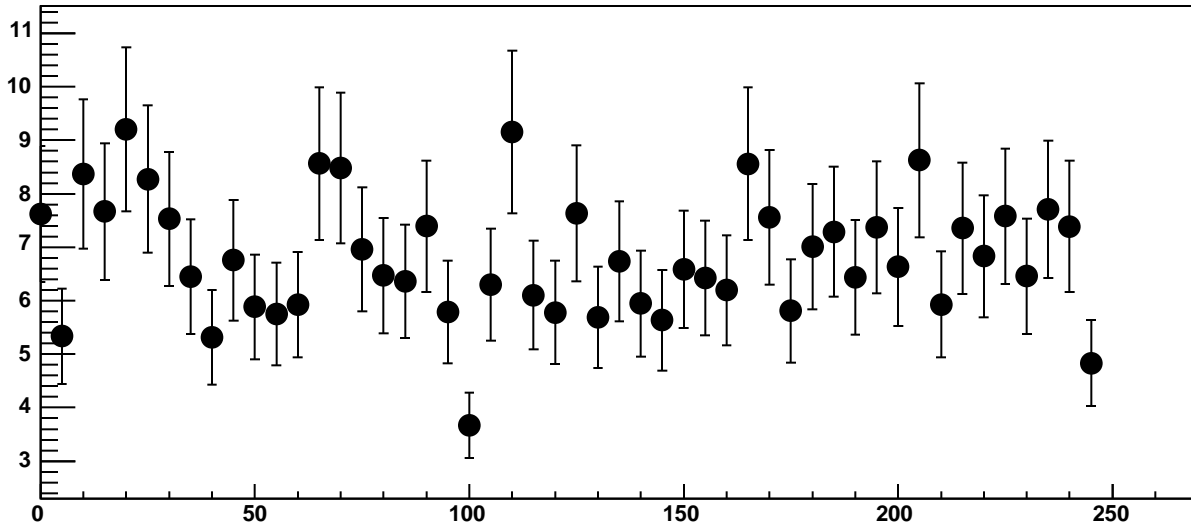


Chip 5, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

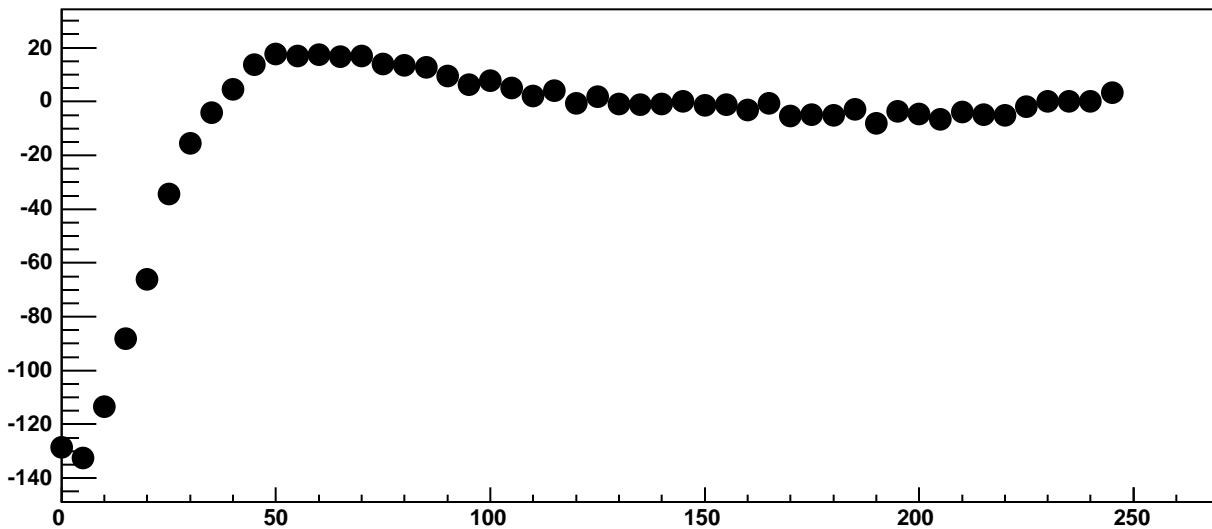


$\chi^2 / \text{ndf}$	5172 / 41
p0	$201.9 \pm 0.877$
p1	$228.6 \pm 3.934$
p2	$-3.75 \pm 3.036$
p3	$4.177 \pm 12.25$
p4	$-0.1707 \pm 0.003913$

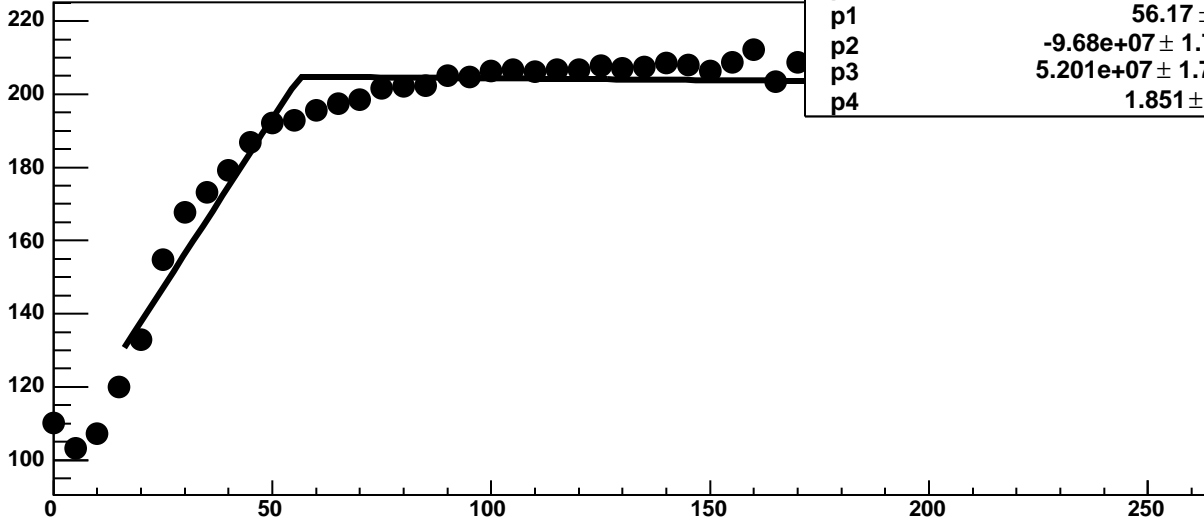
Chip 5, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

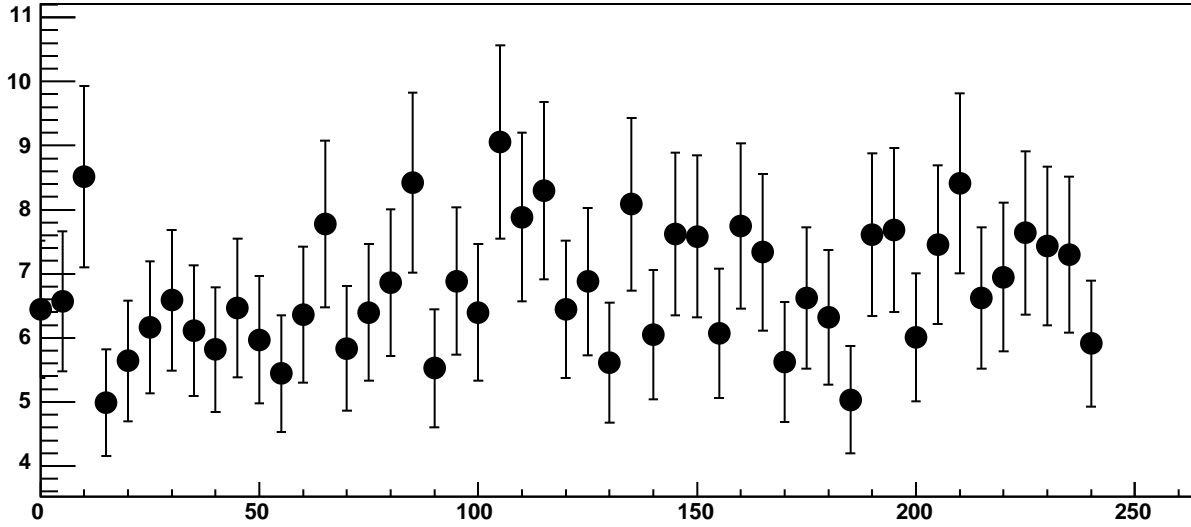


Chip 5, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold

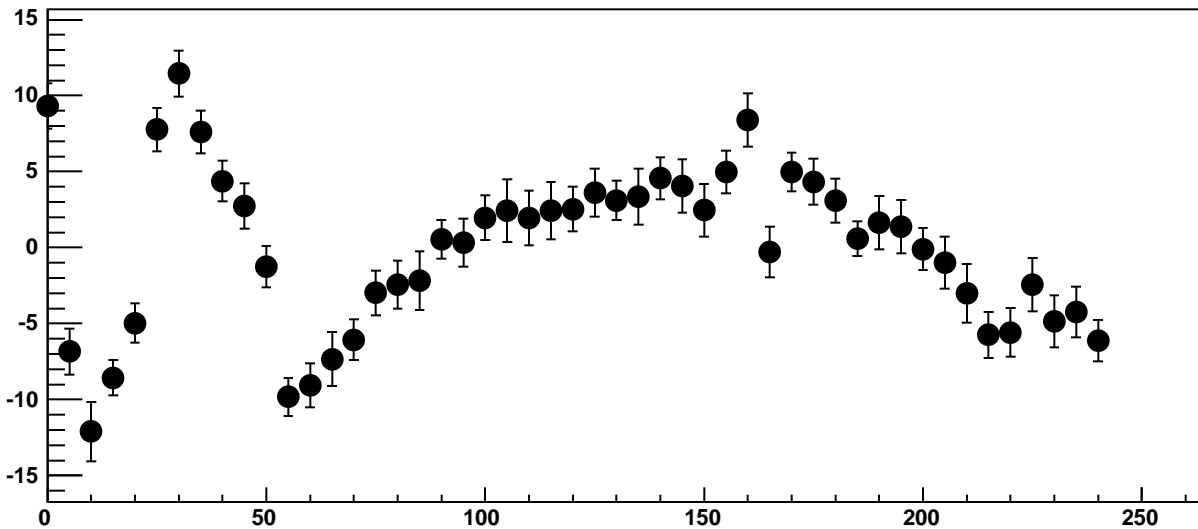


$\chi^2 / \text{ndf}$	520.9 / 41
p0	$204.8 \pm 0.5127$
p1	$56.17 \pm 0.5304$
p2	$-9.68\text{e}+07 \pm 1.746\text{e}+06$
p3	$5.201\text{e}+07 \pm 1.727\text{e}+05$
p4	$1.851 \pm 0.03271$

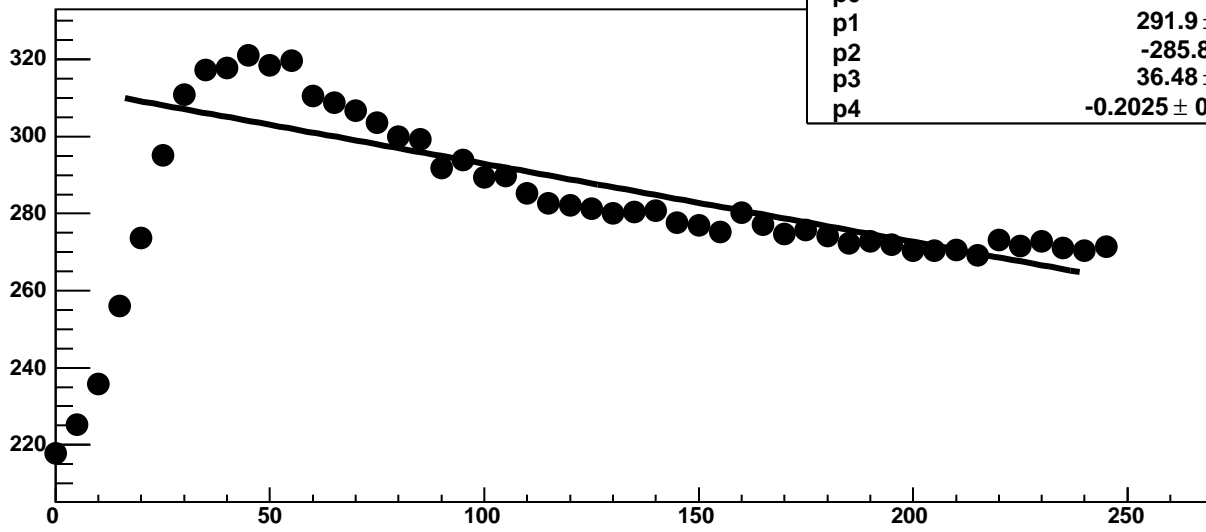
Chip 5, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

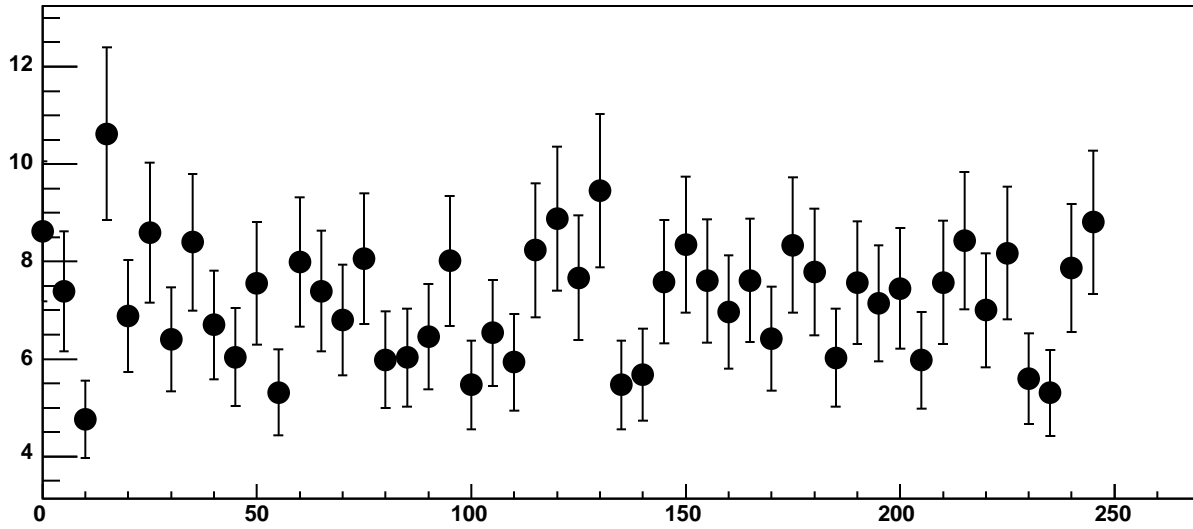


Chip 5, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

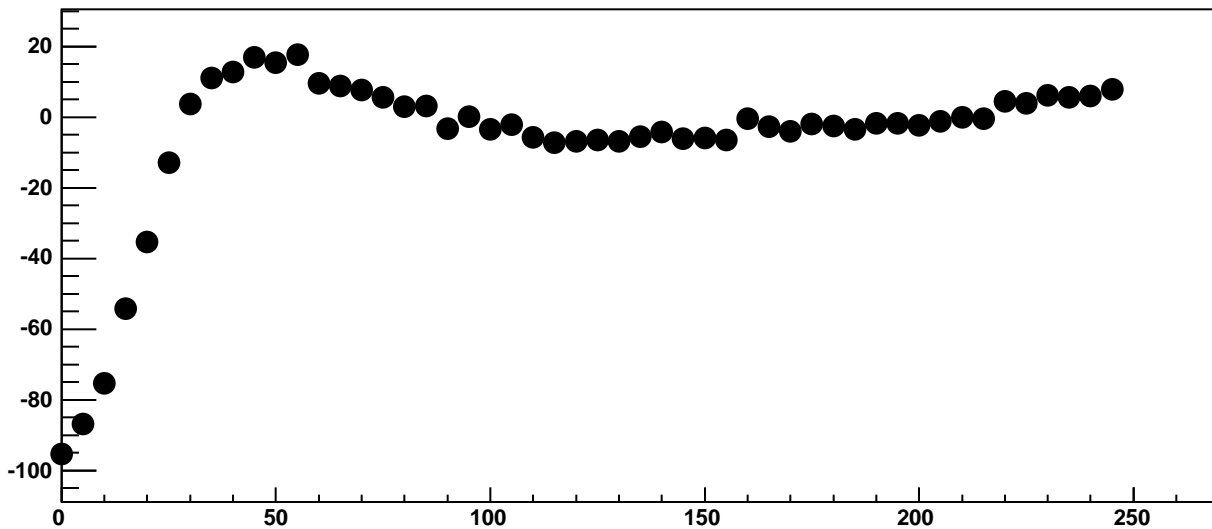


$\chi^2 / \text{ndf}$	1919 / 41
p0	$254.1 \pm 0.6589$
p1	$291.9 \pm 0.4423$
p2	$-285.8 \pm 9.657$
p3	$36.48 \pm 0.4517$
p4	$-0.2025 \pm 0.003554$

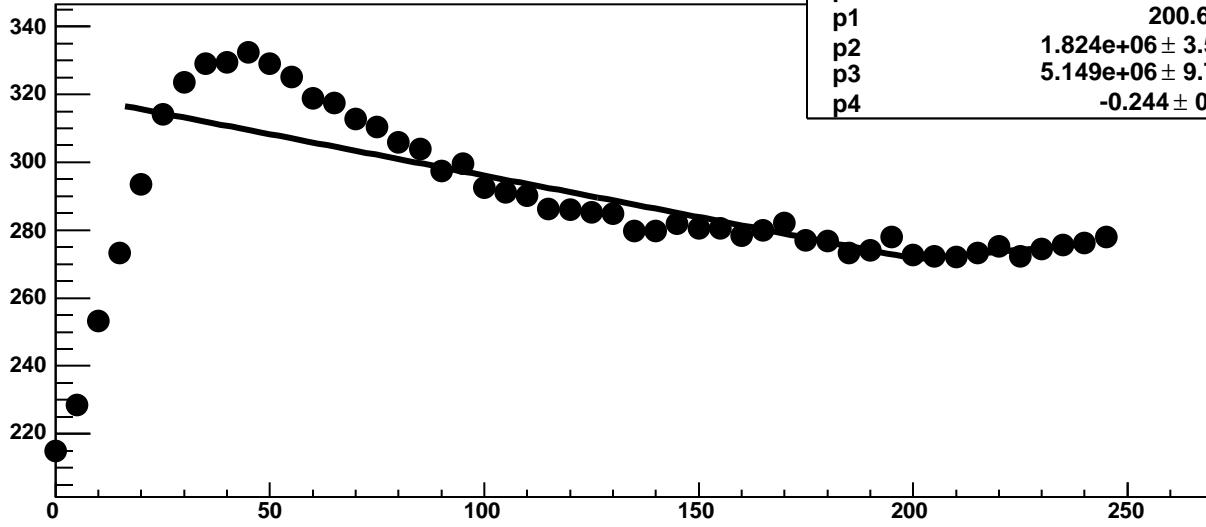
Chip 5, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

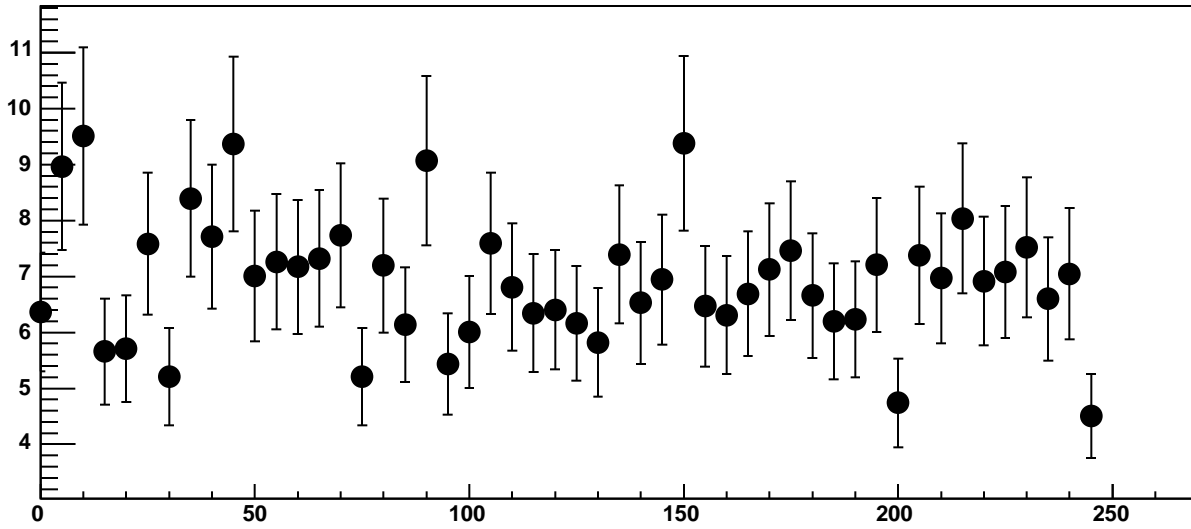


Chip 5, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold

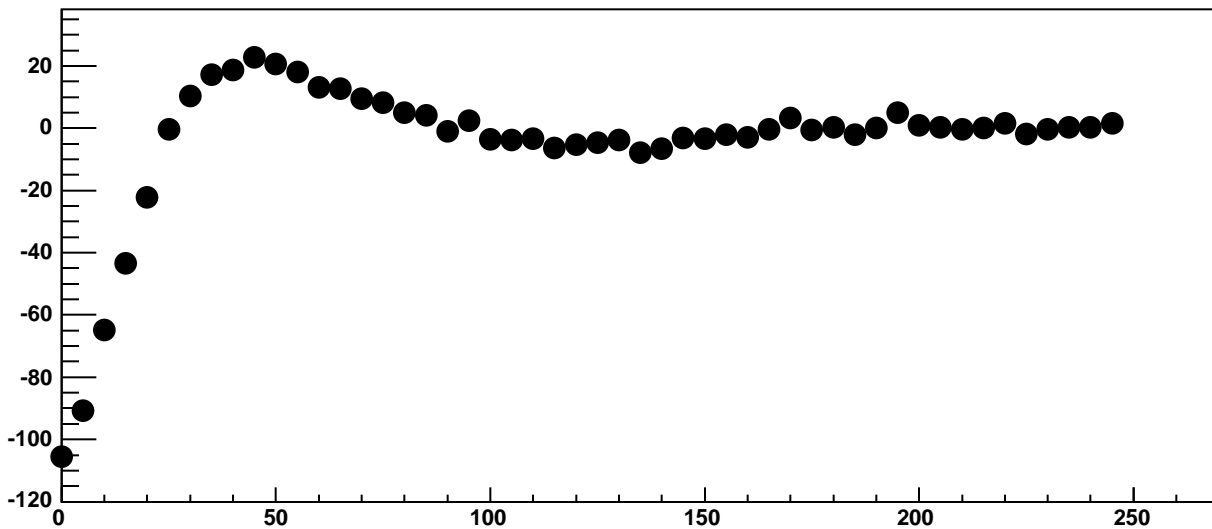


$\chi^2 / \text{ndf}$	2430 / 41
p0	$271.5 \pm 0.8898$
p1	$200.6 \pm 3.828$
p2	$1.824\text{e}+06 \pm 3.561\text{e}+06$
p3	$5.149\text{e}+06 \pm 9.794\text{e}+06$
p4	$-0.244 \pm 0.004348$

Chip 5, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold

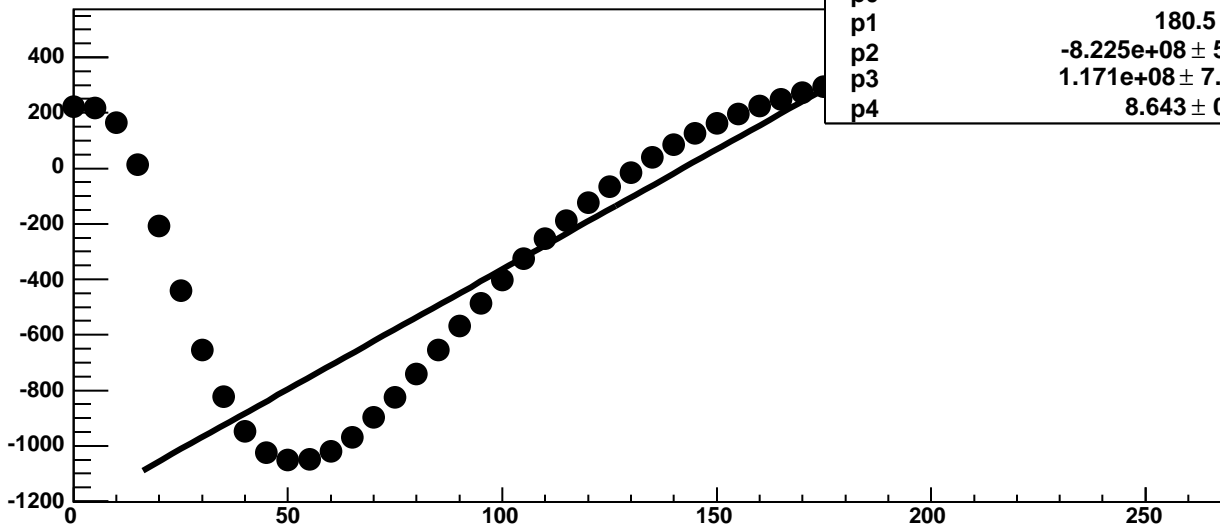


Chip 5, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold



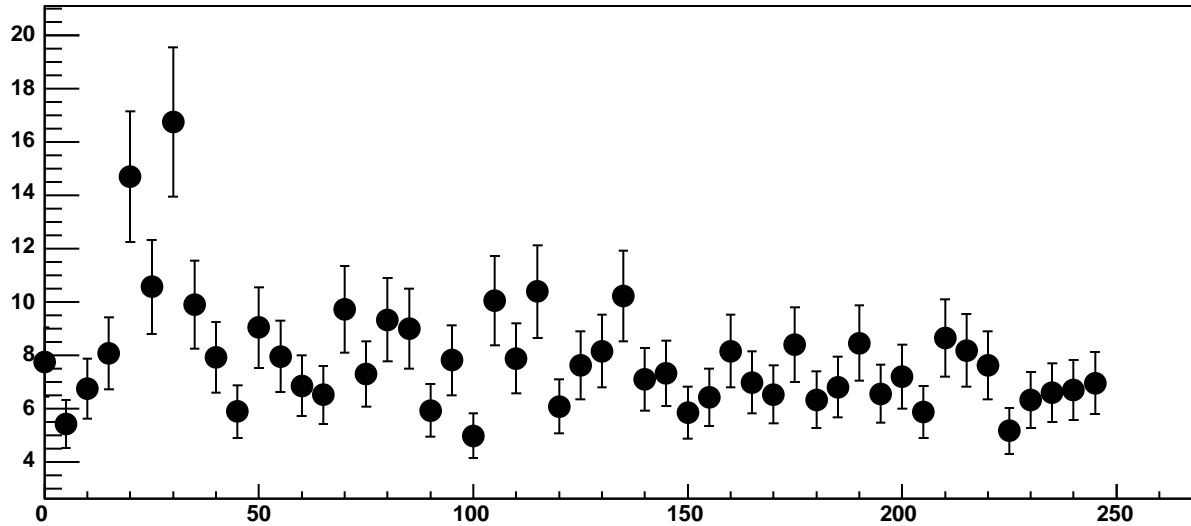


Chip 5, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

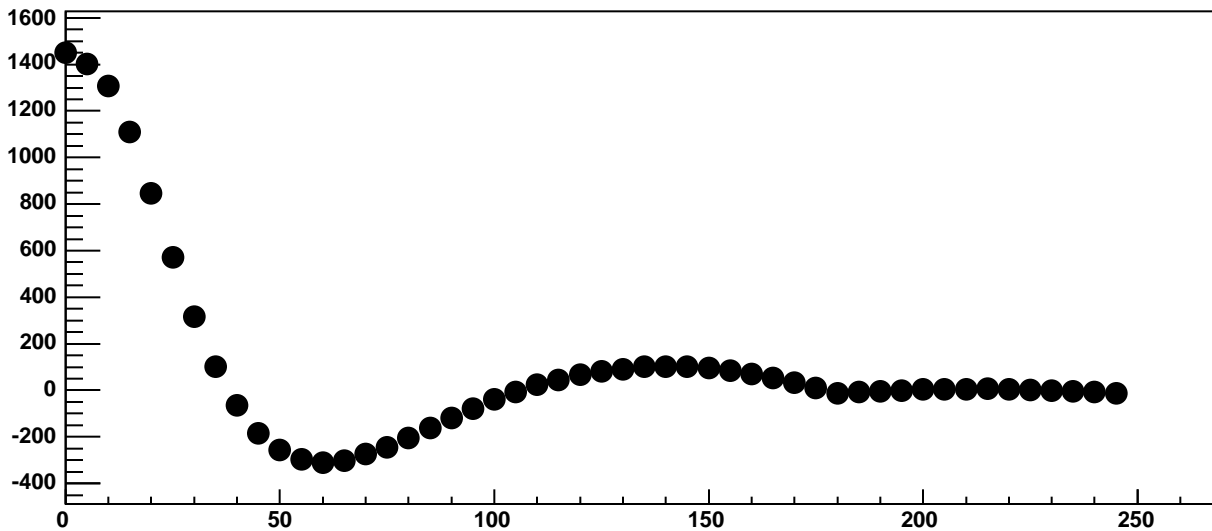


$\chi^2 / \text{ndf}$	7.186e+05 / 41
p0	331.6 ± 1.21
p1	180.5 ± 0.1607
p2	-8.225e+08 ± 5.48e+07
p3	1.171e+08 ± 7.717e+06
p4	8.643 ± 0.006384

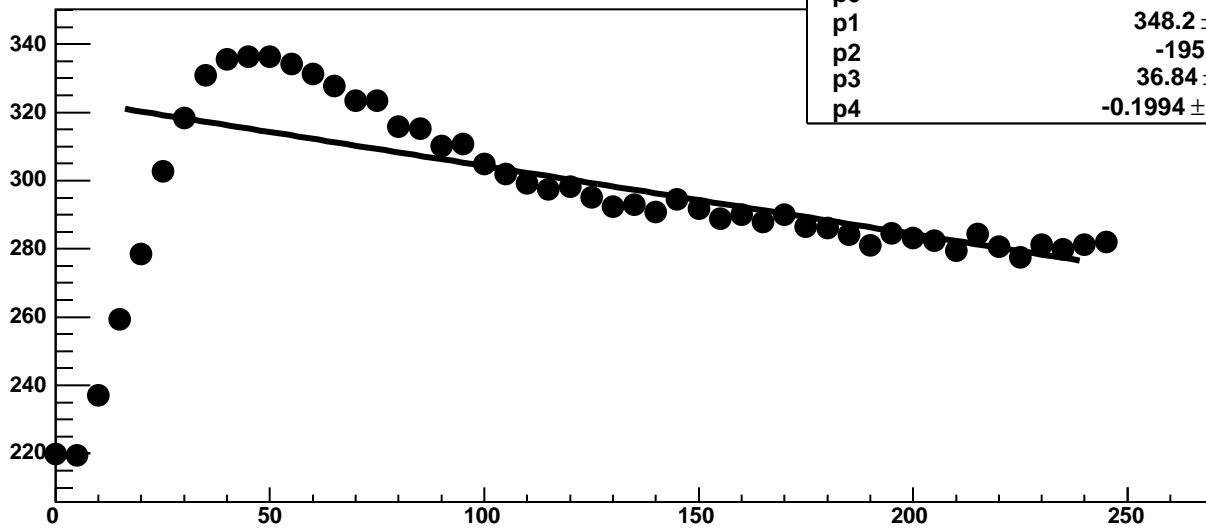
Chip 5, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

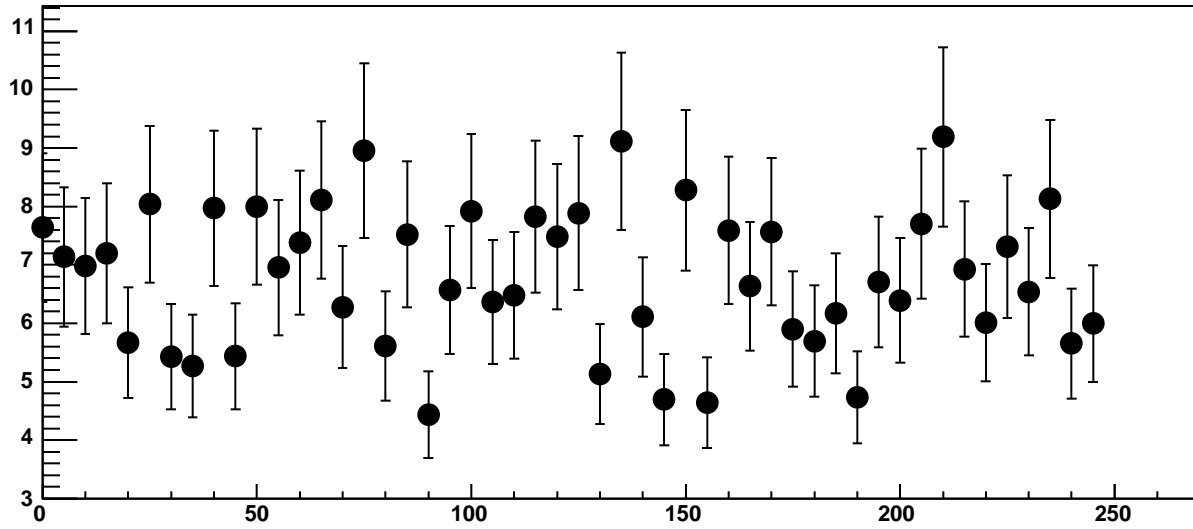


Chip 5, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

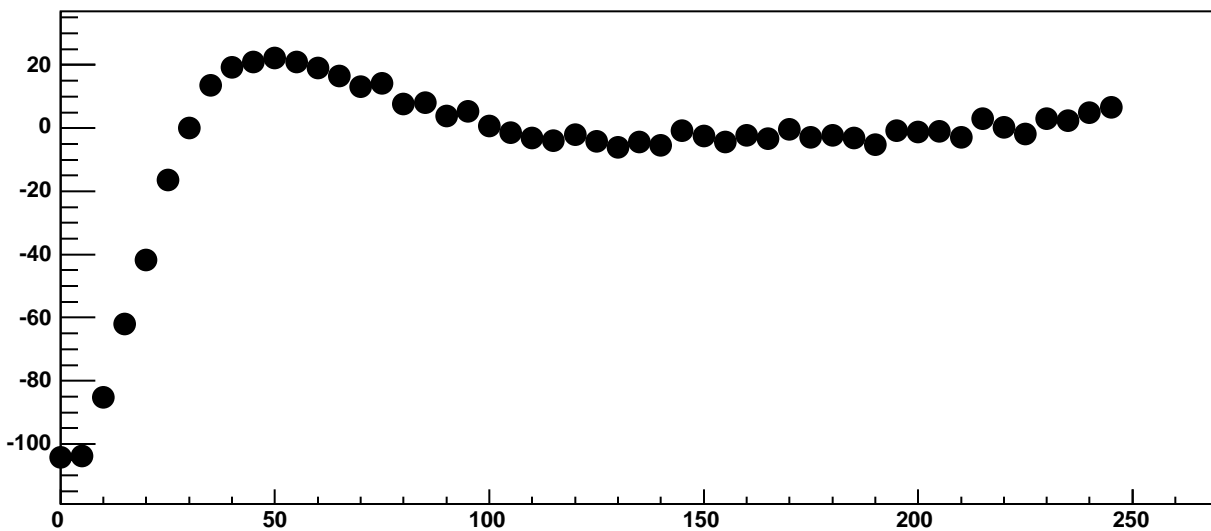


$\chi^2 / \text{ndf}$	3924 / 41
p0	$254.8 \pm 0.876$
p1	$348.2 \pm 0.8644$
p2	$-195.6 \pm 6.7$
p3	$36.84 \pm 0.3568$
p4	$-0.1994 \pm 0.00331$

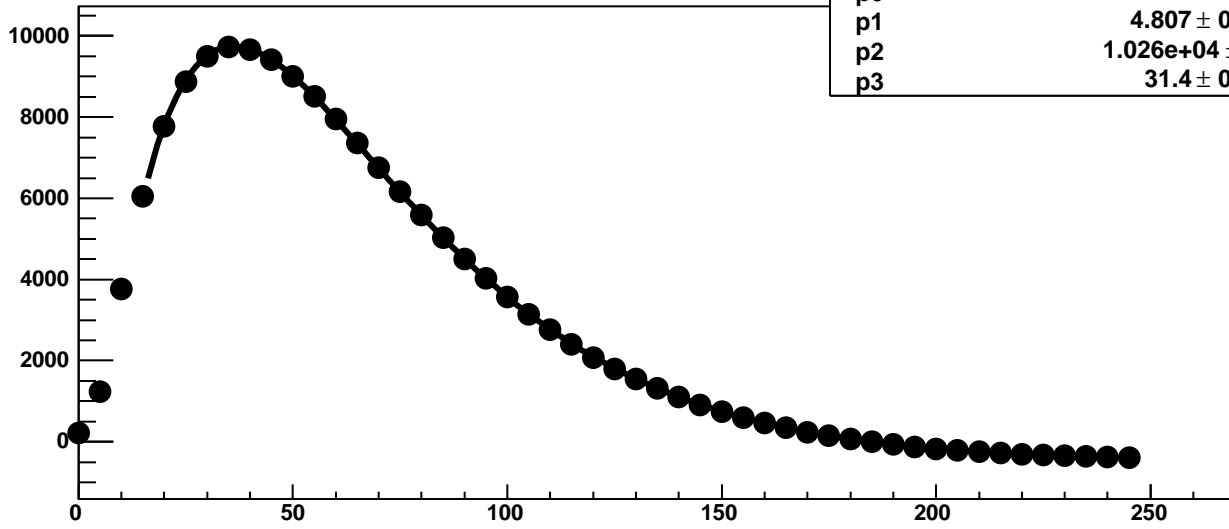
Chip 5, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

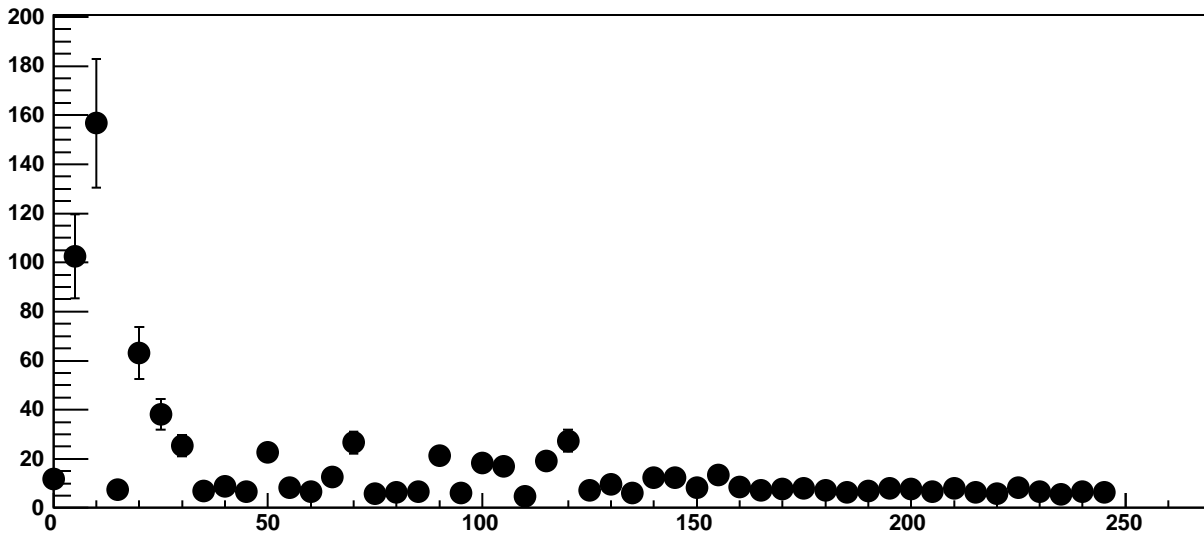


Chip 5, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold

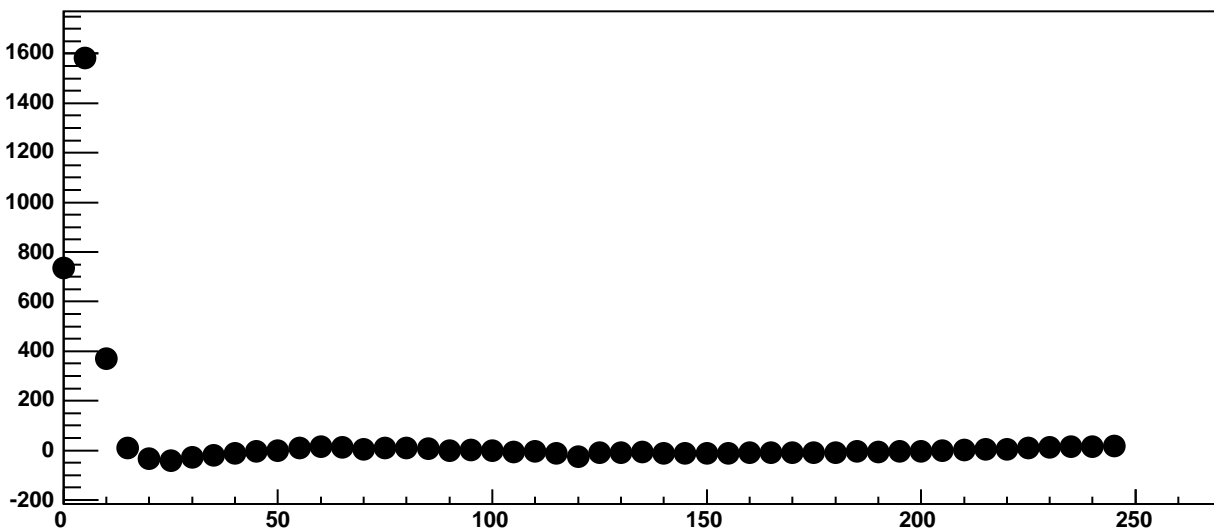


$\chi^2 / \text{ndf}$	1335 / 42
p0	-514.6 ± 0.5049
p1	4.807 ± 0.004946
p2	1.026e+04 ± 0.8562
p3	31.4 ± 0.004285

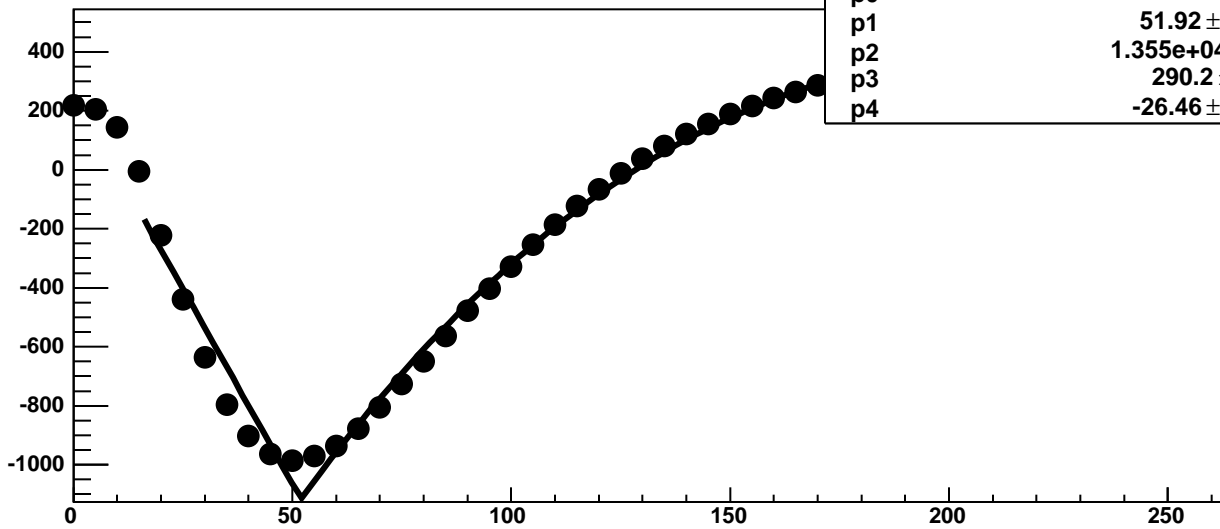
Chip 5, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold



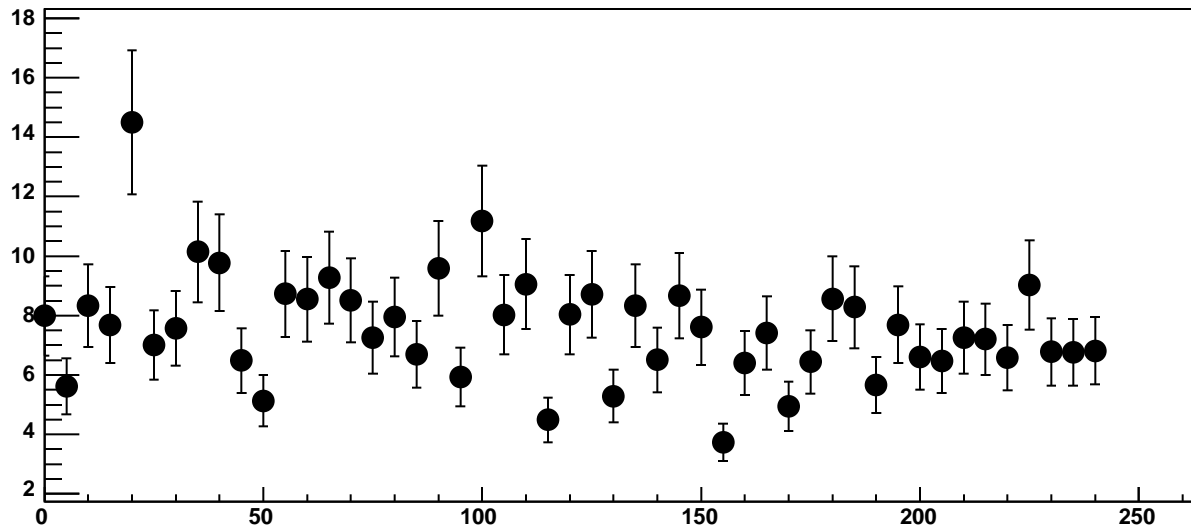
Chip 5, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold



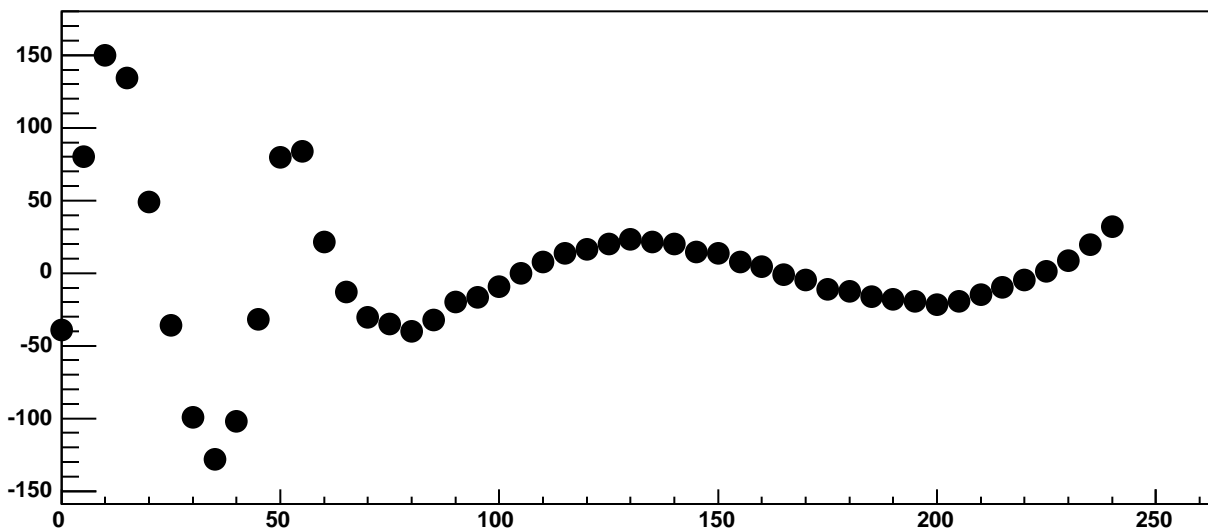
Chip 5, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold



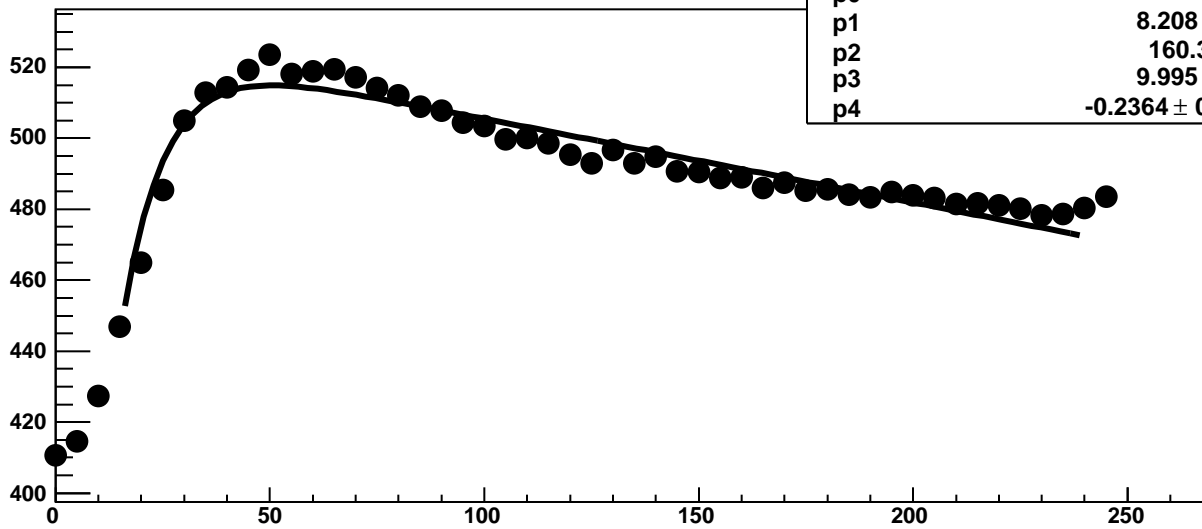
Chip 5, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold

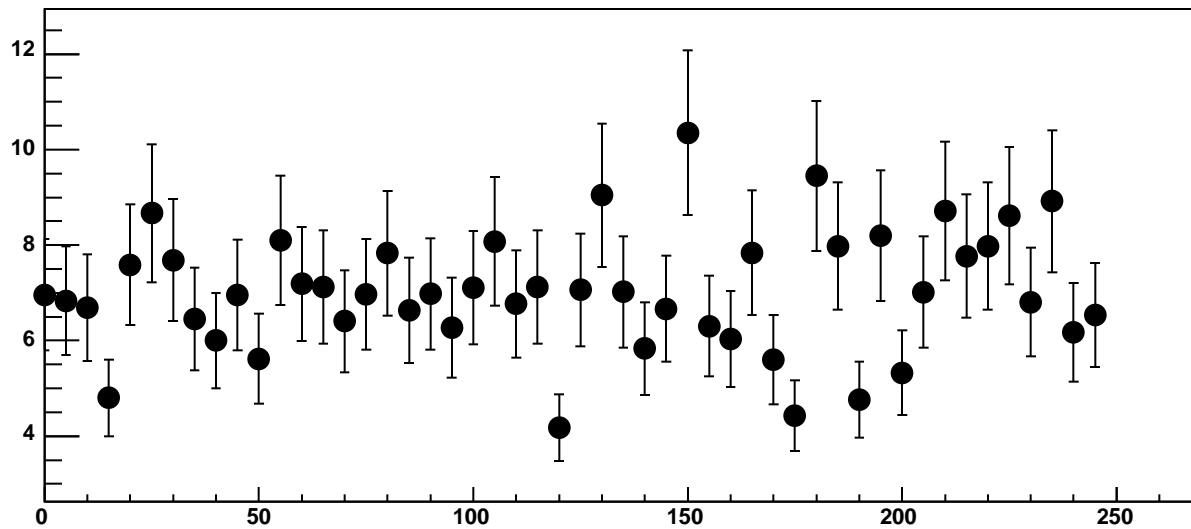


Chip 5, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold

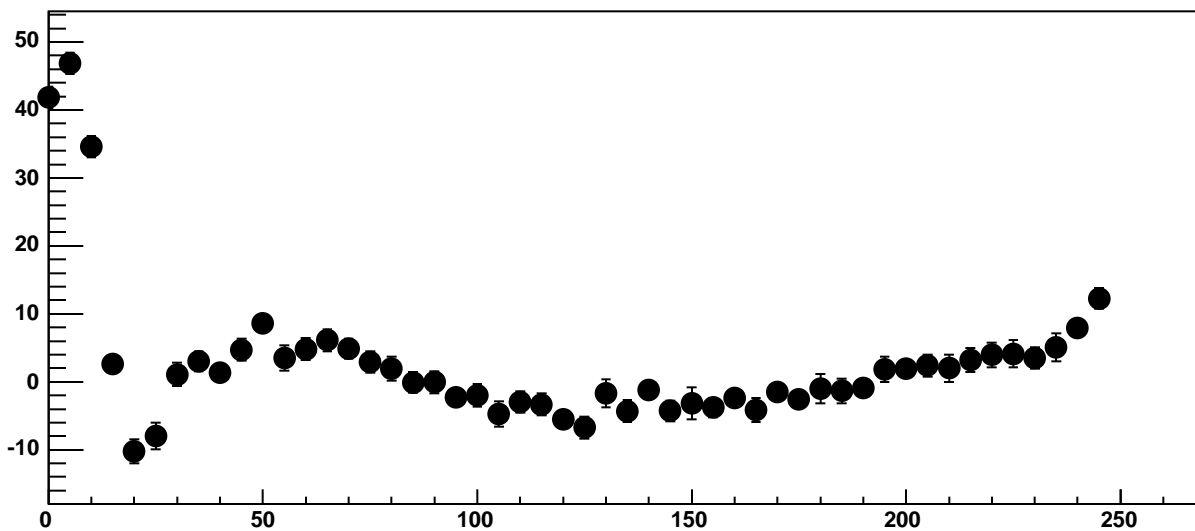


$\chi^2 / \text{ndf}$	328.5 / 41
p0	$366.9 \pm 2.938$
p1	$8.208 \pm 0.3189$
p2	$160.3 \pm 2.982$
p3	$9.995 \pm 0.3133$
p4	$-0.2364 \pm 0.004438$

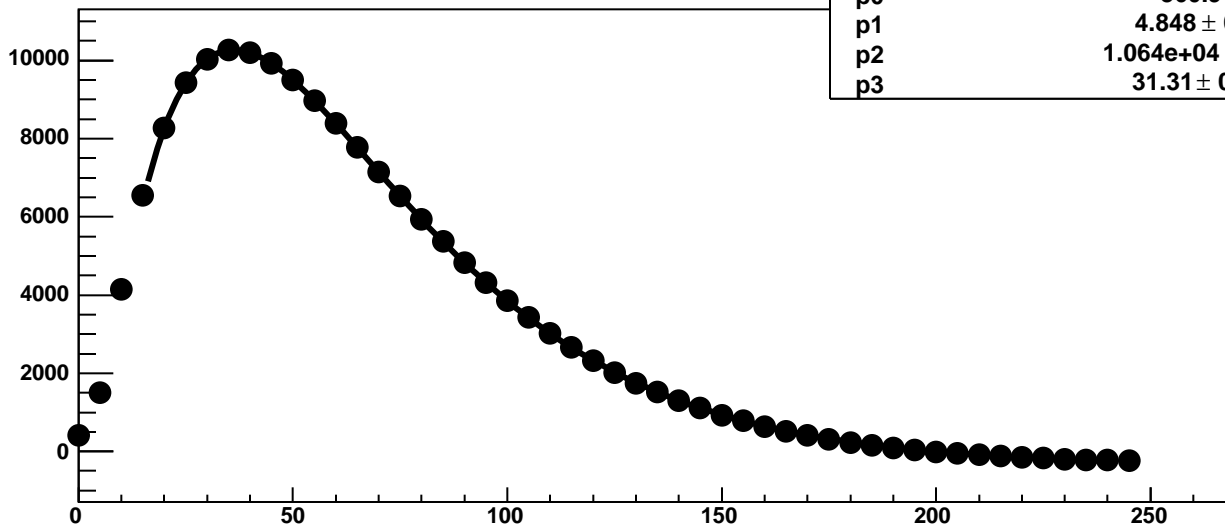
Chip 5, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold



Chip 5, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold



$\chi^2 / \text{ndf}$

806 / 42

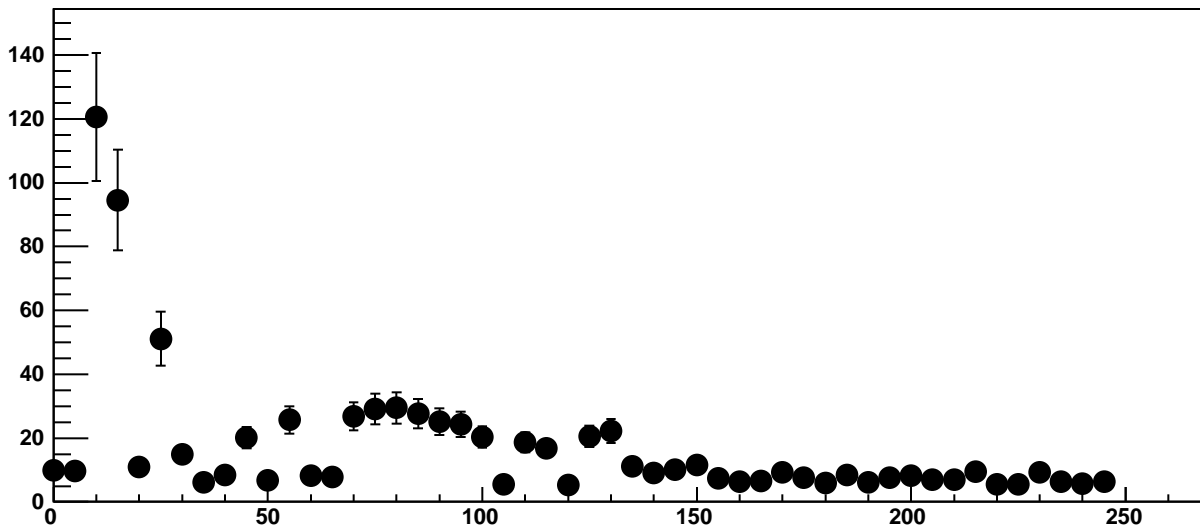
p0 -360.9 ± 0.5489

p1 4.848 ± 0.009481

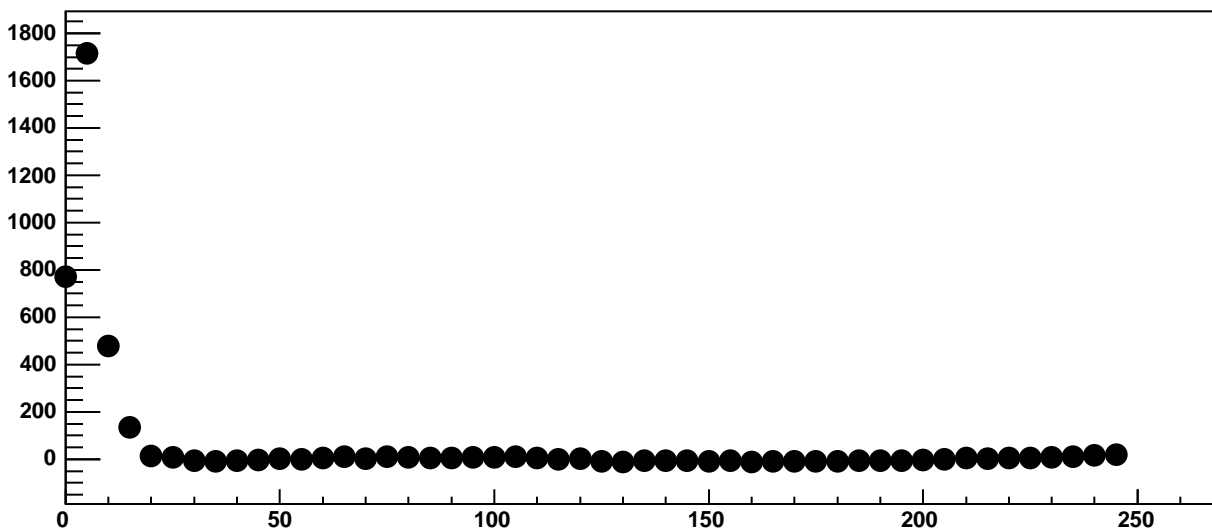
p2 1.064e+04 ± 0.9002

p3 31.31 ± 0.005723

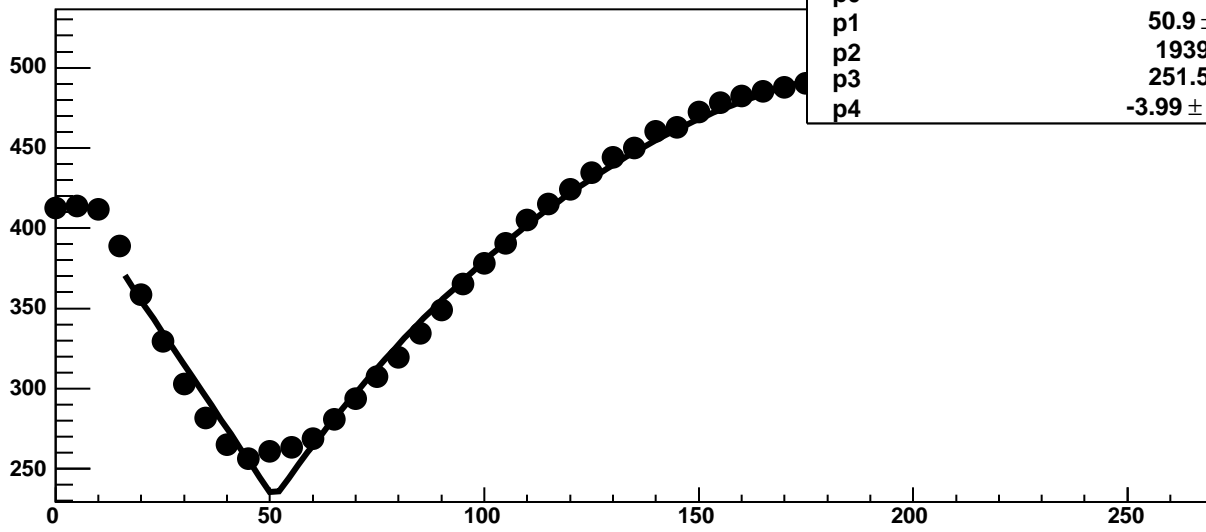
Chip 5, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold

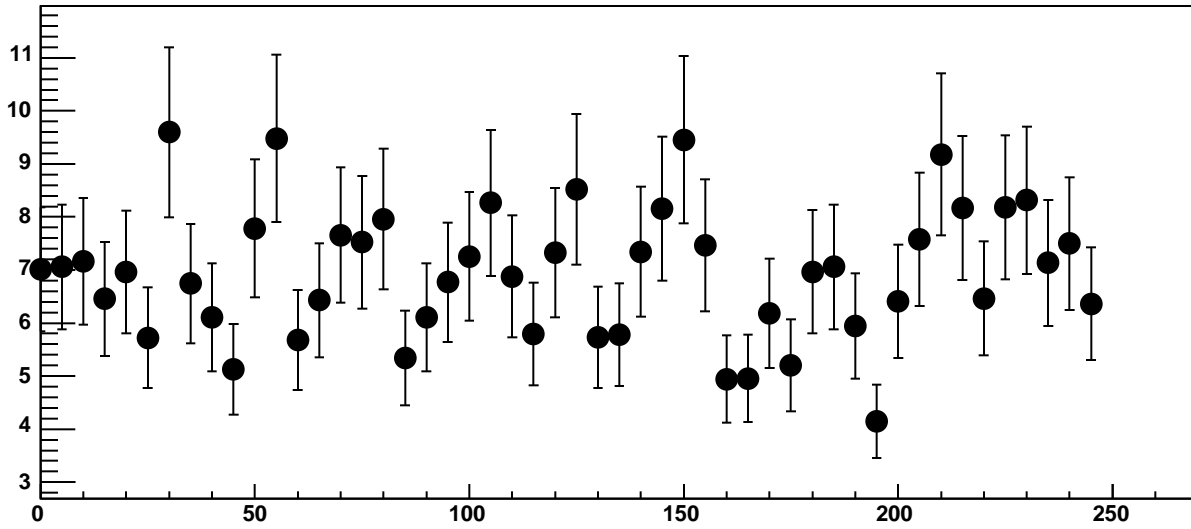


Chip 5, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

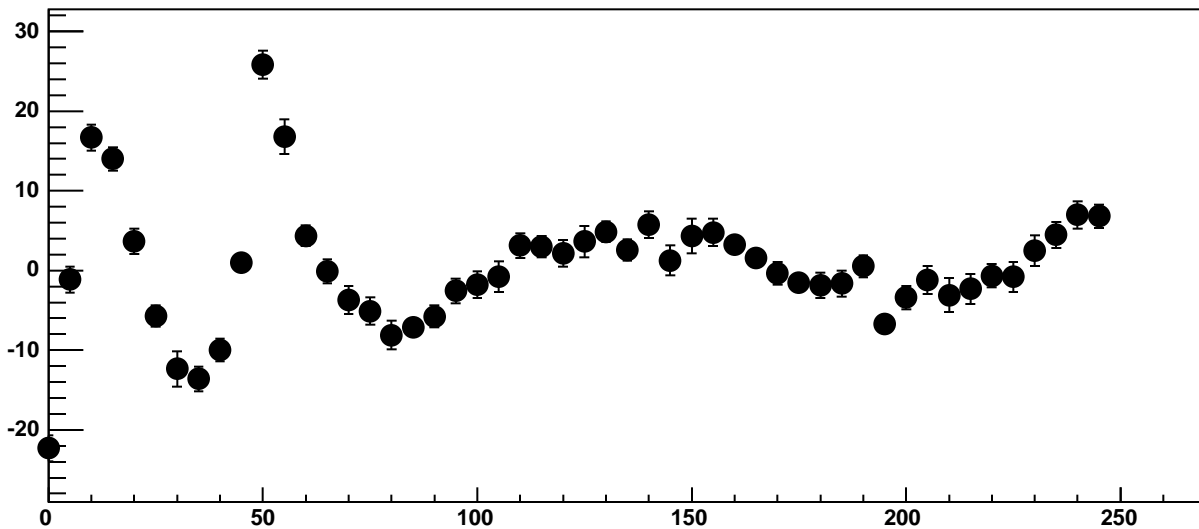


$\chi^2 / \text{ndf}$	795.7 / 41
p0	$231.6 \pm 0.6336$
p1	$50.9 \pm 0.1666$
p2	$1939 \pm 30.04$
p3	$251.5 \pm 3.057$
p4	$-3.99 \pm 0.04509$

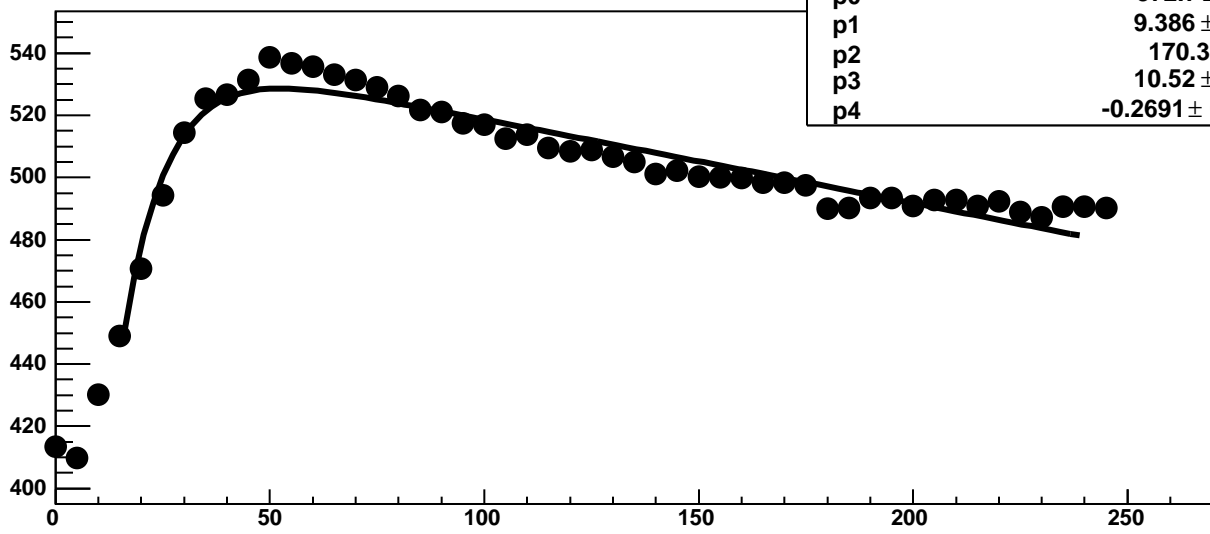
Chip 5, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

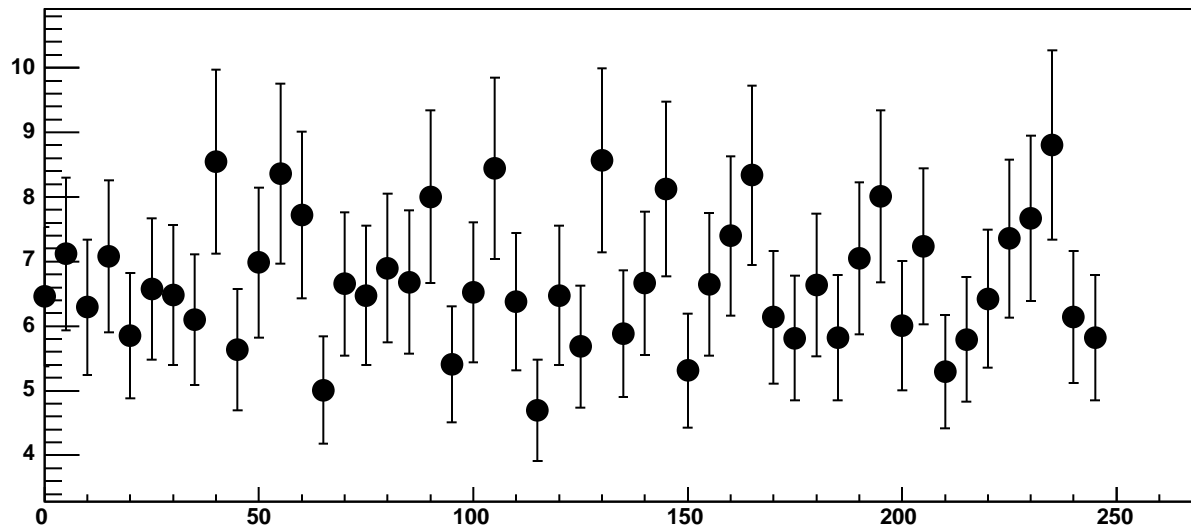


Chip 5, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

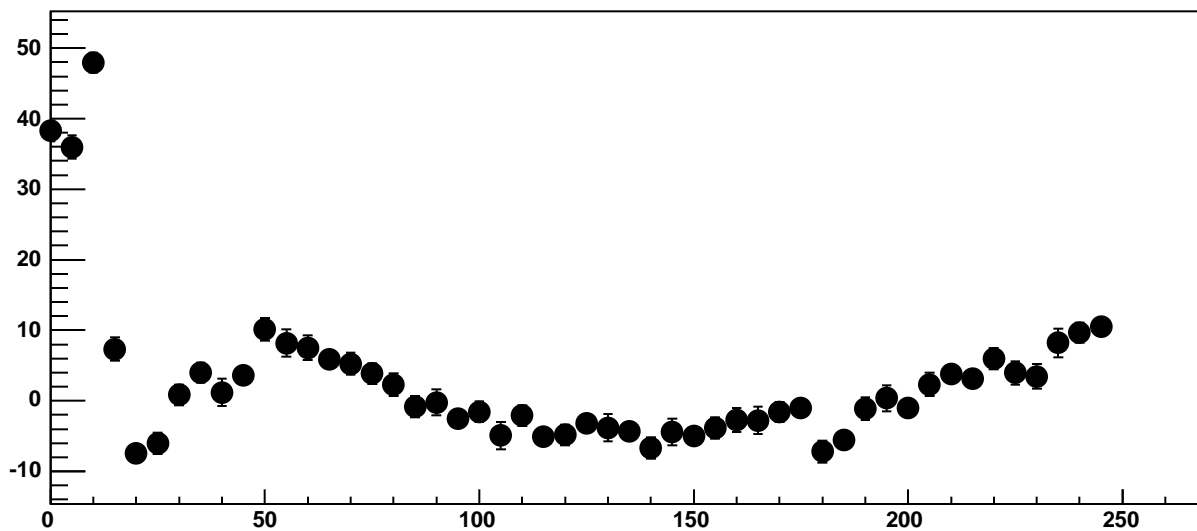


$\chi^2 / \text{ndf}$	475.6 / 41
p0	$372.7 \pm 0.3559$
p1	$9.386 \pm 0.2404$
p2	$170.3 \pm 0.373$
p3	$10.52 \pm 0.2656$
p4	$-0.2691 \pm 0.00438$

Chip 5, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold

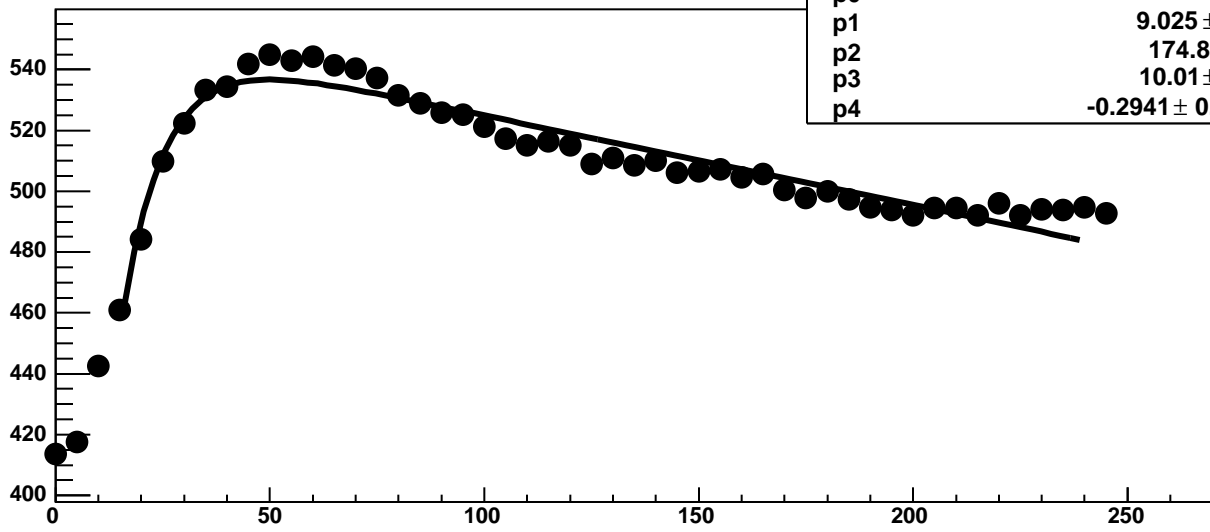


Chip 5, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

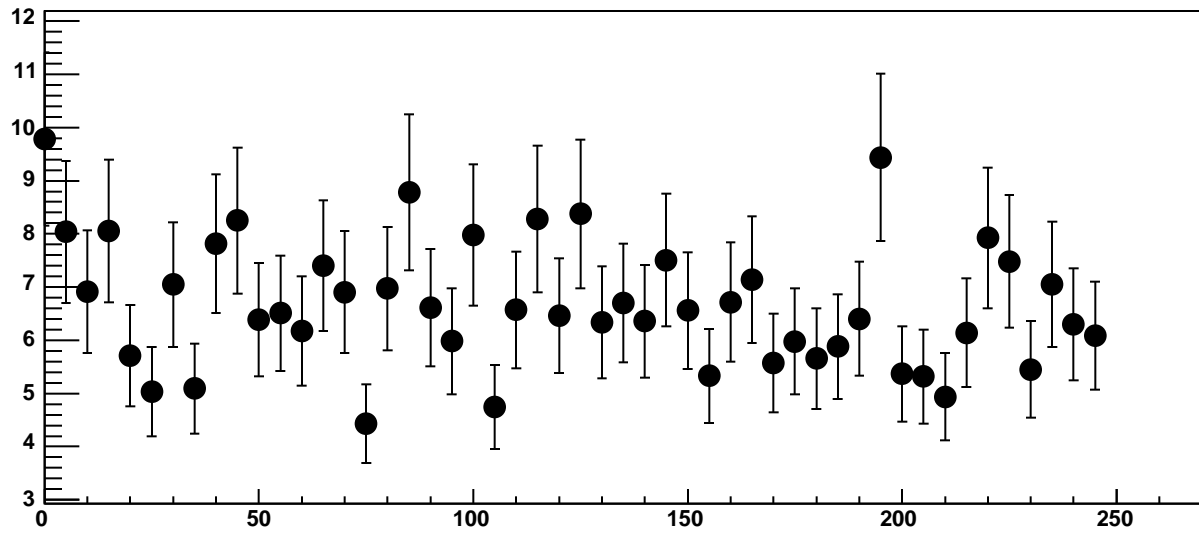




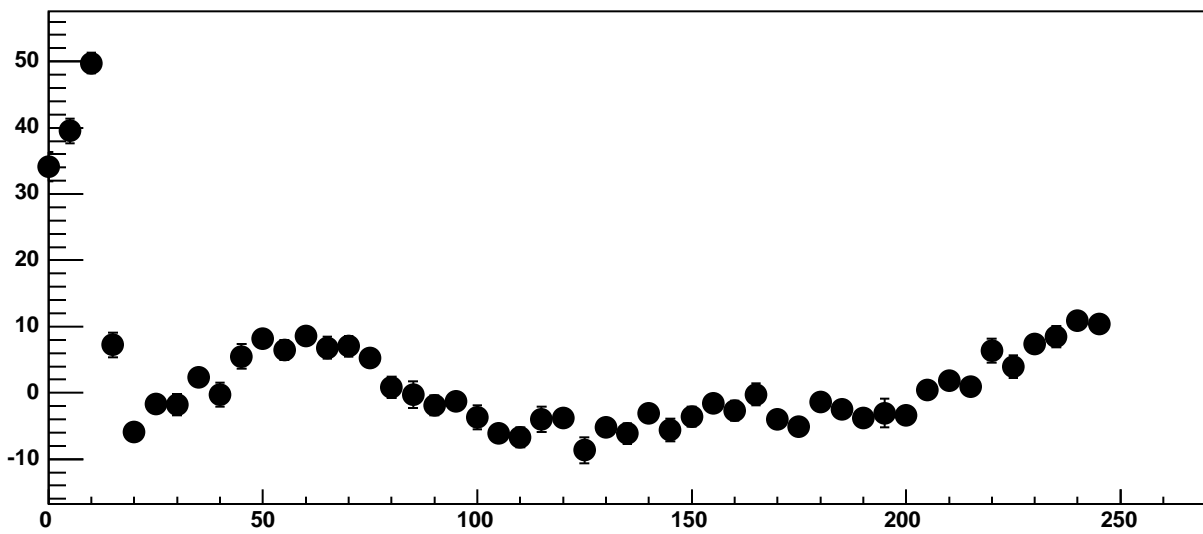
Chip 5, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold



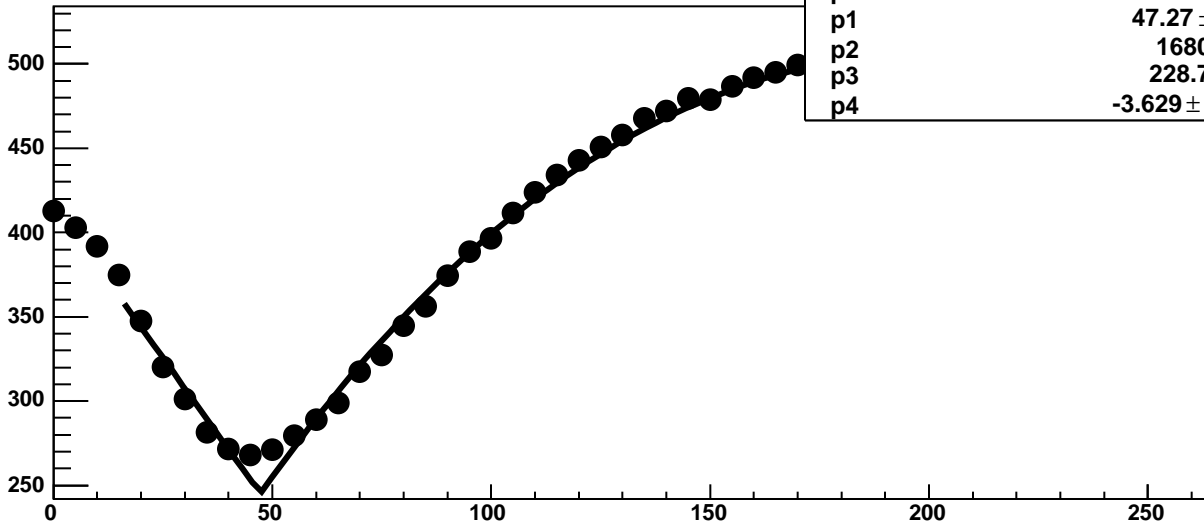
Chip 5, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold

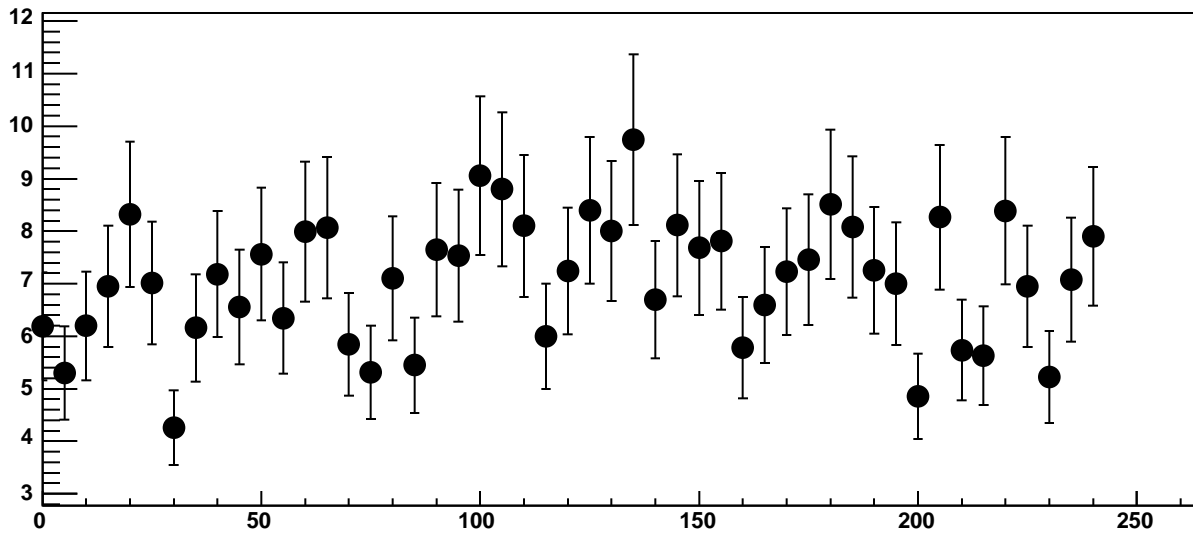


Chip 5, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

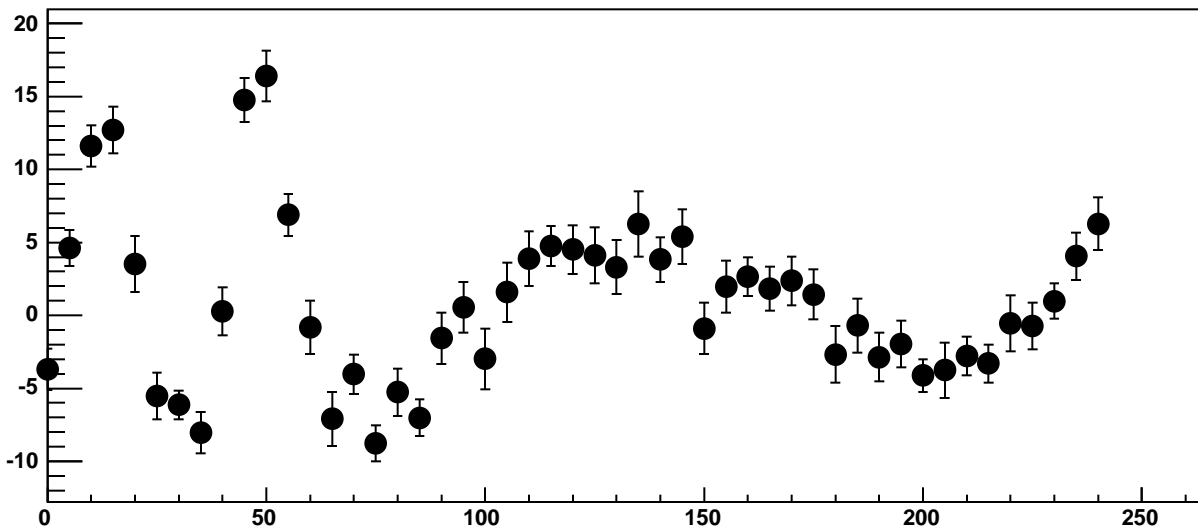


$\chi^2 / \text{ndf}$	596.6 / 41
p0	$244.9 \pm 0.6844$
p1	$47.27 \pm 0.1994$
p2	$1680 \pm 35.51$
p3	$228.7 \pm 3.441$
p4	$-3.629 \pm 0.05977$

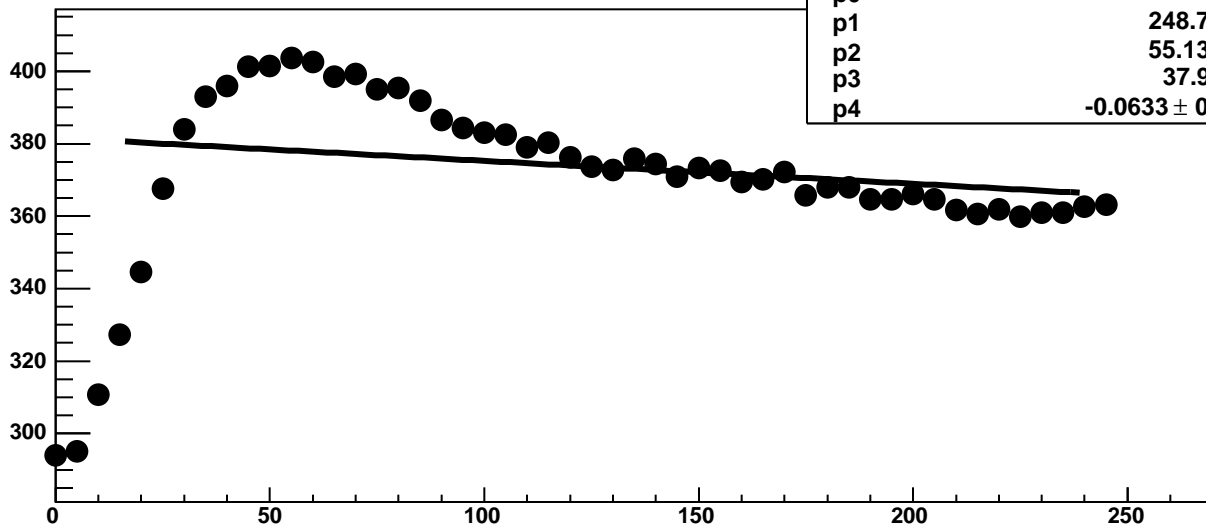
Chip 5, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold

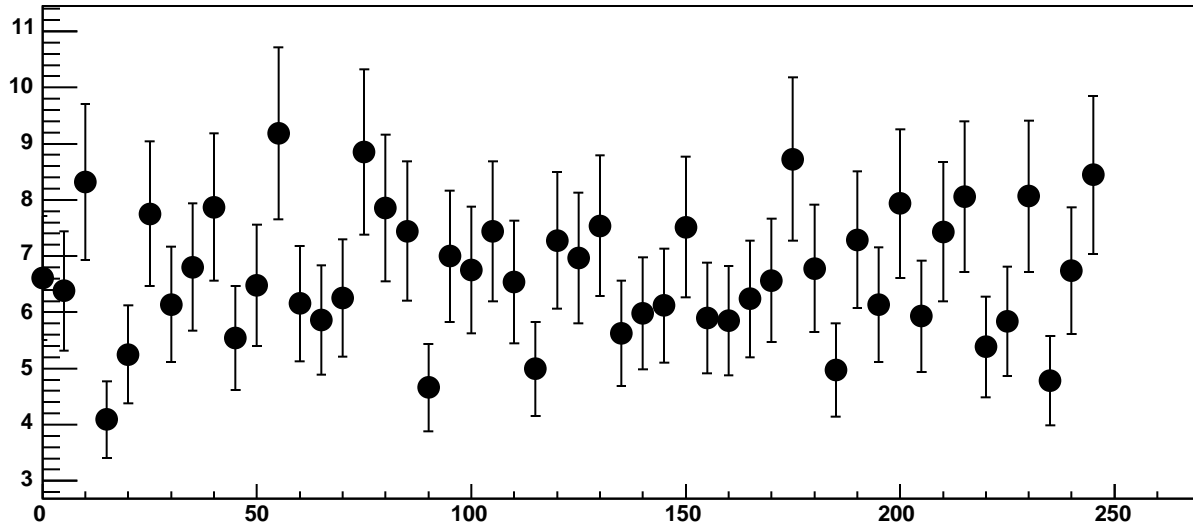


Chip 5, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

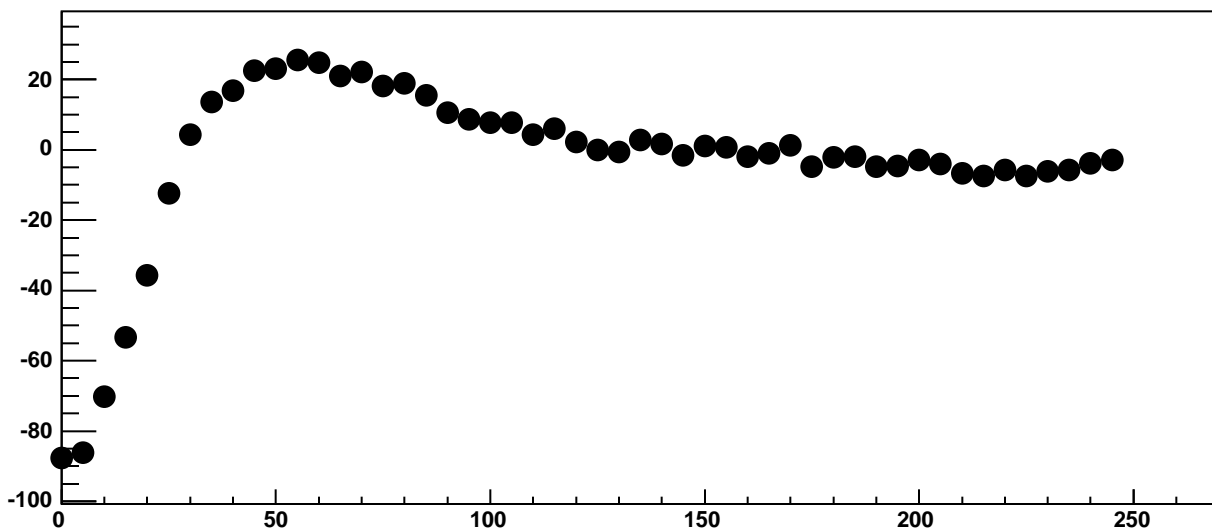


$\chi^2 / \text{ndf}$	6494 / 41
p0	$365.8 \pm 0.2135$
p1	$248.7 \pm 3.373$
p2	$55.13 \pm 84.85$
p3	$37.9 \pm 1.414$
p4	$-0.0633 \pm 0.001513$

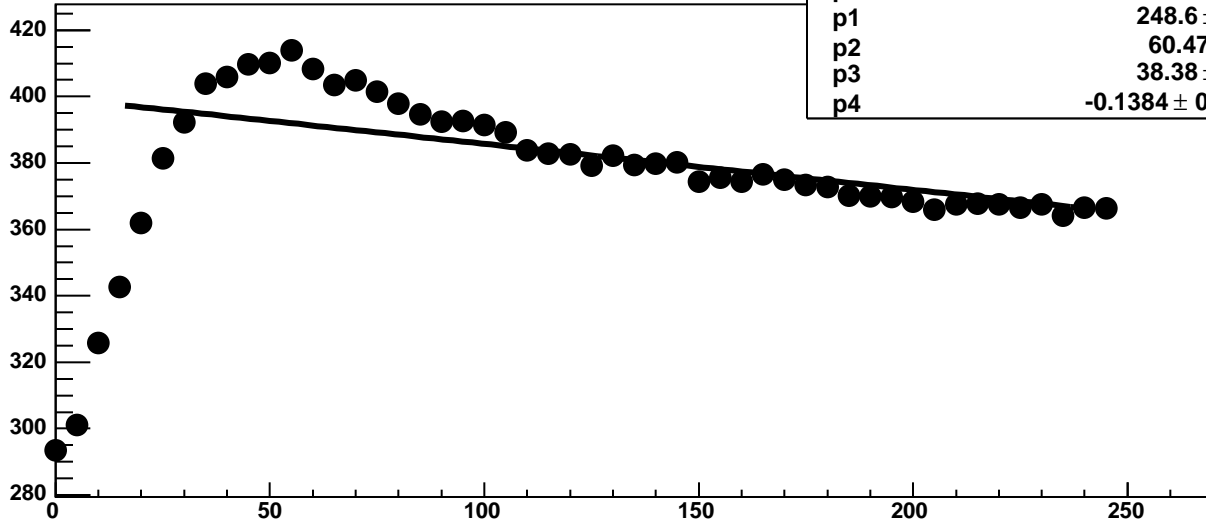
Chip 5, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

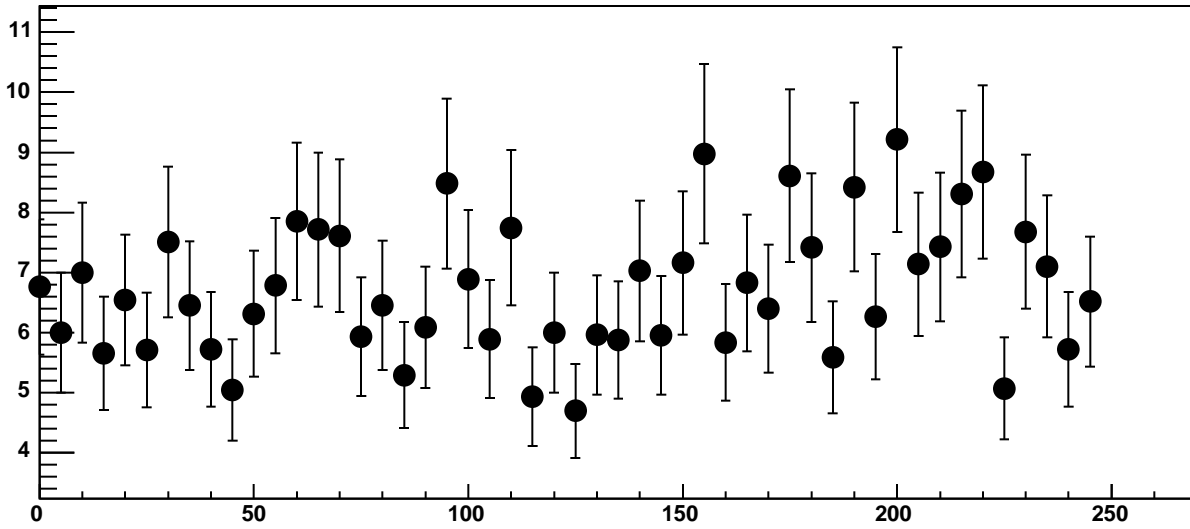


Chip 5, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold

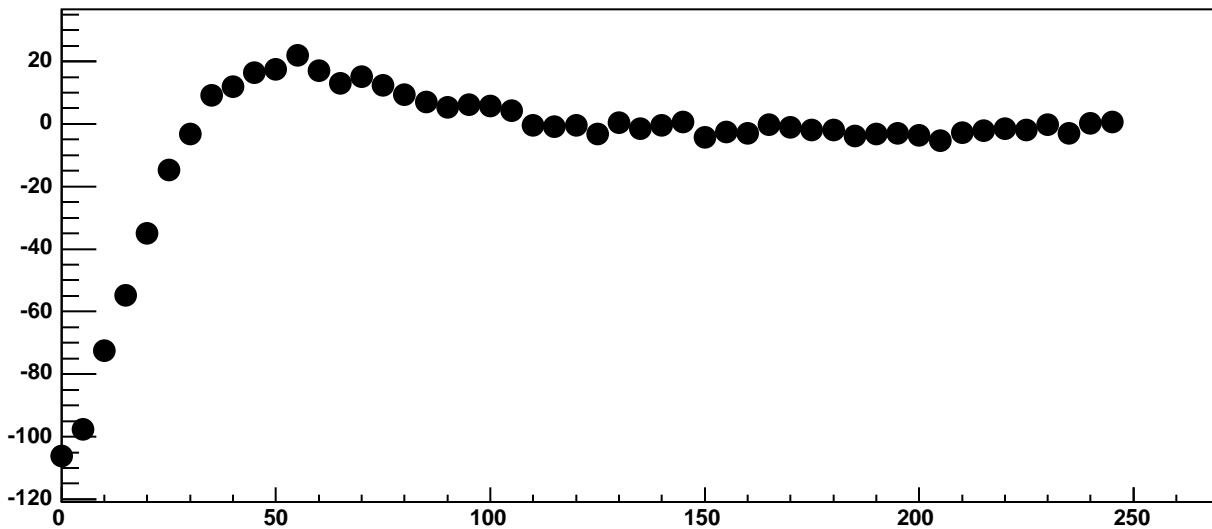


$\chi^2 / \text{ndf}$	3604 / 41
p0	$365.2 \pm 0.458$
p1	$248.6 \pm 0.2857$
p2	$60.47 \pm 1.474$
p3	$38.38 \pm 0.1723$
p4	$-0.1384 \pm 0.003318$

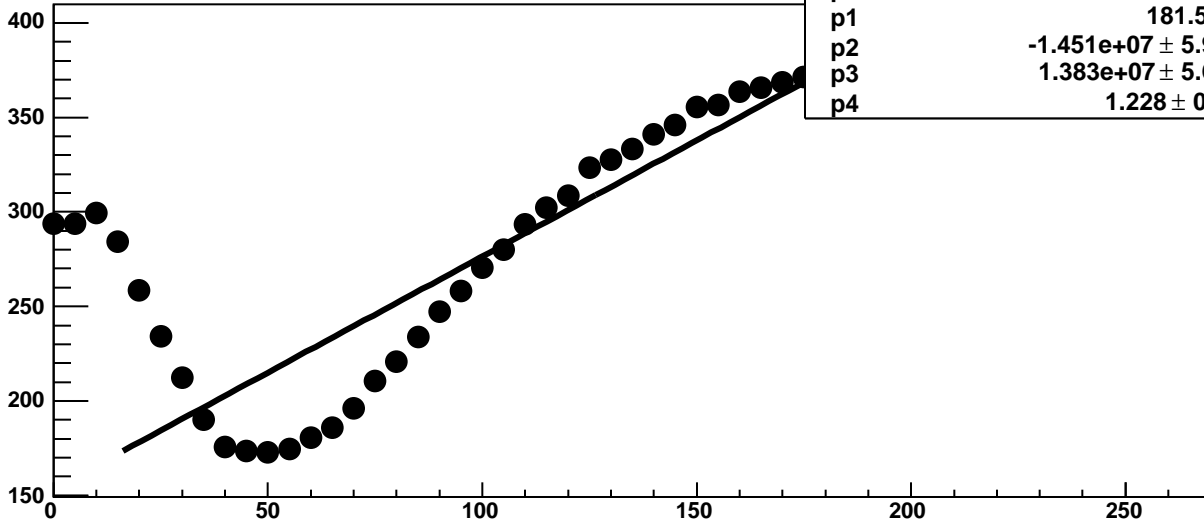
Chip 5, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold

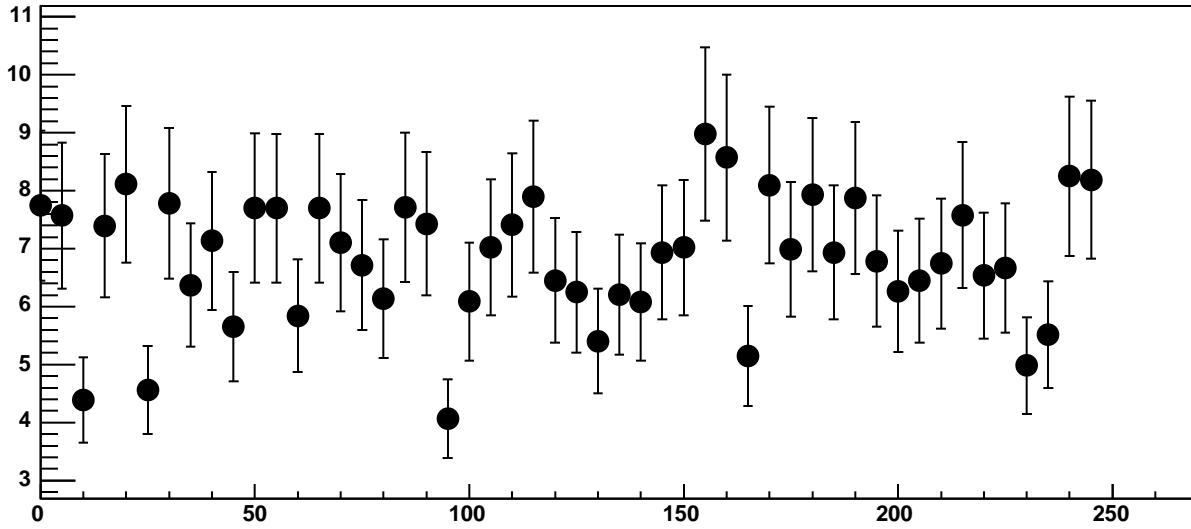


Chip 5, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

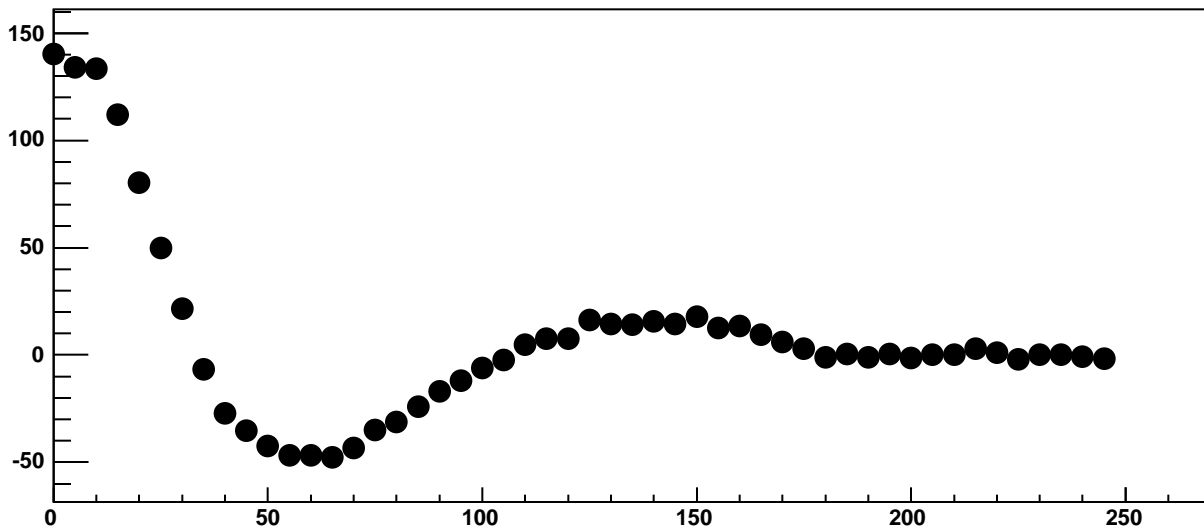


$\chi^2 / \text{ndf}$	1.599e+04 / 41
p0	376.6 ± 1.104
p1	181.5 ± 1.028
p2	-1.451e+07 ± 5.977e+06
p3	1.383e+07 ± 5.632e+06
p4	1.228 ± 0.005419

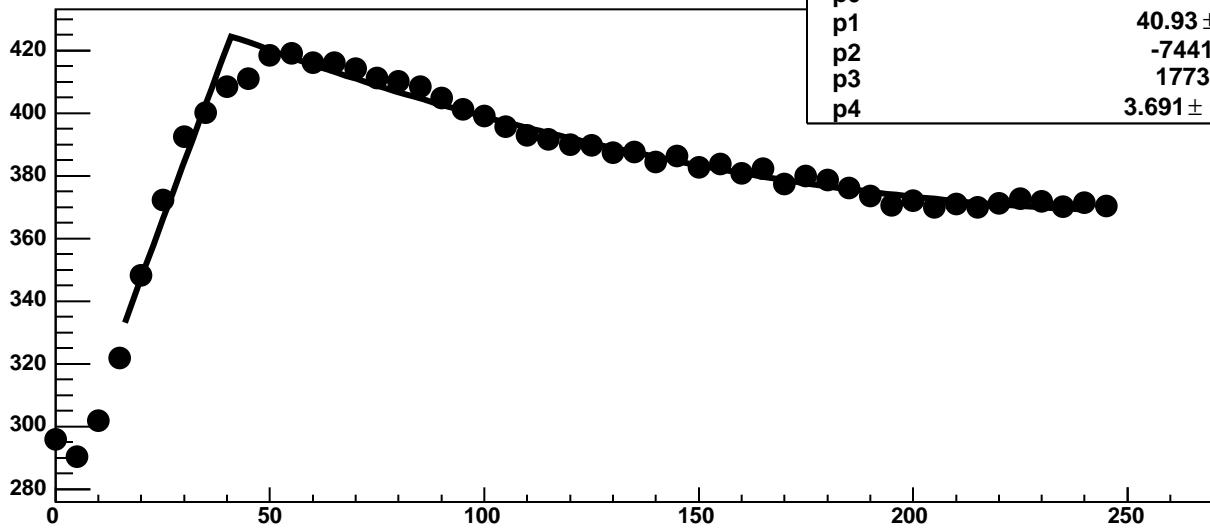
Chip 5, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

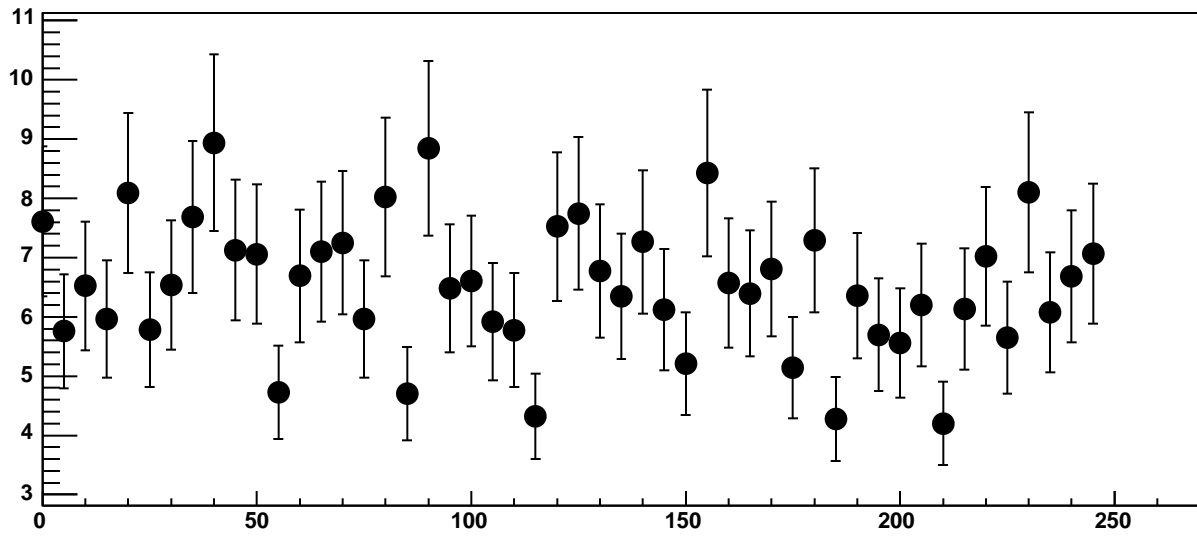


Chip 5, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

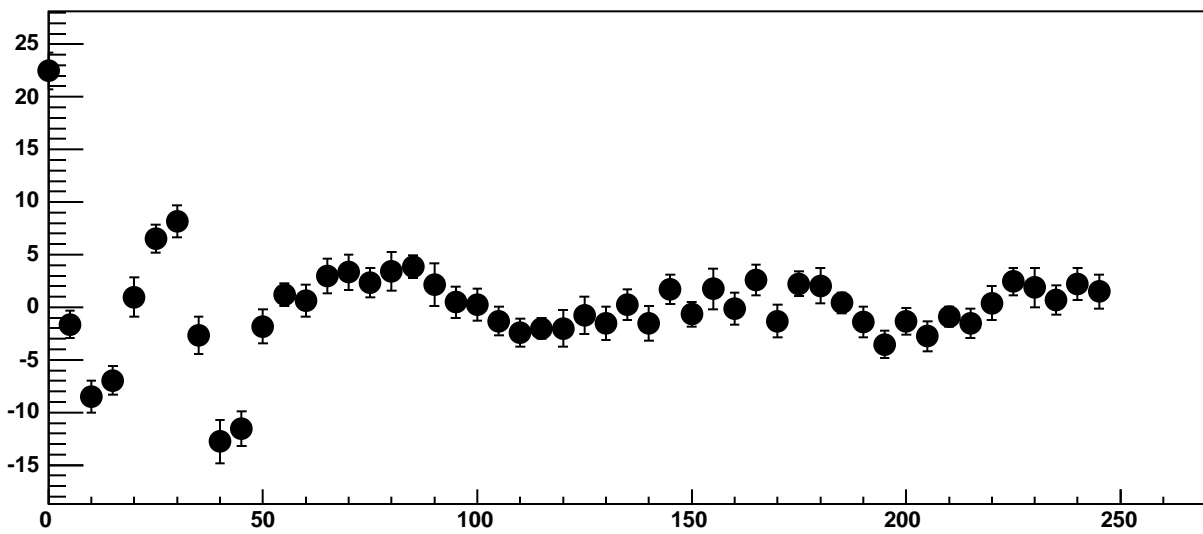


$\chi^2 / \text{ndf}$	246.3 / 41
p0	$424.7 \pm 0.6619$
p1	$40.93 \pm 0.3689$
p2	$-7441 \pm 567.7$
p3	$1773 \pm 127.8$
p4	$3.691 \pm 0.08062$

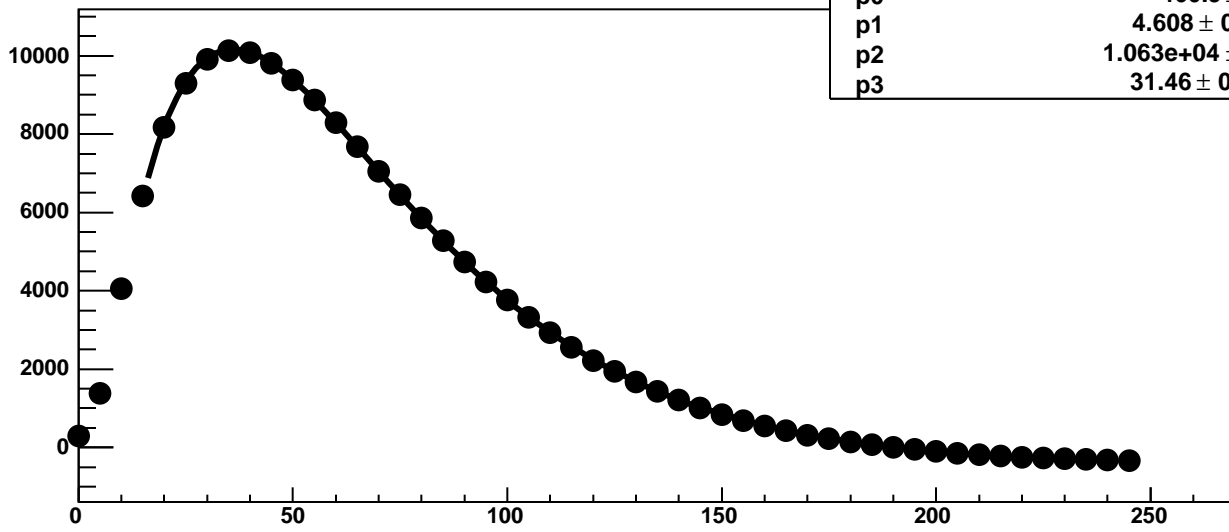
Chip 5, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

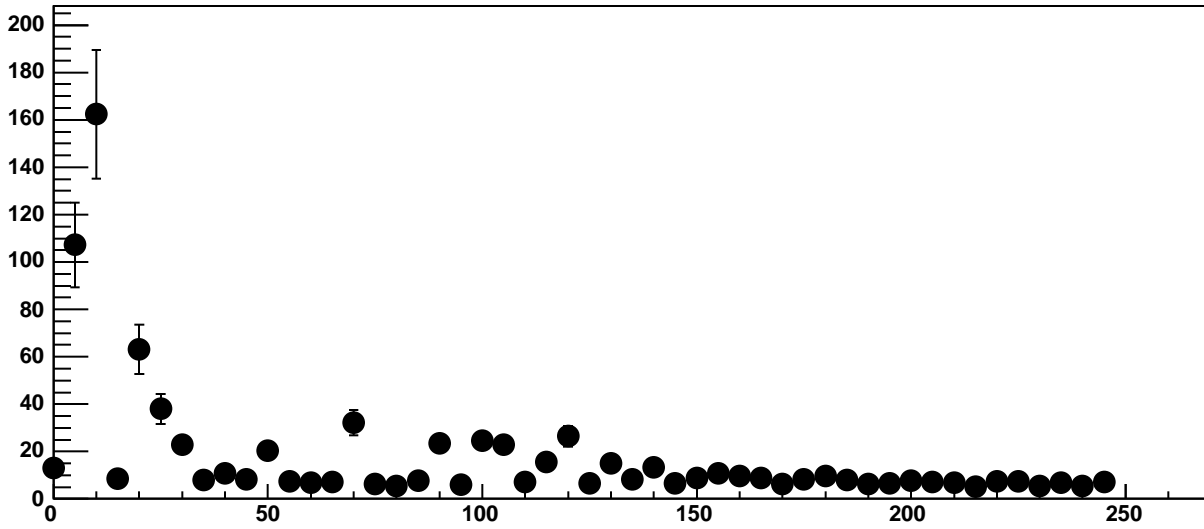


Chip 5, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold

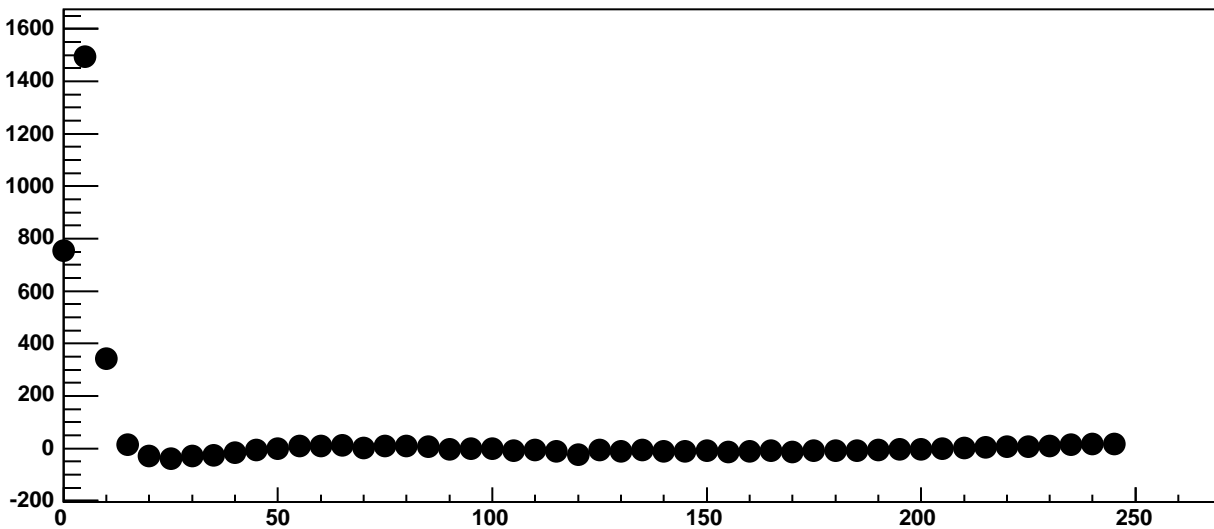


$\chi^2 / \text{ndf}$	1362 / 42
p0	-460.9 ± 0.5038
p1	4.608 ± 0.005522
p2	1.063e+04 ± 0.9293
p3	31.46 ± 0.004614

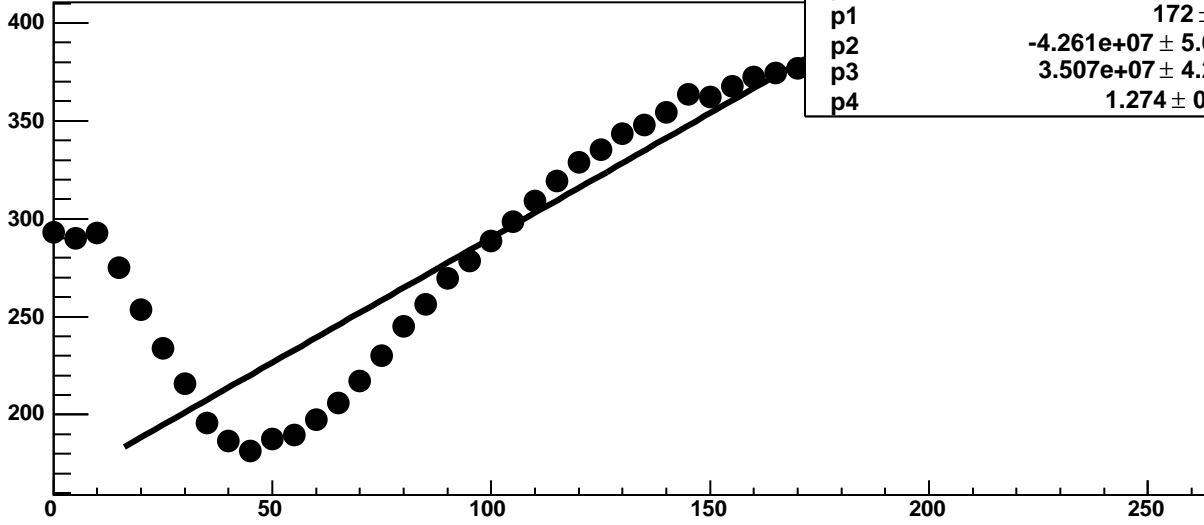
Chip 5, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold

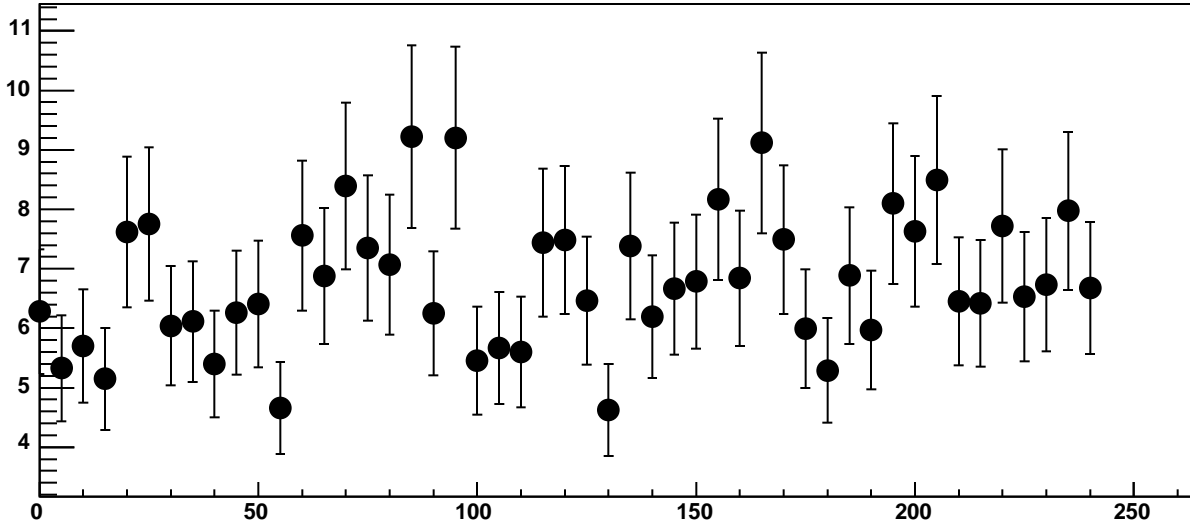


Chip 5, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

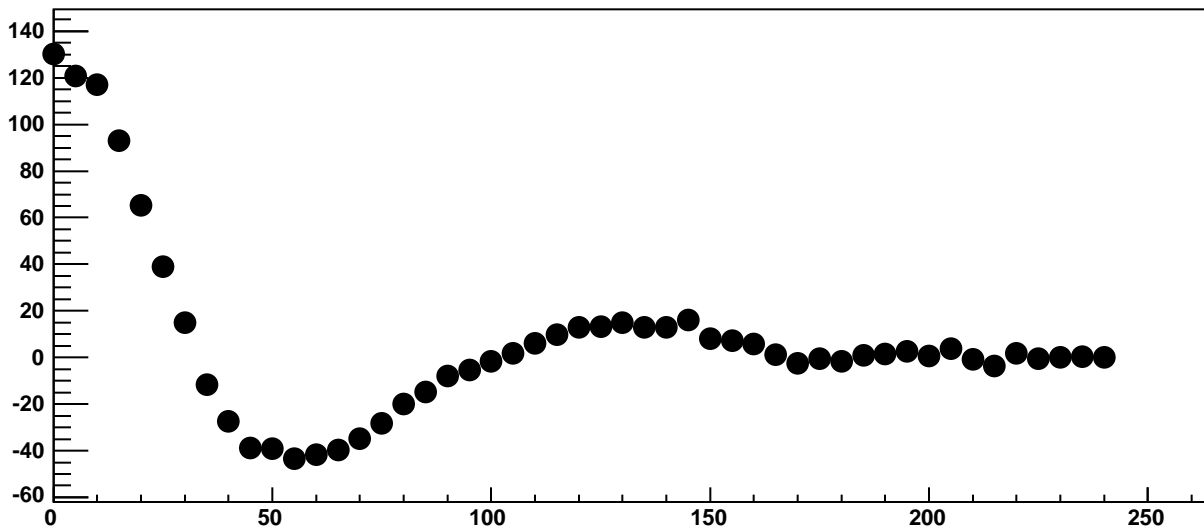


$\chi^2 / \text{ndf}$	1.462e+04 / 41
p0	382.1 ± 0.7745
p1	172 ± 0.7435
p2	-4.261e+07 ± 5.094e+06
p3	3.507e+07 ± 4.221e+06
p4	1.274 ± 0.005689

Chip 5, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold

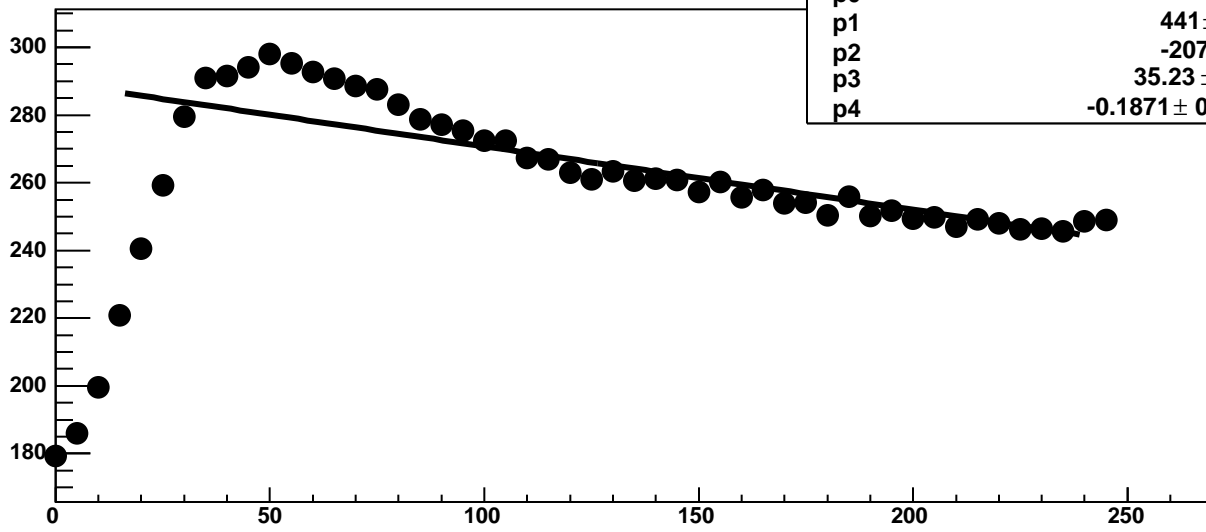


Chip 5, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold



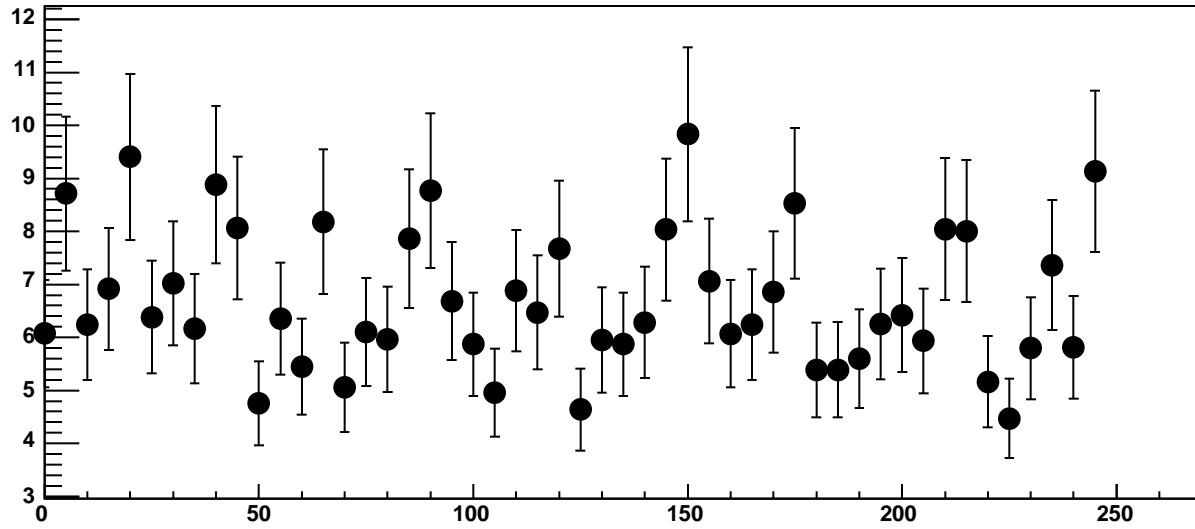


Chip 5, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

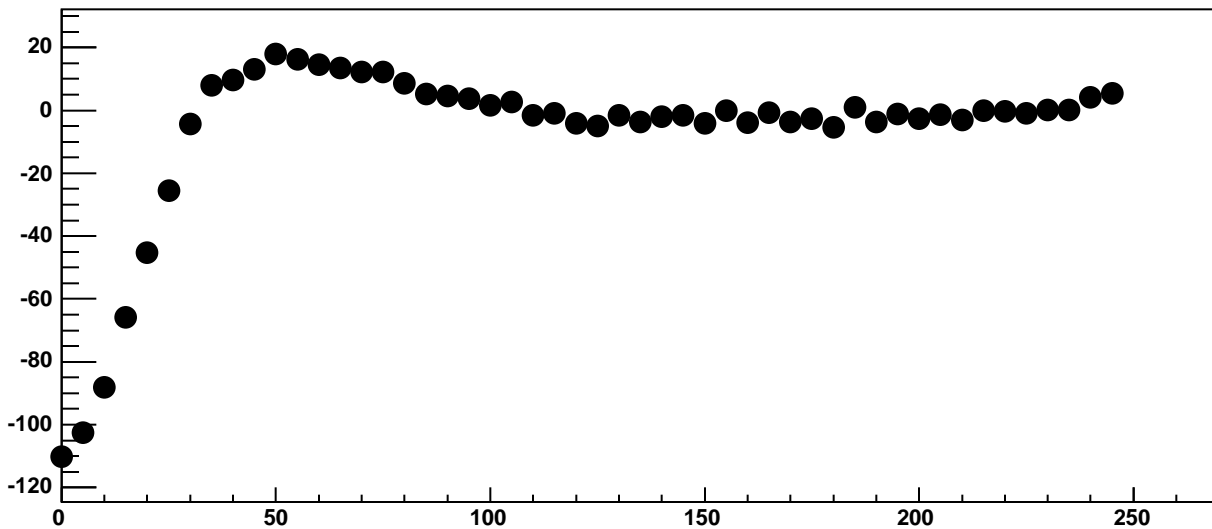


$\chi^2 / \text{ndf}$	3515 / 41
p0	$206.9 \pm 1.104$
p1	$441 \pm 0.8558$
p2	$-207 \pm 8.895$
p3	$35.23 \pm 0.3946$
p4	$-0.1871 \pm 0.003213$

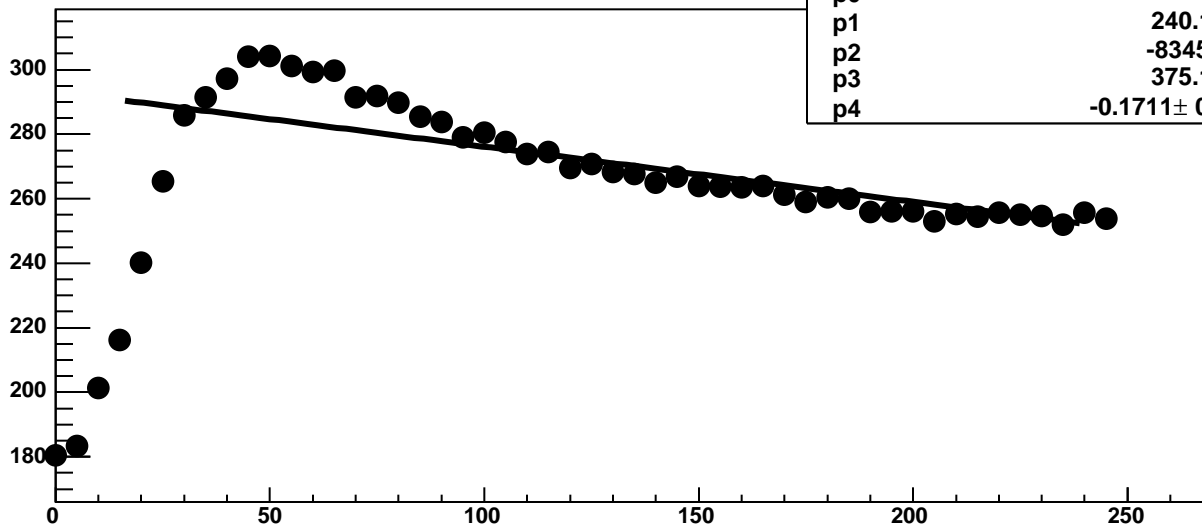
Chip 5, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold

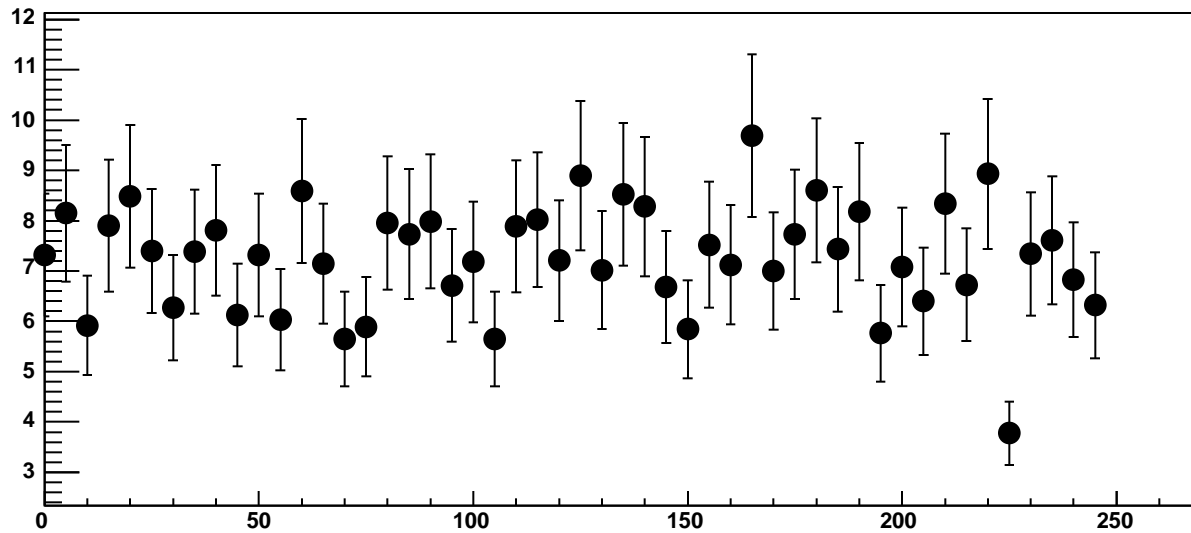


Chip 5, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

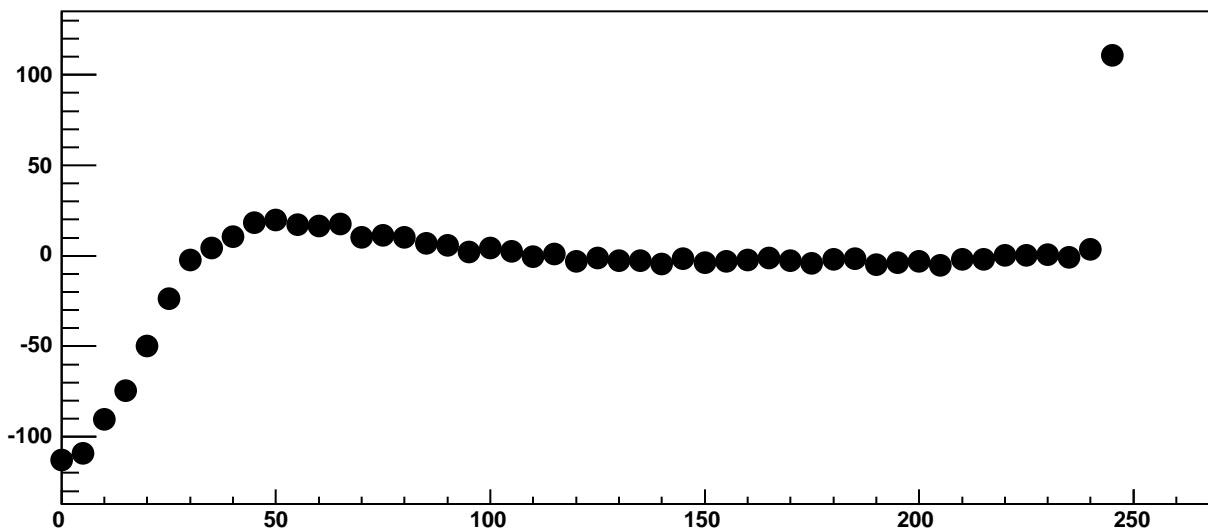


$\chi^2 / \text{ndf}$	3505 / 41
p0	$252.2 \pm 0.2364$
p1	$240.1 \pm 1.382$
p2	$-8345 \pm 84.85$
p3	$375.1 \pm 1.414$
p4	$-0.1711 \pm 0.001839$

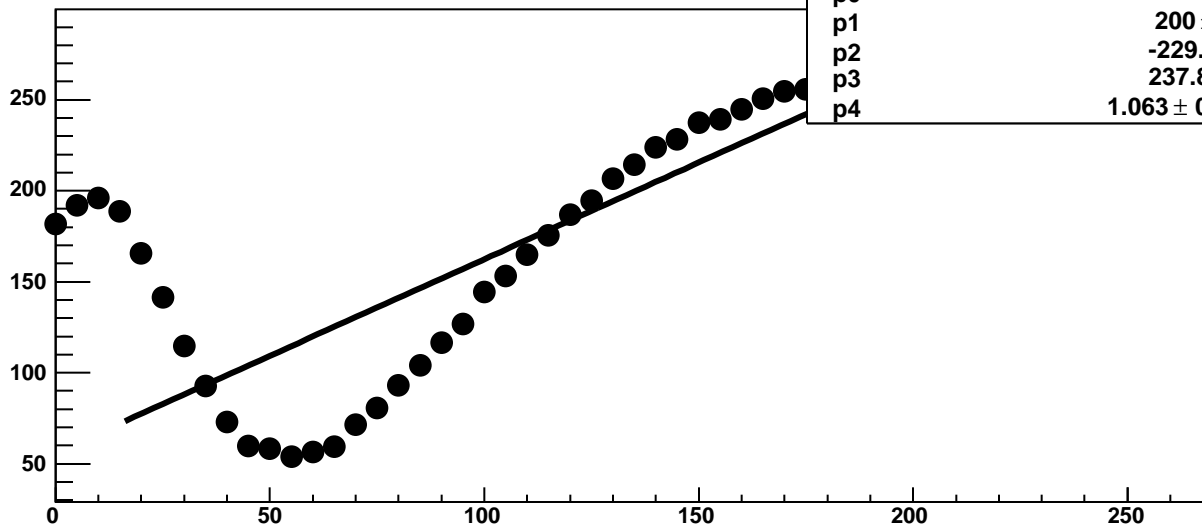
Chip 5, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



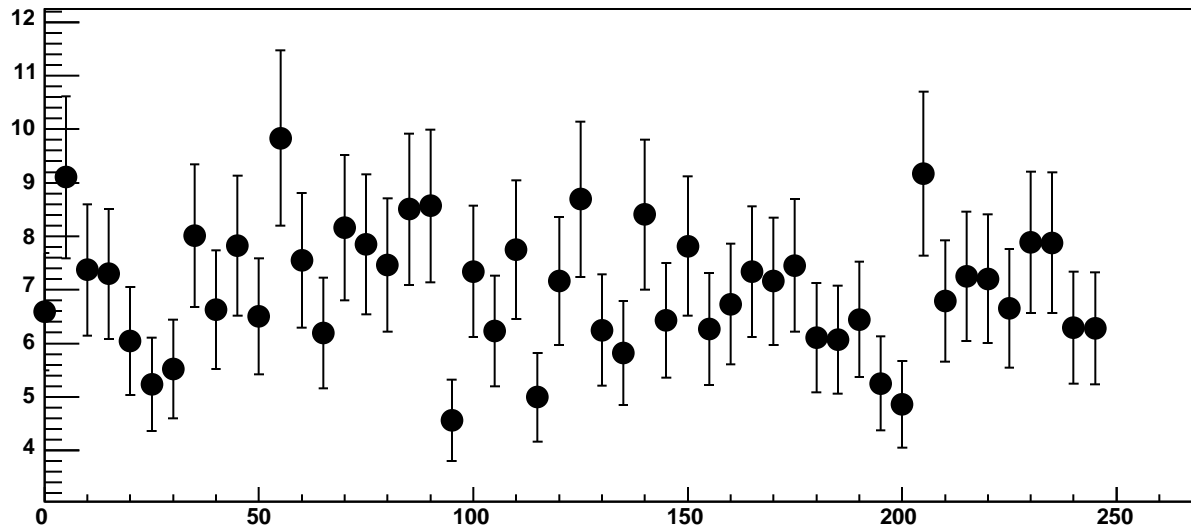
Chip 5, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold



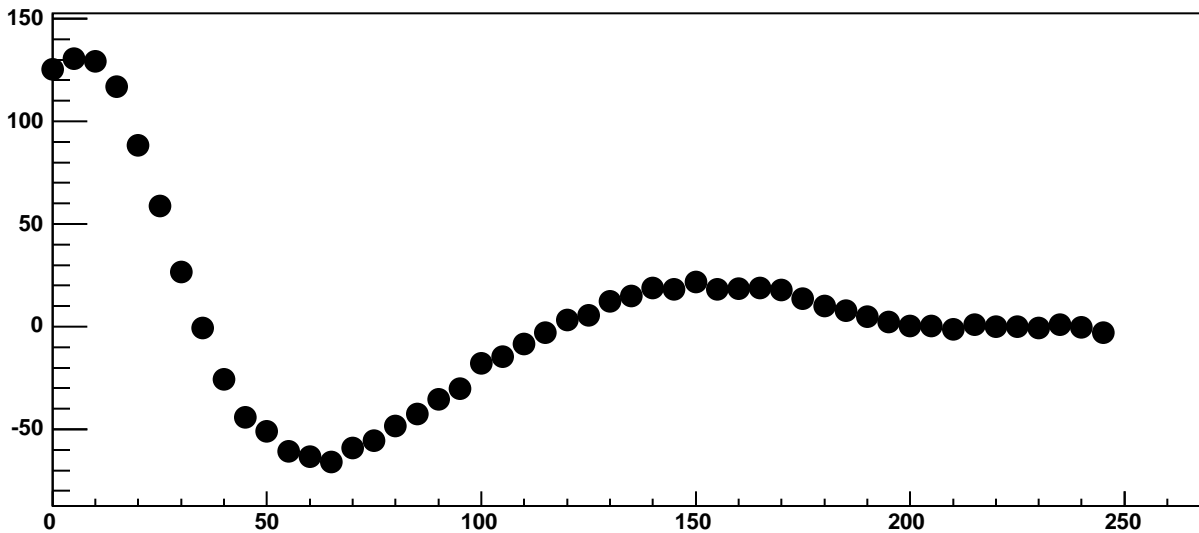
Chip 5, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold



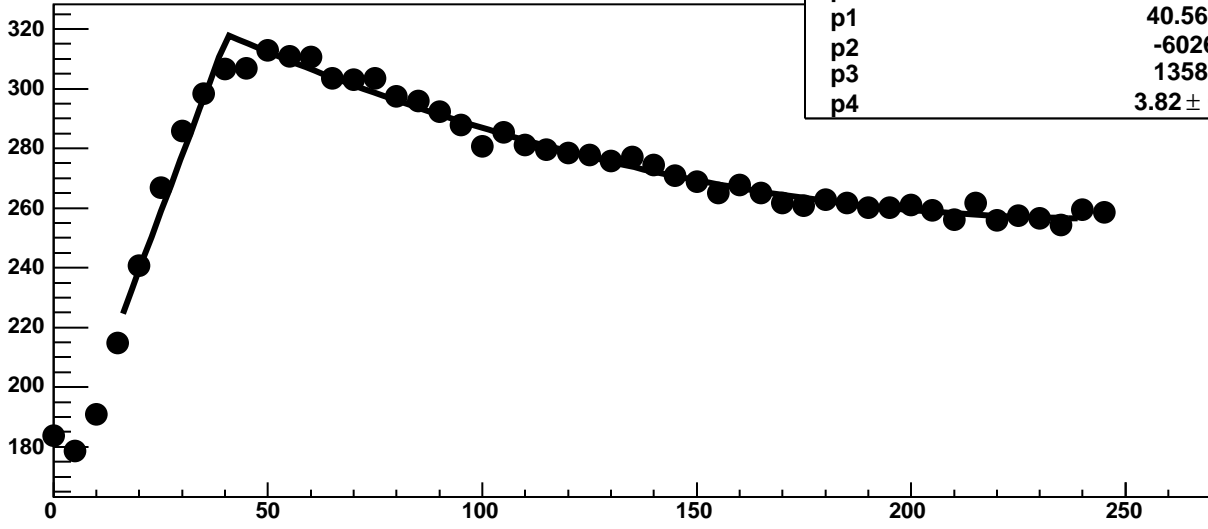
Chip 5, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold

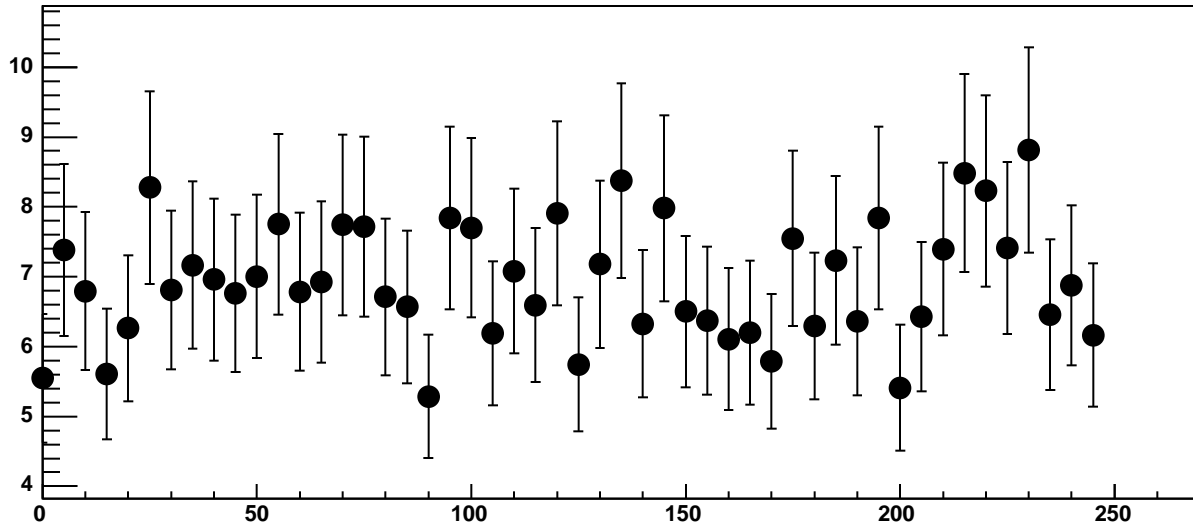


Chip 5, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

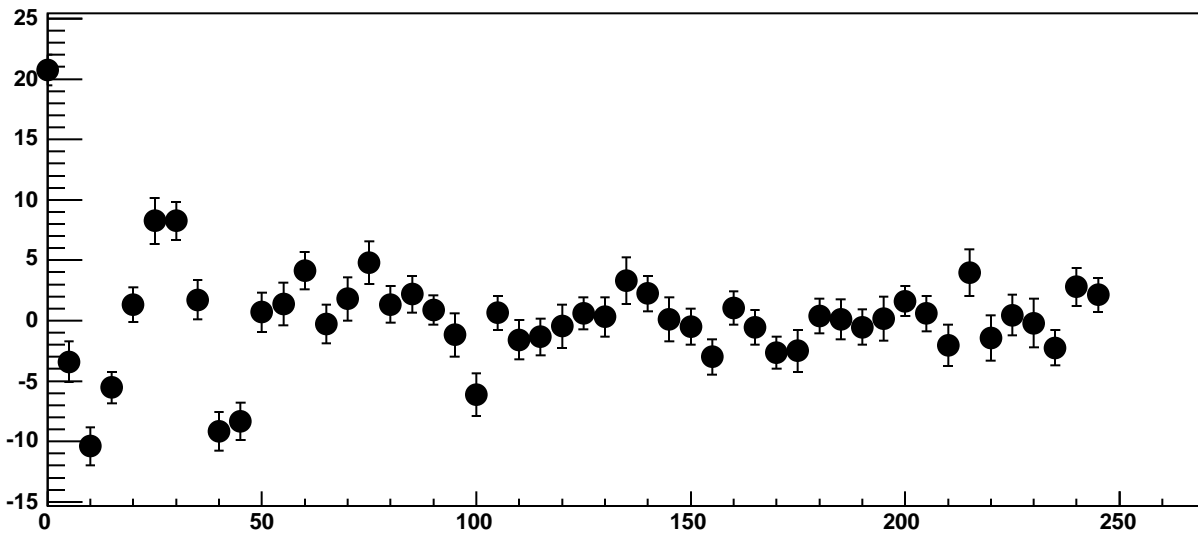


$\chi^2 / \text{ndf}$	193.8 / 41
p0	$317.9 \pm 0.6839$
p1	$40.56 \pm 0.308$
p2	$-6026 \pm 331$
p3	$1358 \pm 70.66$
p4	$3.82 \pm 0.06904$

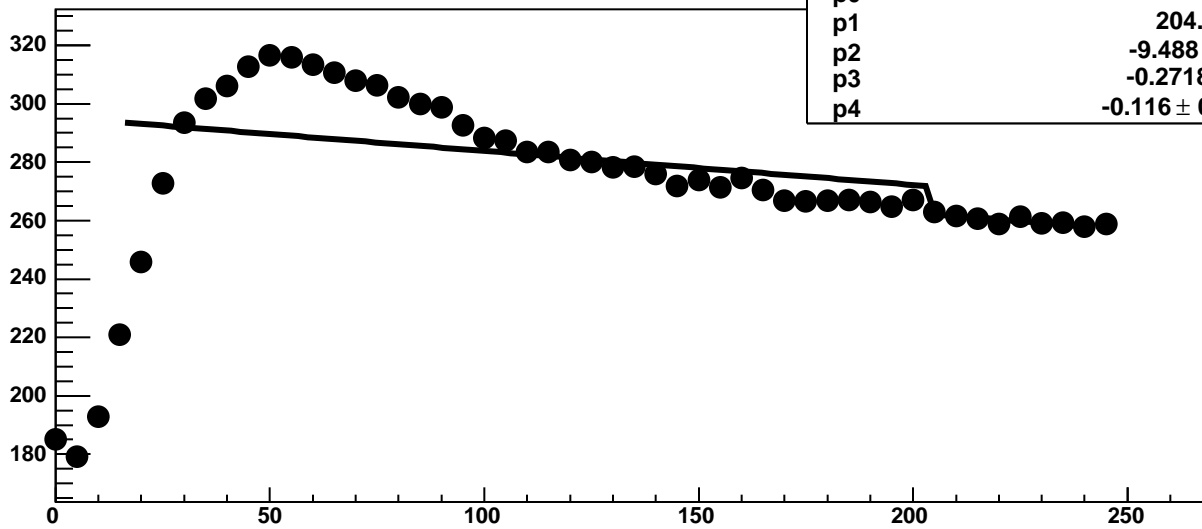
Chip 5, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

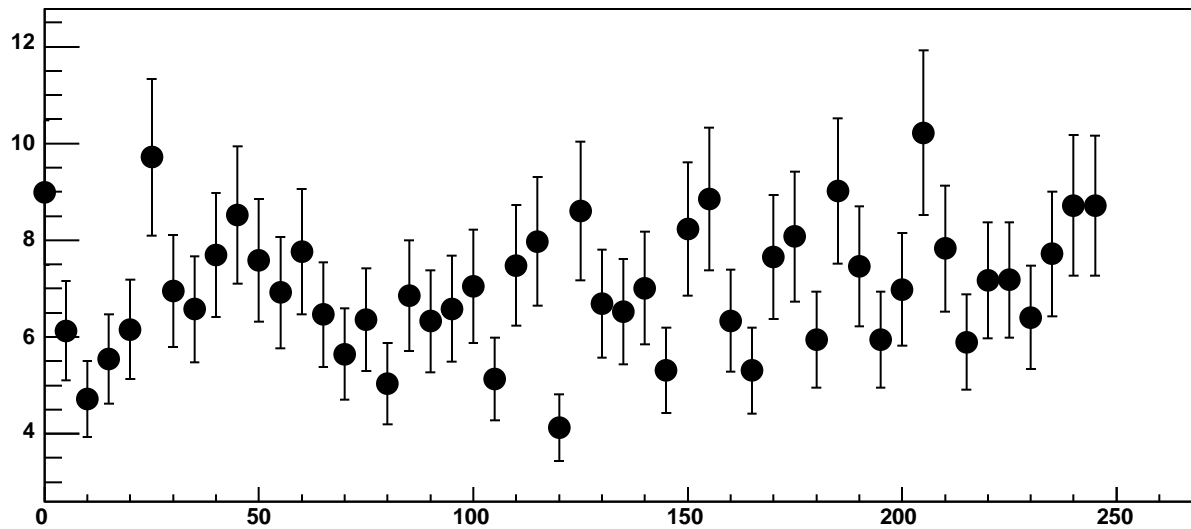


Chip 5, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

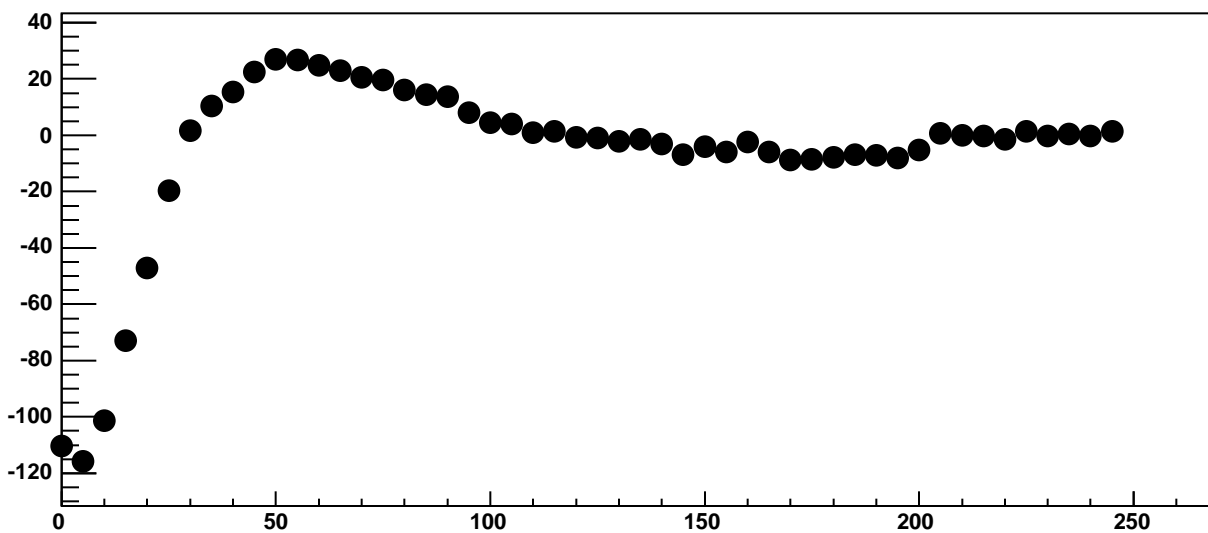


$\chi^2 / \text{ndf}$	6781 / 41
p0	$271.7 \pm 0.2122$
p1	$204.5 \pm 15.4$
p2	$-9.488 \pm 0.2529$
p3	$-0.2718 \pm 6.836$
p4	$-0.116 \pm 0.001942$

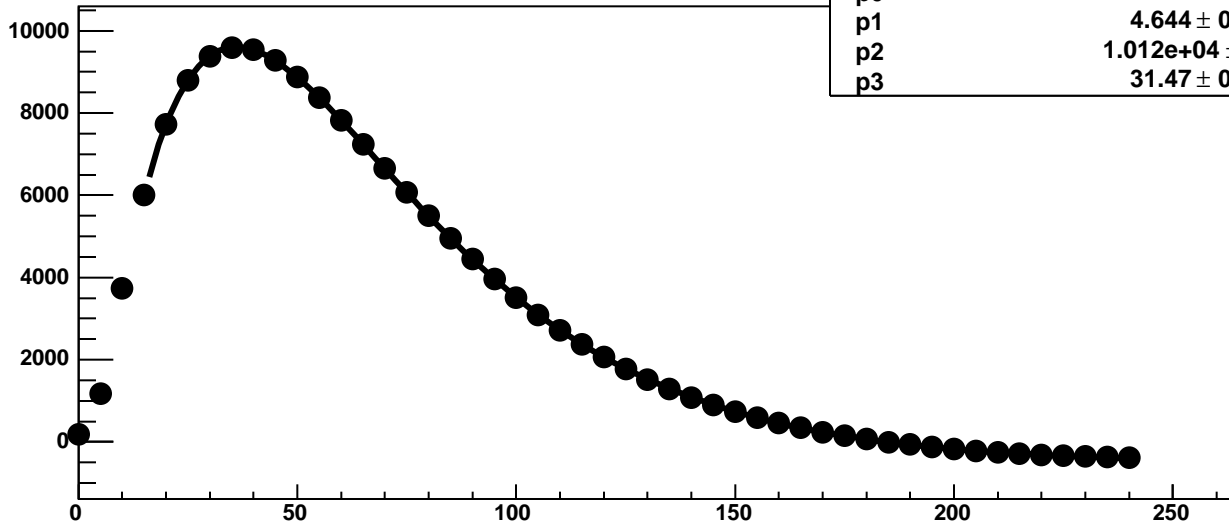
Chip 5, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

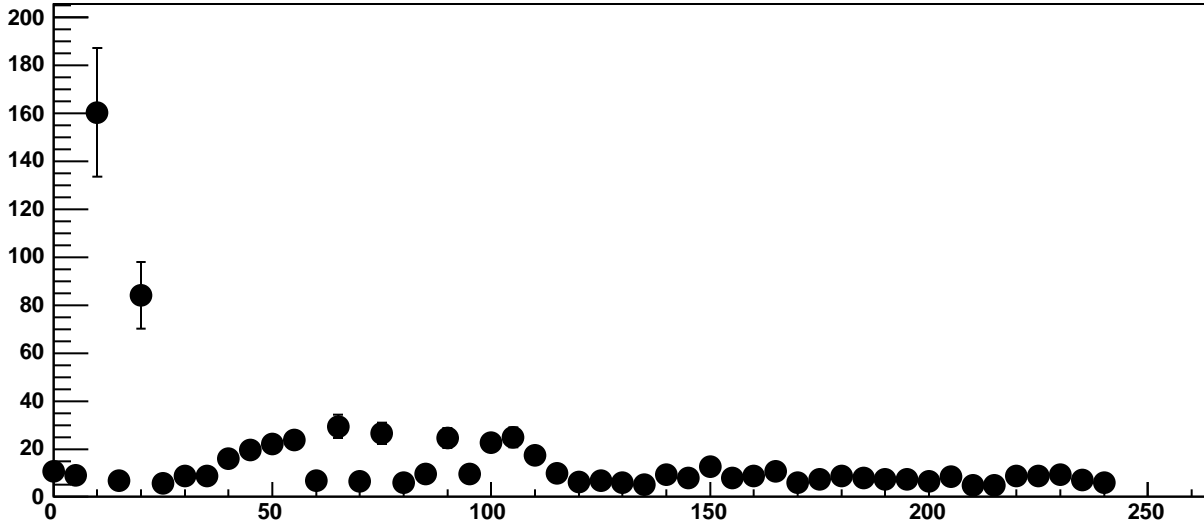


Chip 5, Channel 15, Enable 5!, DAC=4000, ADC Mean vs Hold

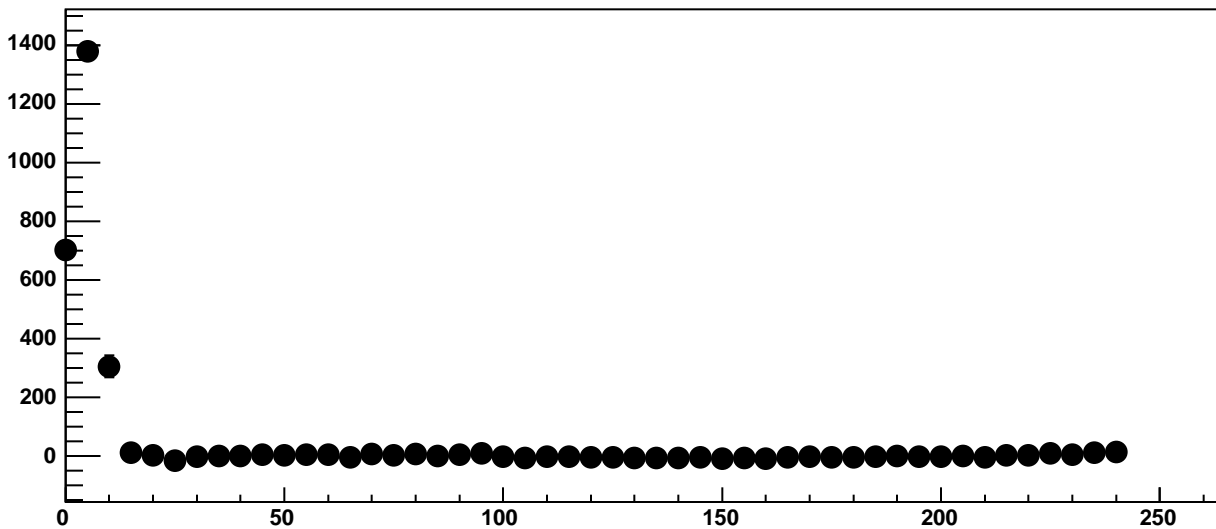


$\chi^2 / \text{ndf}$	594.7 / 42
p0	-514.7 ± 0.5239
p1	4.644 ± 0.005109
p2	1.012e+04 ± 0.9027
p3	31.47 ± 0.004628

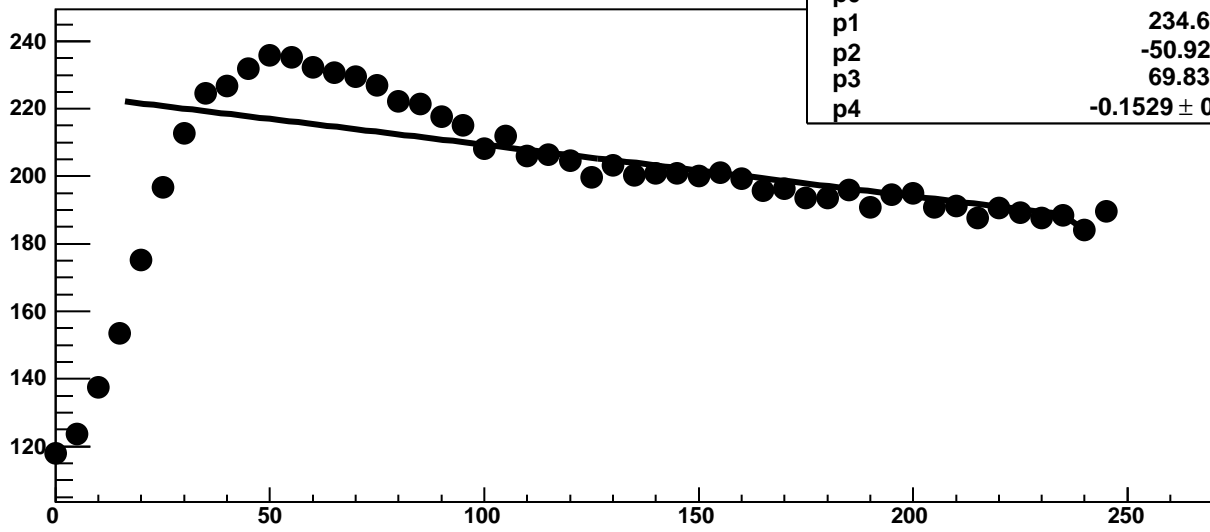
Chip 5, Channel 15, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 15, Enable 5!, DAC=4000, ADC Residuals vs Hold

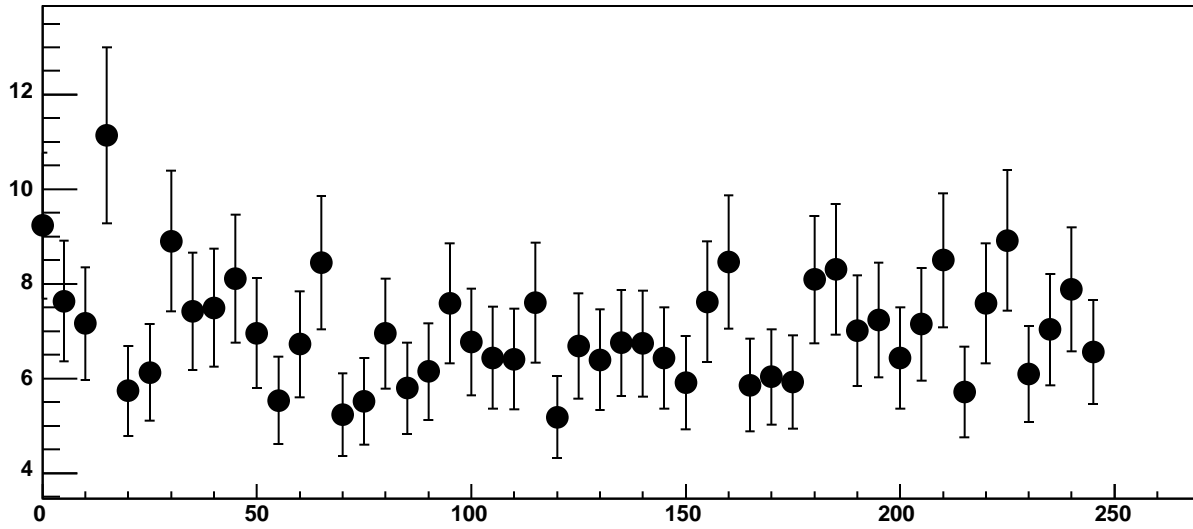


Chip 5, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

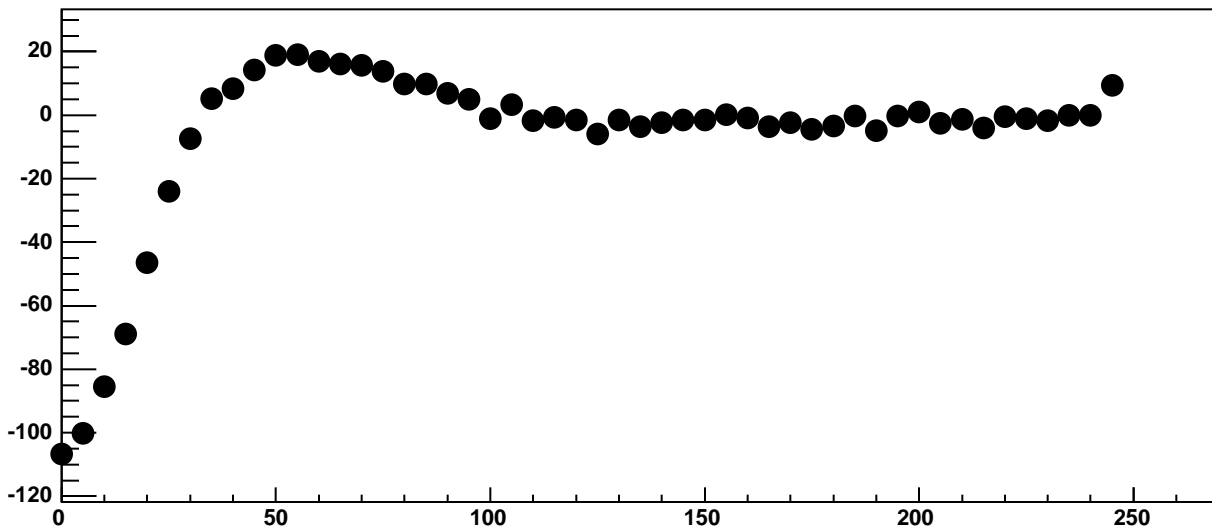


$\chi^2 / \text{ndf}$	3415 / 41
p0	$188.8 \pm 0.6884$
p1	$234.6 \pm 2.445$
p2	$-50.92 \pm 87.52$
p3	$69.83 \pm 123.6$
p4	$-0.1529 \pm 0.003791$

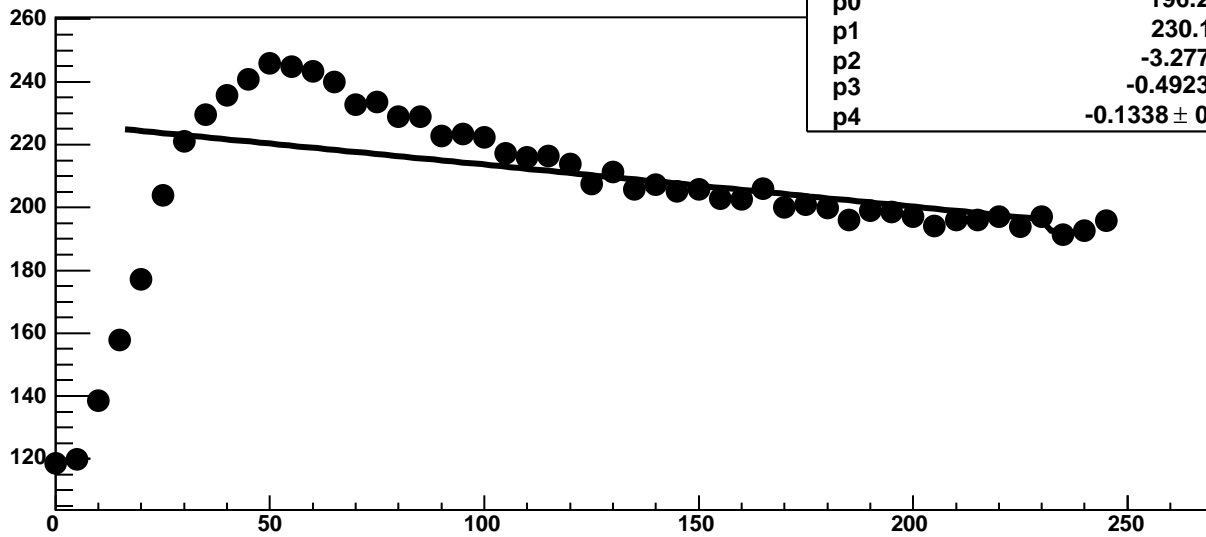
Chip 5, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

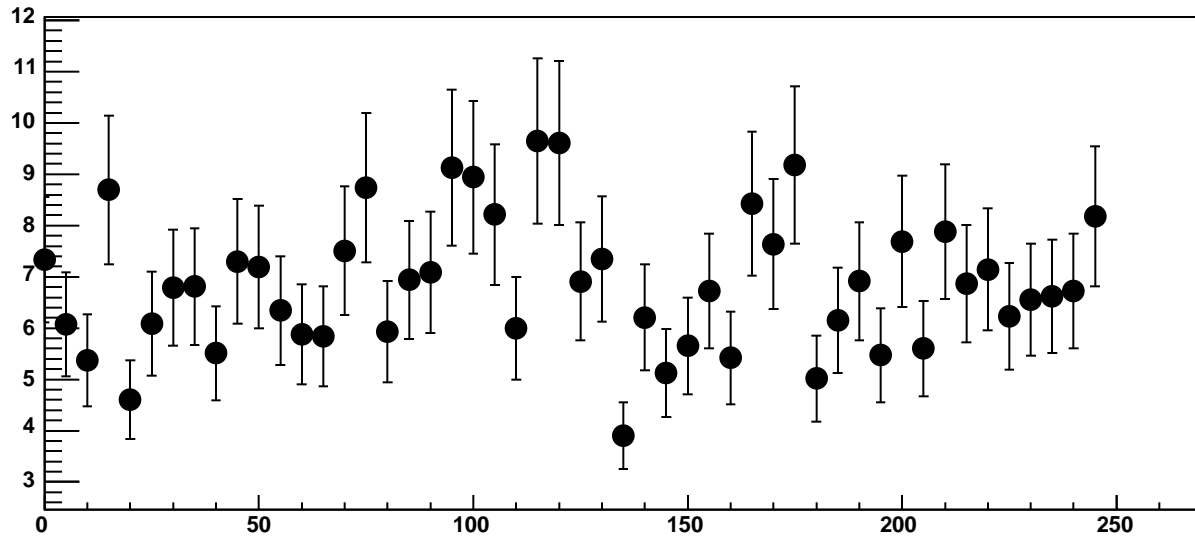


Chip 5, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold

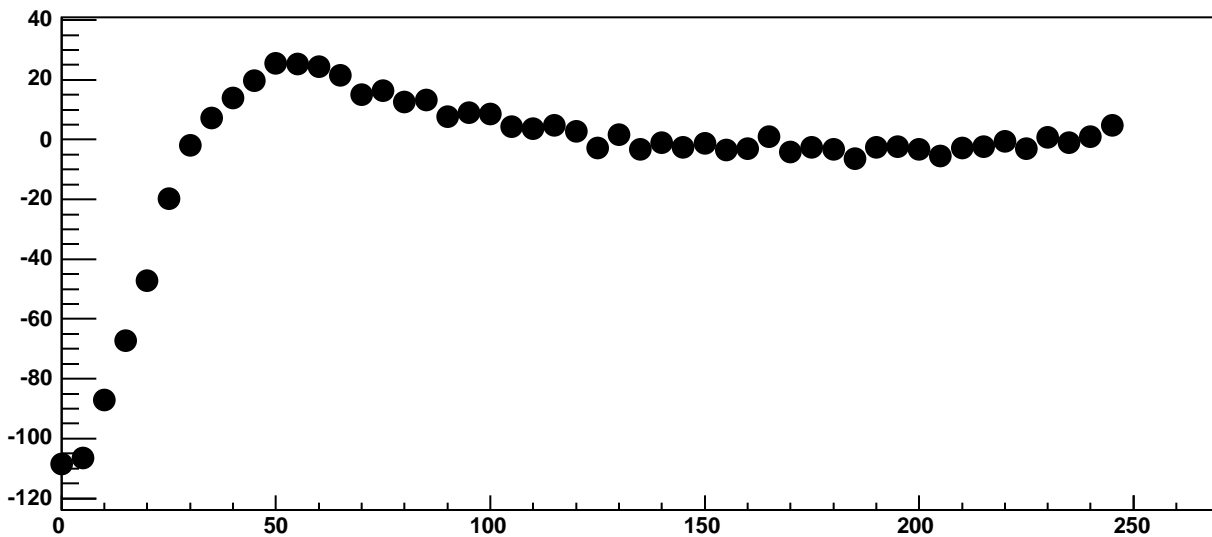


$\chi^2 / \text{ndf}$	5222 / 41
p0	$196.2 \pm 2.025$
p1	$230.1 \pm 22.92$
p2	$-3.277 \pm 2.223$
p3	$-0.4923 \pm 42.23$
p4	$-0.1338 \pm 0.007059$

Chip 5, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold

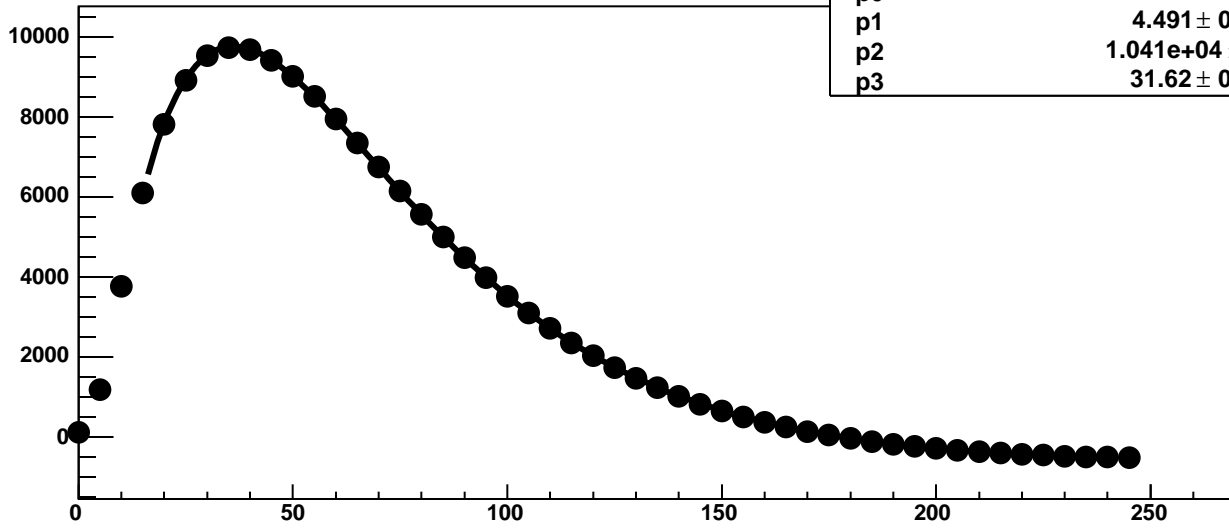


Chip 5, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold



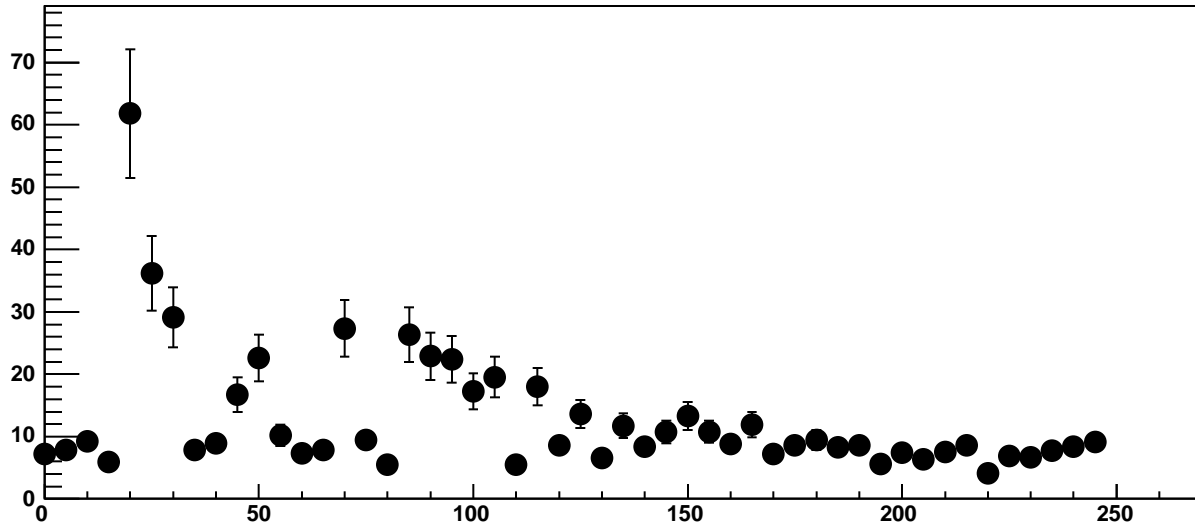


Chip 5, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold

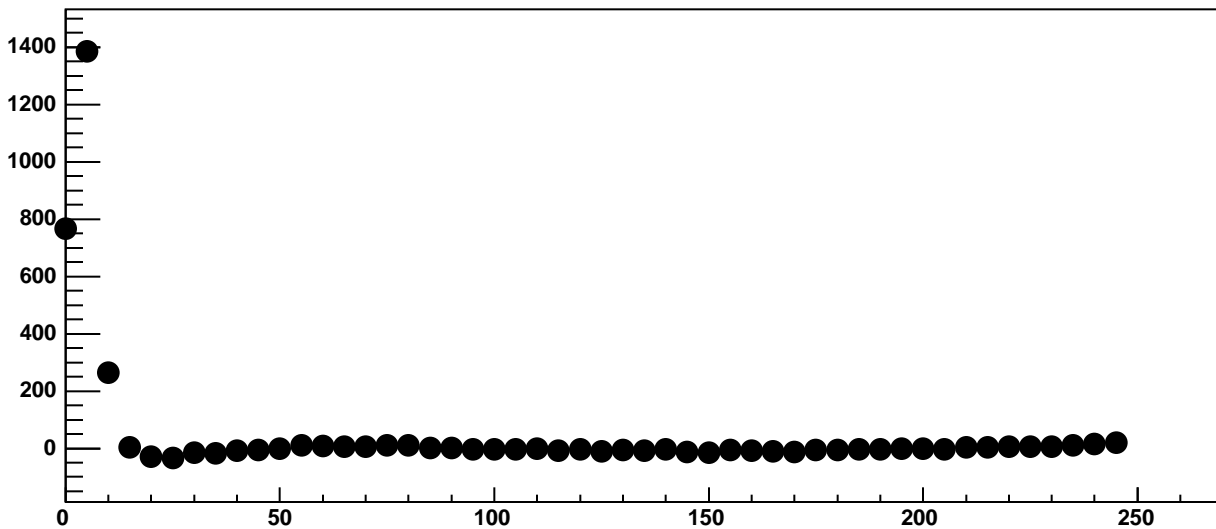


$\chi^2 / \text{ndf}$	684.7 / 42
p0	-647.3 ± 0.5297
p1	4.491 ± 0.004666
p2	1.041e+04 ± 0.9831
p3	31.62 ± 0.004896

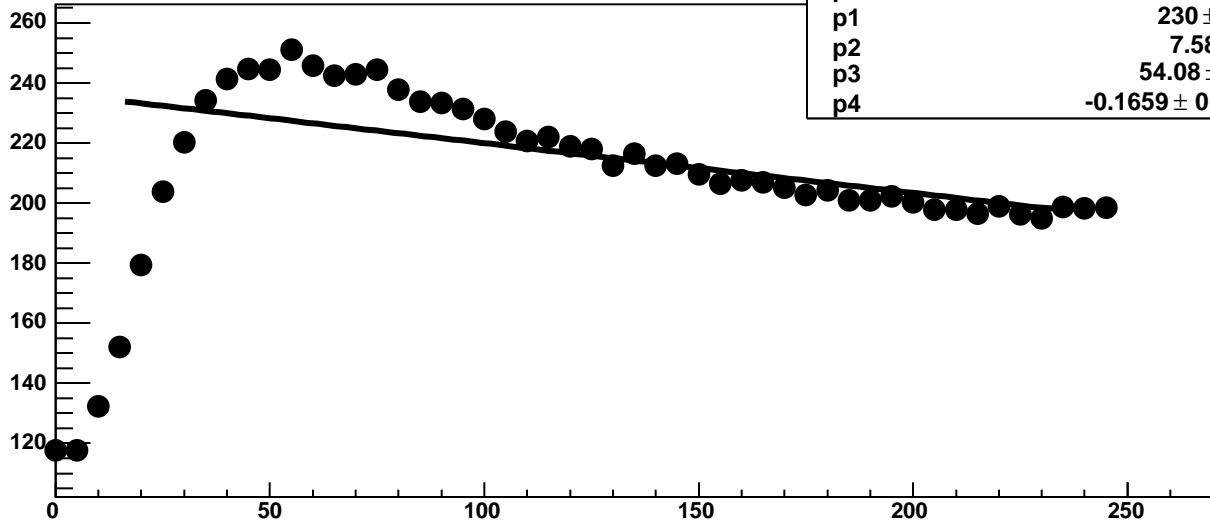
Chip 5, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold

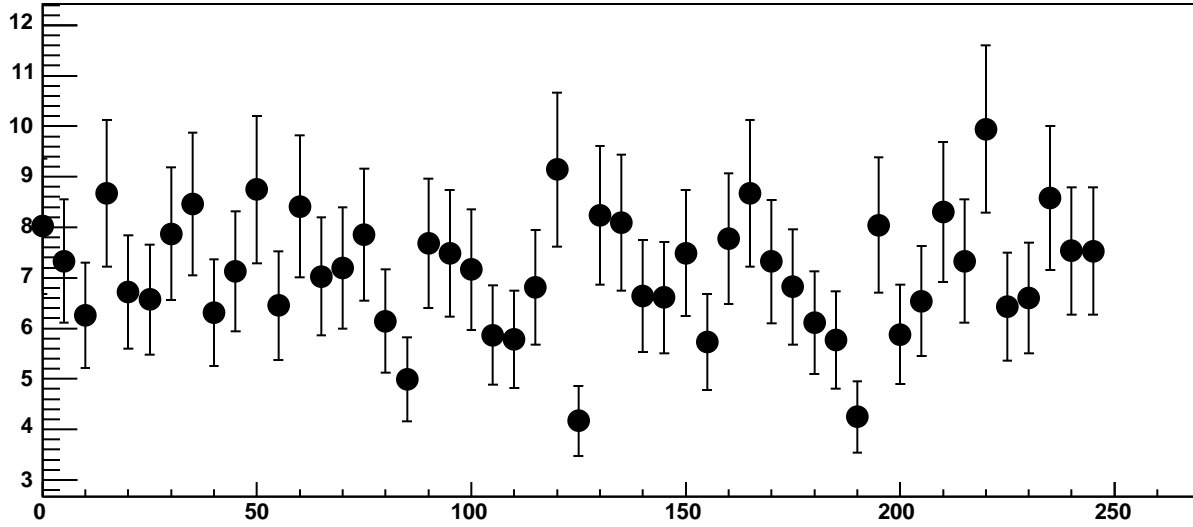


Chip 5, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

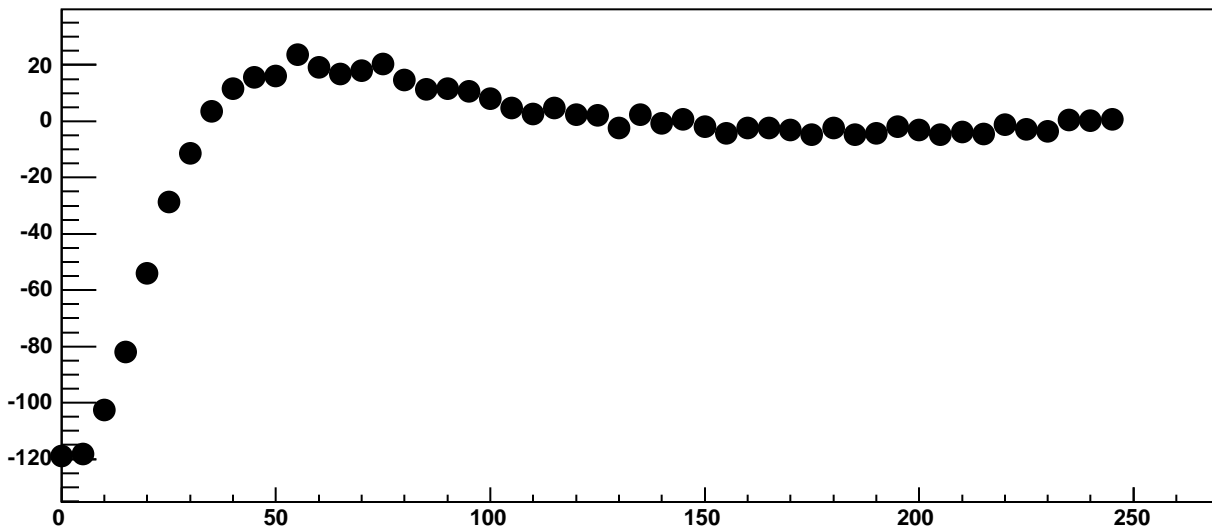


$\chi^2 / \text{ndf}$	4700 / 41
p0	$198.4 \pm 0.4115$
p1	$230 \pm 0.2649$
p2	$7.58 \pm 8.95$
p3	$54.08 \pm 0.2831$
p4	$-0.1659 \pm 0.003794$

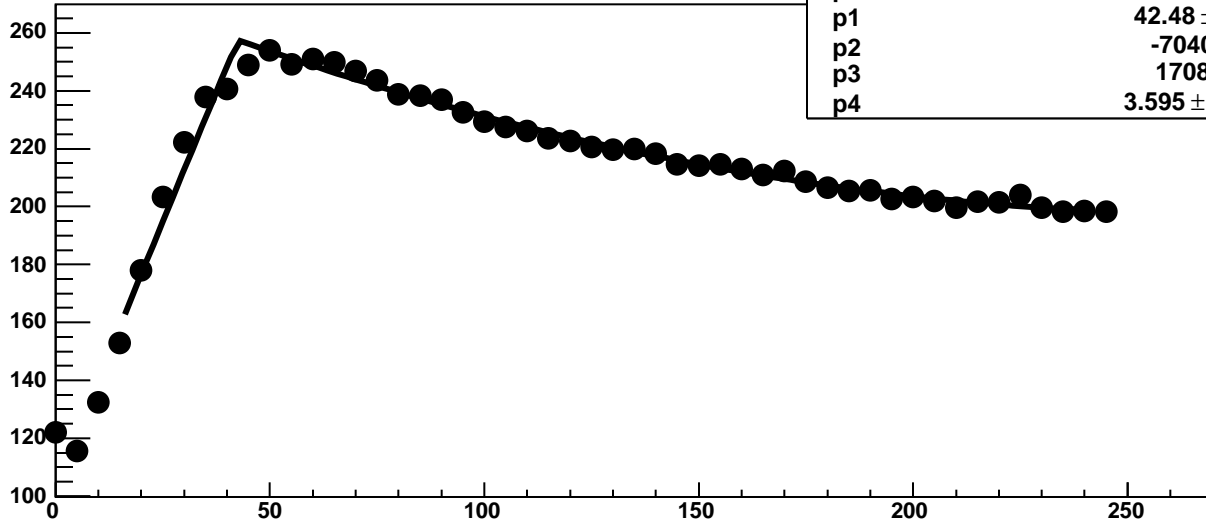
Chip 5, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

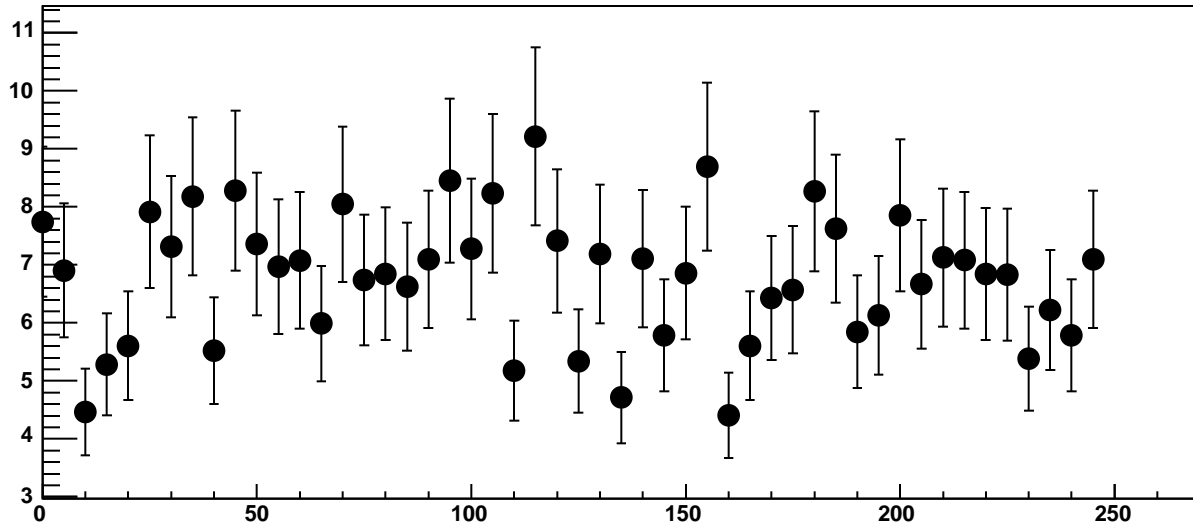


Chip 5, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

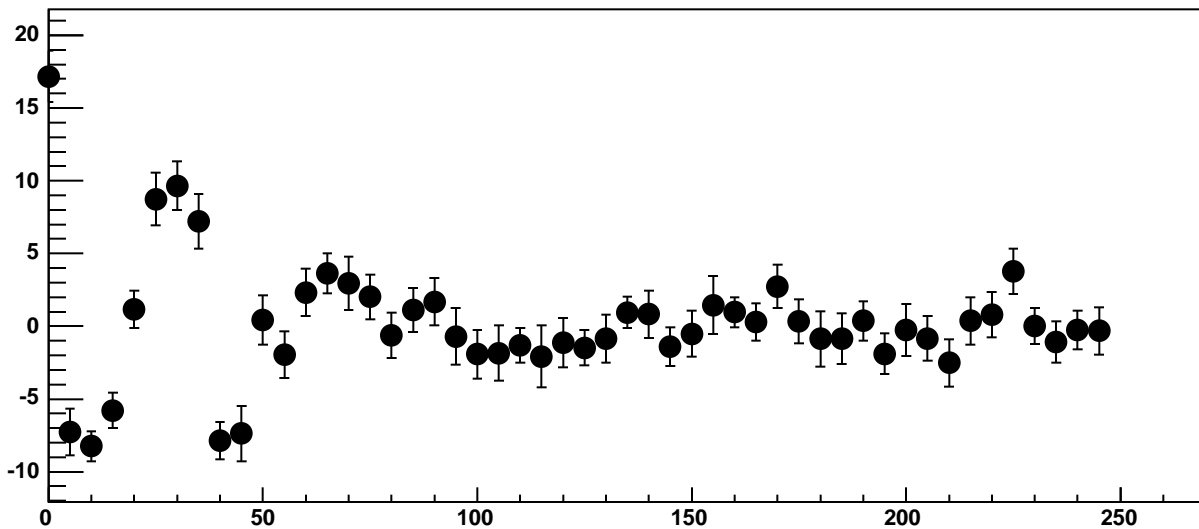


$\chi^2 / \text{ndf}$	190.4 / 41
p0	$257.5 \pm 0.6951$
p1	$42.48 \pm 0.3425$
p2	$-7040 \pm 511.3$
p3	$1708 \pm 121.7$
p4	$3.595 \pm 0.06291$

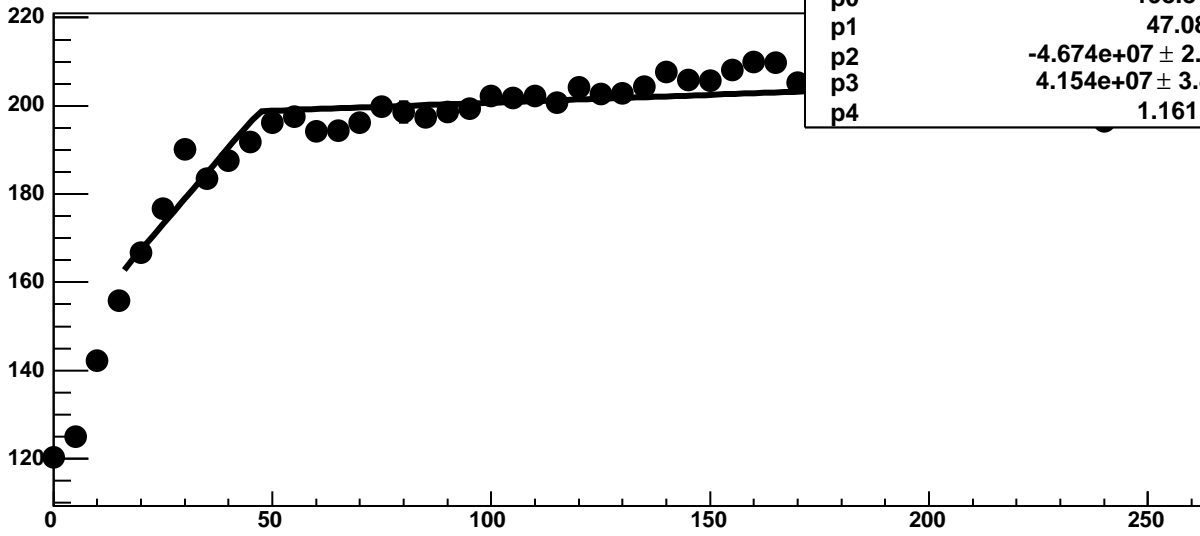
Chip 5, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

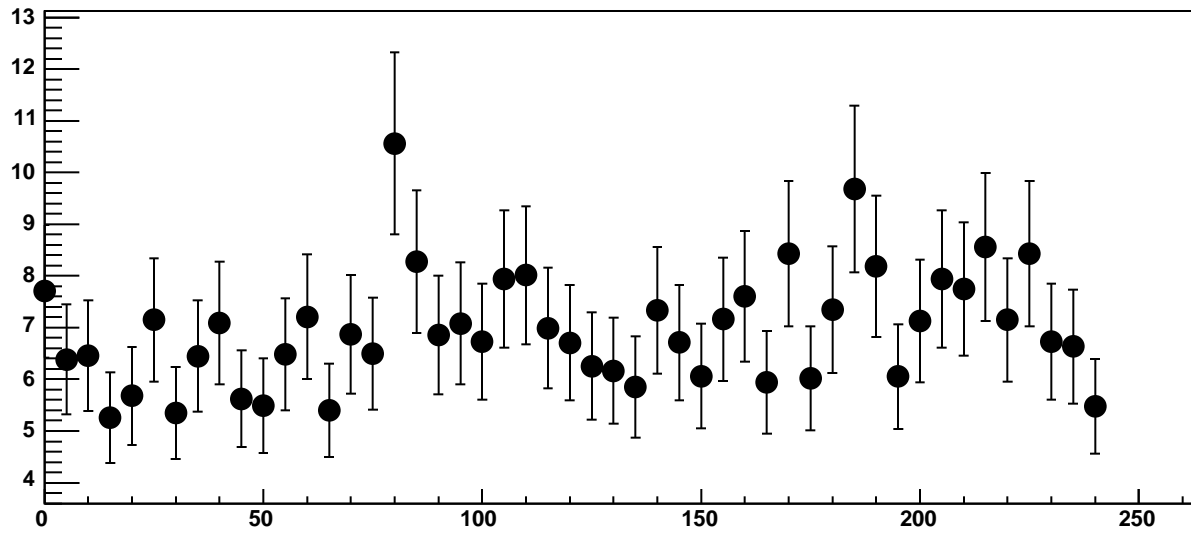


Chip 5, Channel 16, Enable 5, DAC=4000, ADC Mean vs Hold

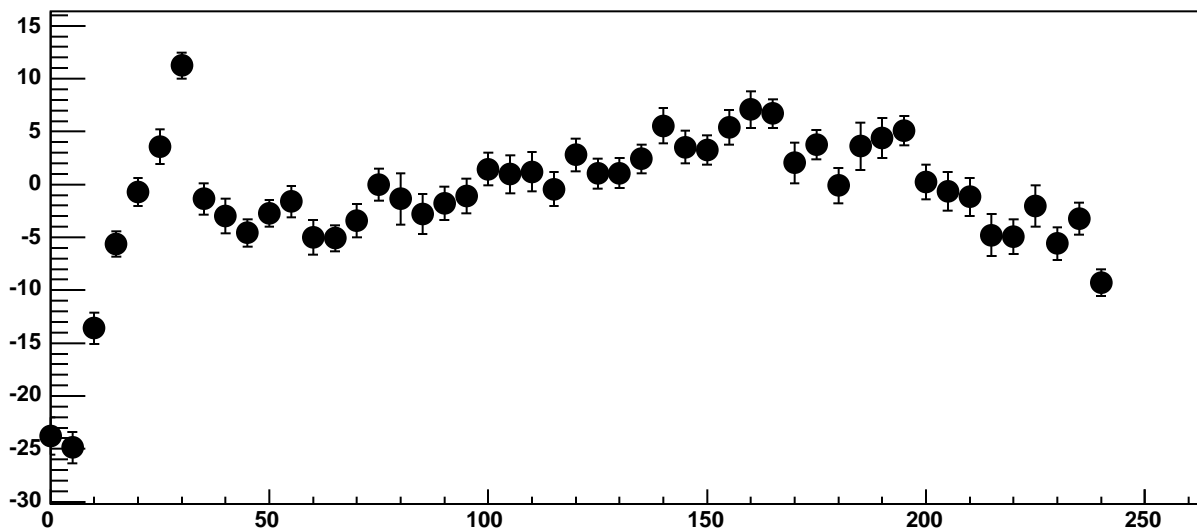


$\chi^2 / \text{ndf}$	369.9 / 41
p0	$198.8 \pm 0.5077$
p1	$47.08 \pm 1.013$
p2	$-4.674e+07 \pm 2.088e+06$
p3	$4.154e+07 \pm 3.445e+05$
p4	$1.161 \pm 0.0497$

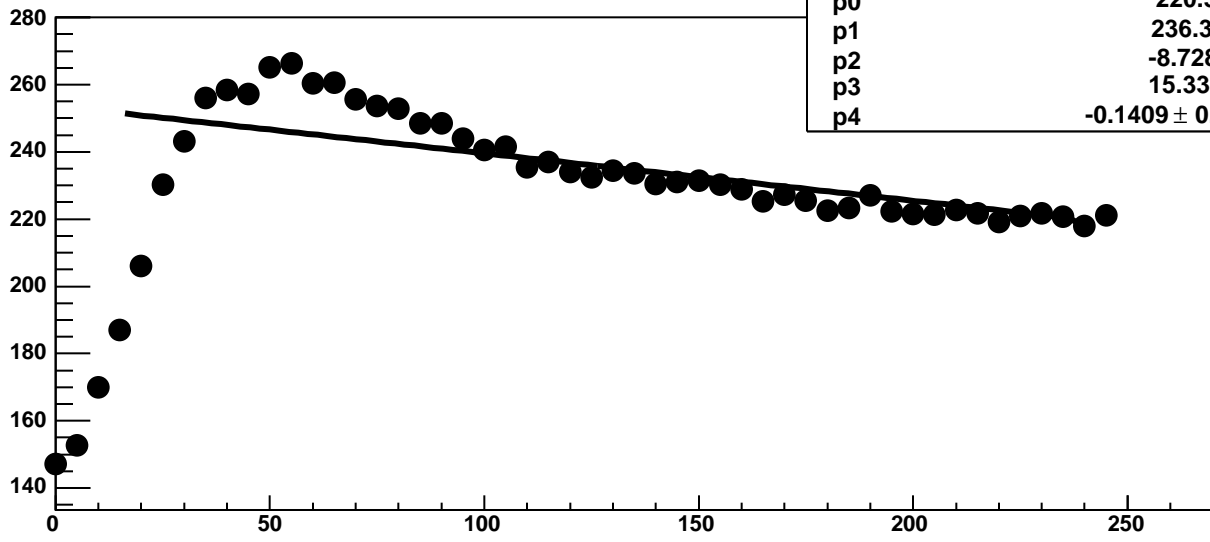
Chip 5, Channel 16, Enable 5, DAC=4000, ADC Noise vs Hold



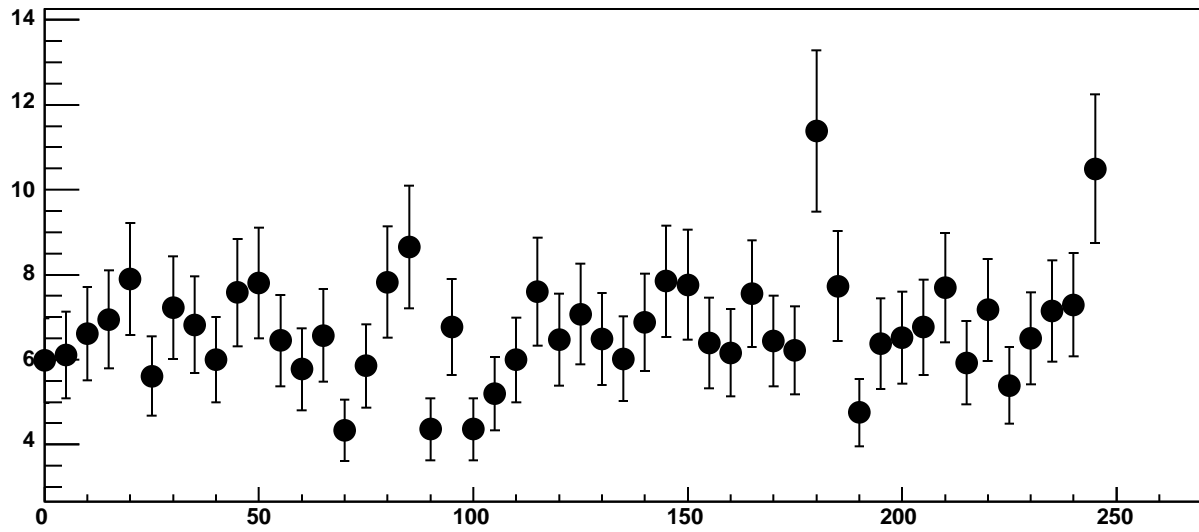
Chip 5, Channel 16, Enable 5, DAC=4000, ADC Residuals vs Hold



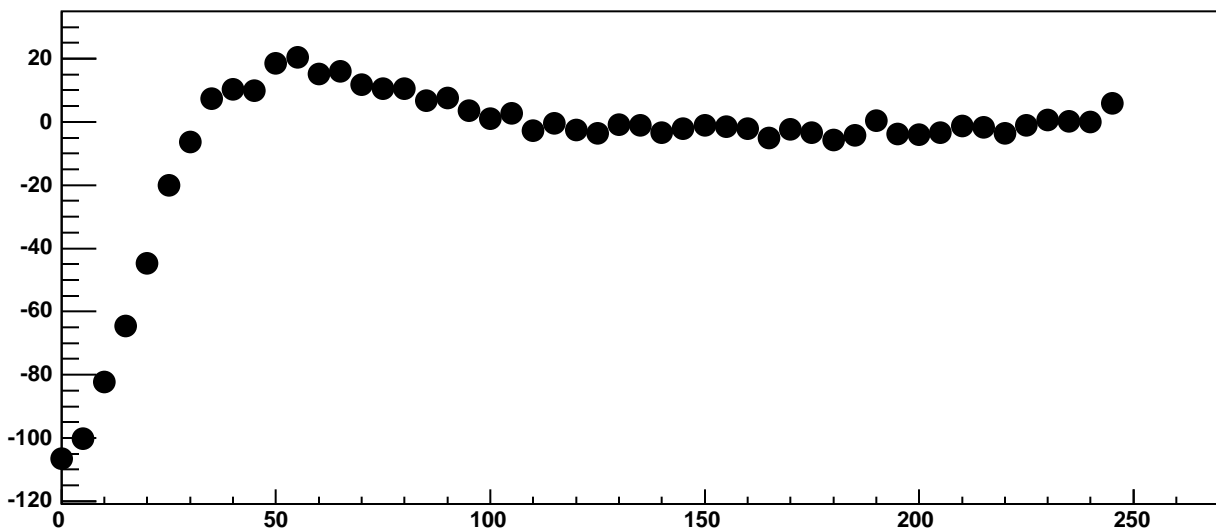
Chip 5, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold



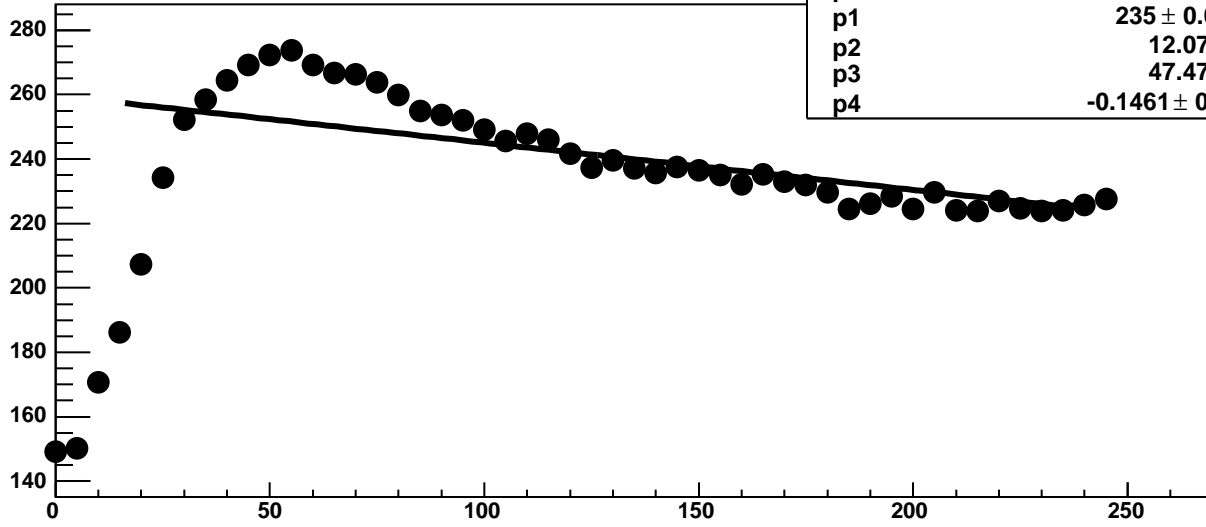
Chip 5, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

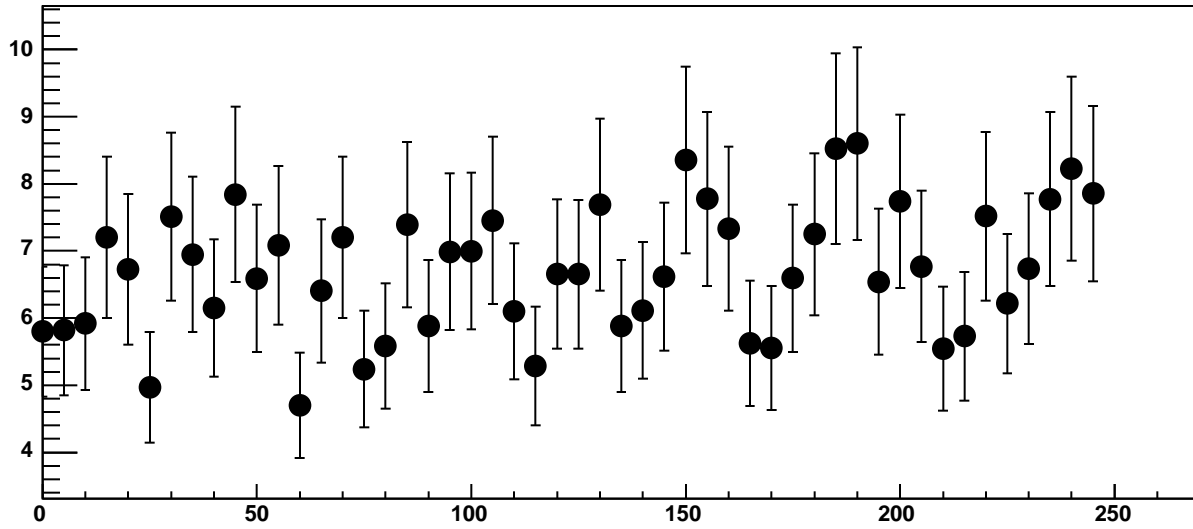


Chip 5, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

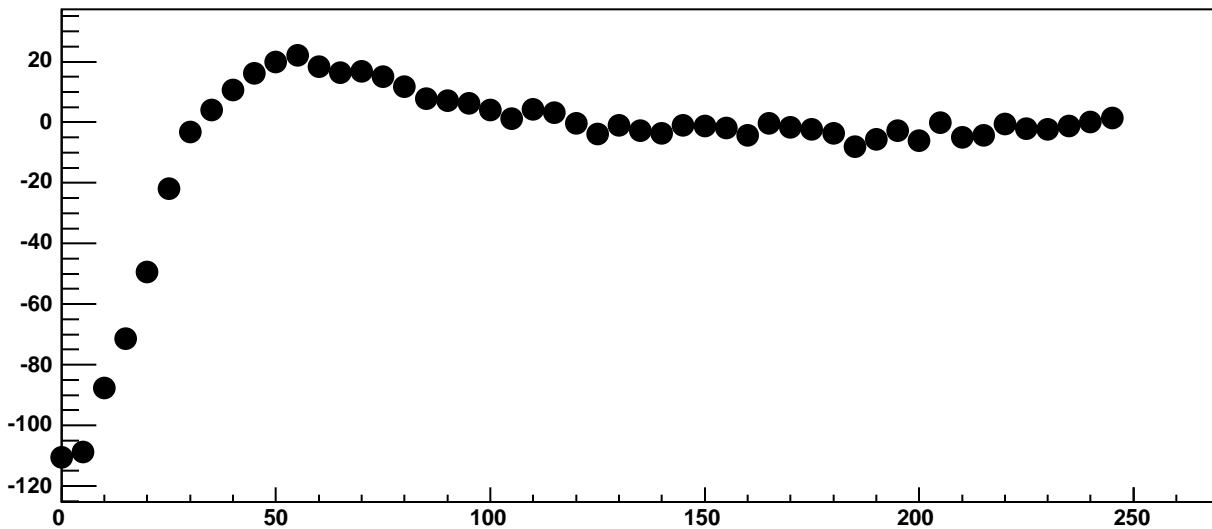


$\chi^2 / \text{ndf}$	4712 / 41
p0	$225.3 \pm 1.096$
p1	$235 \pm 0.0008285$
p2	$12.07 \pm 6.925$
p3	$47.47 \pm 58.19$
p4	$-0.1461 \pm 0.007486$

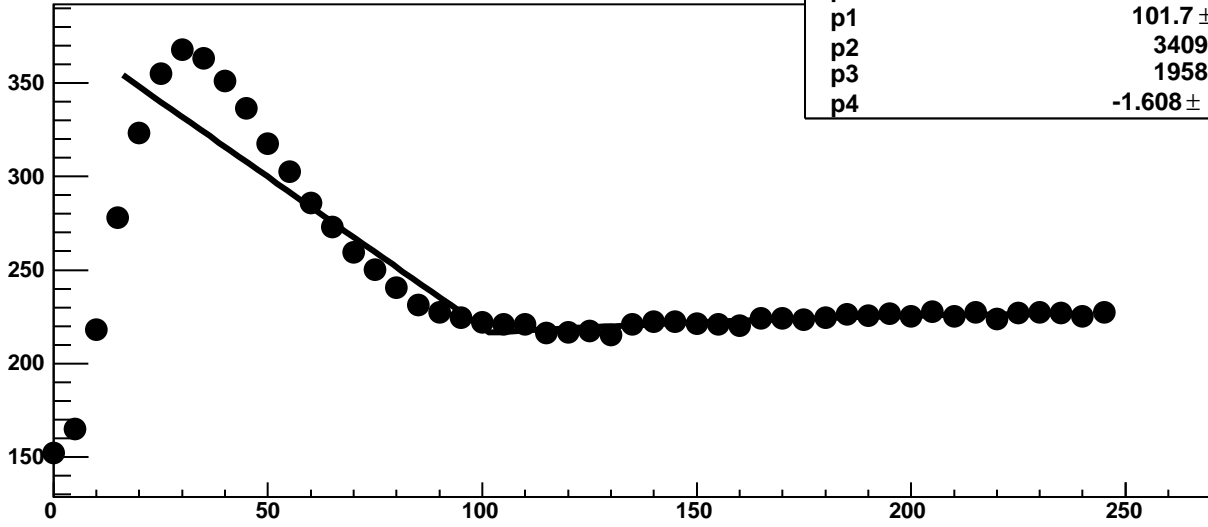
Chip 5, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



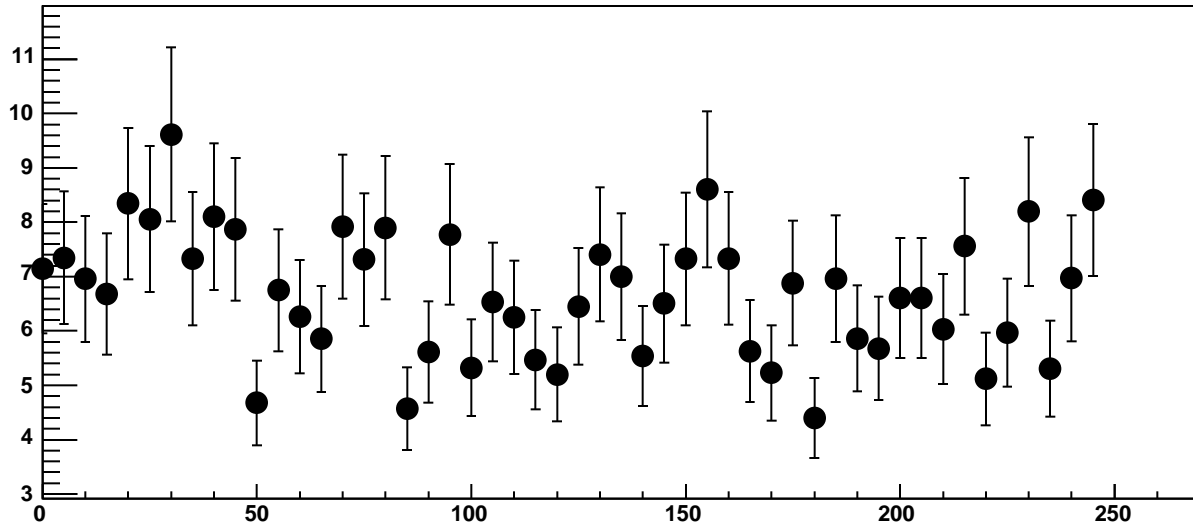
Chip 5, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold



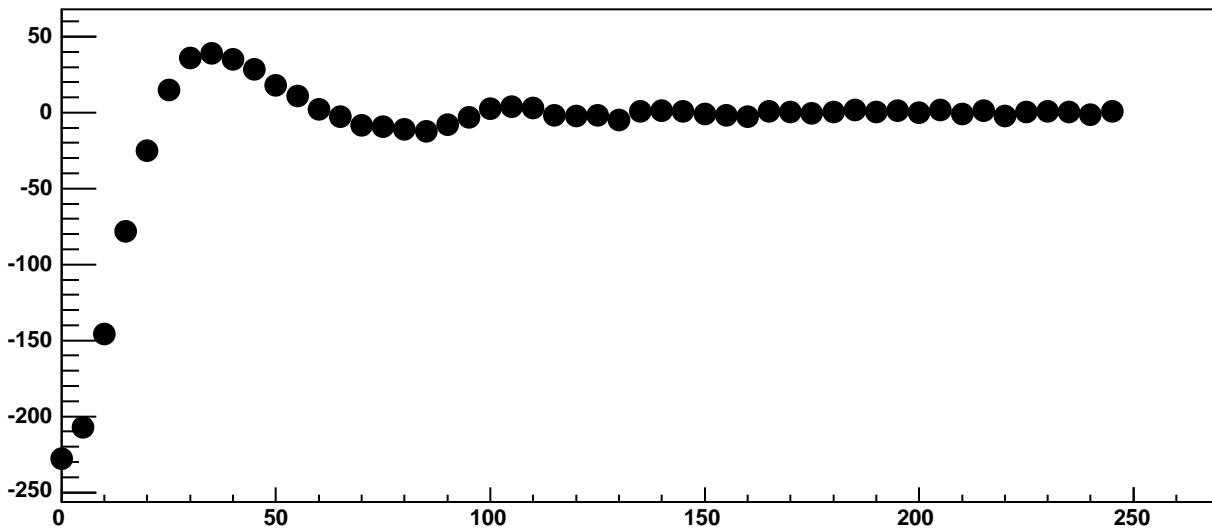
Chip 5, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold



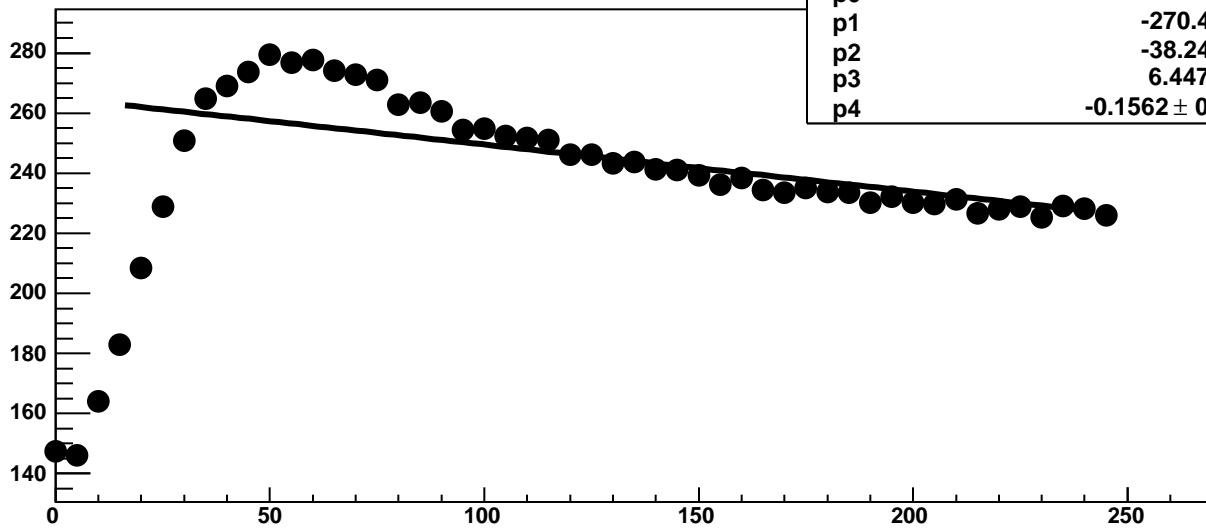
Chip 5, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold

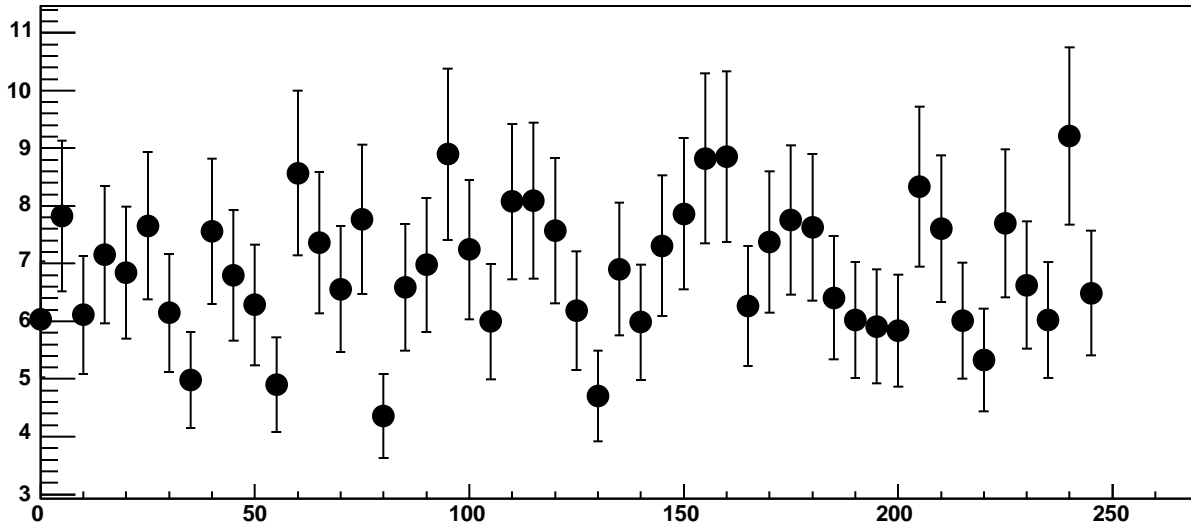


Chip 5, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold

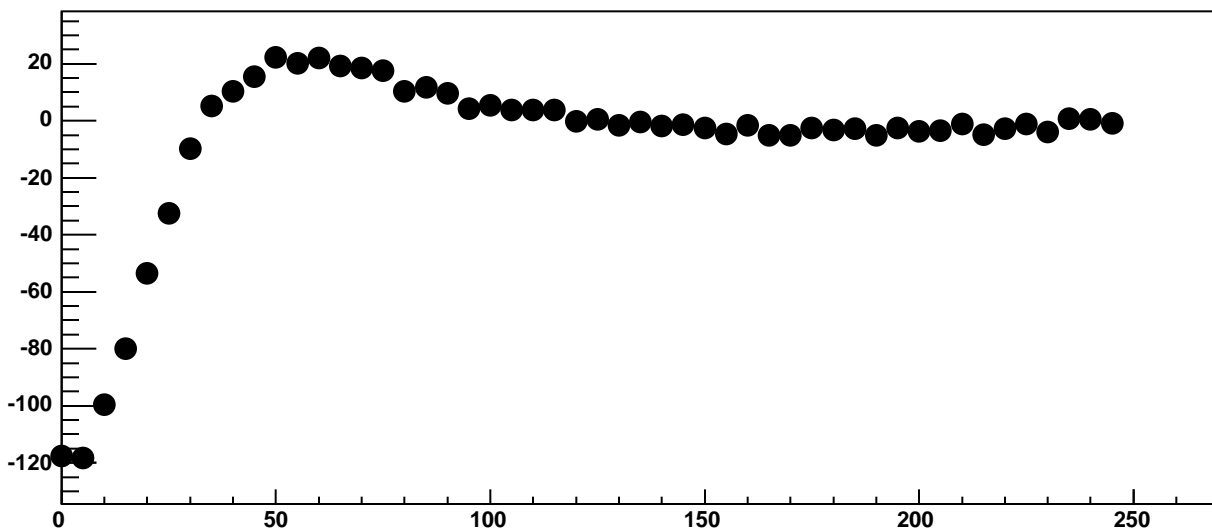


$\chi^2 / \text{ndf}$	5463 / 41
p0	$345.6 \pm 2.659$
p1	$-270.4 \pm 18.77$
p2	$-38.24 \pm 2.727$
p3	$6.447 \pm 37.63$
p4	$-0.1562 \pm 0.003019$

Chip 5, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold

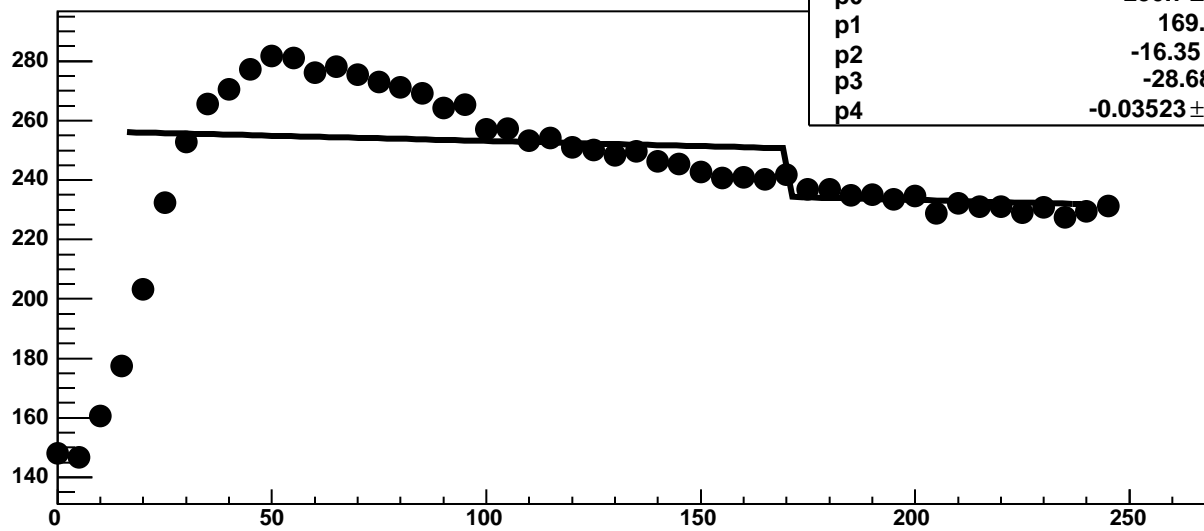


Chip 5, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold



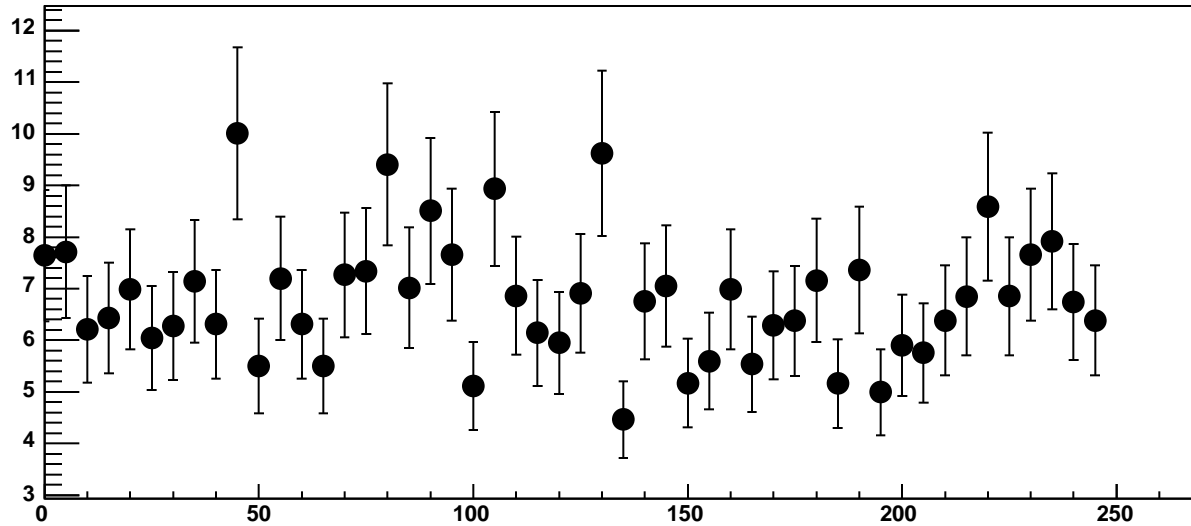


Chip 5, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



$\chi^2 / \text{ndf}$	6620 / 41
p0	$250.7 \pm 0.08865$
p1	$169.6 \pm 48.4$
p2	$-16.35 \pm 0.1599$
p3	$-28.68 \pm 37.19$
p4	$-0.03523 \pm 0.00269$

Chip 5, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

