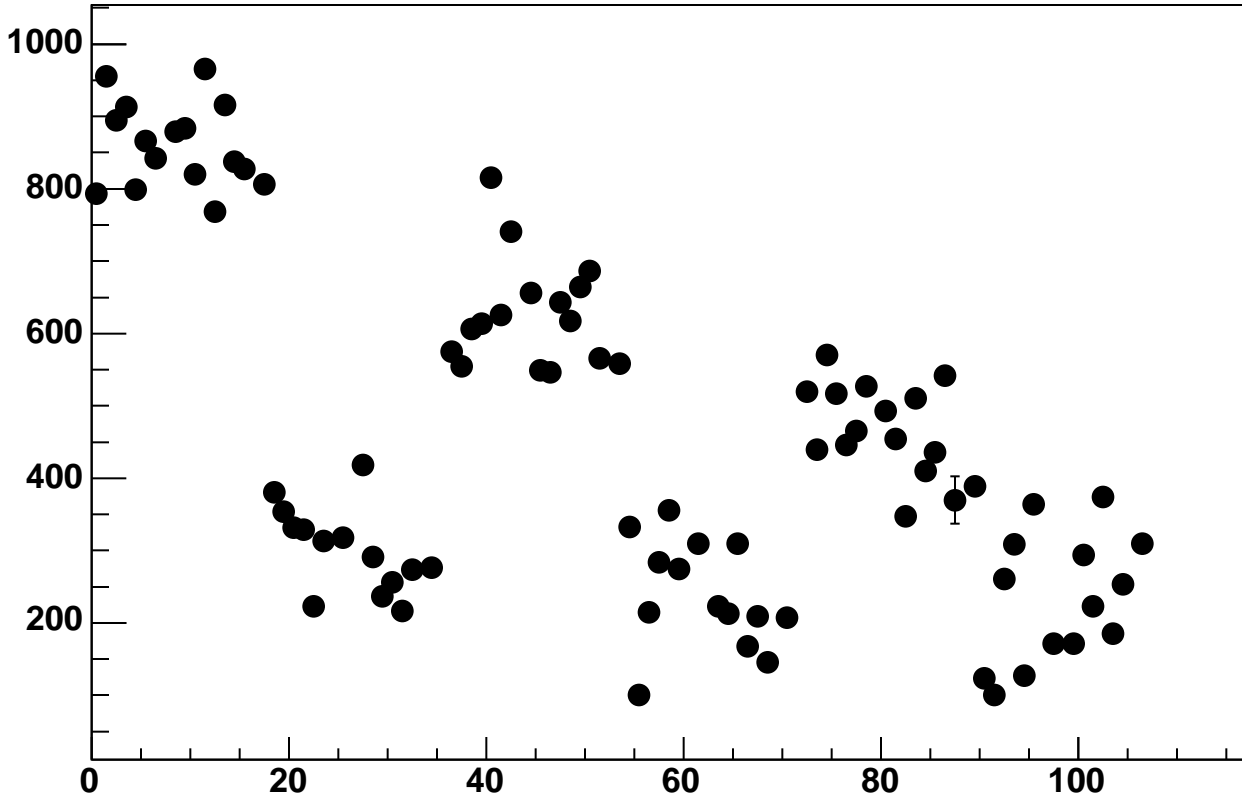
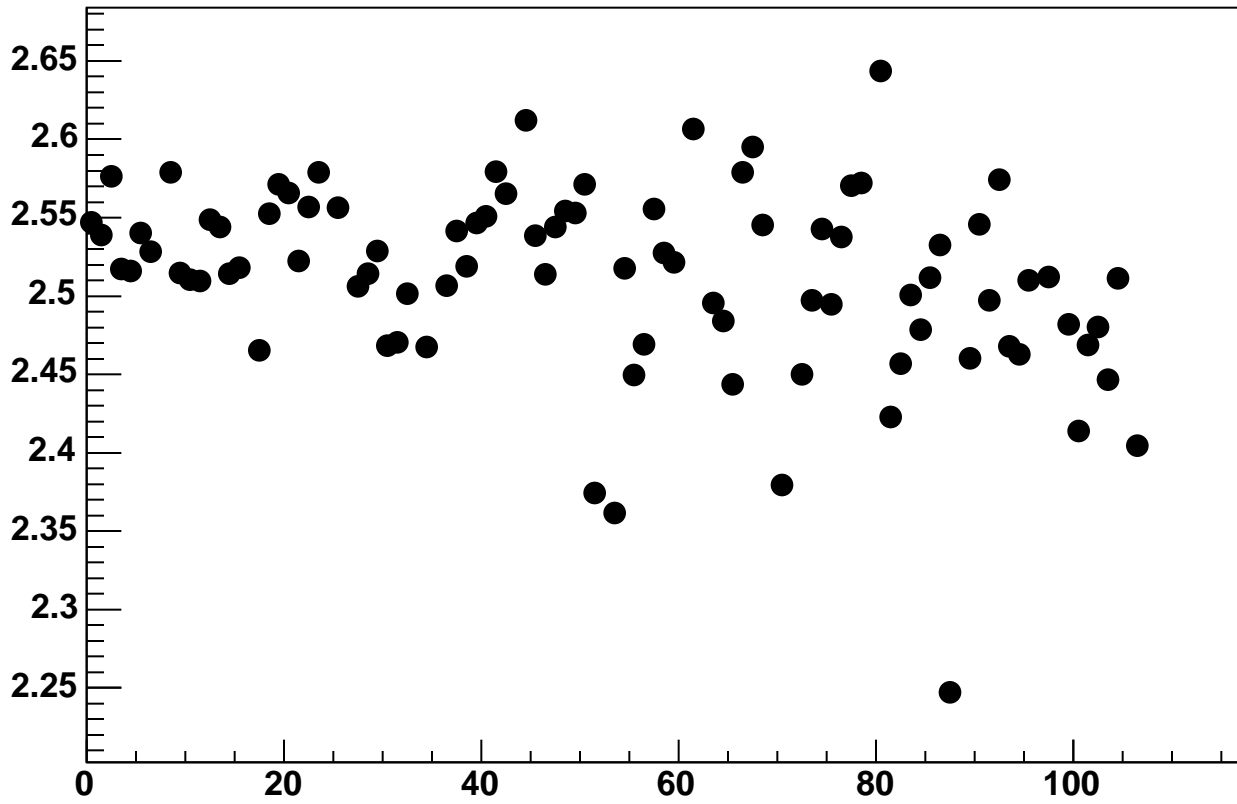


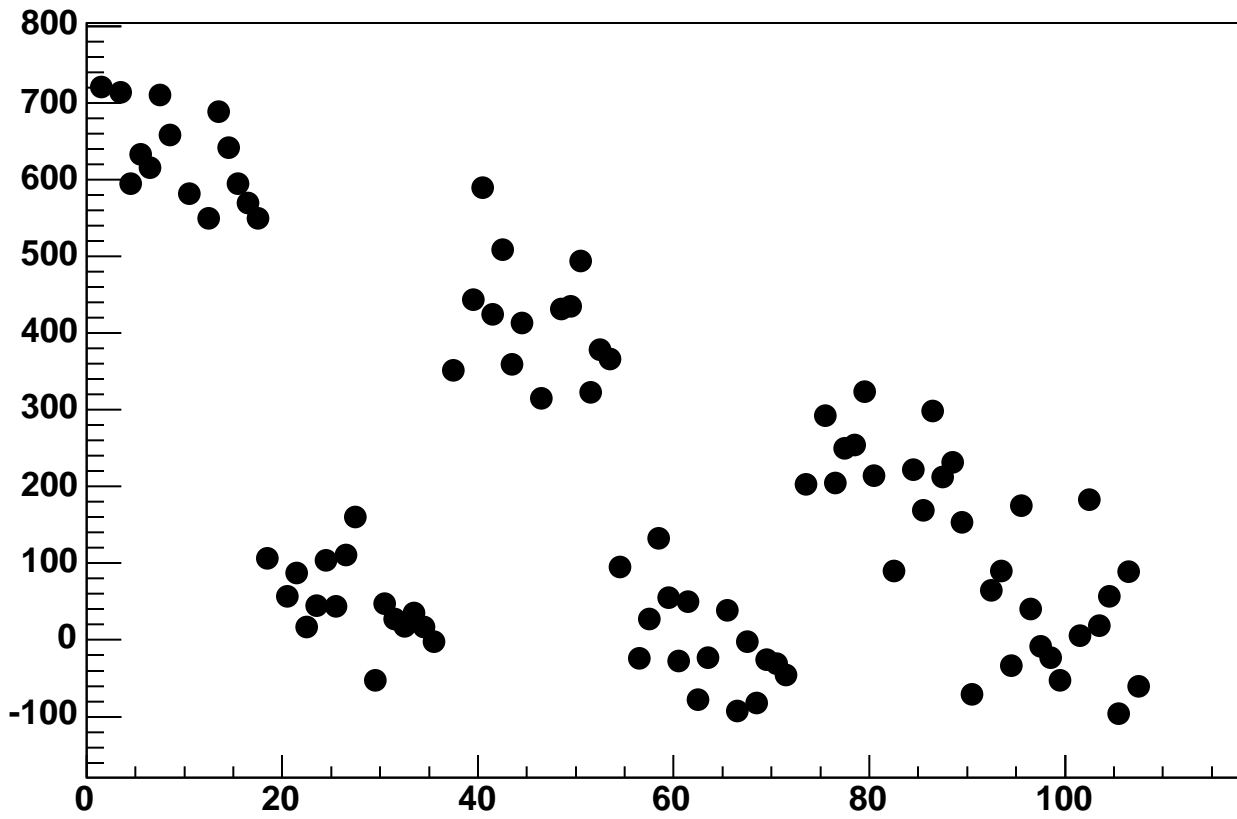
Channel Enabled, Hold=35, Fit Intercept vs 18*Chip+Chan



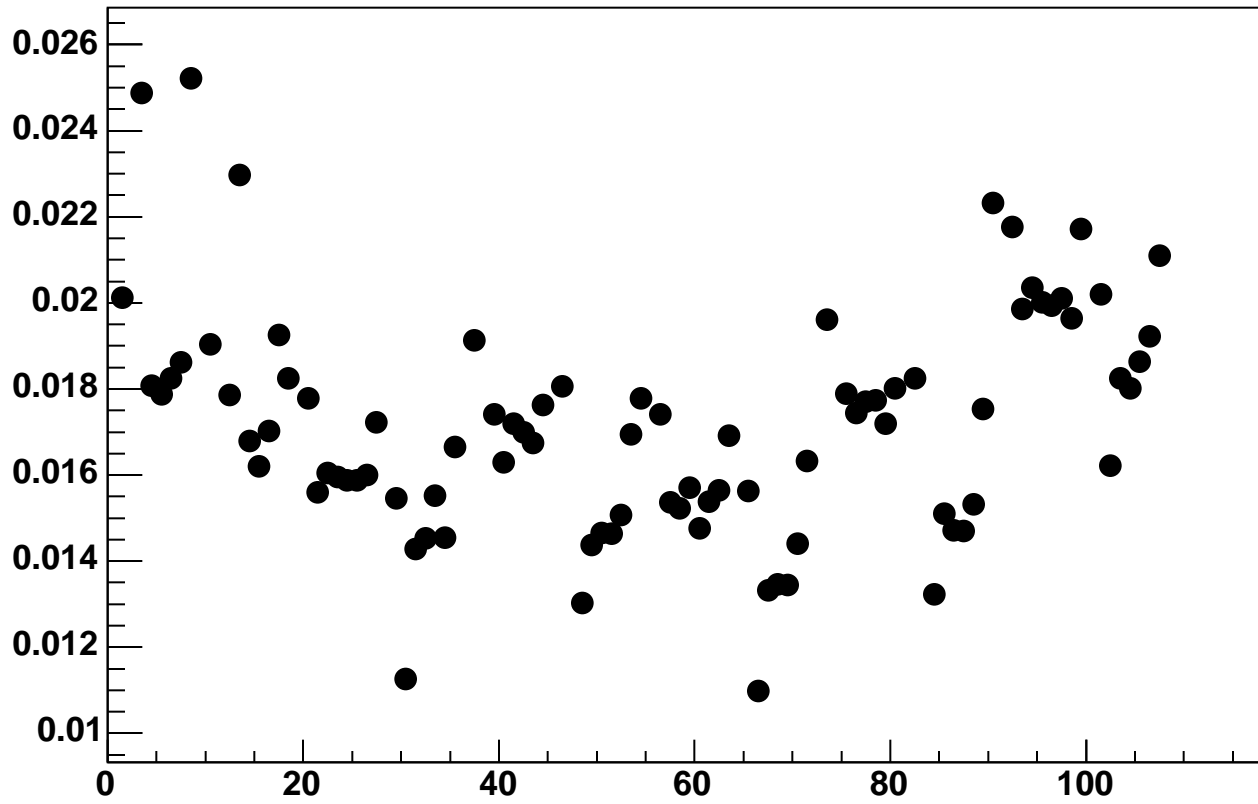
Channel Enabled, Hold=35, Fit Slope vs 18*Chip+Chan



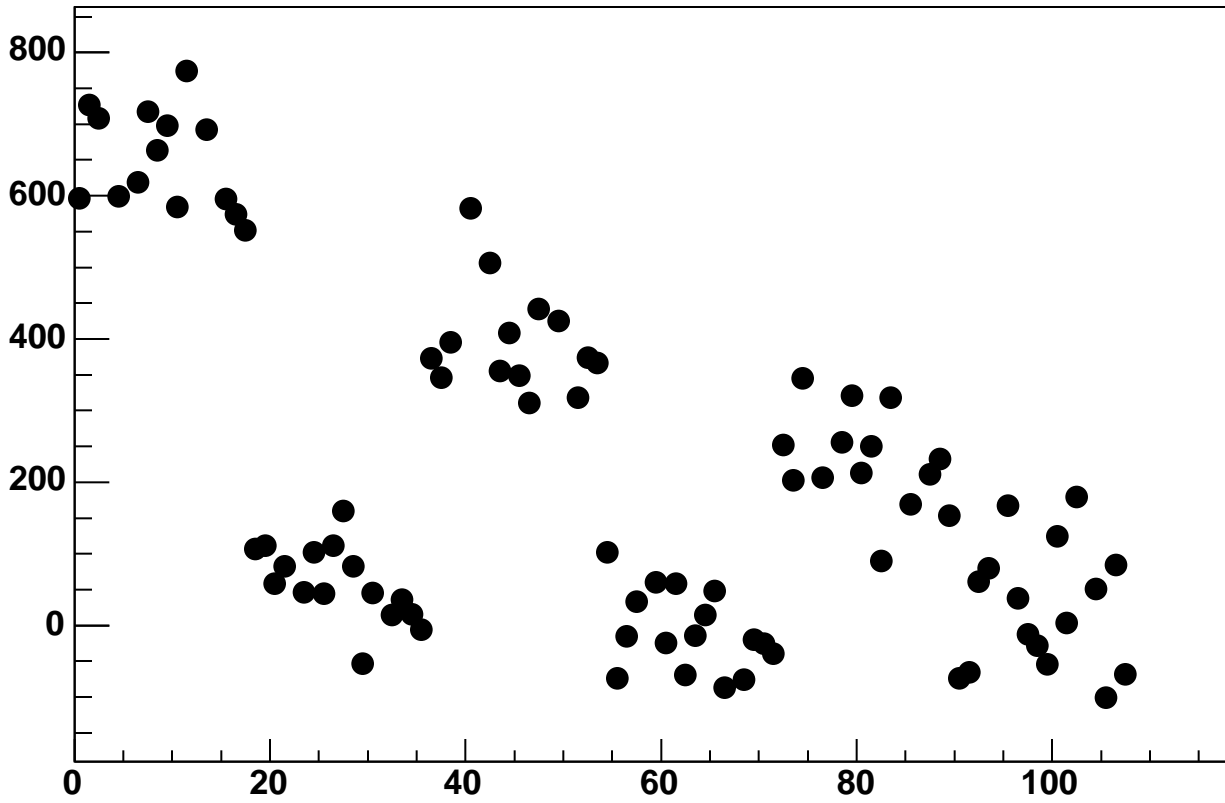
Disabled 0, Hold=35, Fit Intercept vs $18 \cdot \text{Chip} + \text{Chan}$



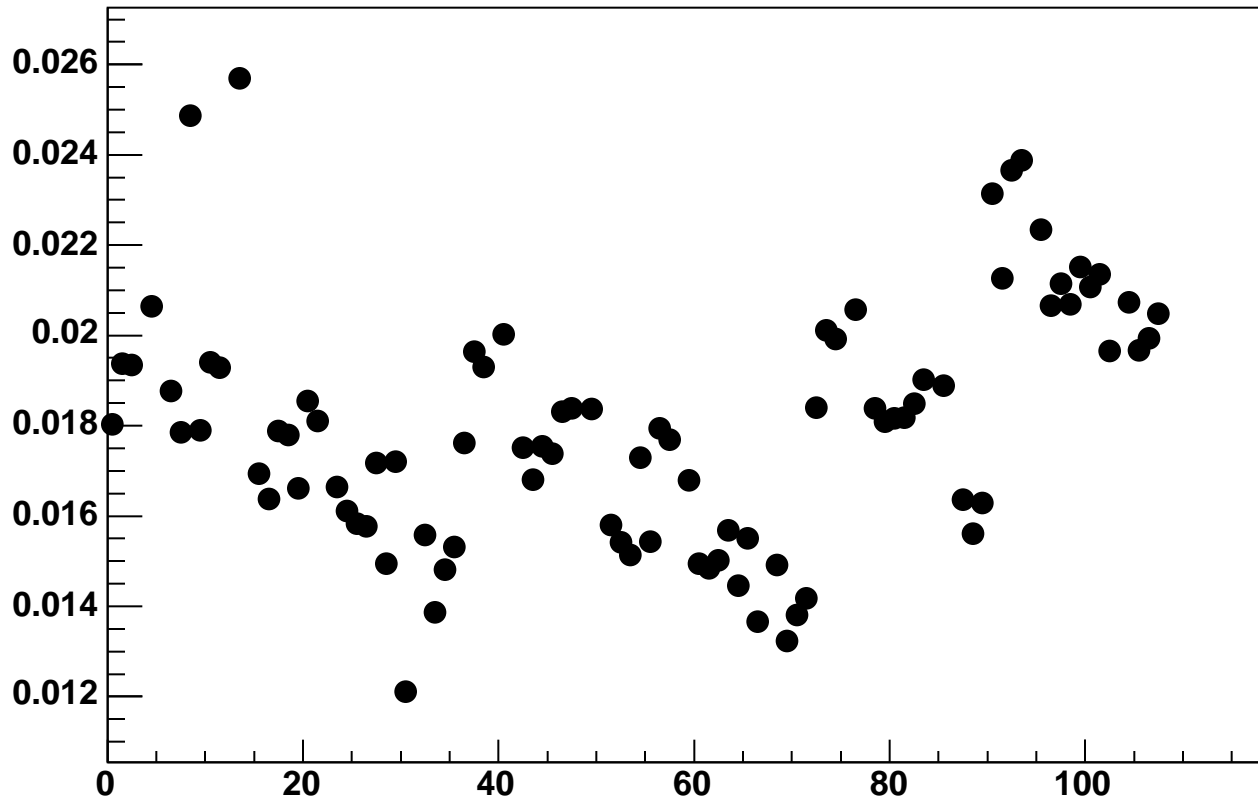
Disabled 0, Hold=35, Fit Slope vs $18 \cdot \text{Chip} + \text{Chan}$



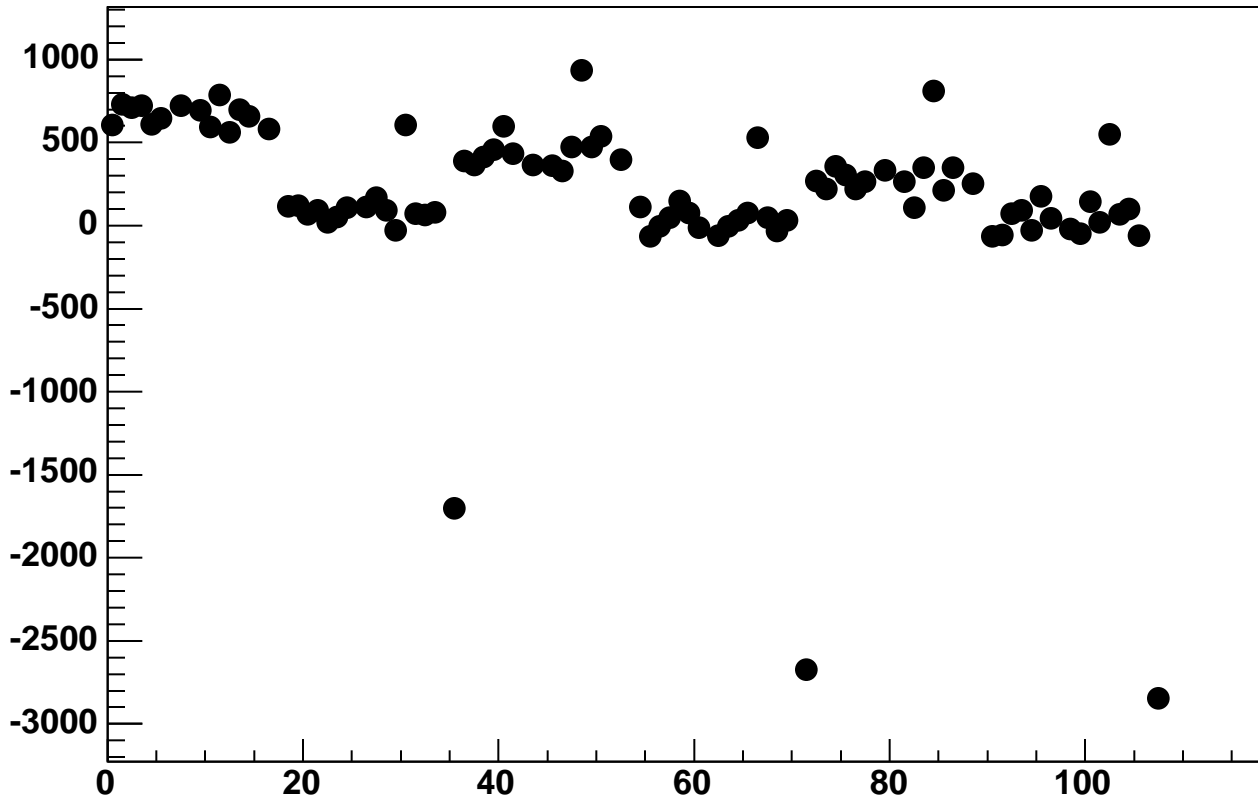
Disabled 1, Hold=35, Fit Intercept vs 18*Chip+Chan



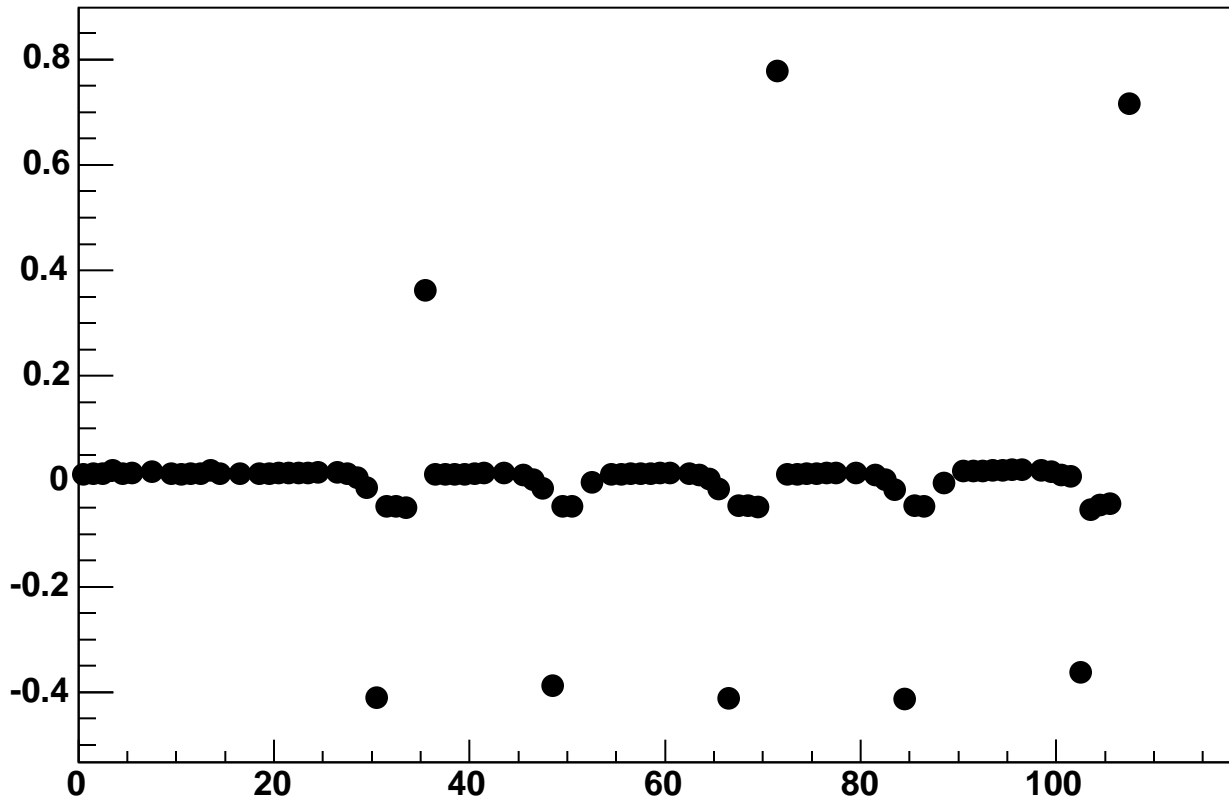
Disabled 1, Hold=35, Fit Slope vs 18*Chip+Chan



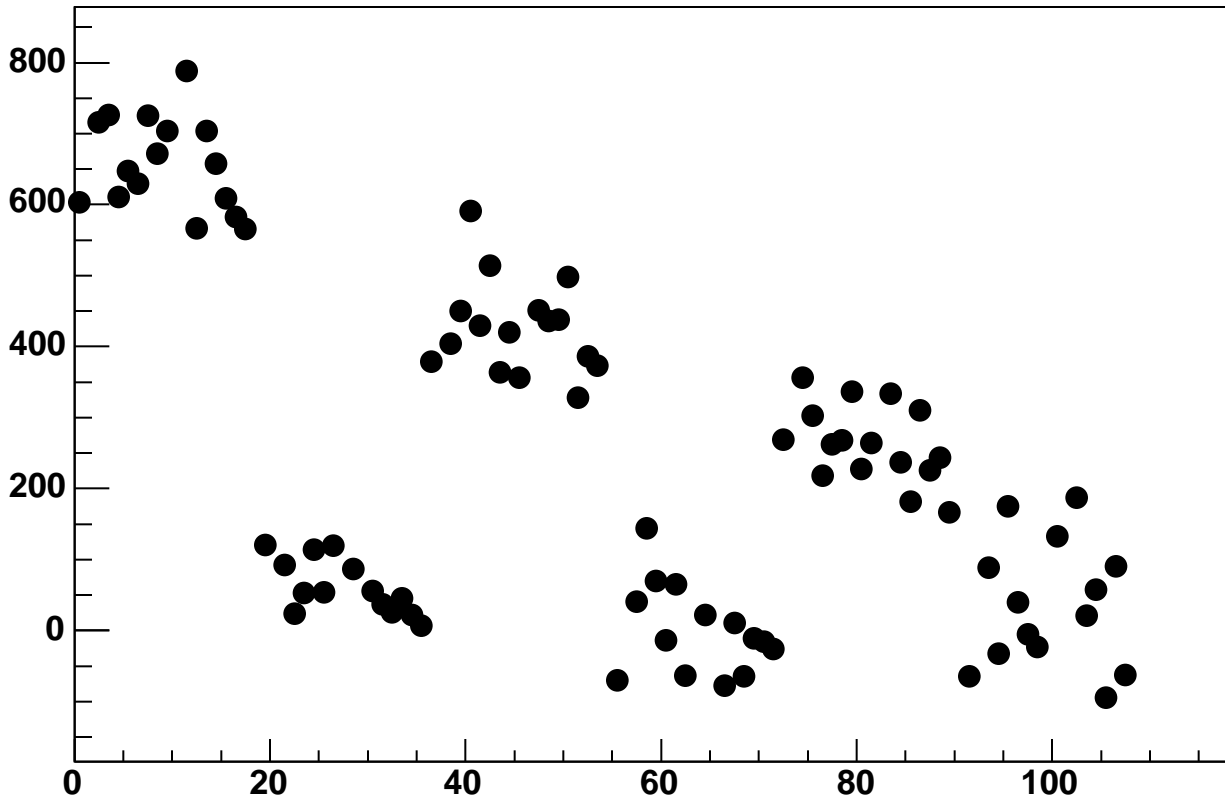
Disabled 2, Hold=35, Fit Intercept vs 18*Chip+Chan



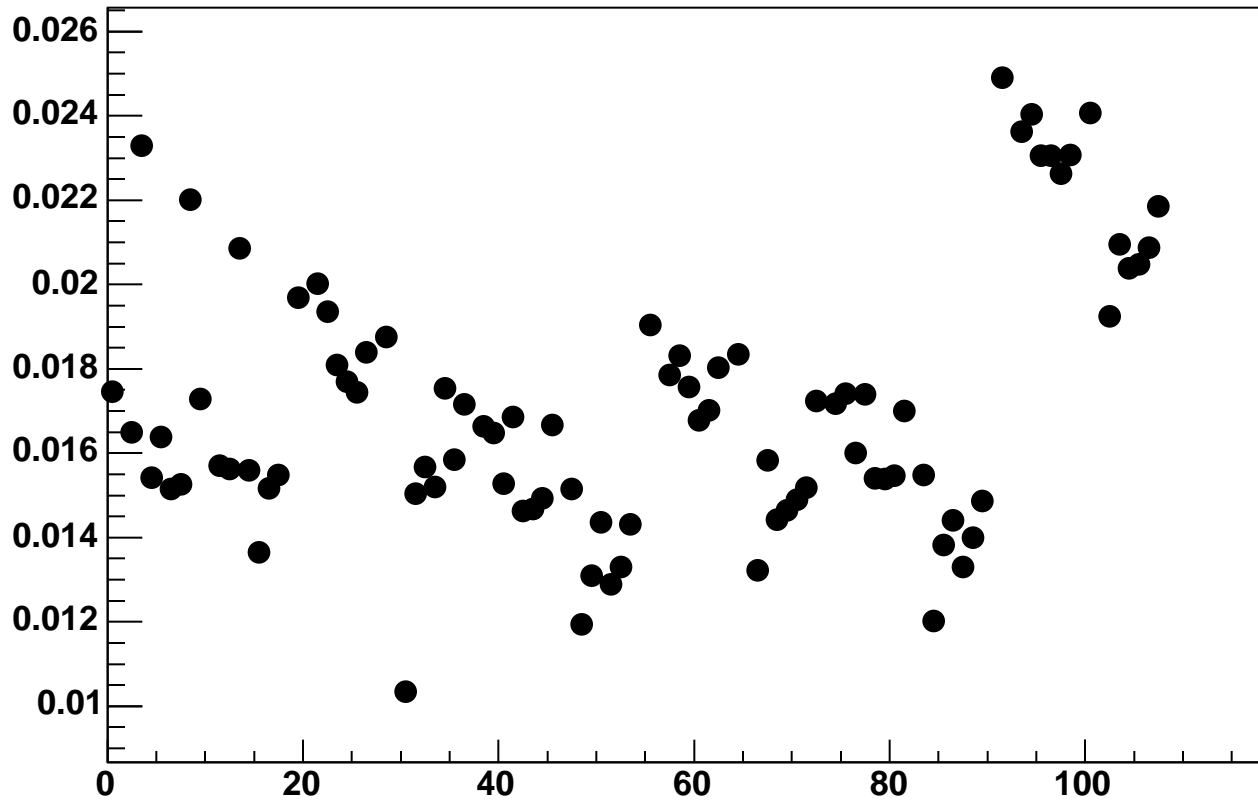
Disabled 2, Hold=35, Fit Slope vs 18*Chip+Chan



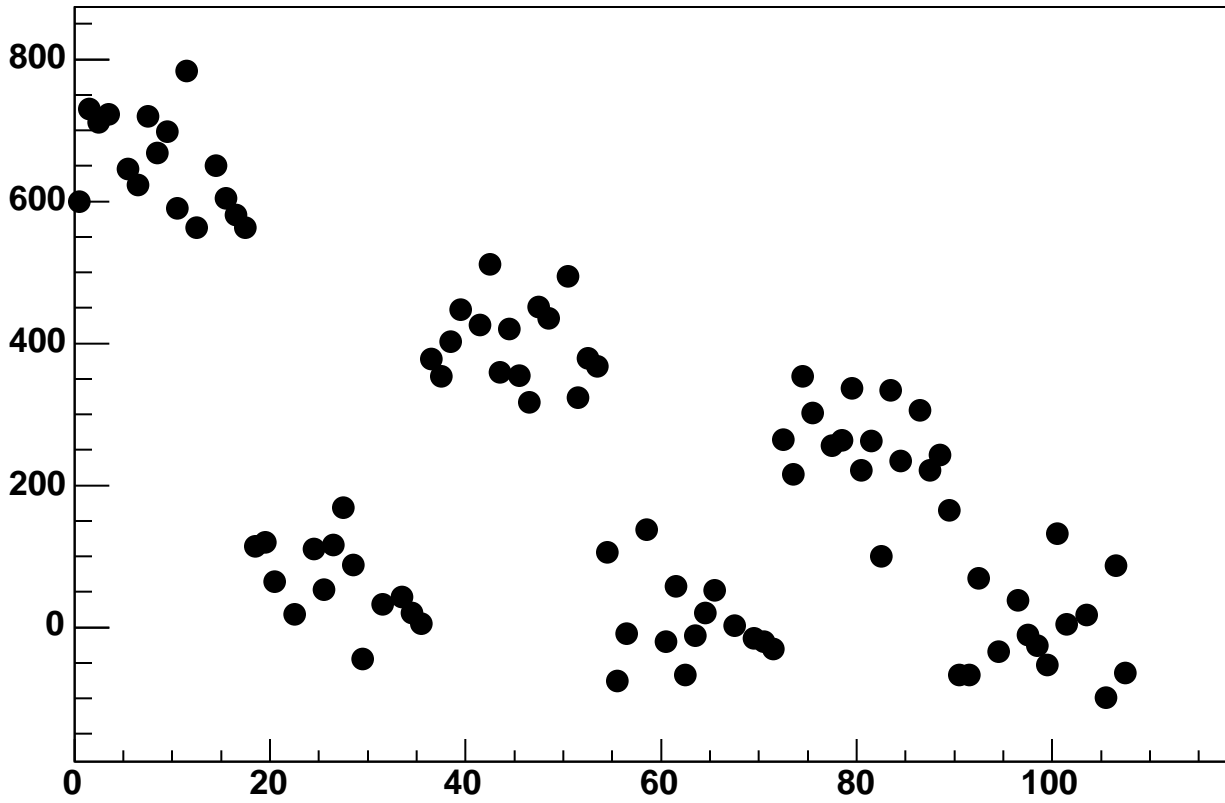
Disabled 3, Hold=35, Fit Intercept vs 18*Chip+Chan



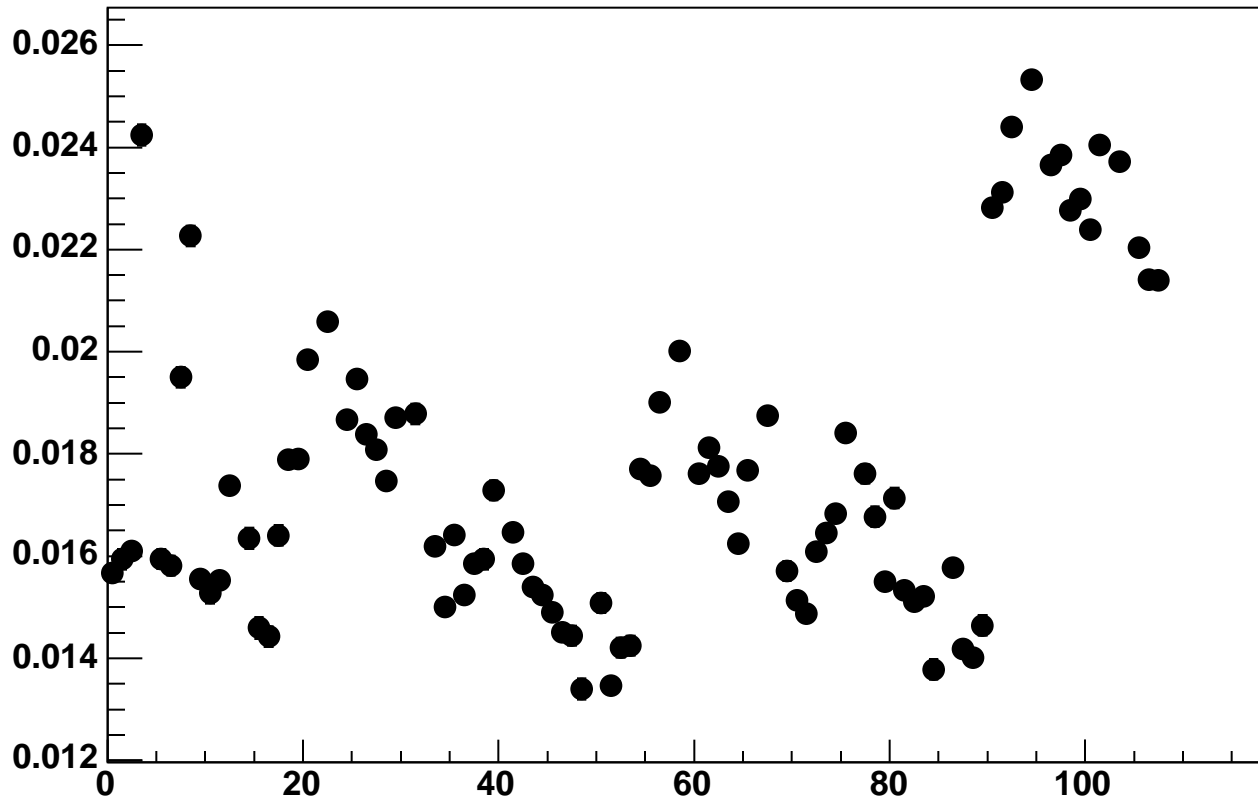
Disabled 3, Hold=35, Fit Slope vs 18*Chip+Chan



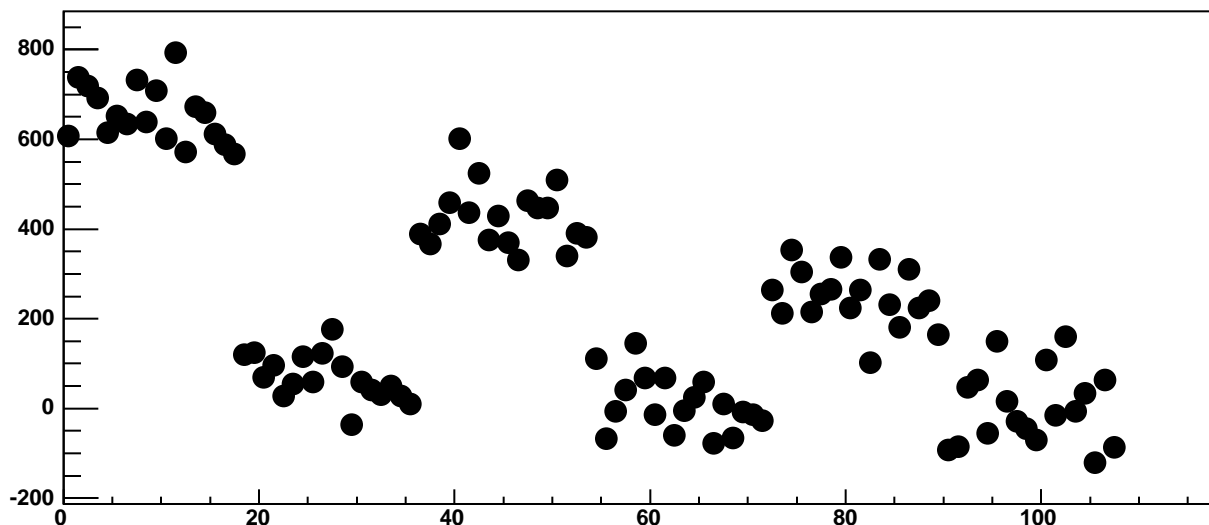
Disabled 4, Hold=35, Fit Intercept vs 18*Chip+Chan



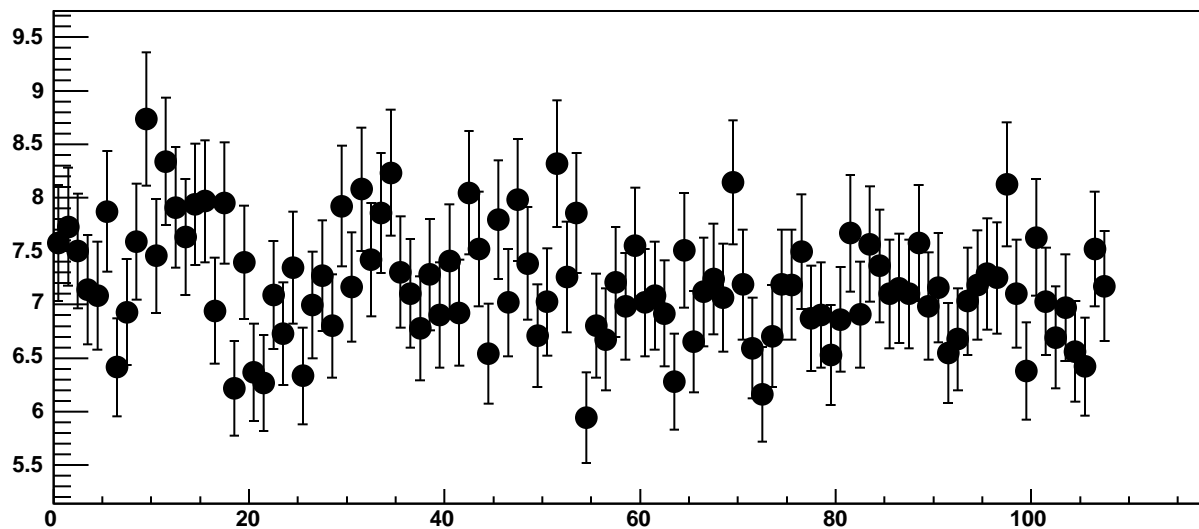
Disabled 4, Hold=35, Fit Slope vs 18*Chip+Chan



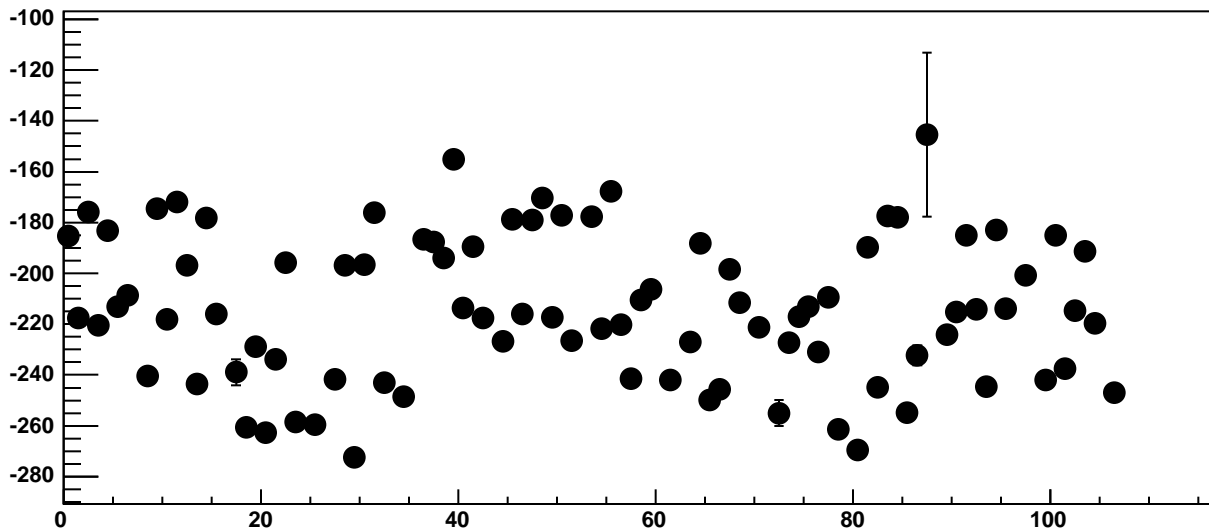
Enables off, ADC Mean vs 18*Chip+Chan



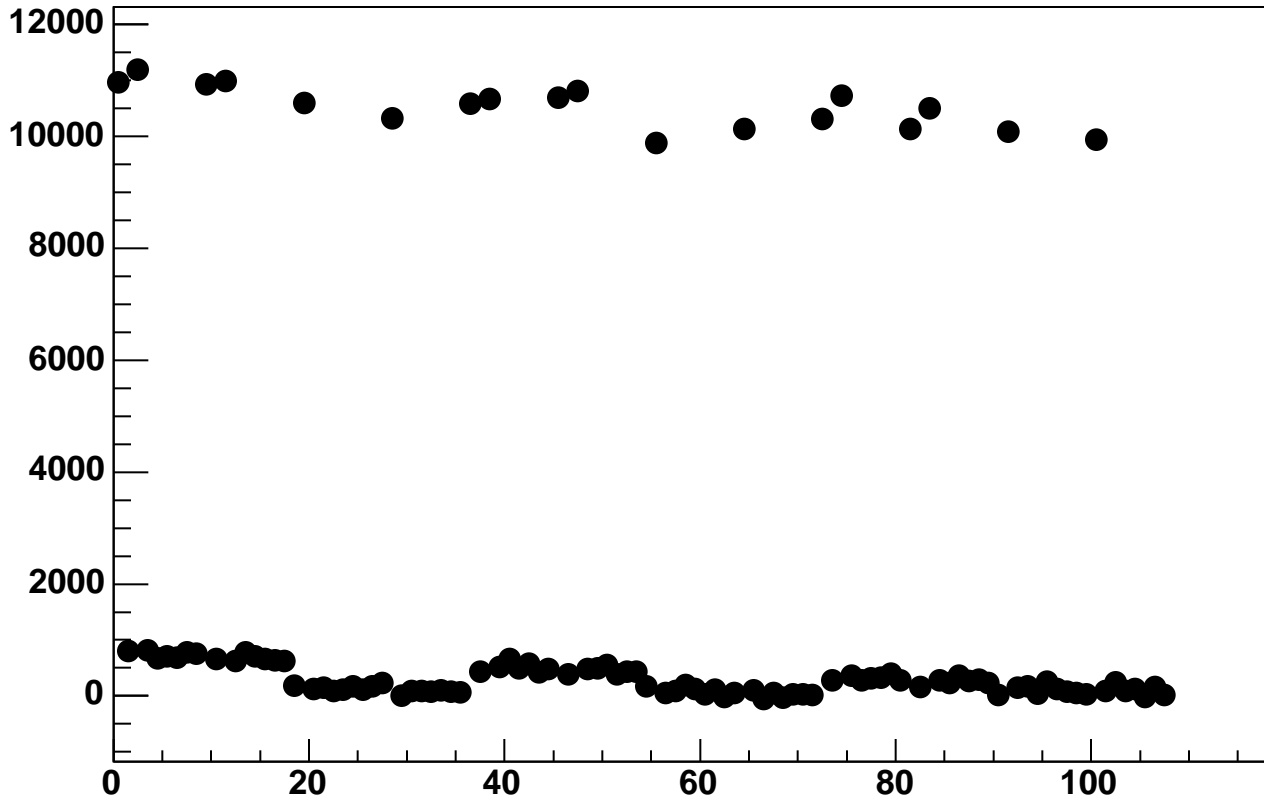
Enables off, ADC Noise vs 18*Chip+Chan



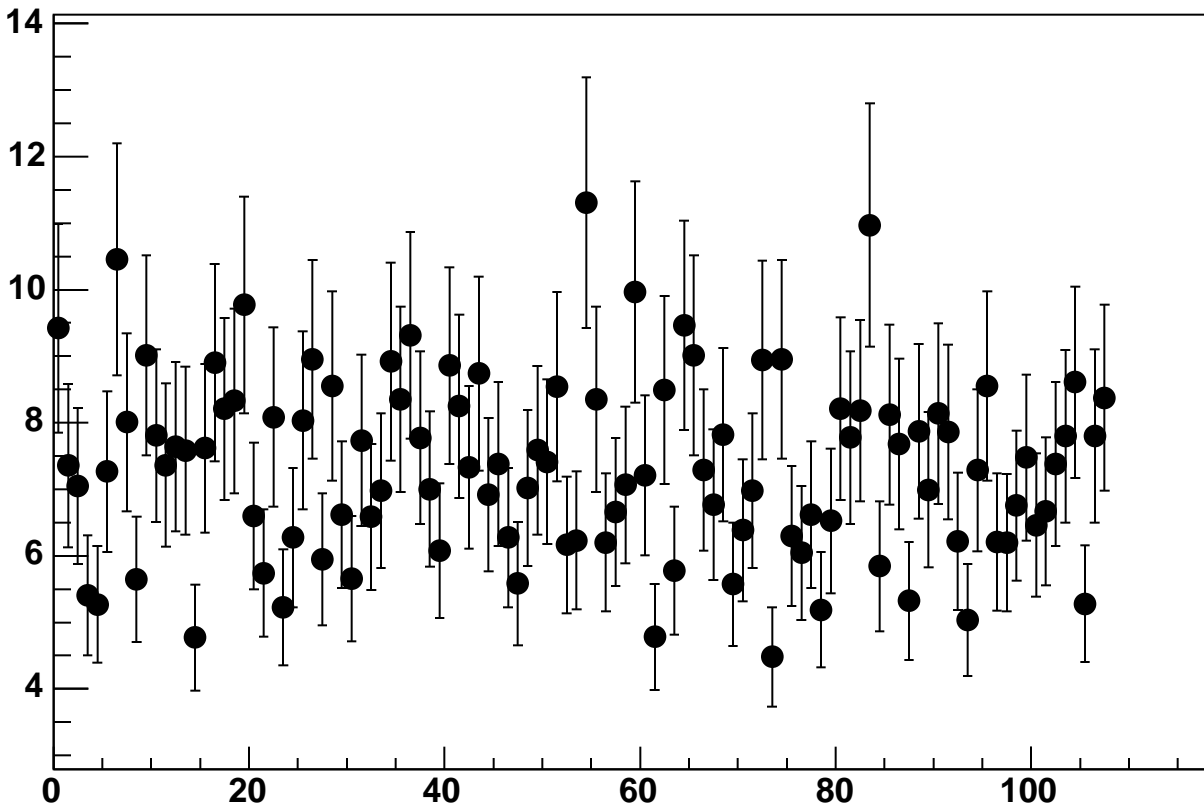
Enables off, ADC Residuals vs 18*Chip+Chan



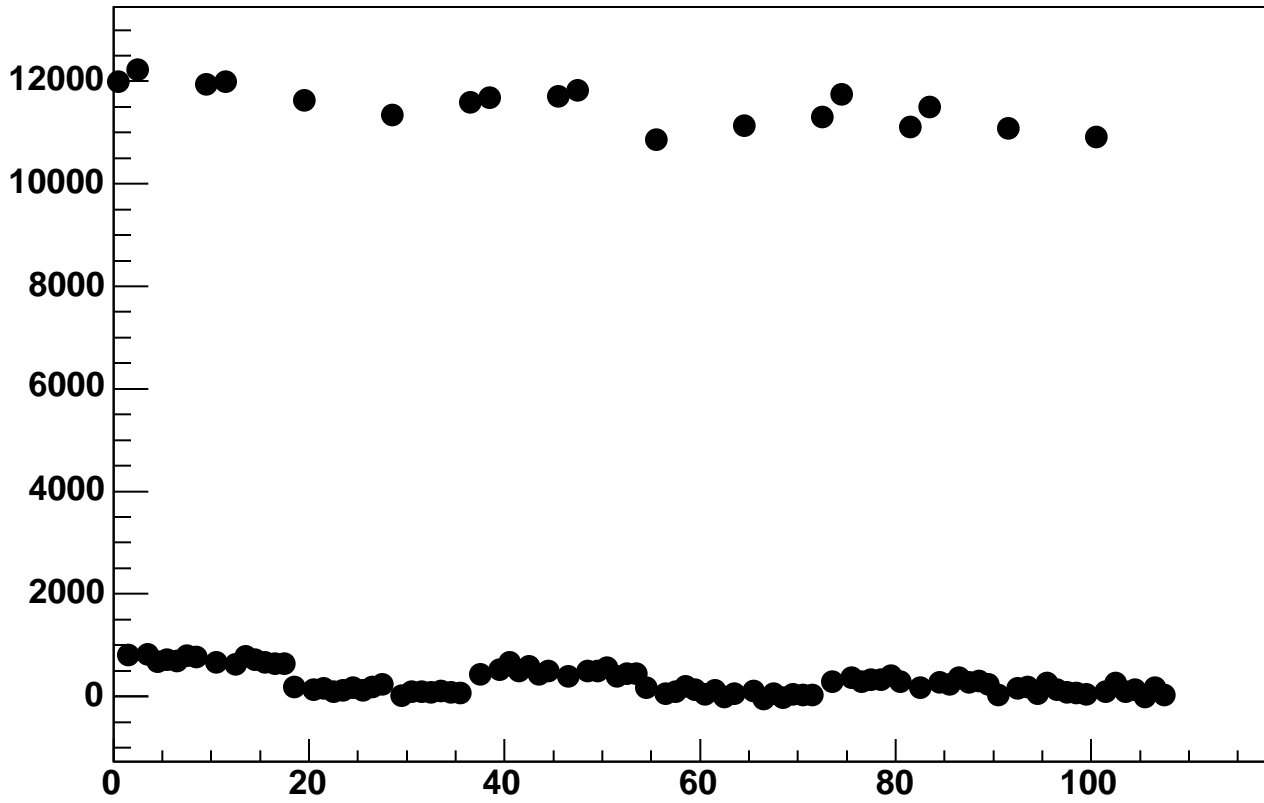
Enable 0, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



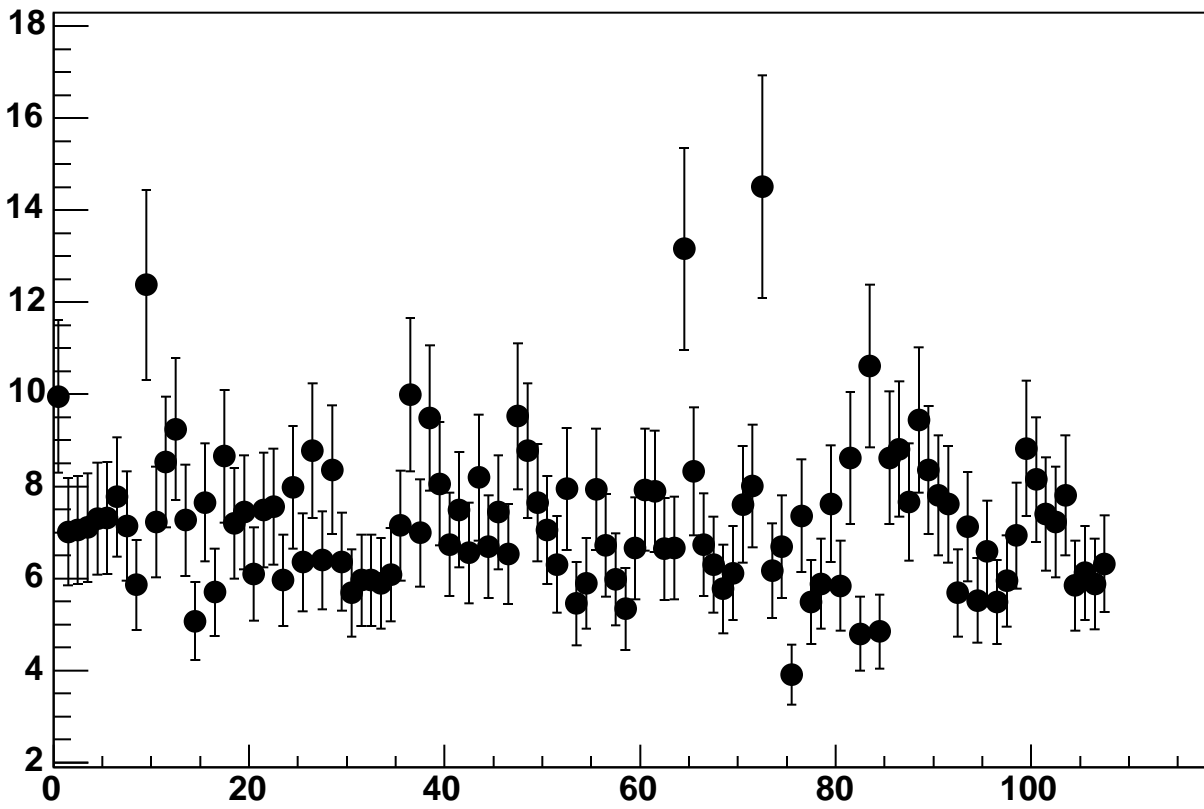
Enable 0, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



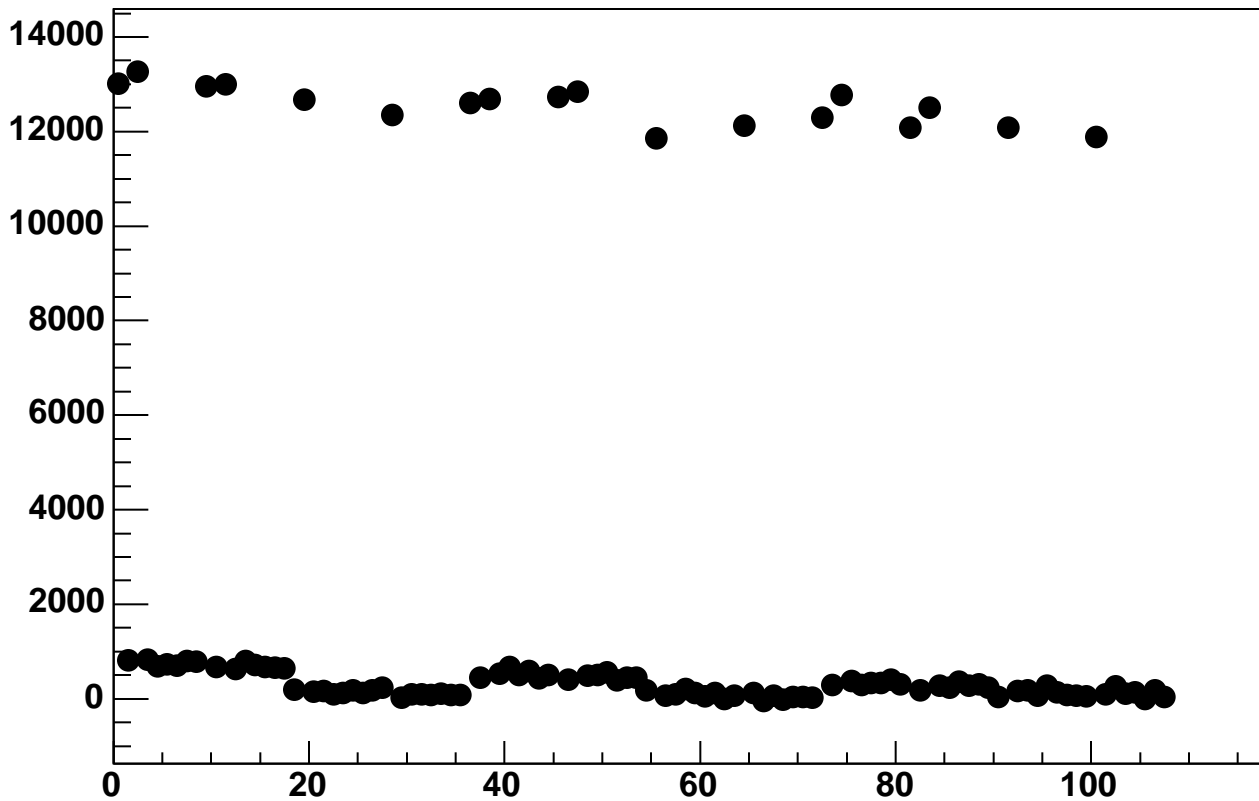
Enable 0, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



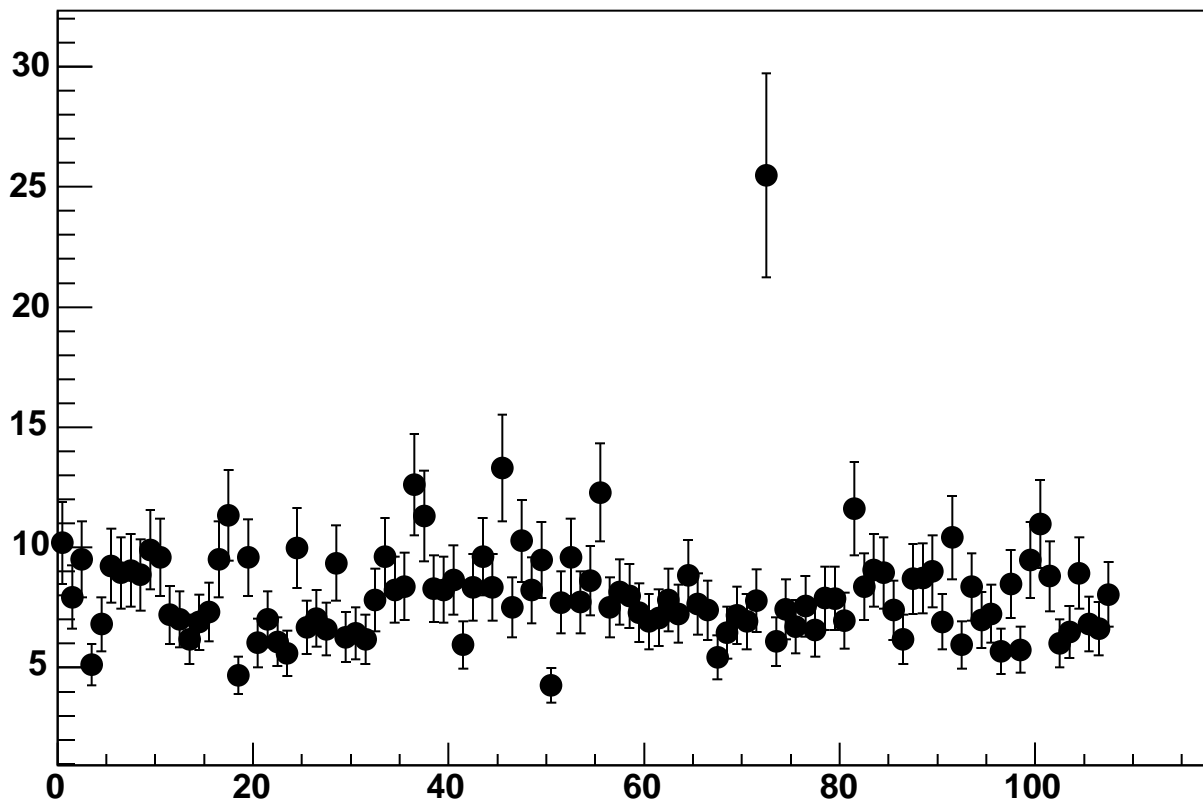
Enable 0, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



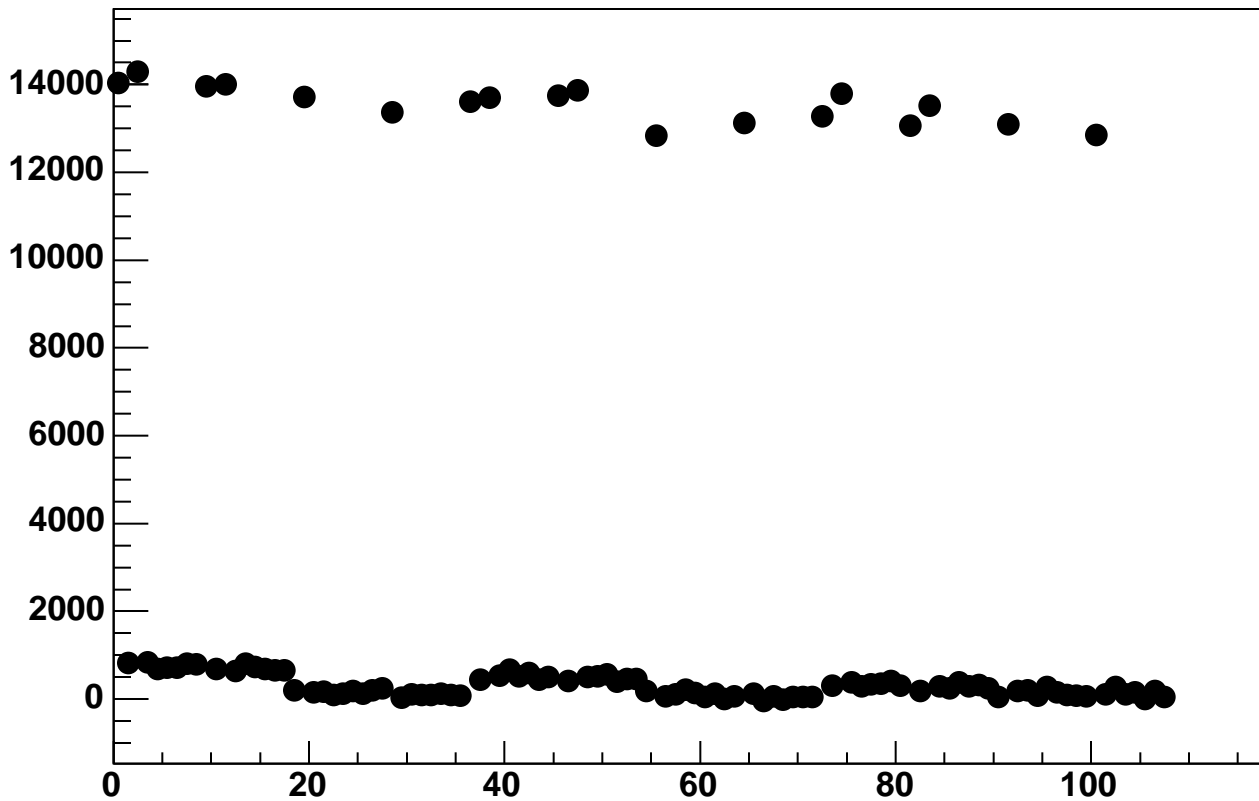
Enable 0, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



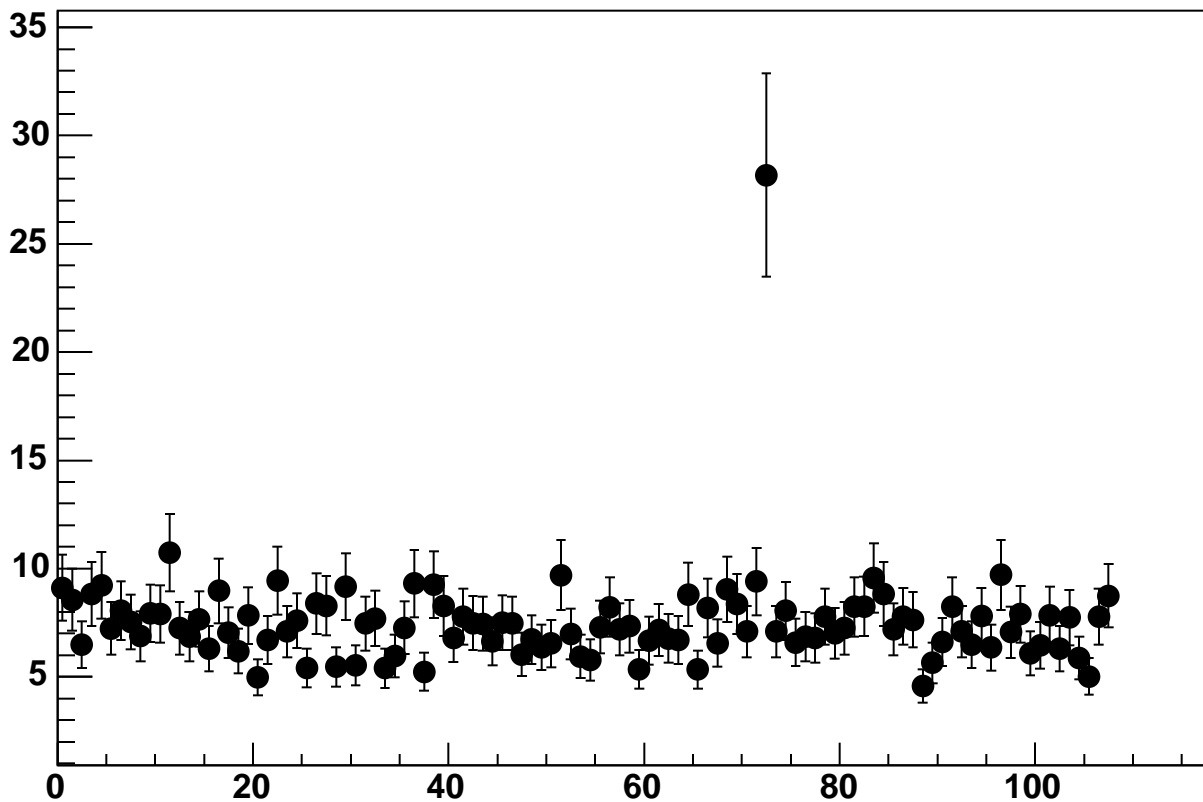
Enable 0, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



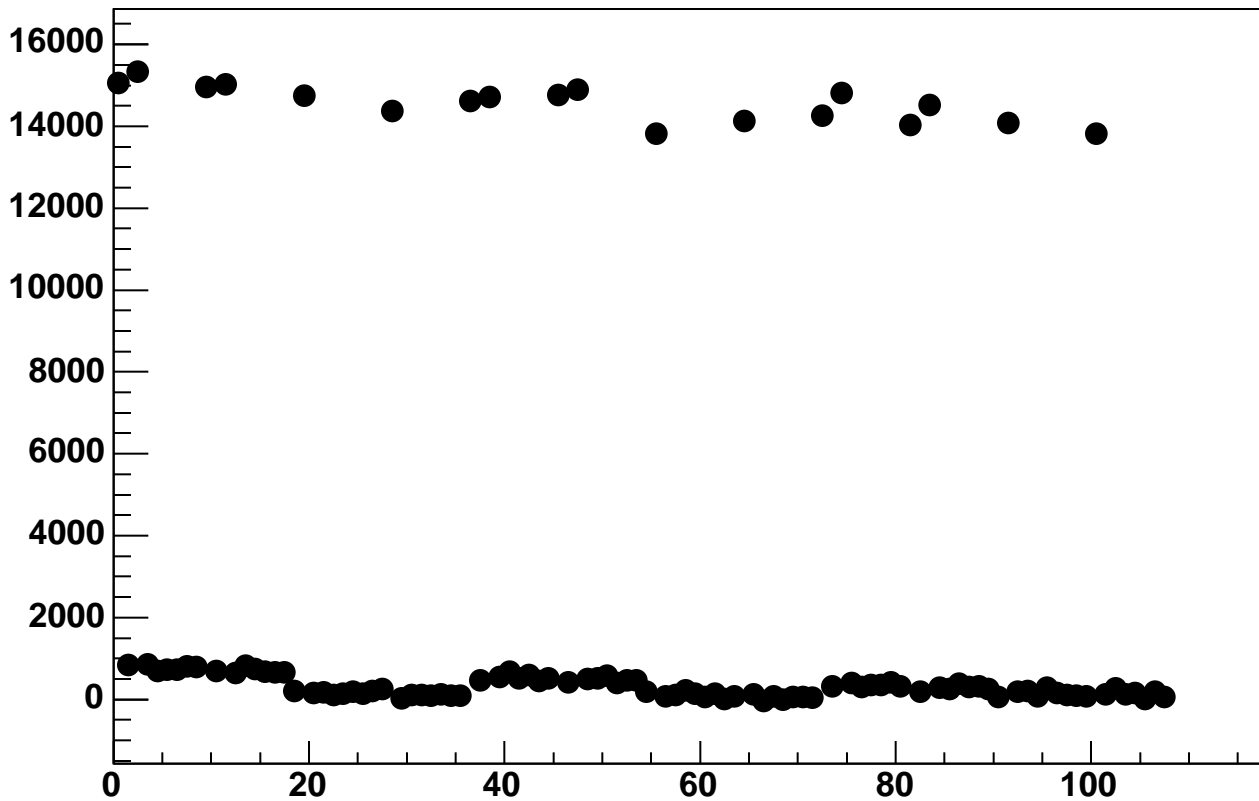
Enable 0, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



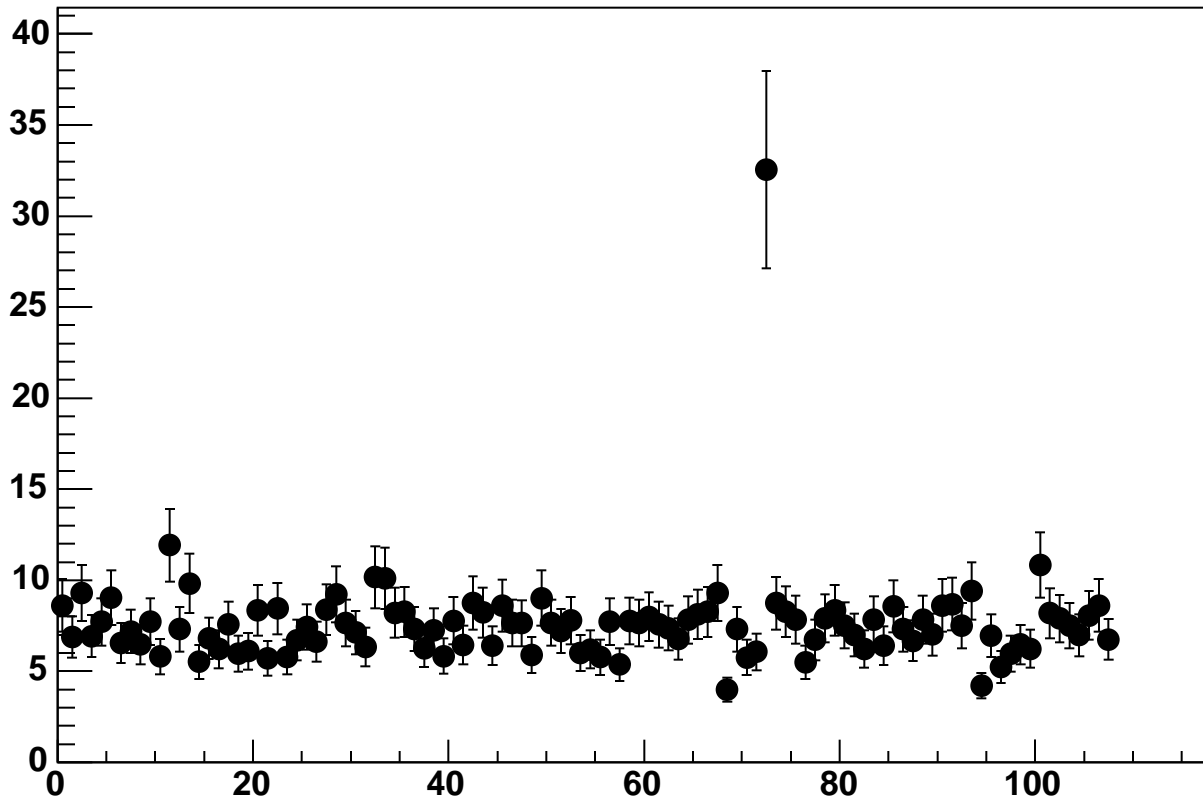
Enable 0, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



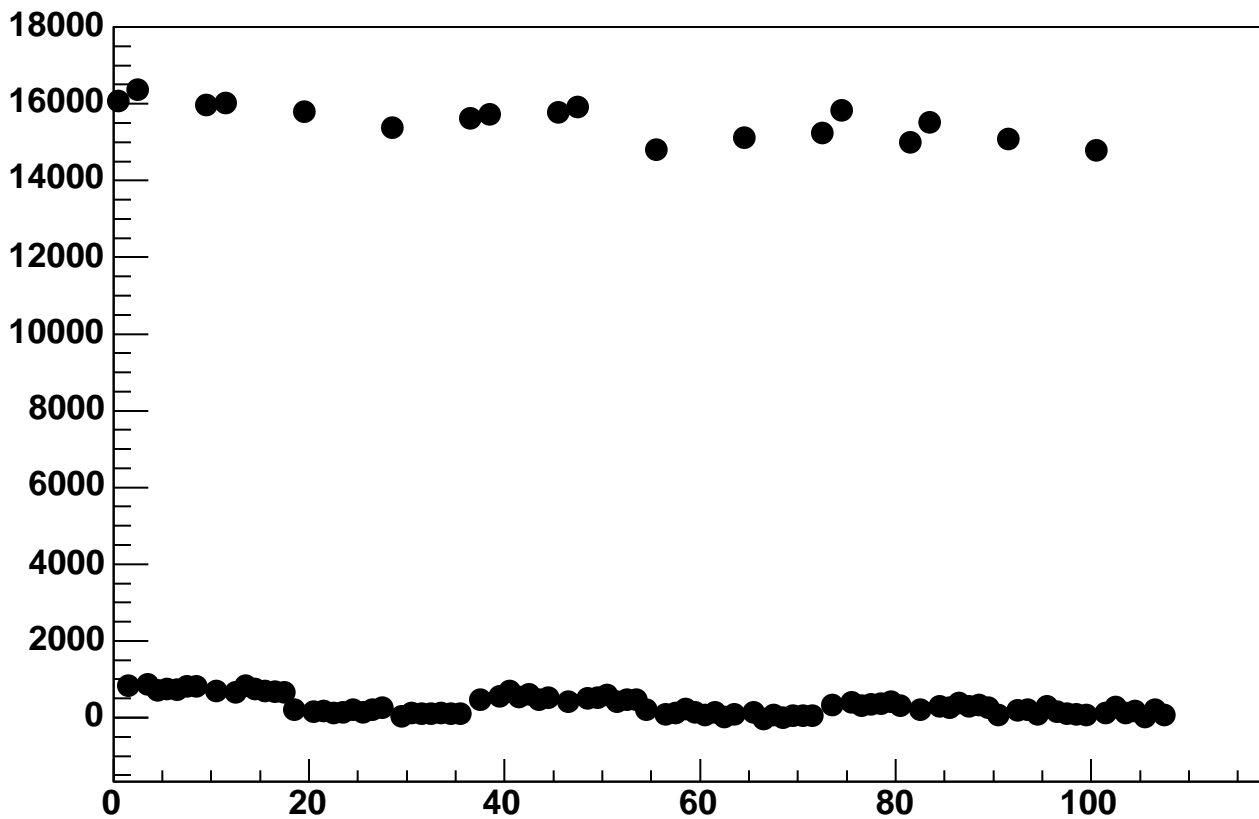
Enable 0, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



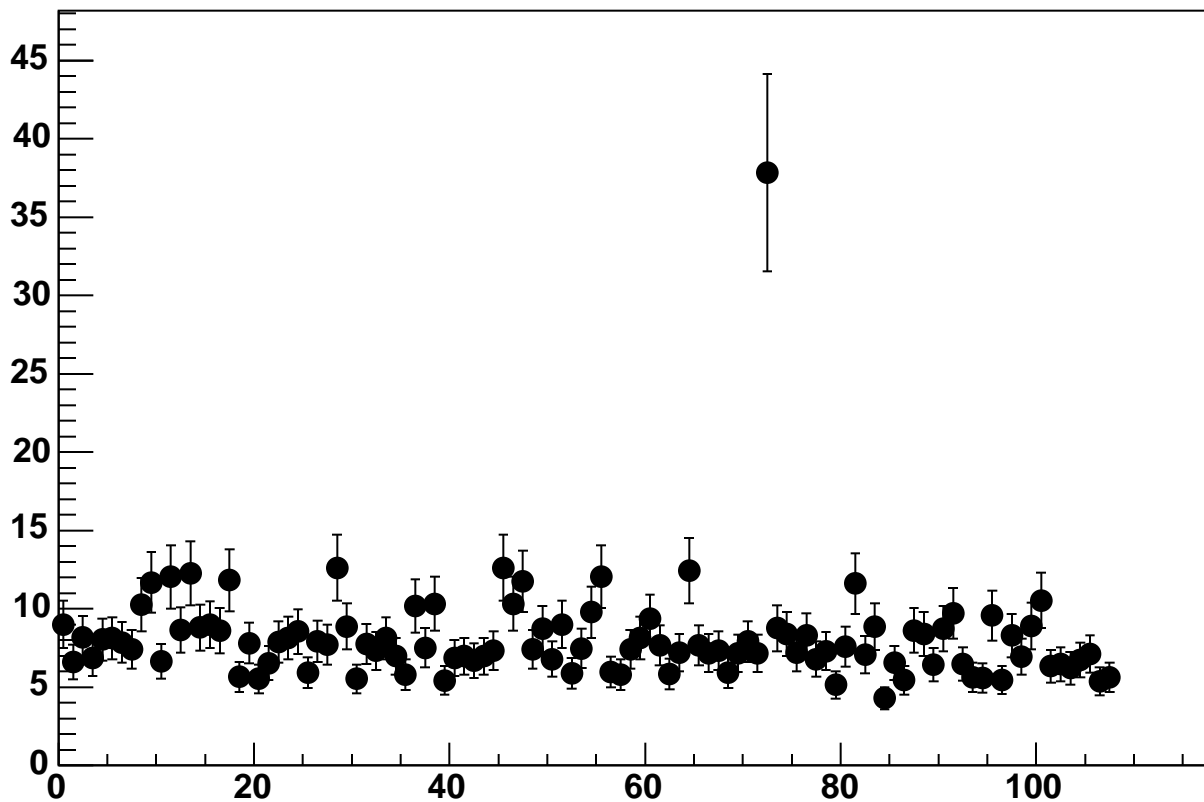
Enable 0, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



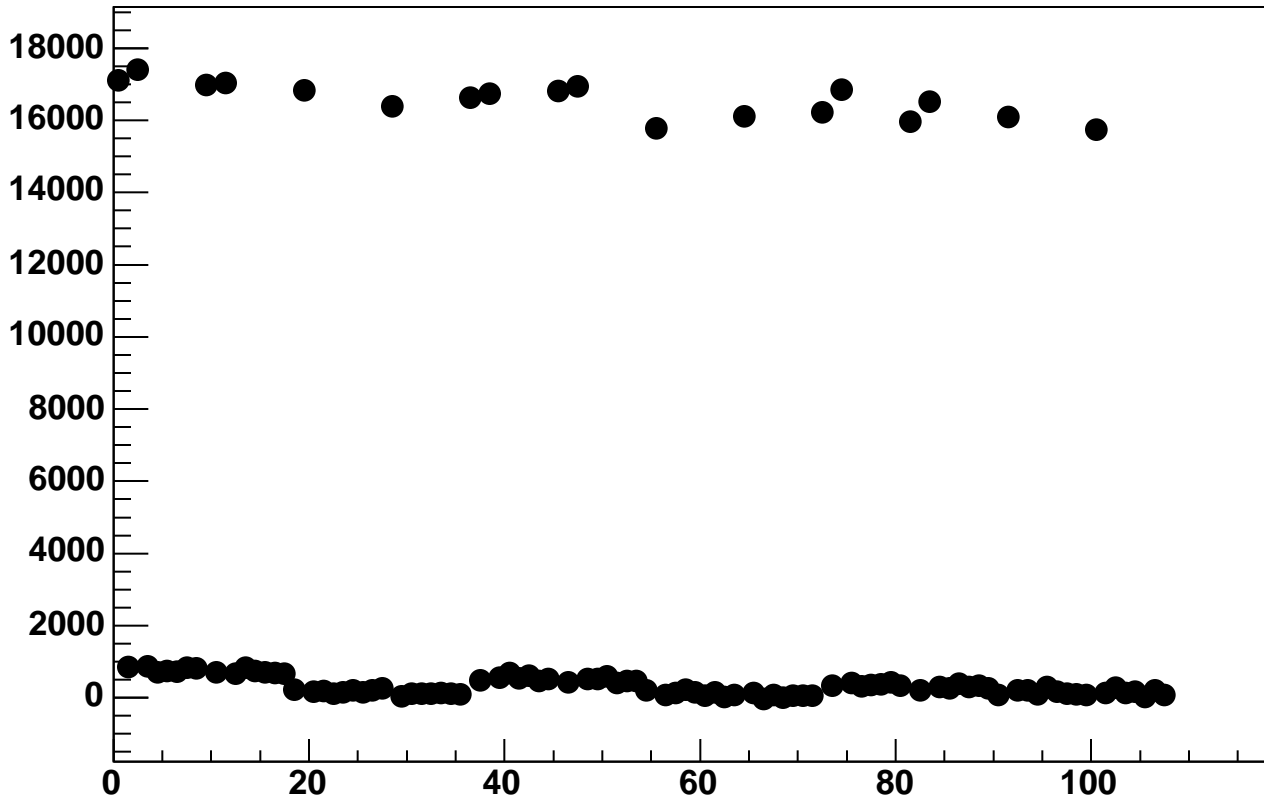
Enable 0, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



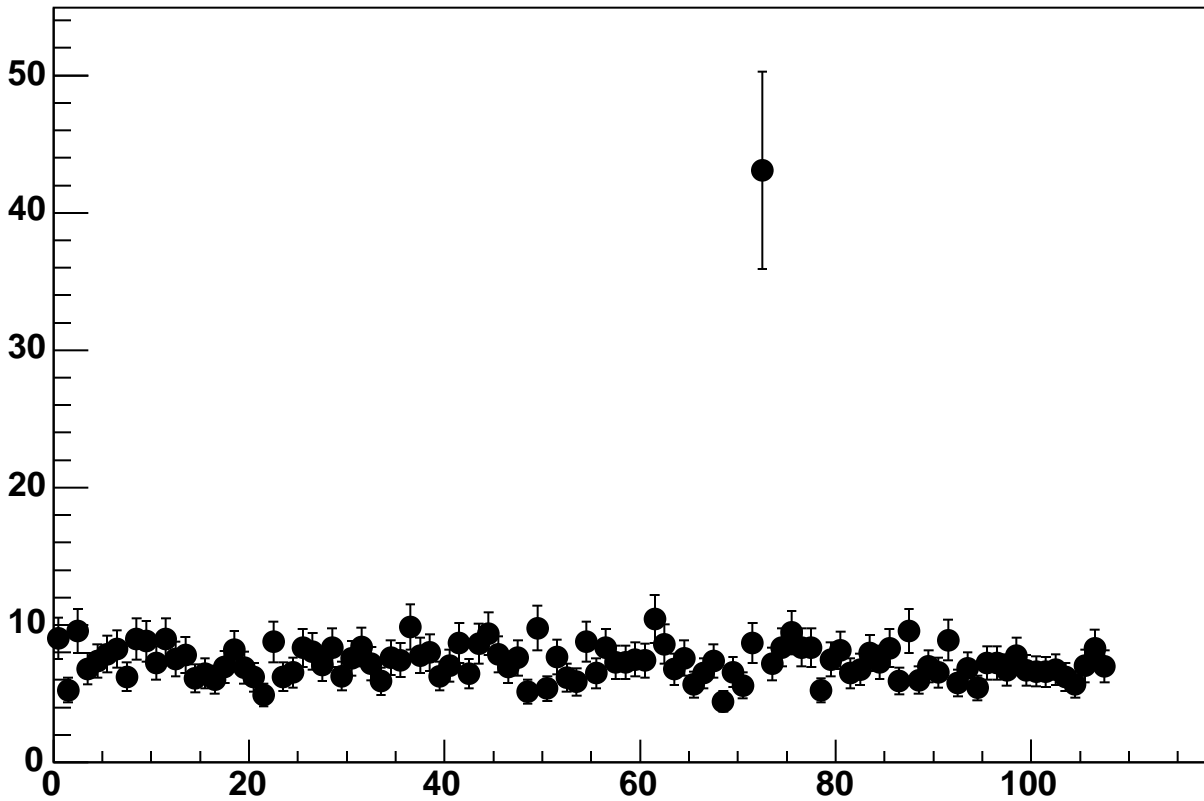
Enable 0, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



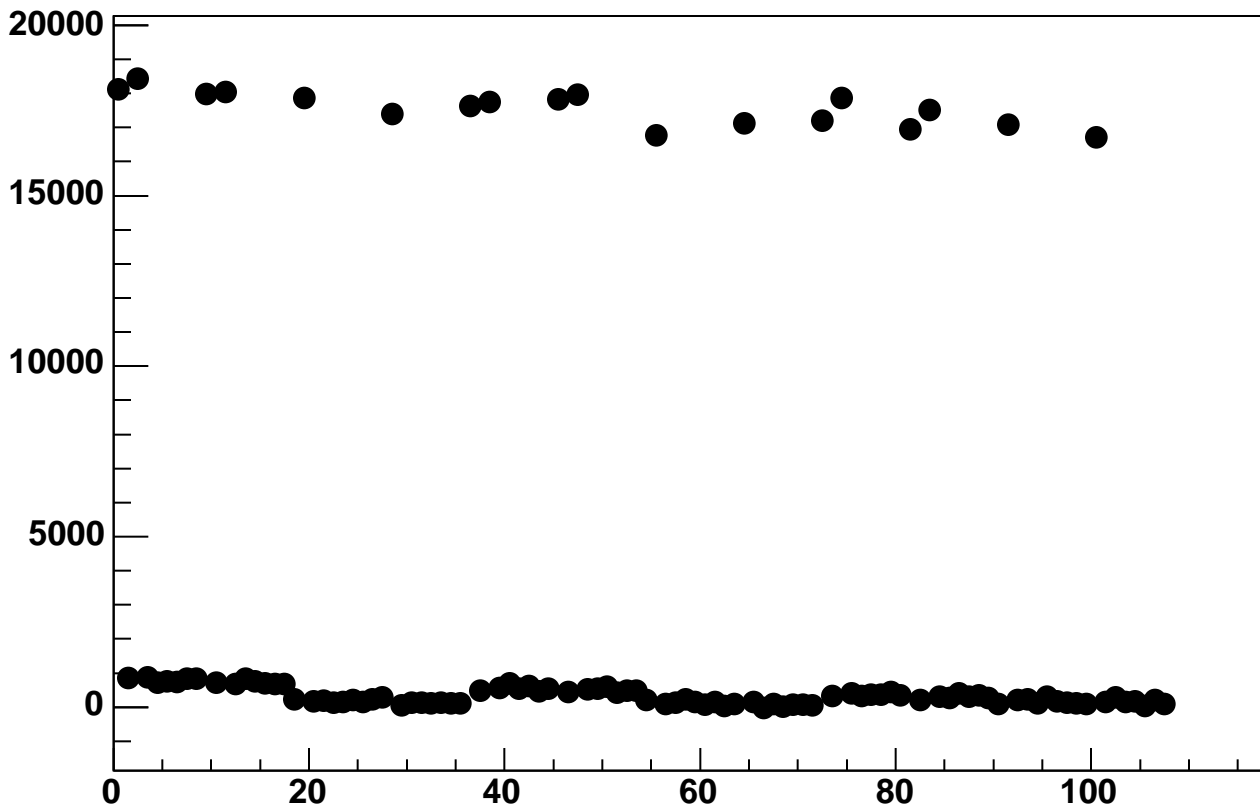
Enable 0, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



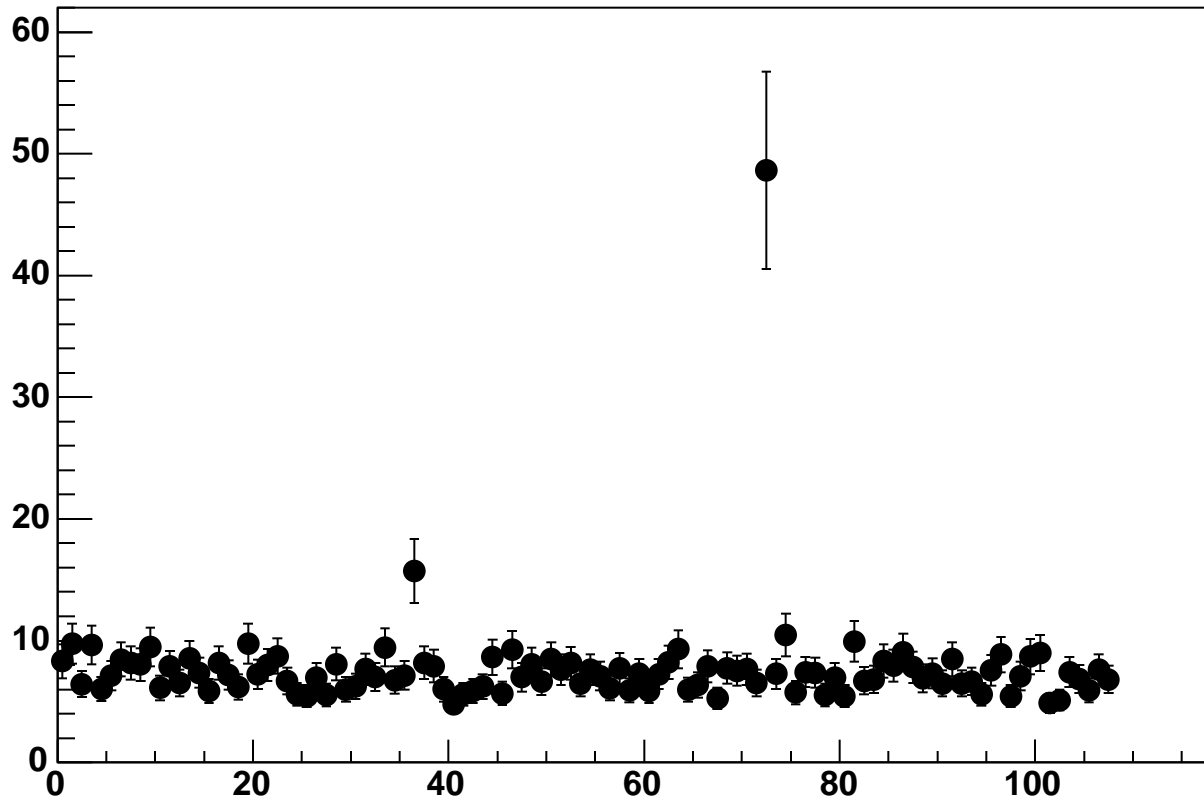
Enable 0, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



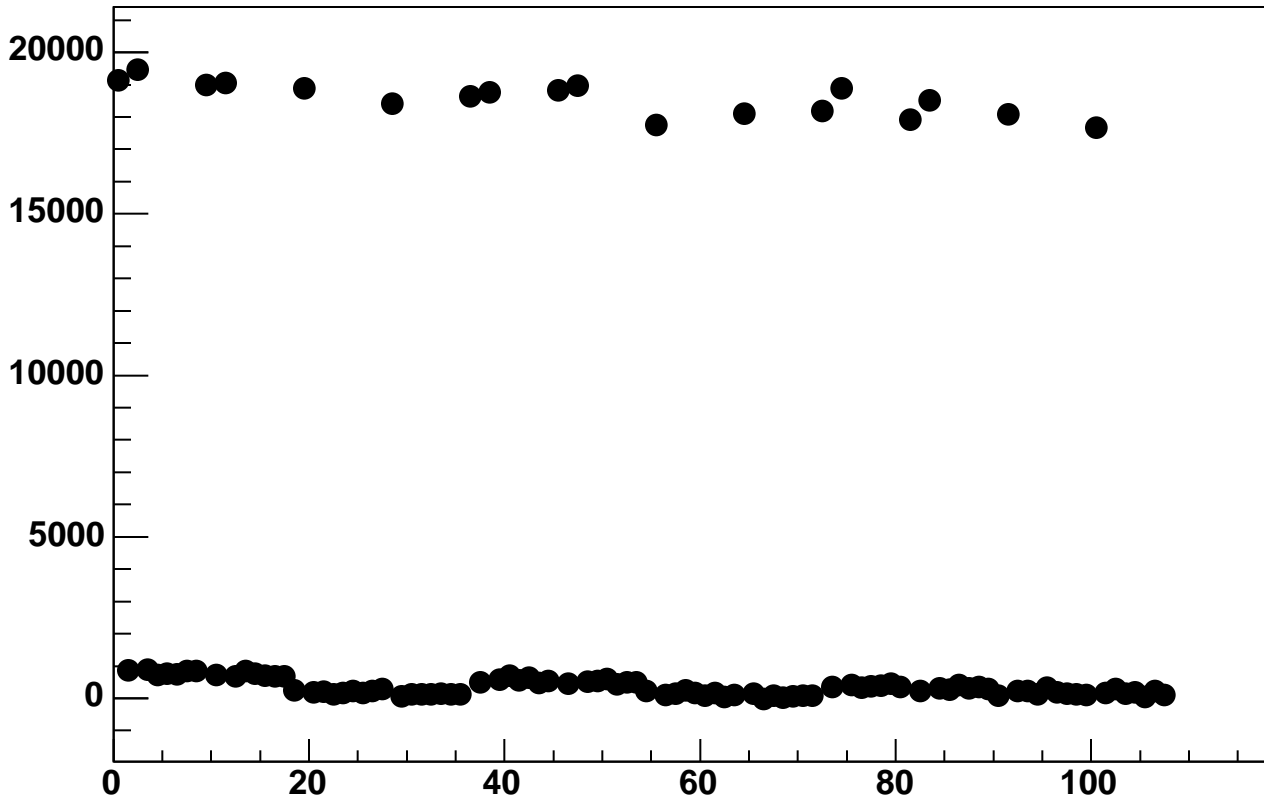
Enable 0, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



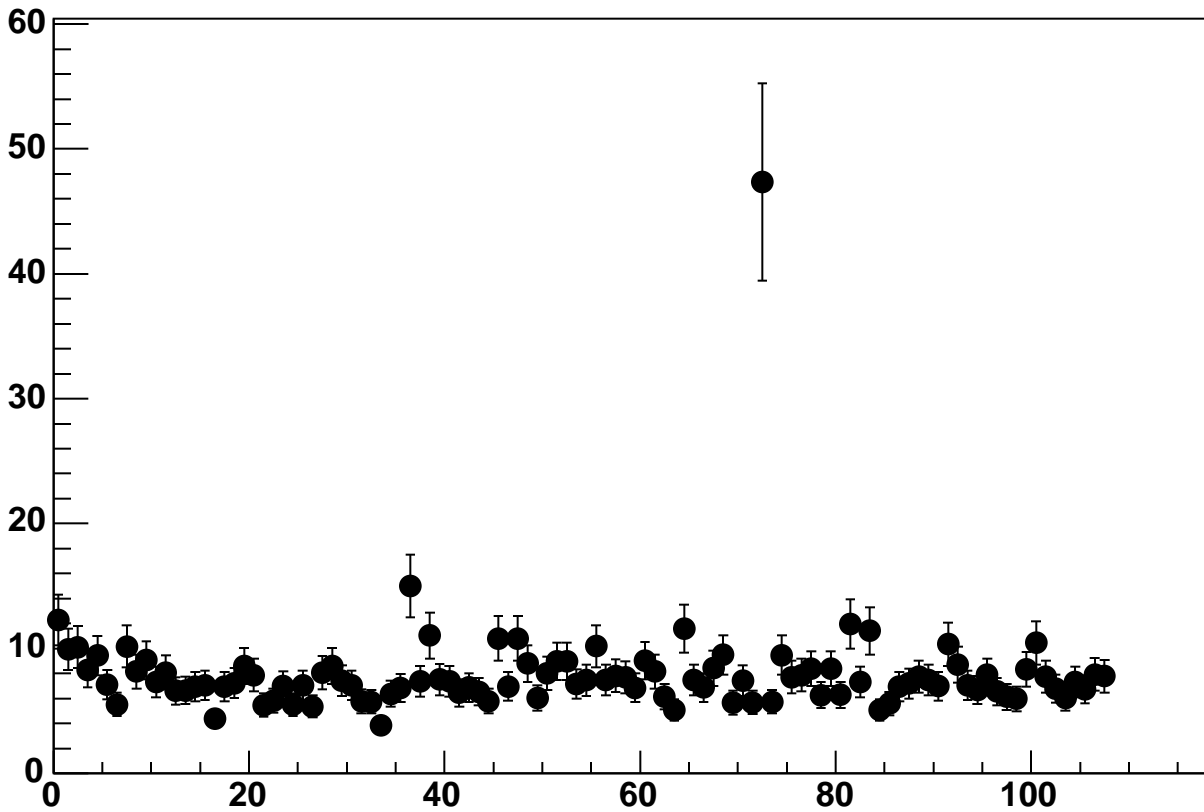
Enable 0, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



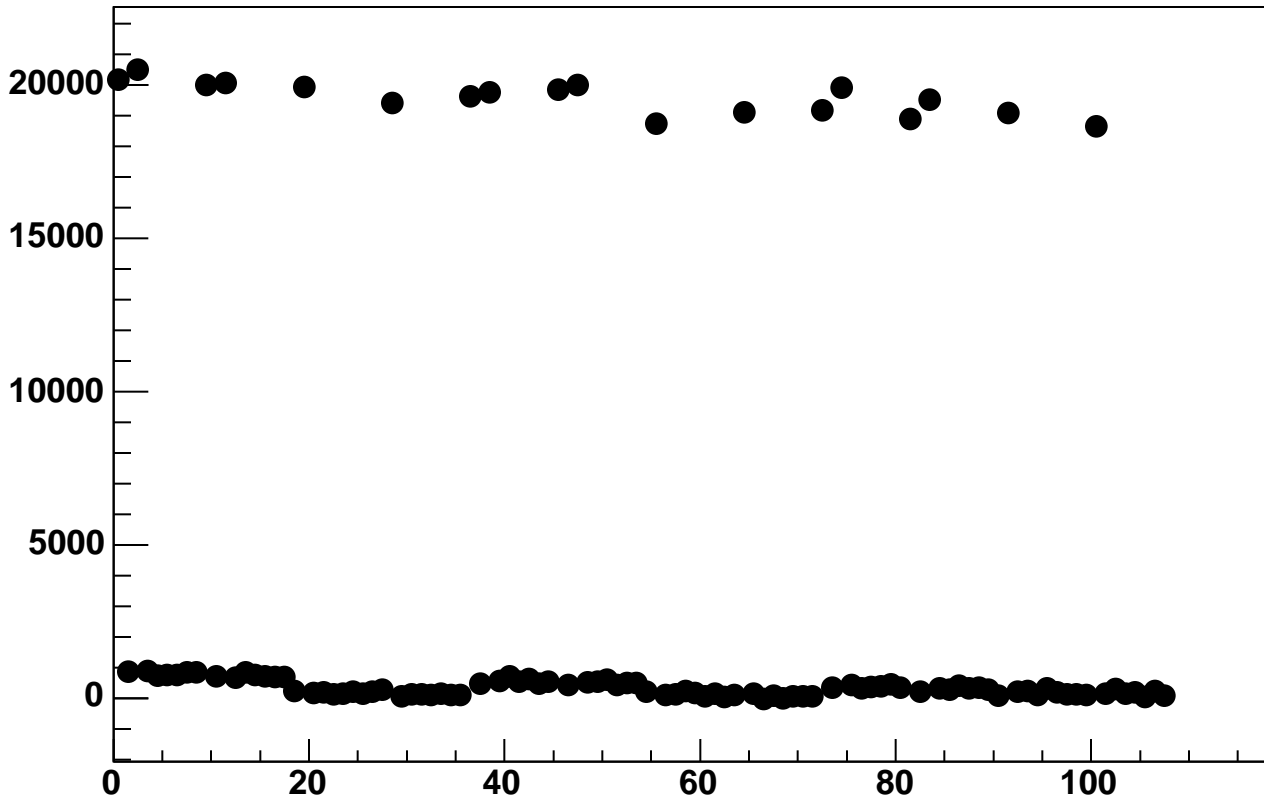
Enable 0, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



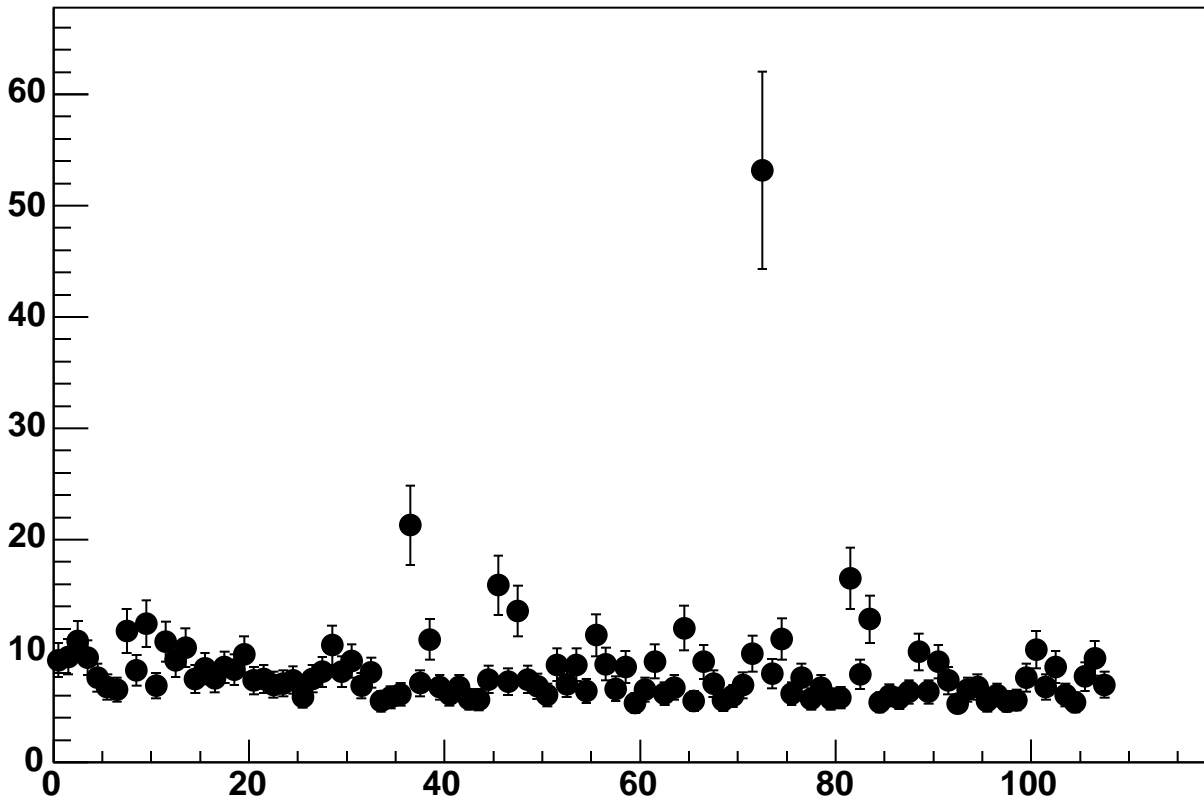
Enable 0, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



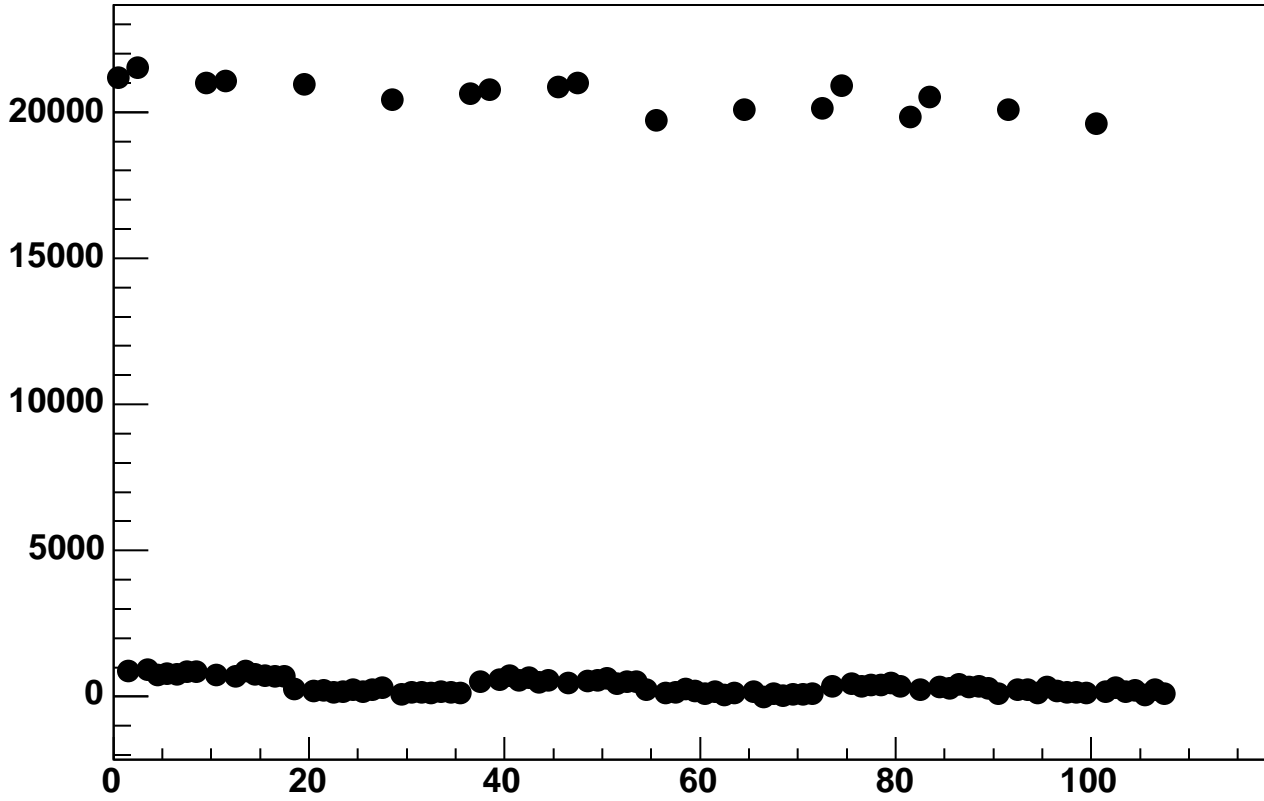
Enable 0, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



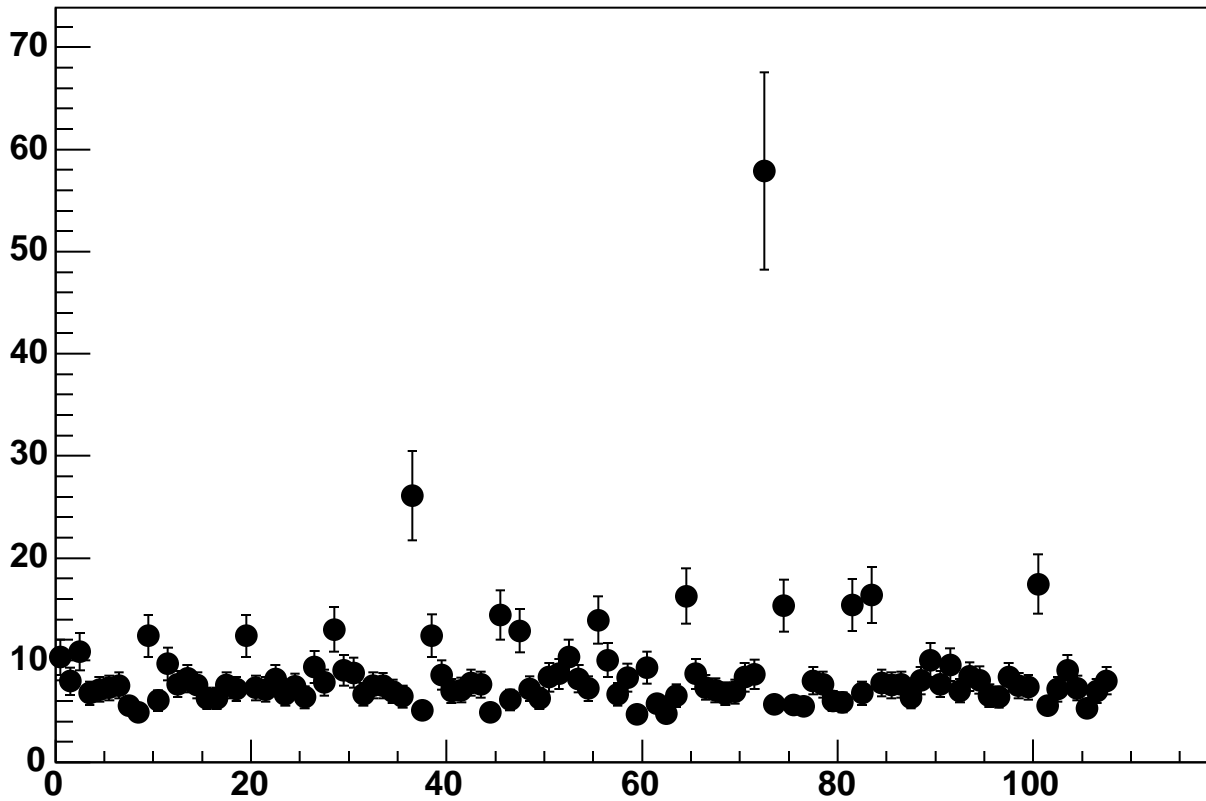
Enable 0, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



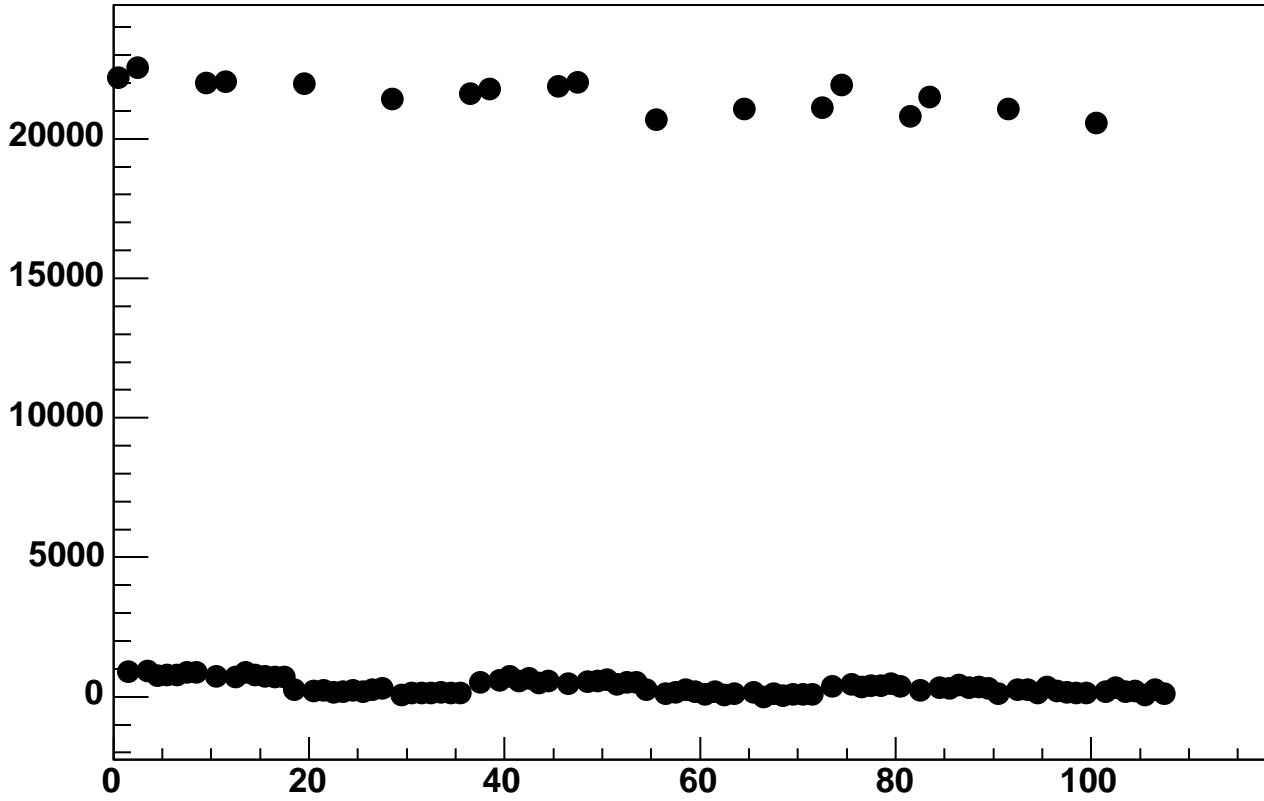
Enable 0, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



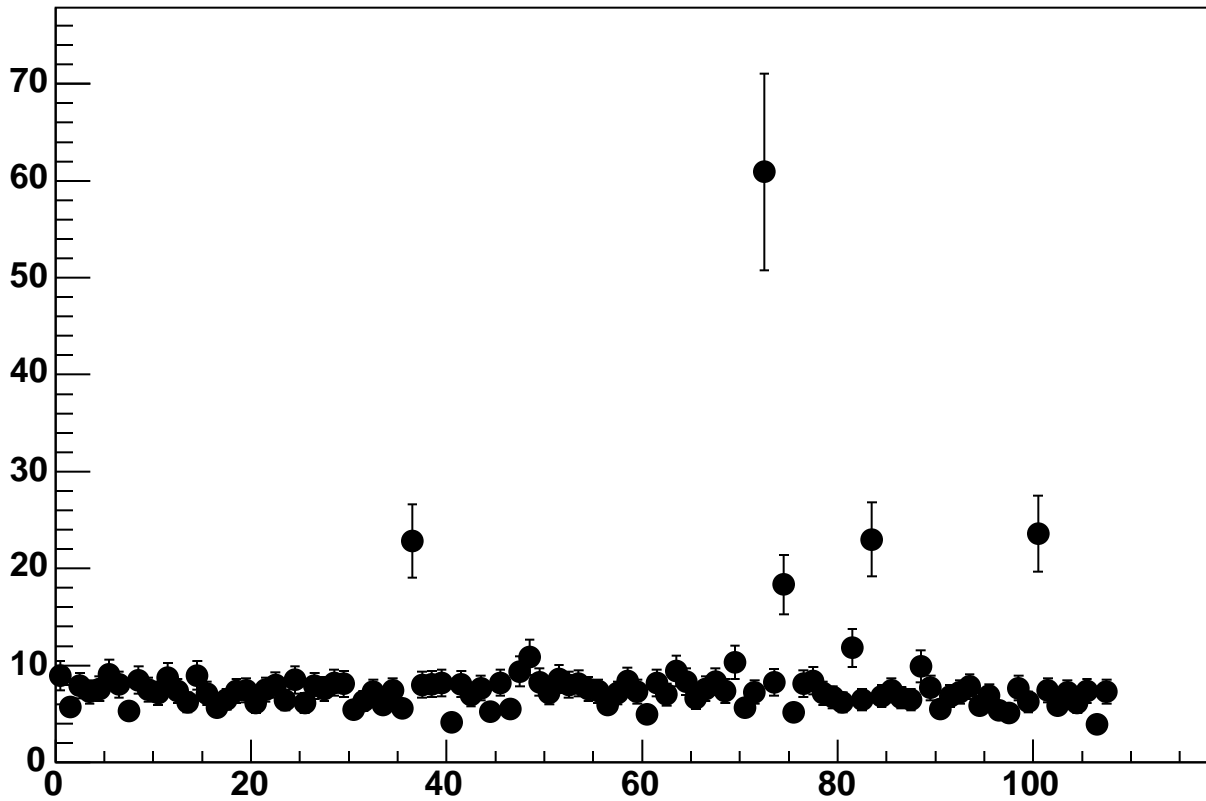
Enable 0, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



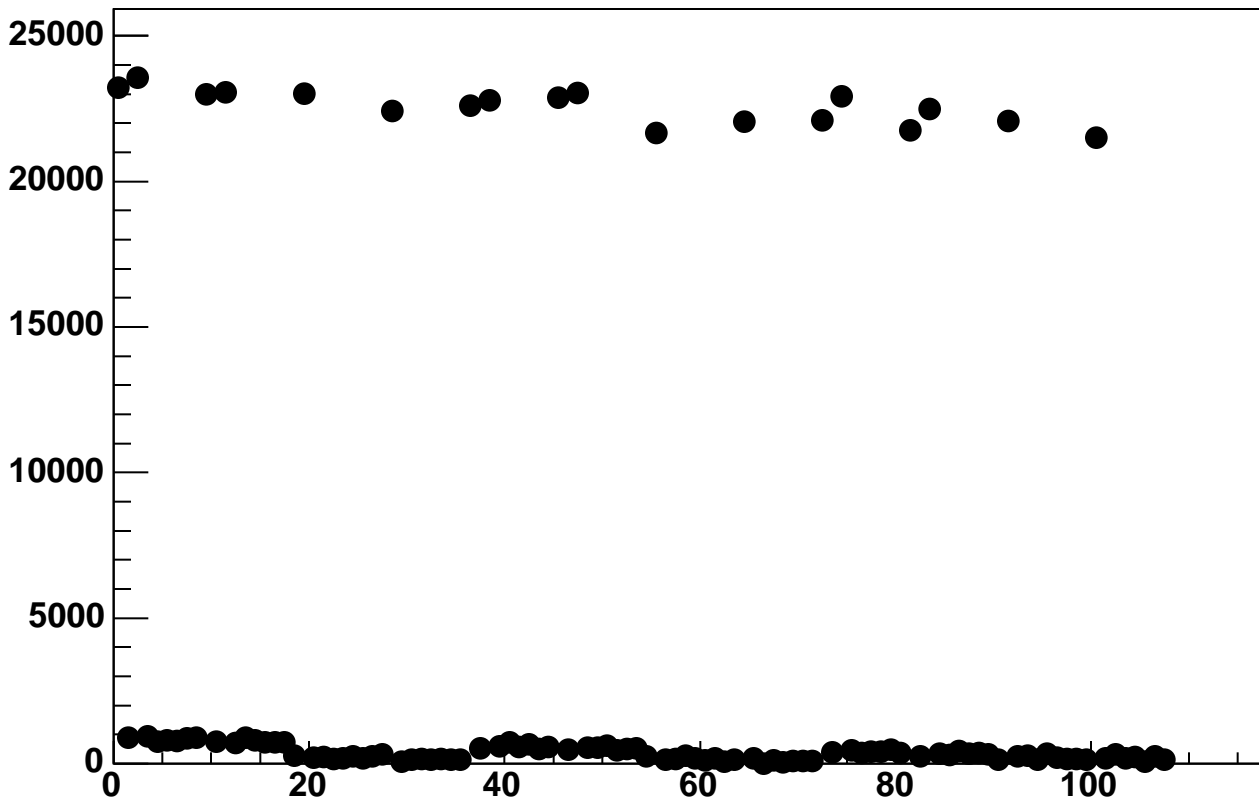
Enable 0, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



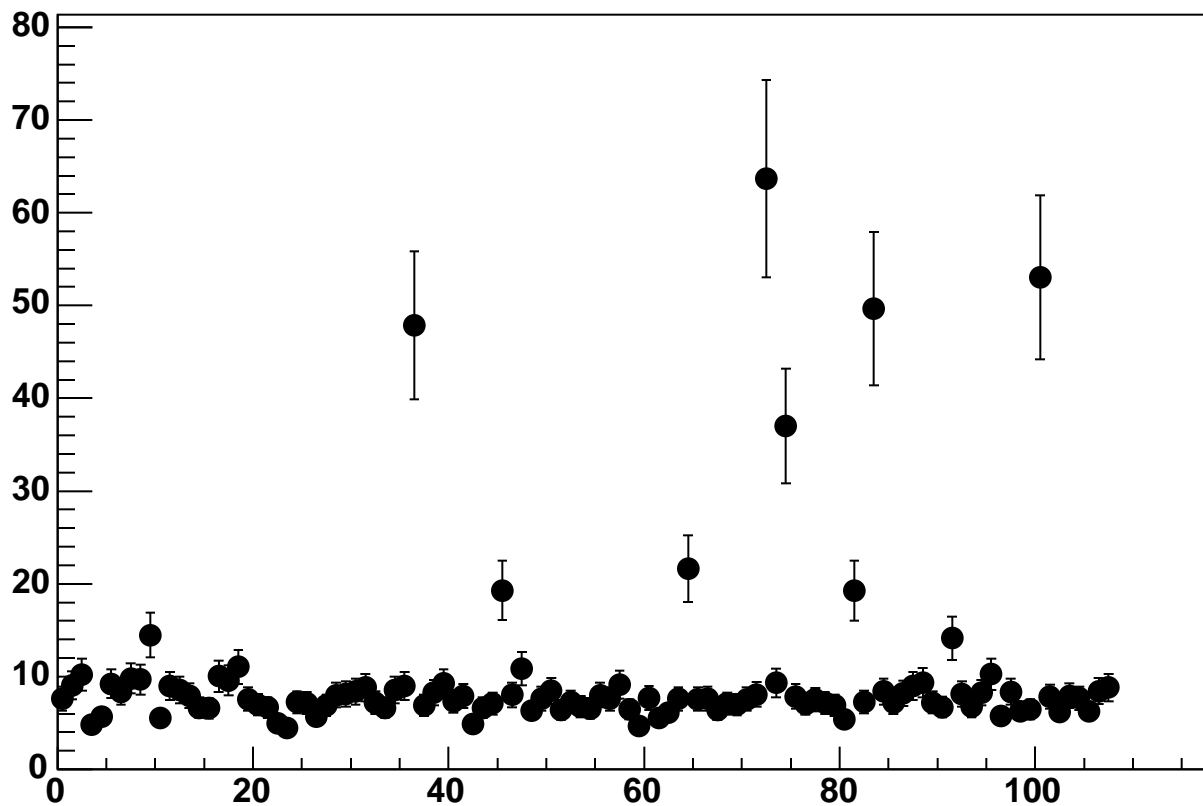
Enable 0, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



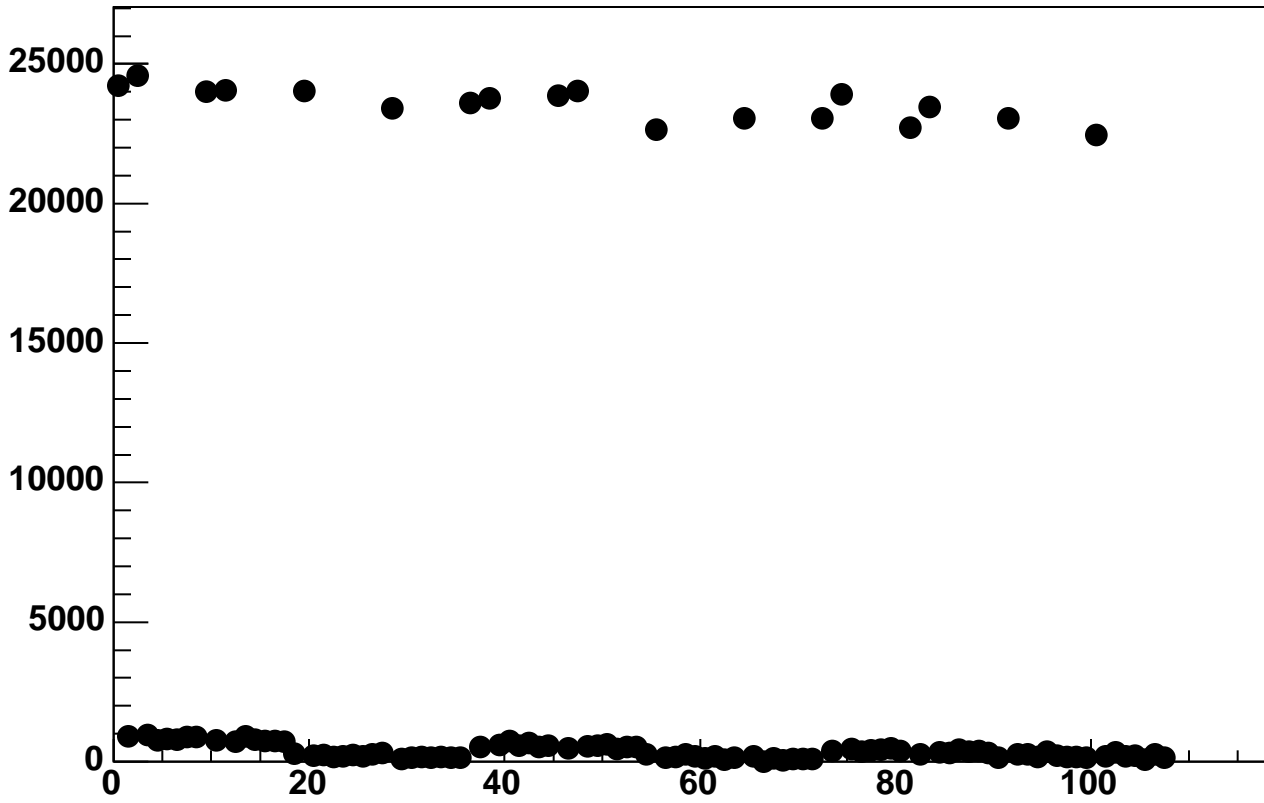
Enable 0, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



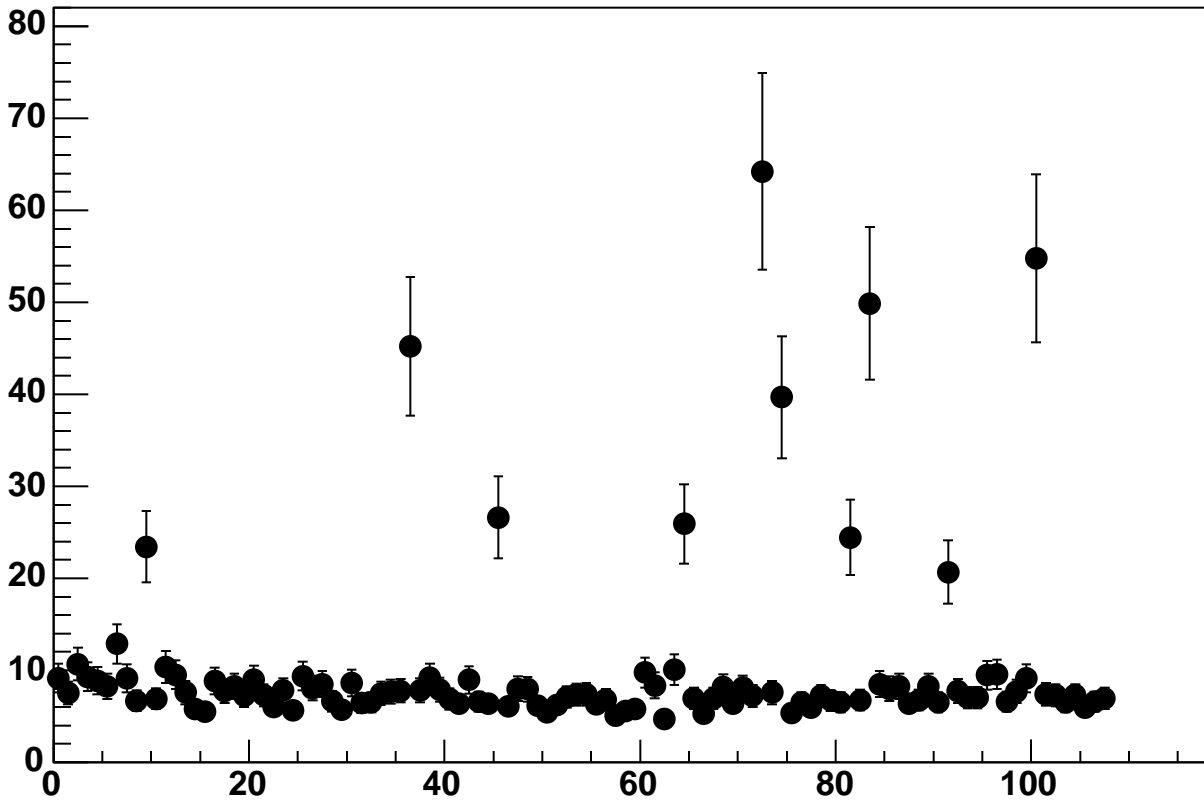
Enable 0, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



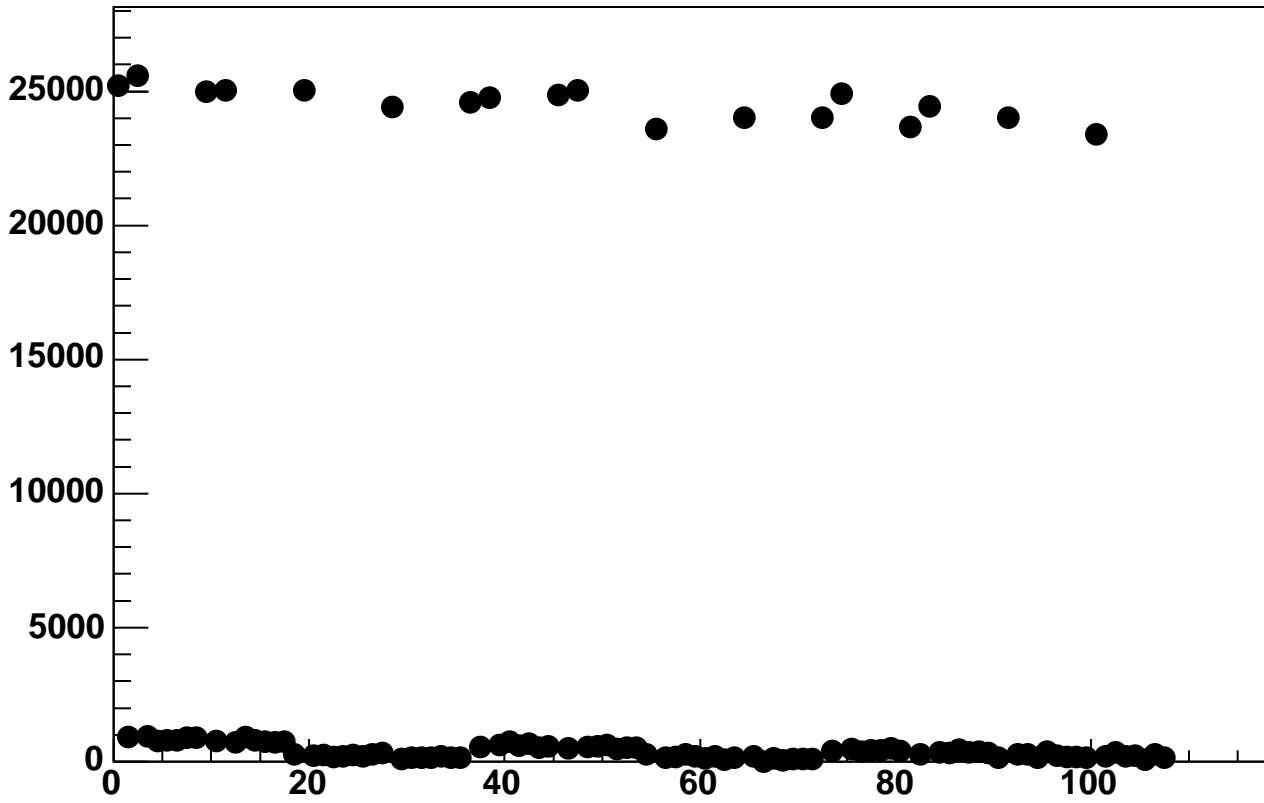
Enable 0, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



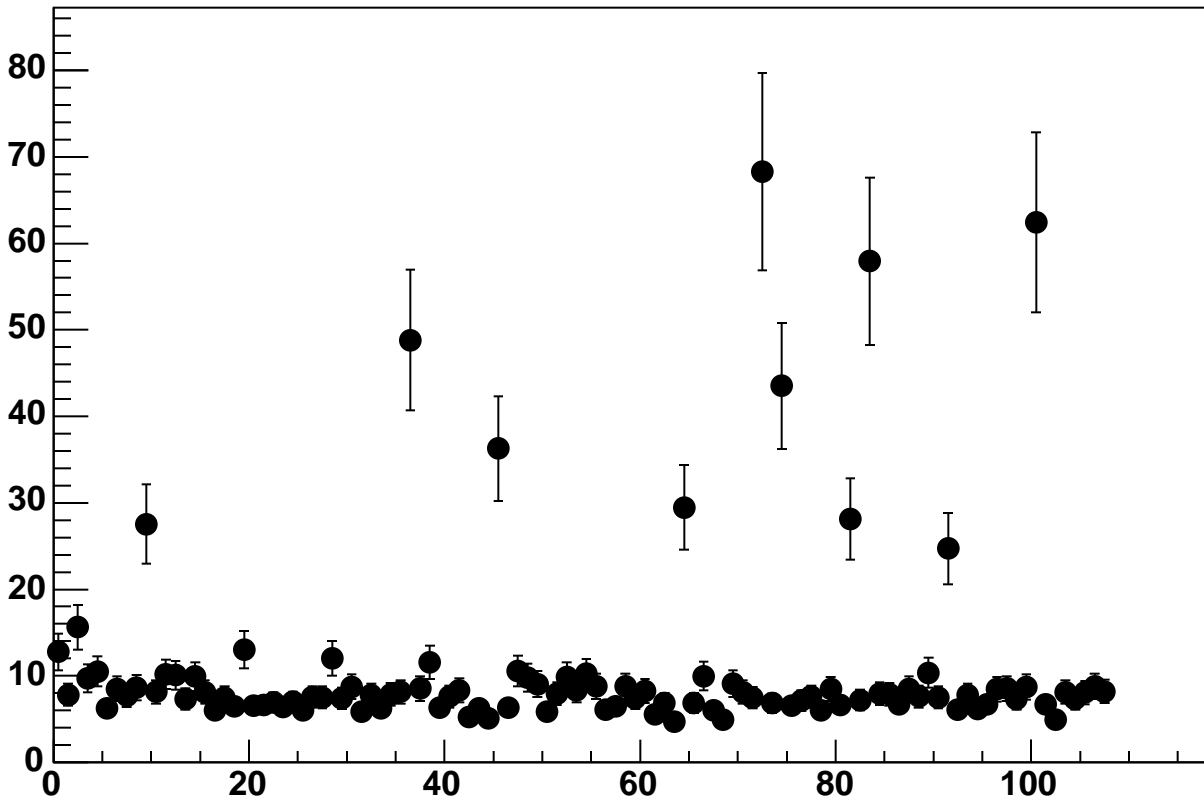
Enable 0, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



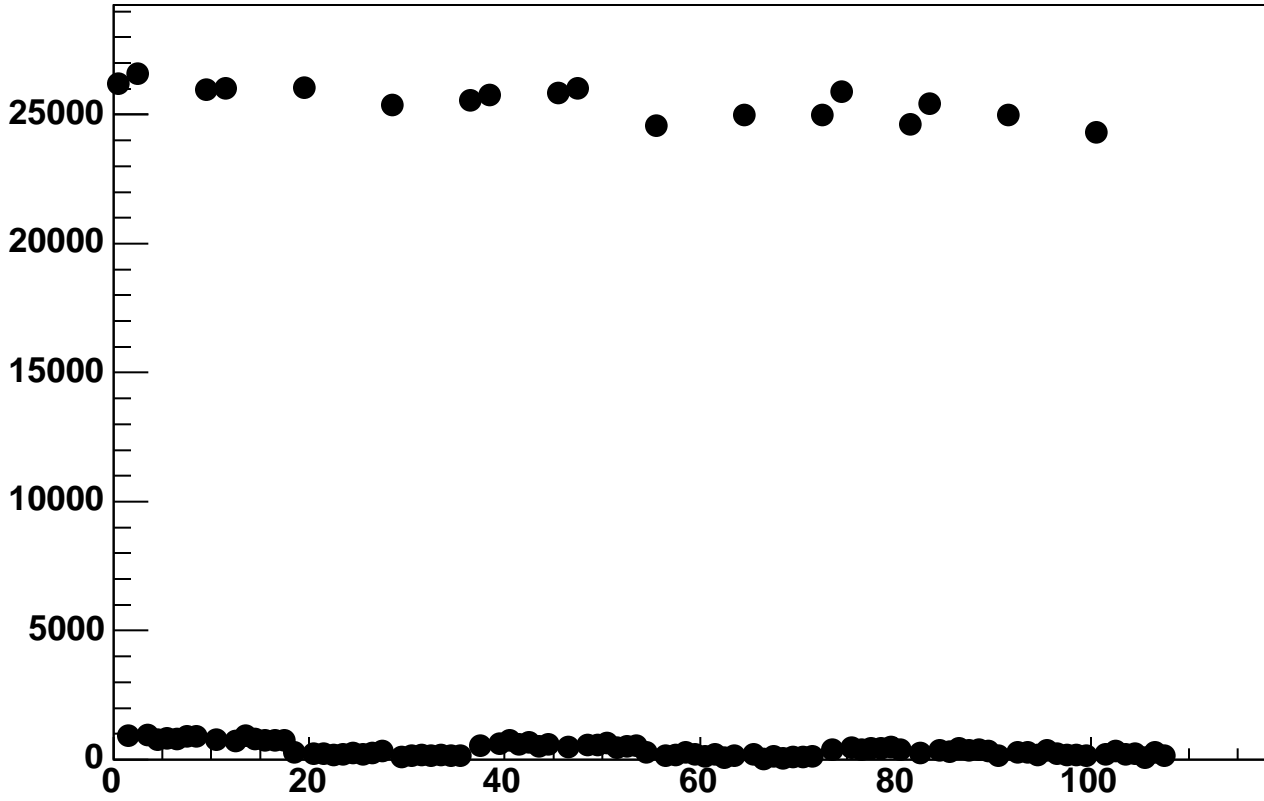
Enable 0, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



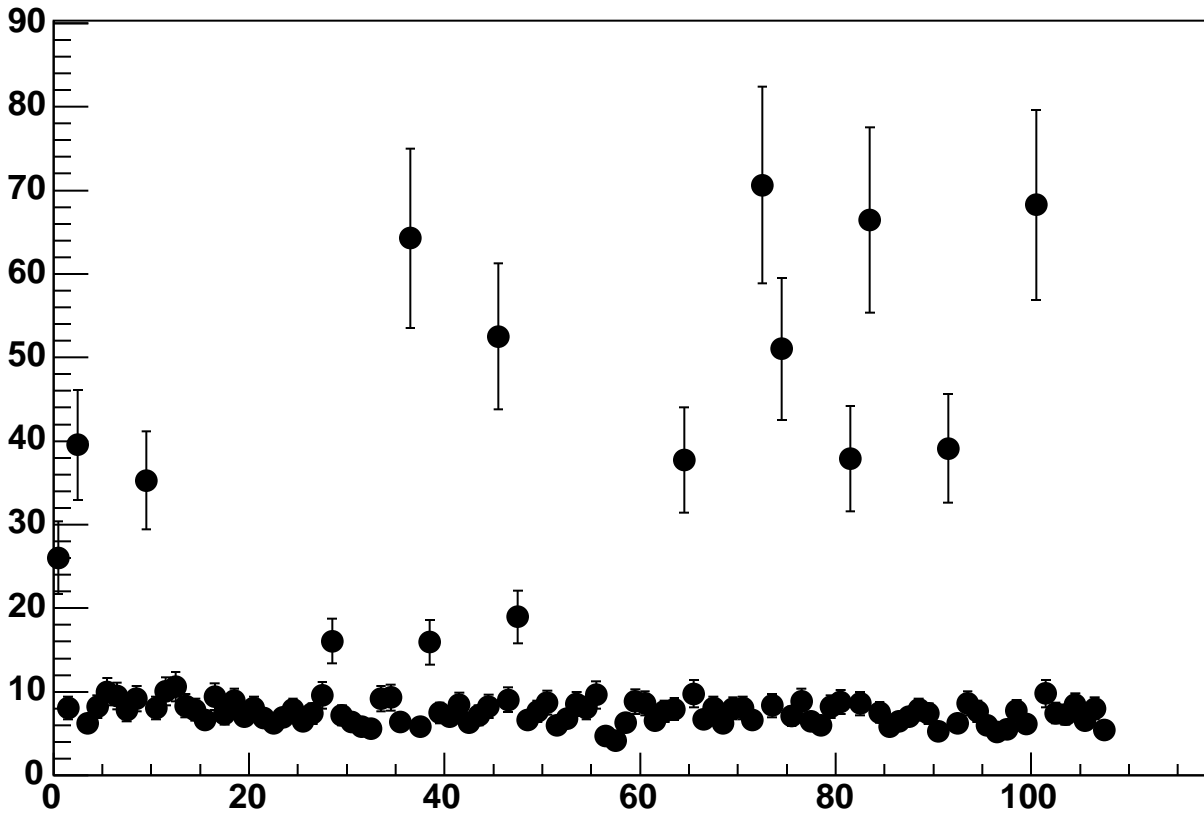
Enable 0, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



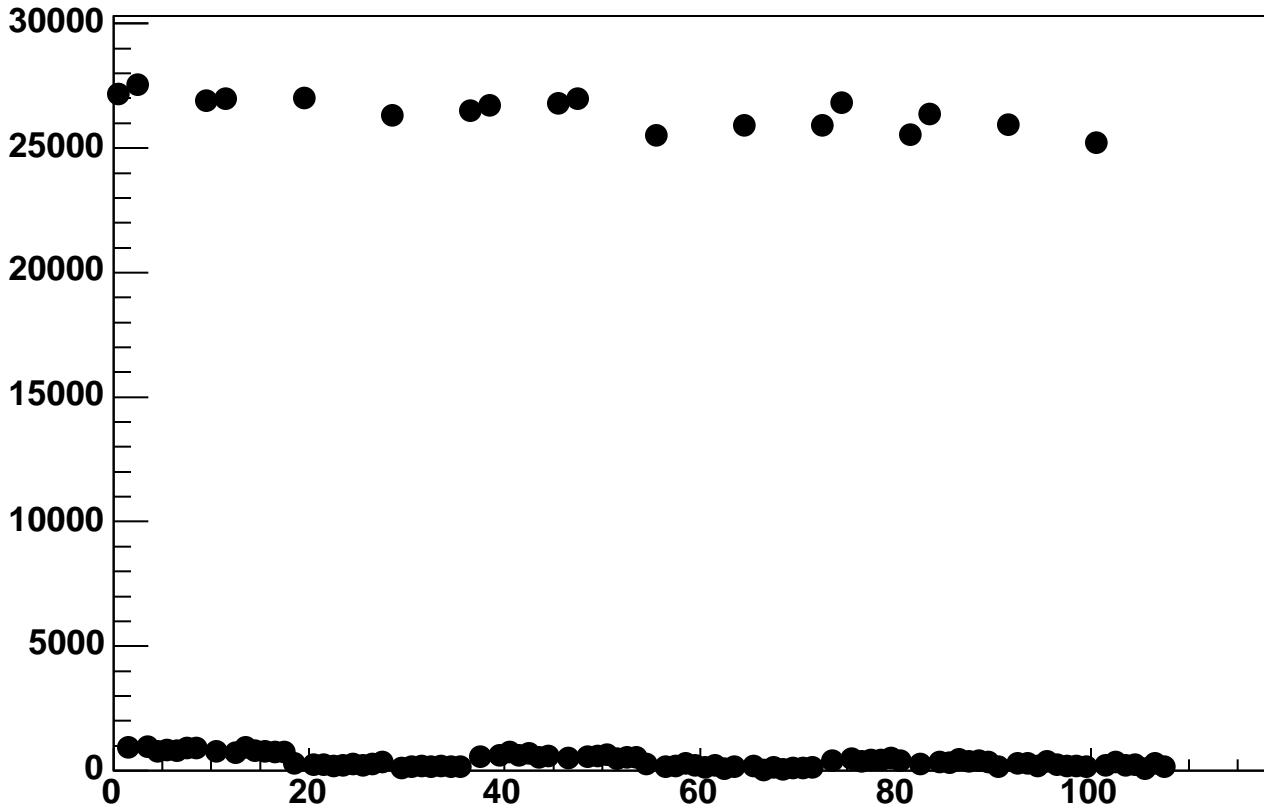
Enable 0, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



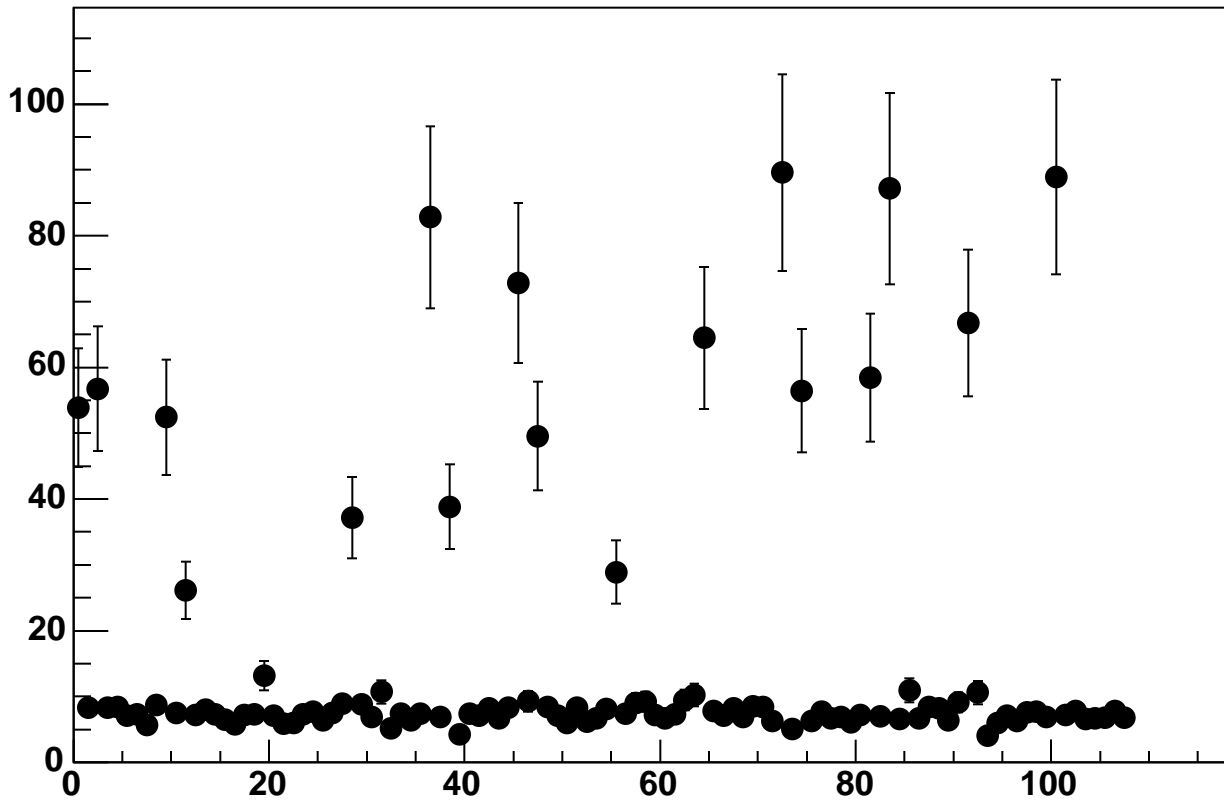
Enable 0, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



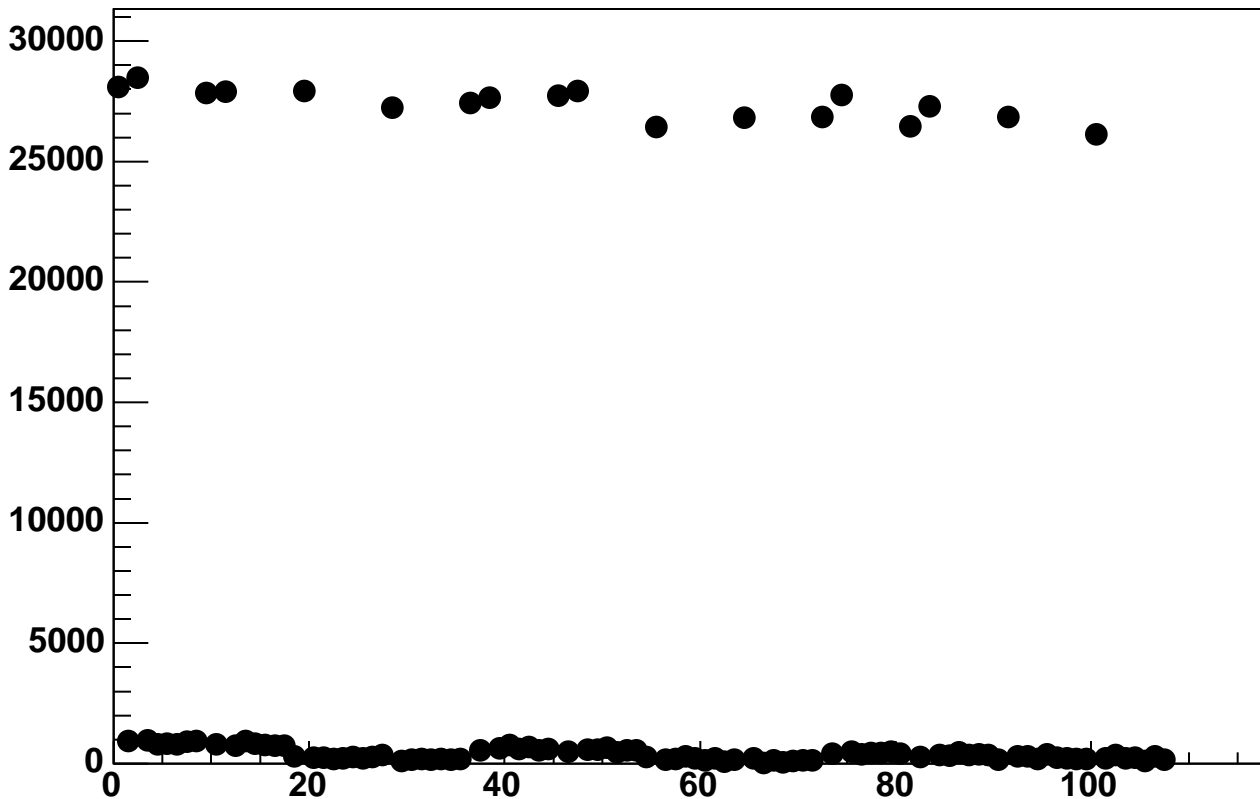
Enable 0, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



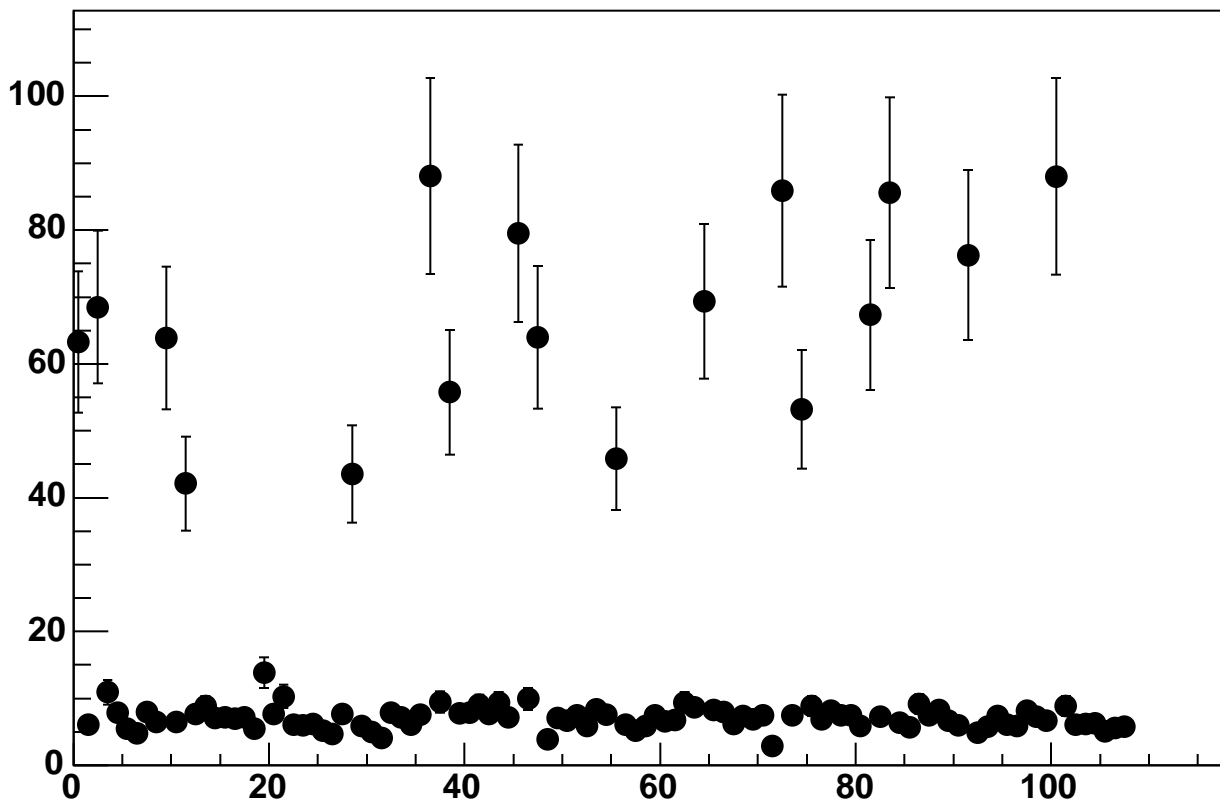
Enable 0, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



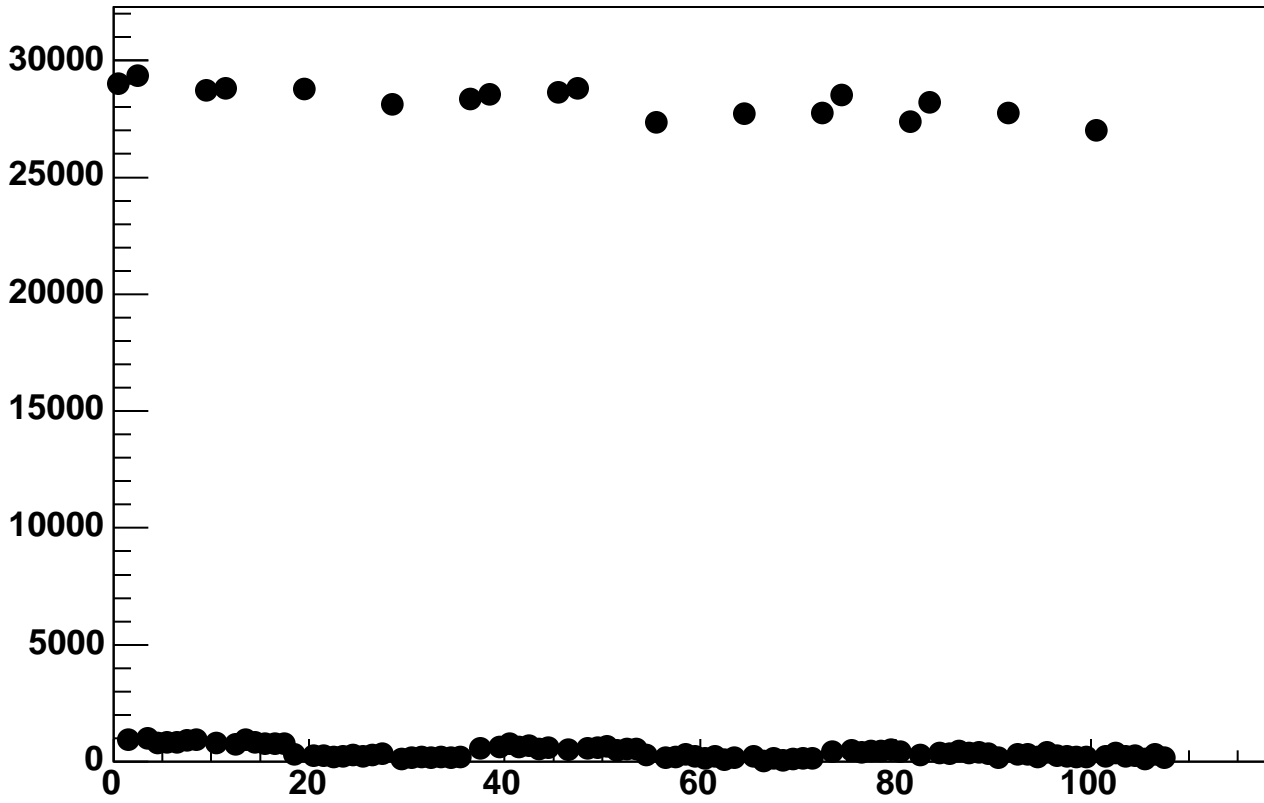
Enable 0, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



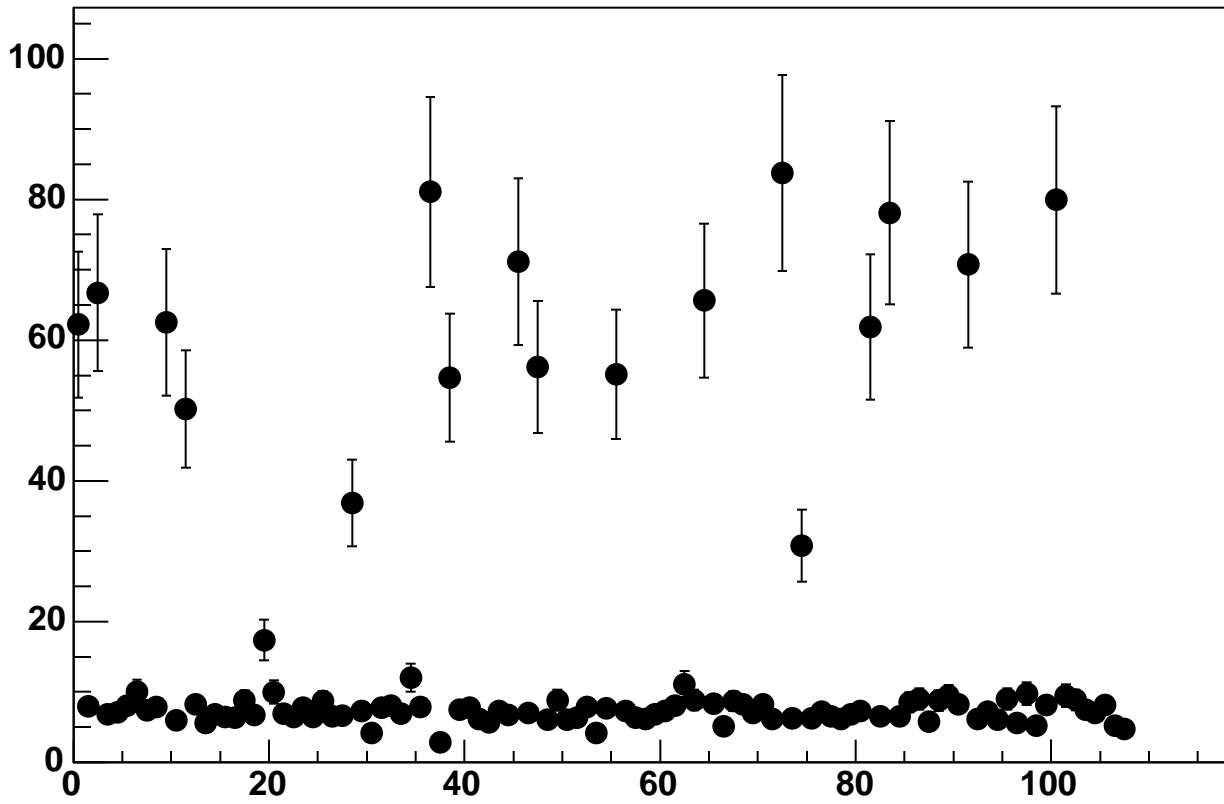
Enable 0, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



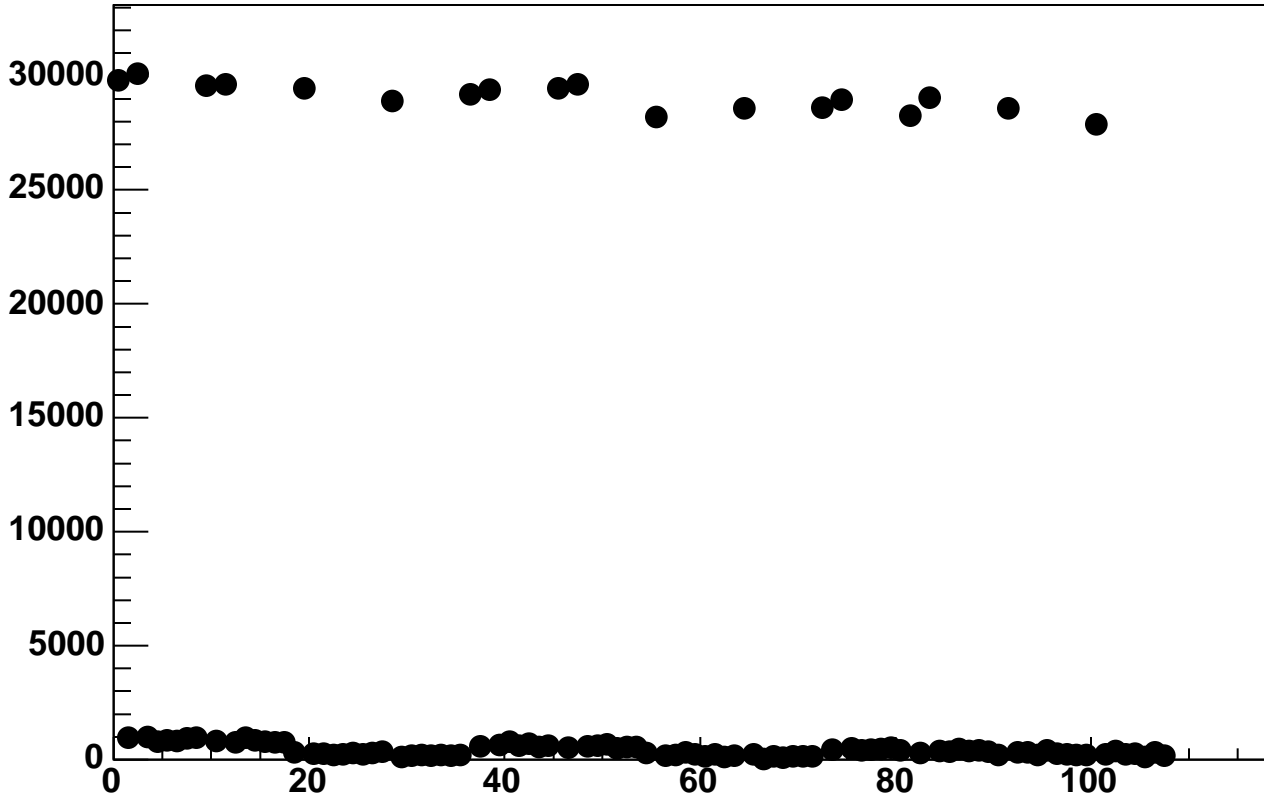
Enable 0, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



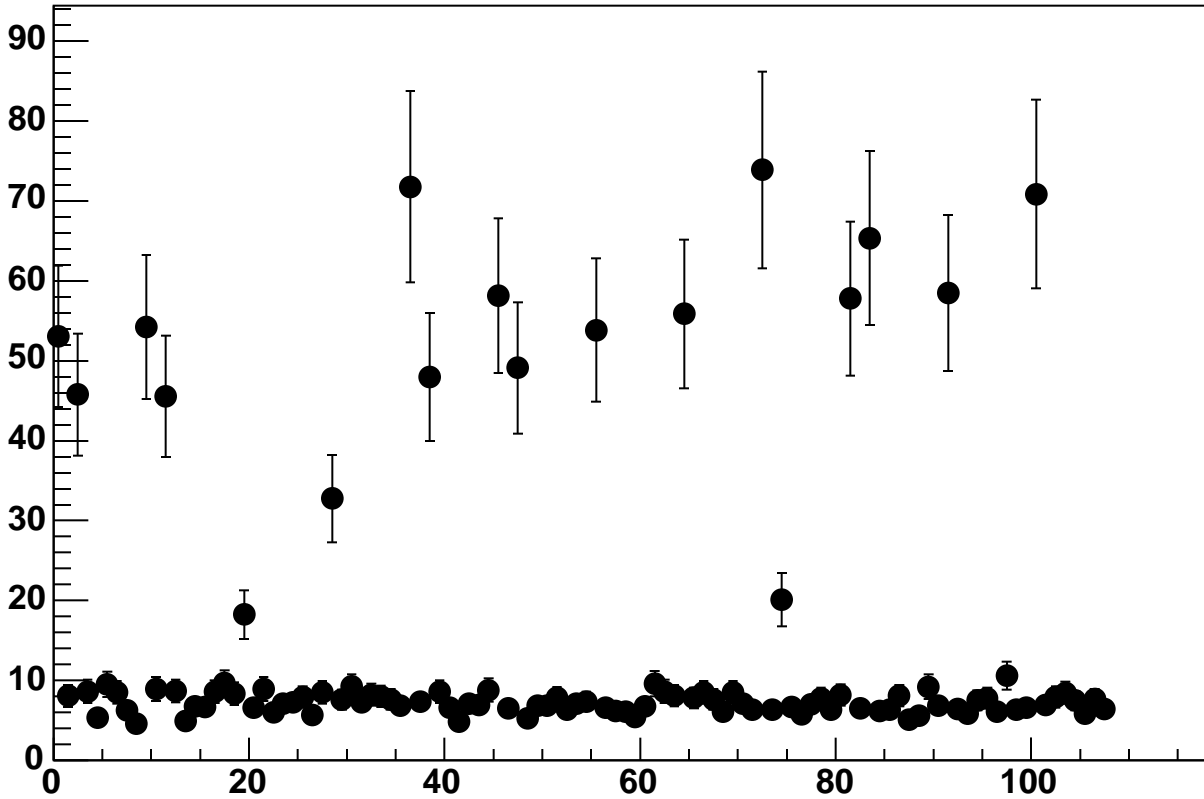
Enable 0, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



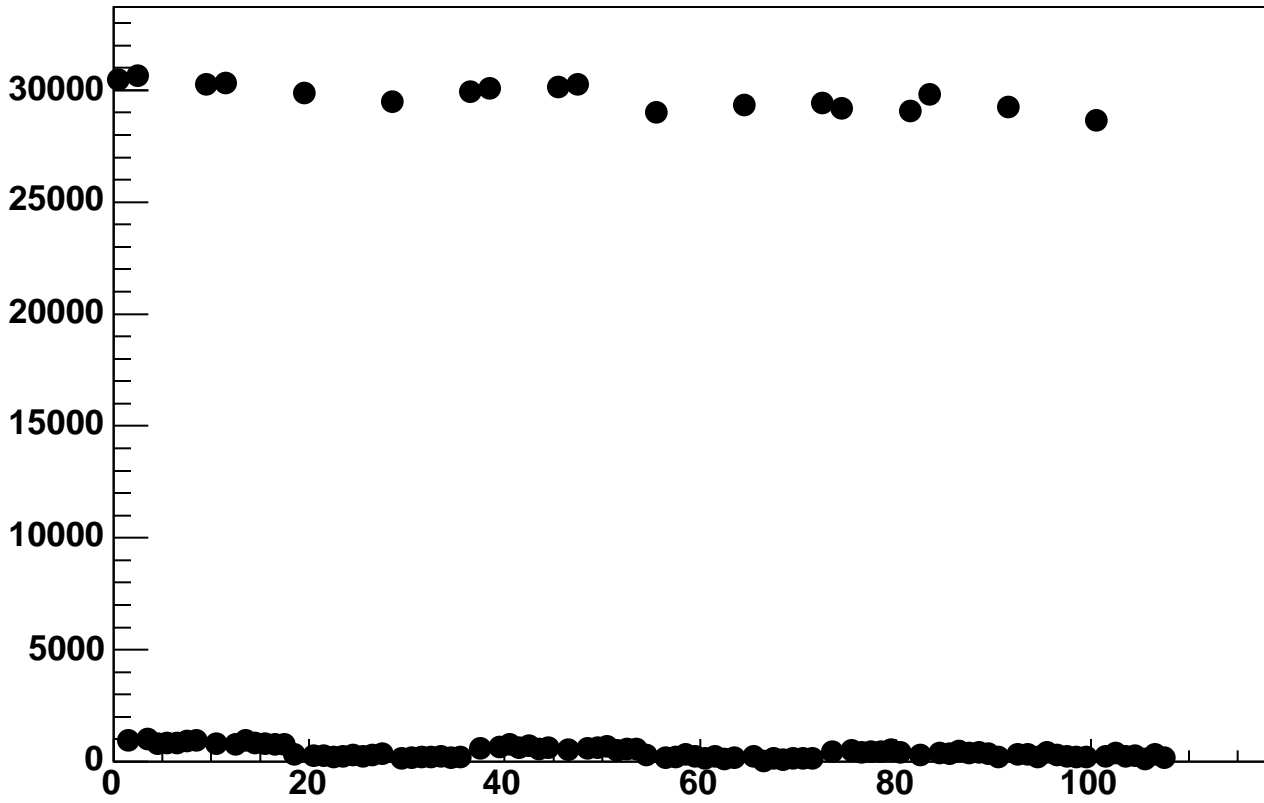
Enable 0, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



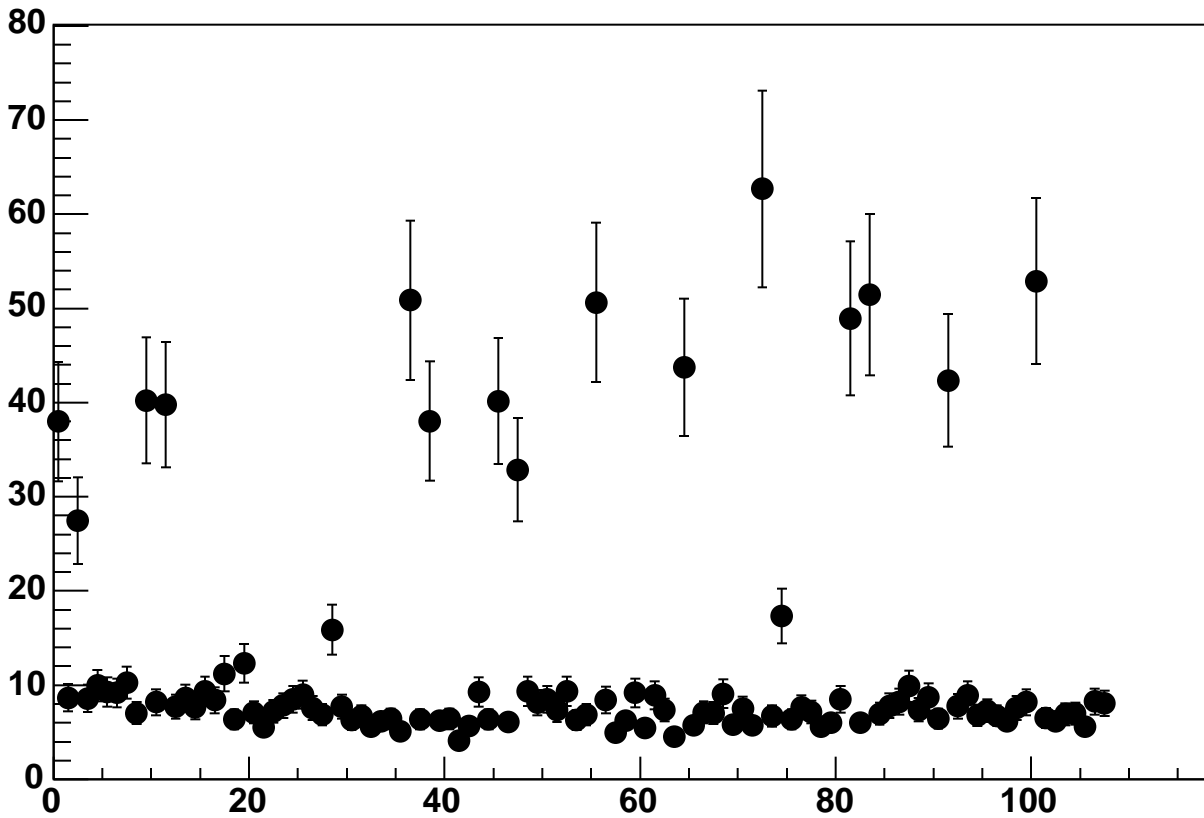
Enable 0, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



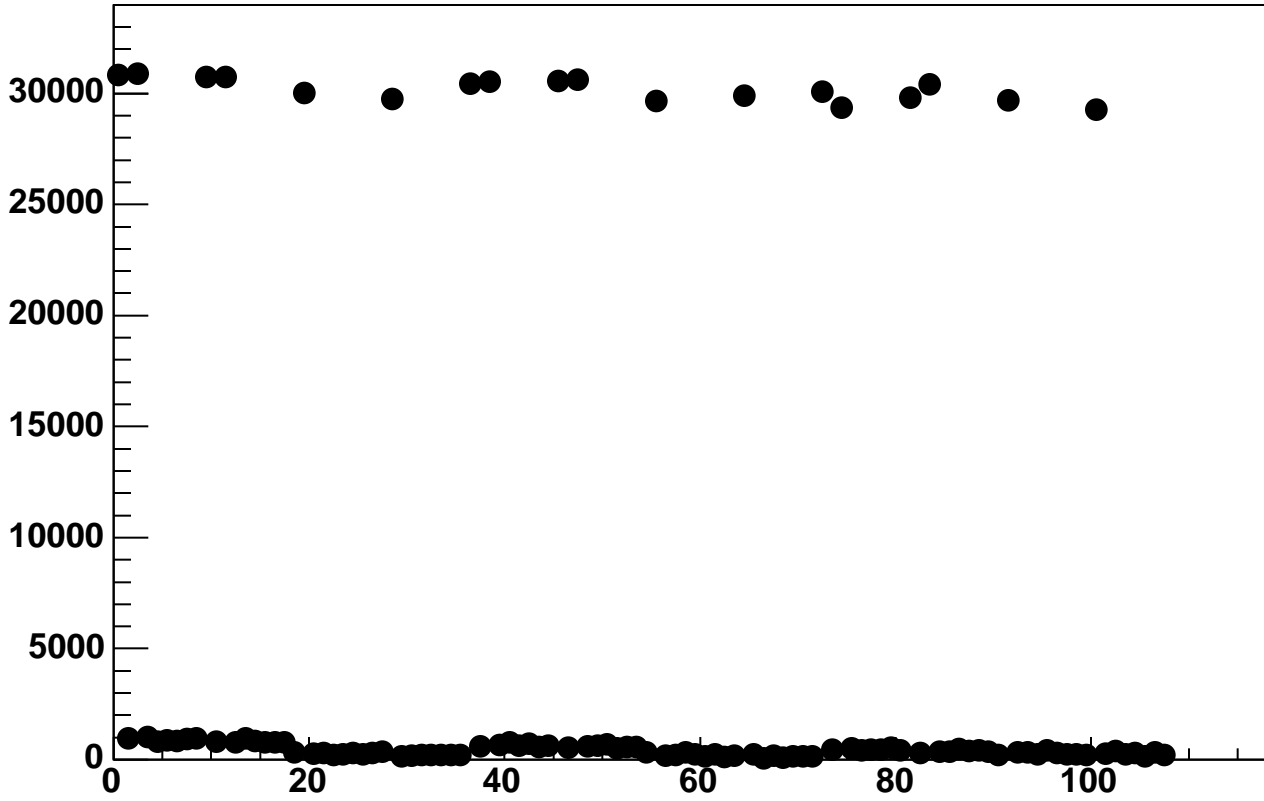
Enable 0, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



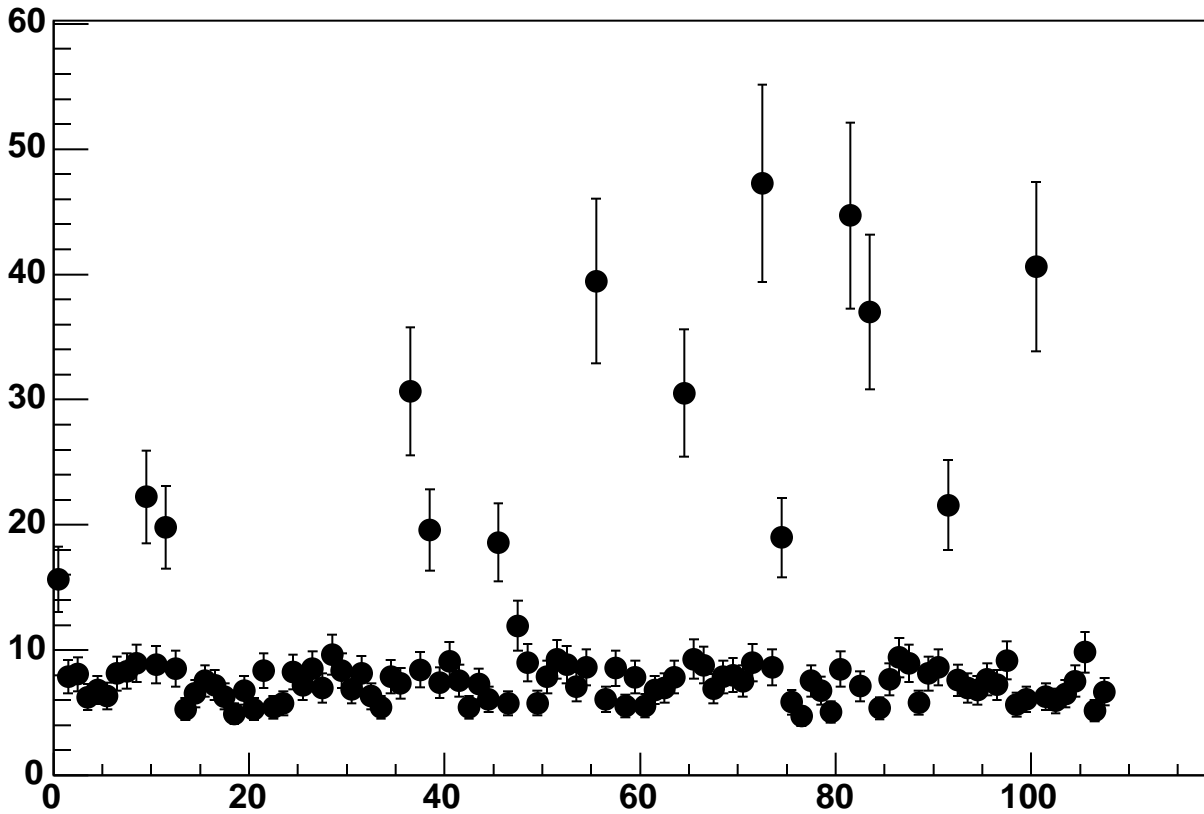
Enable 0, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



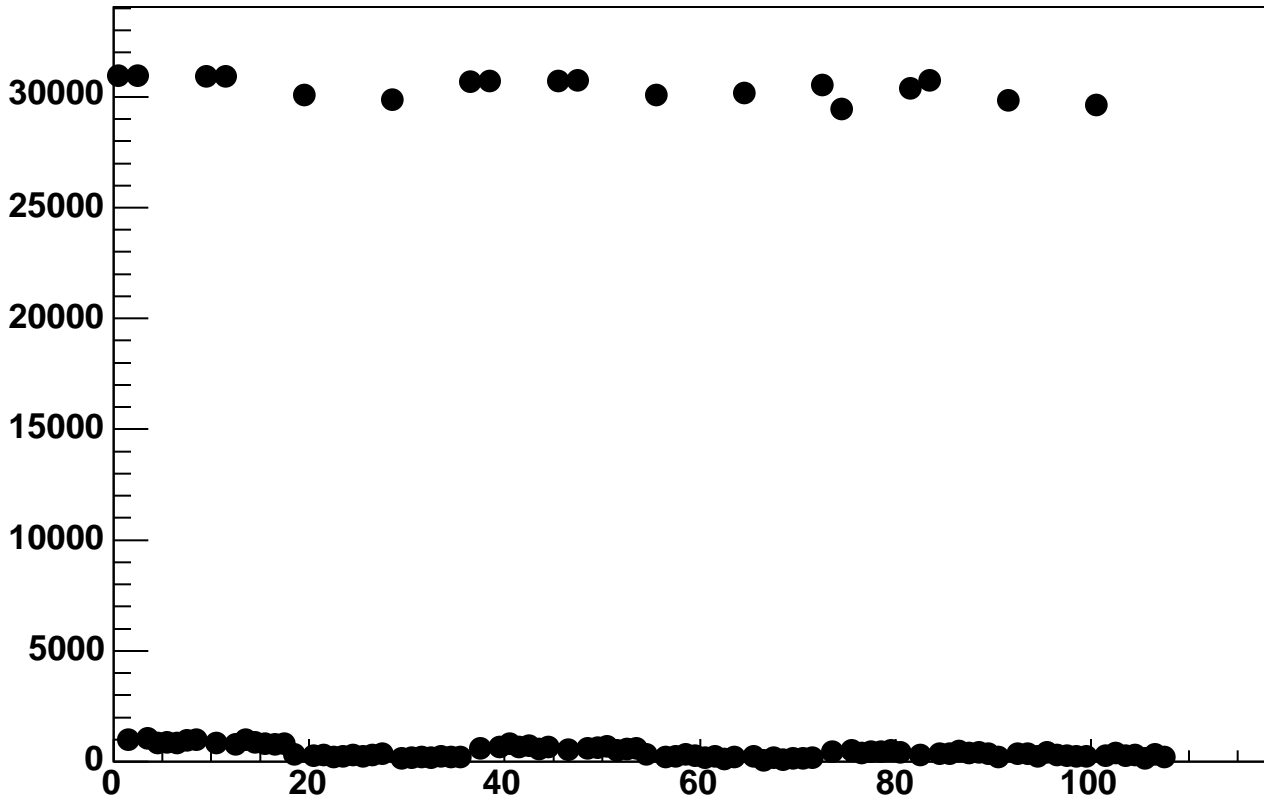
Enable 0, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



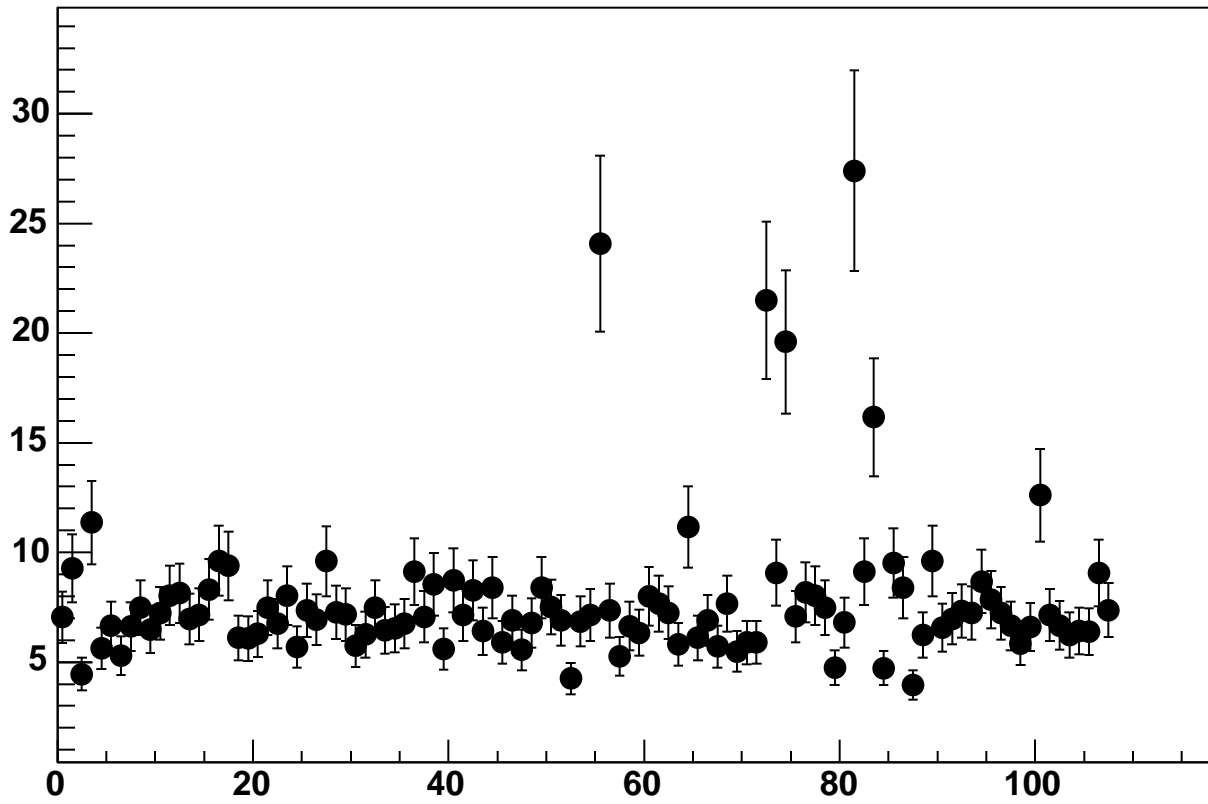
Enable 0, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



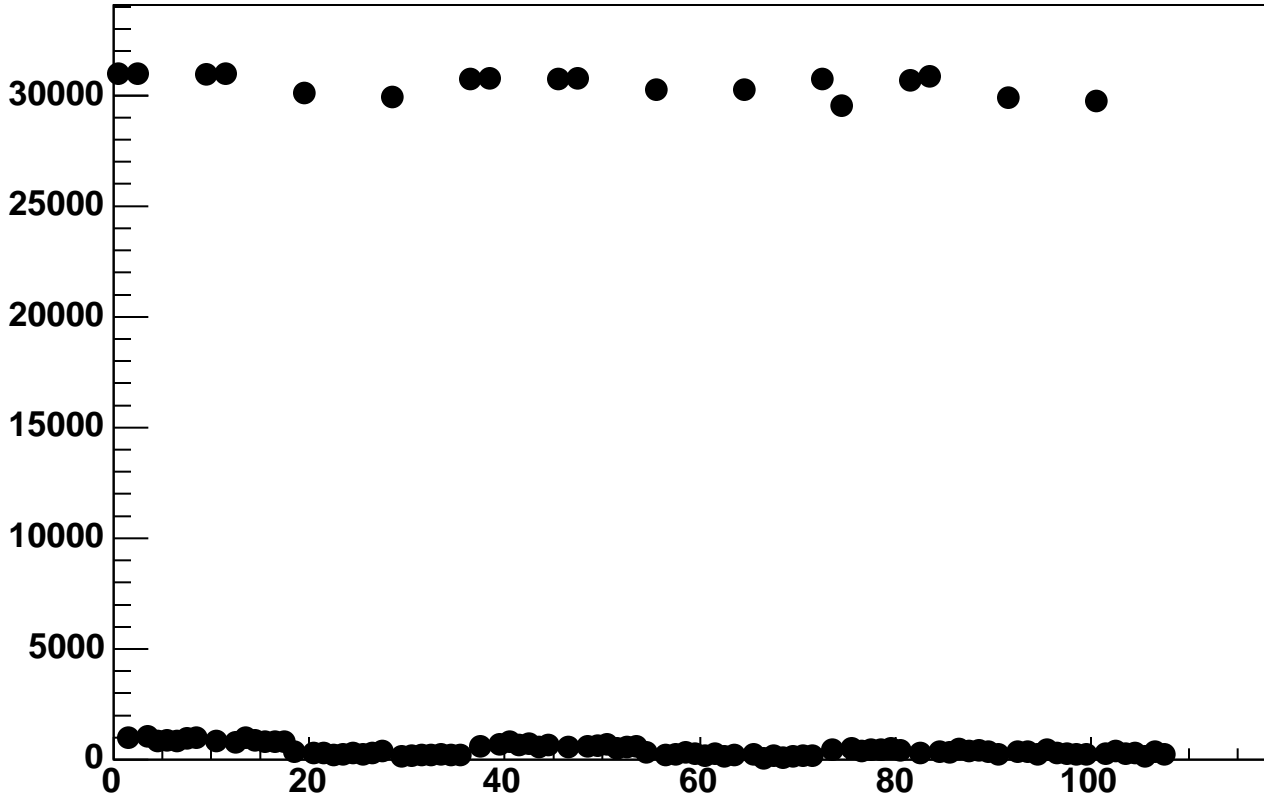
Enable 0, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



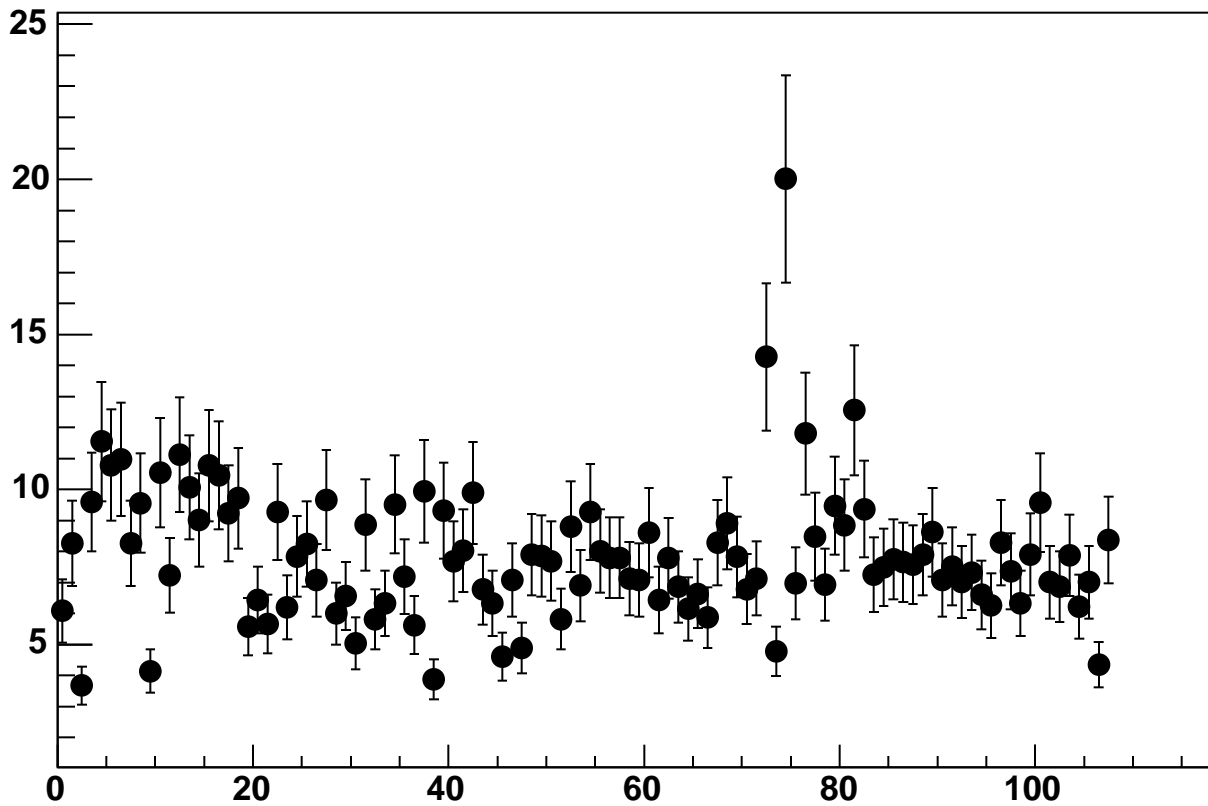
Enable 0, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



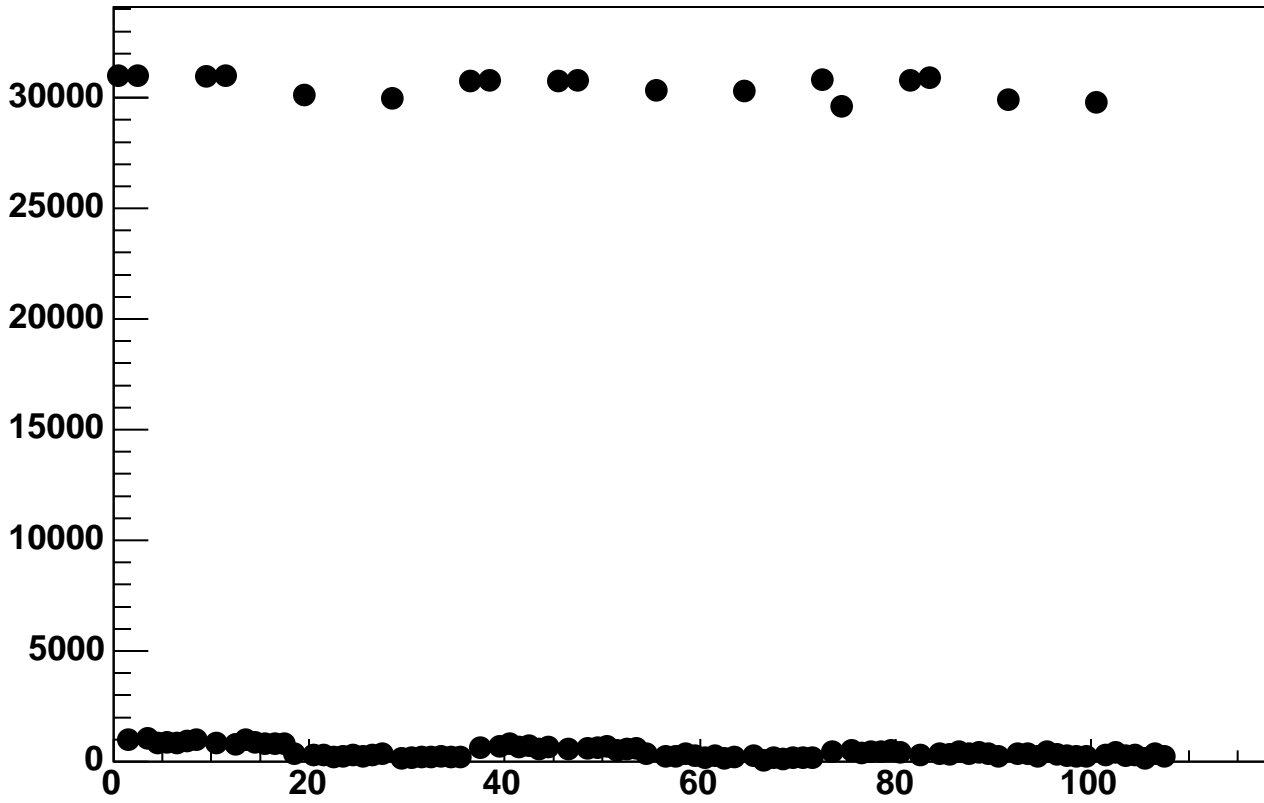
Enable 0, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



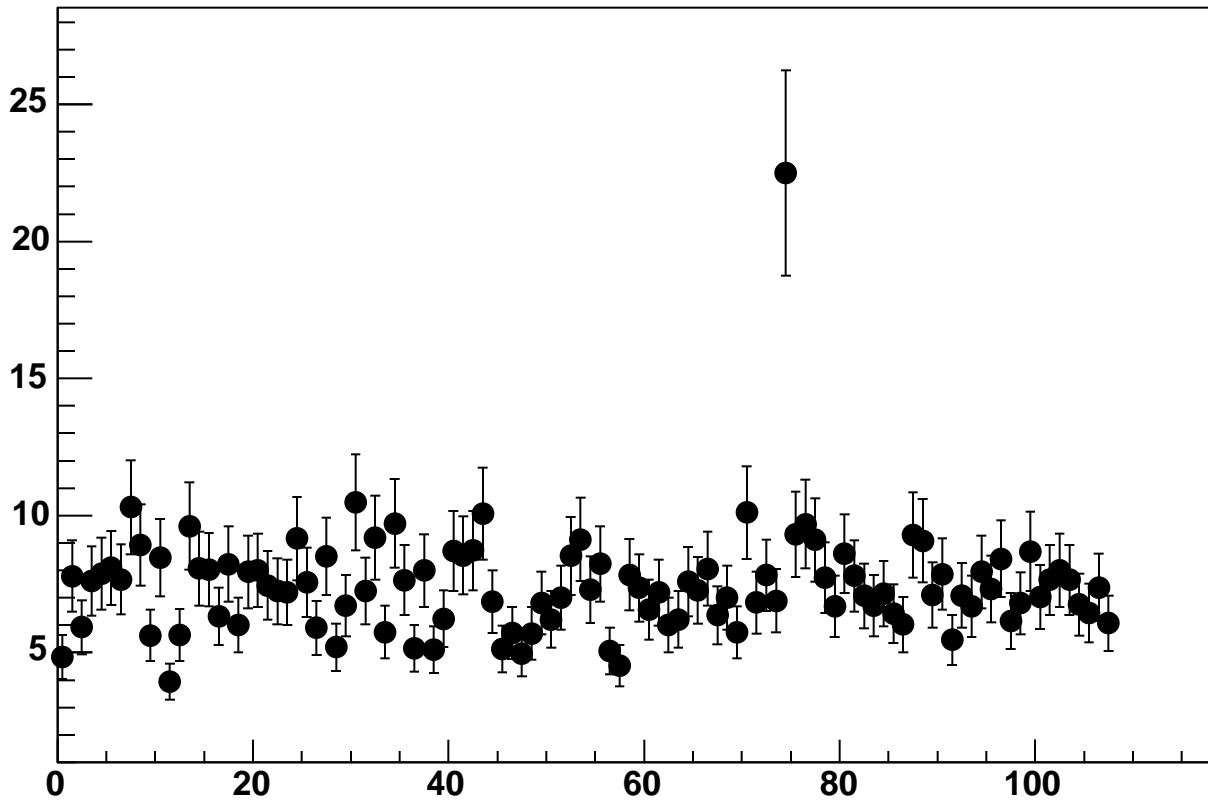
Enable 0, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



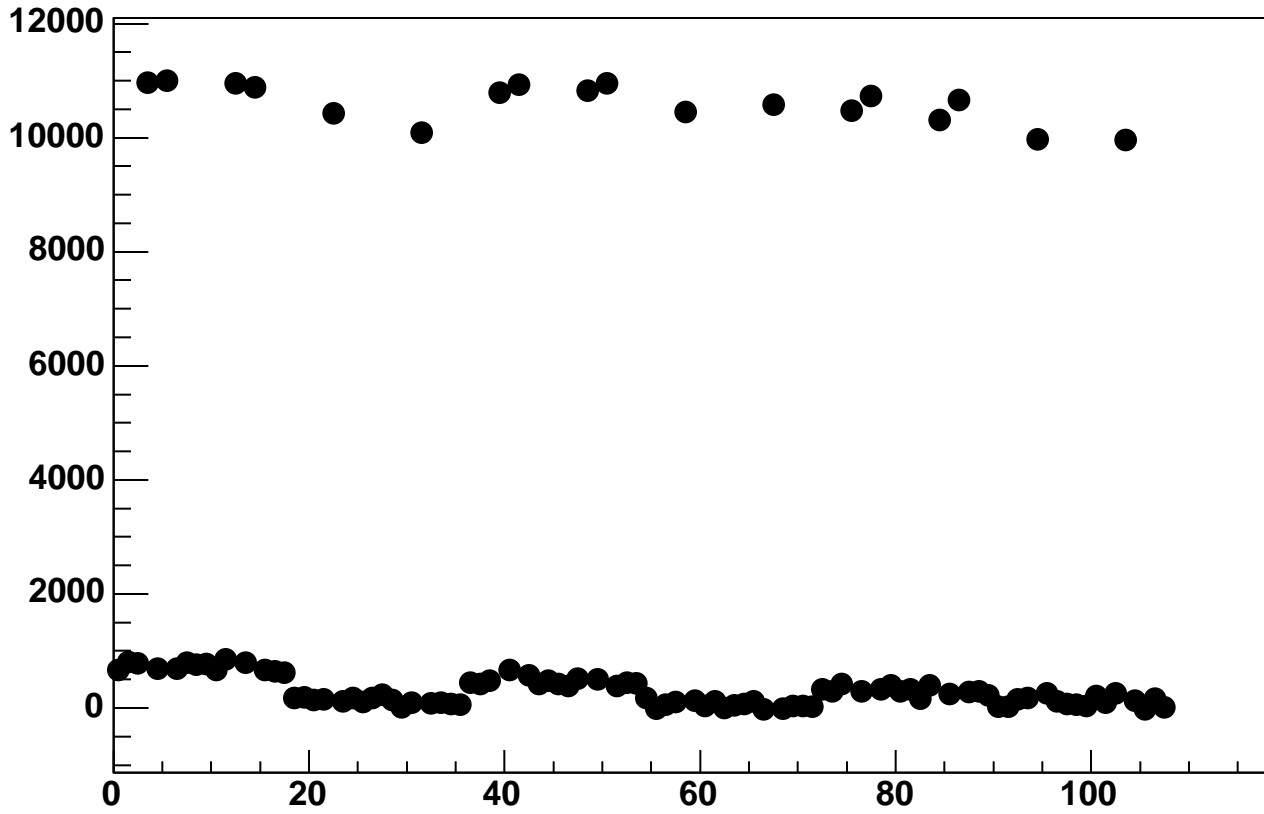
Enable 0, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



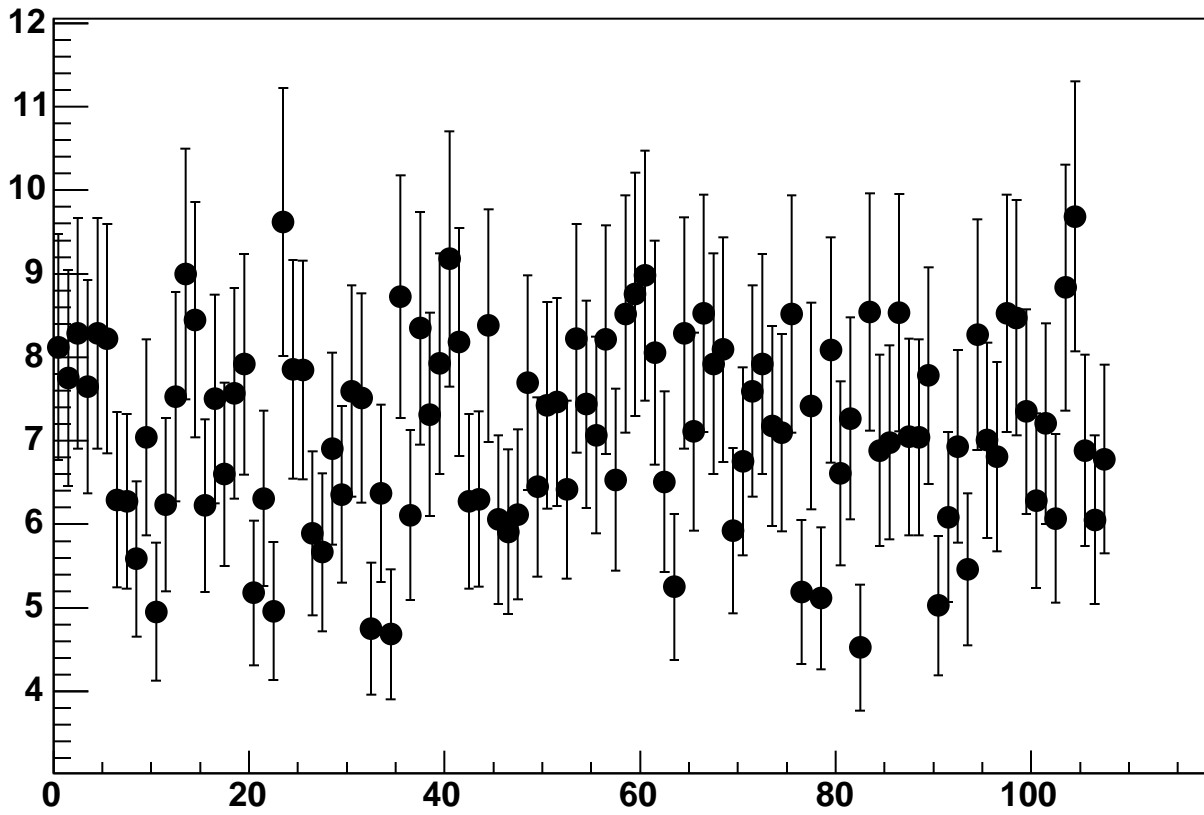
Enable 0, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



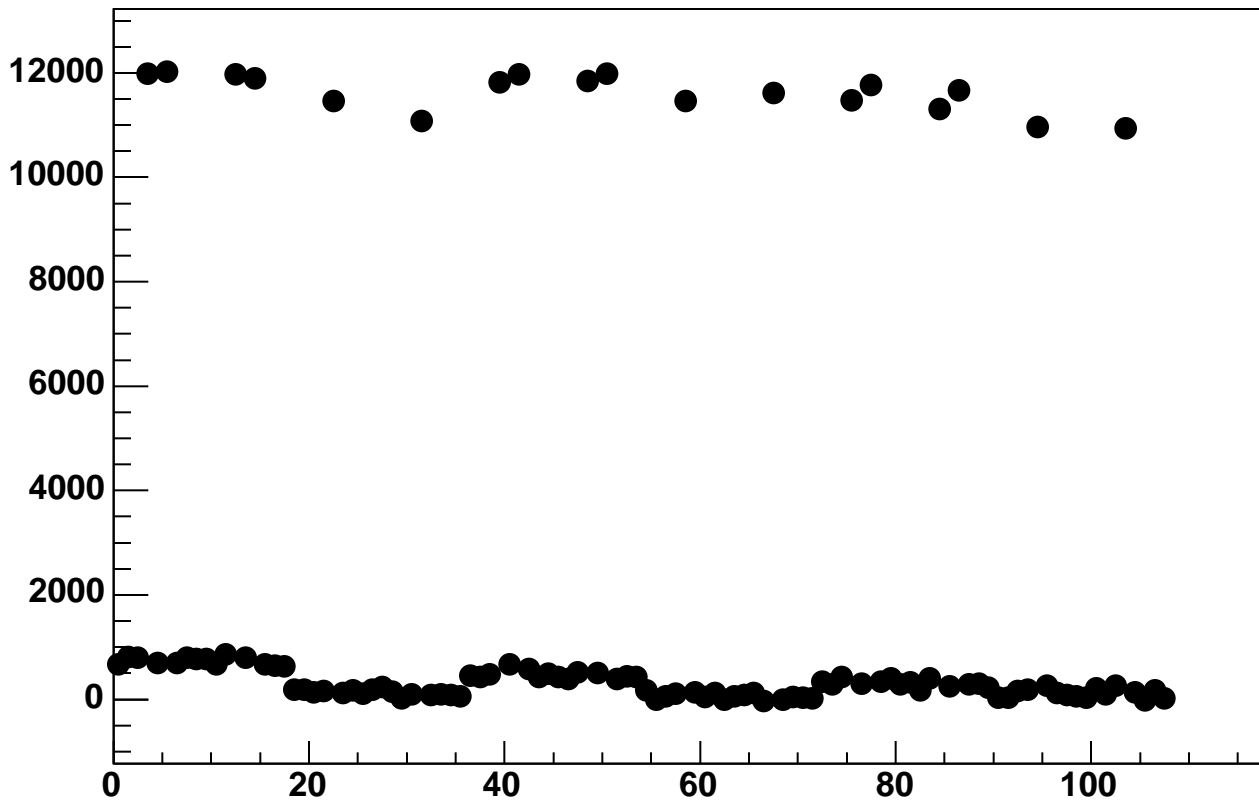
Enable 1, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



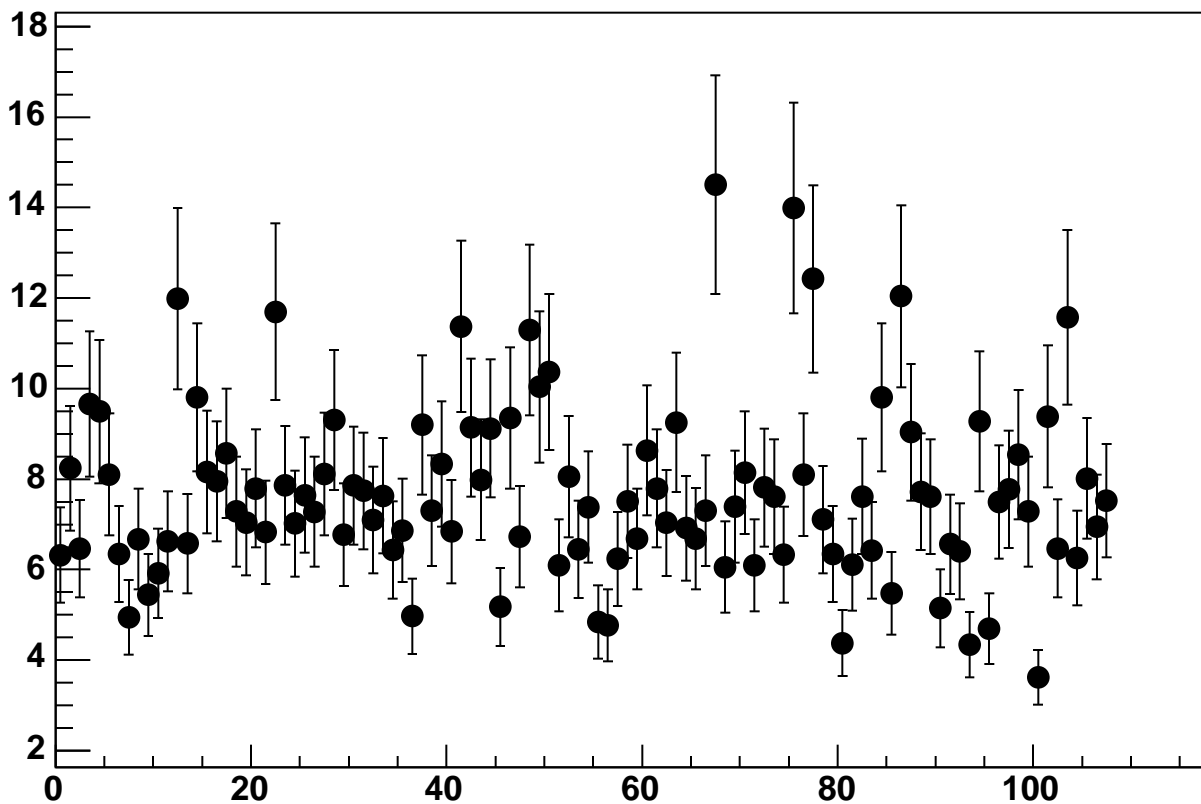
Enable 1, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



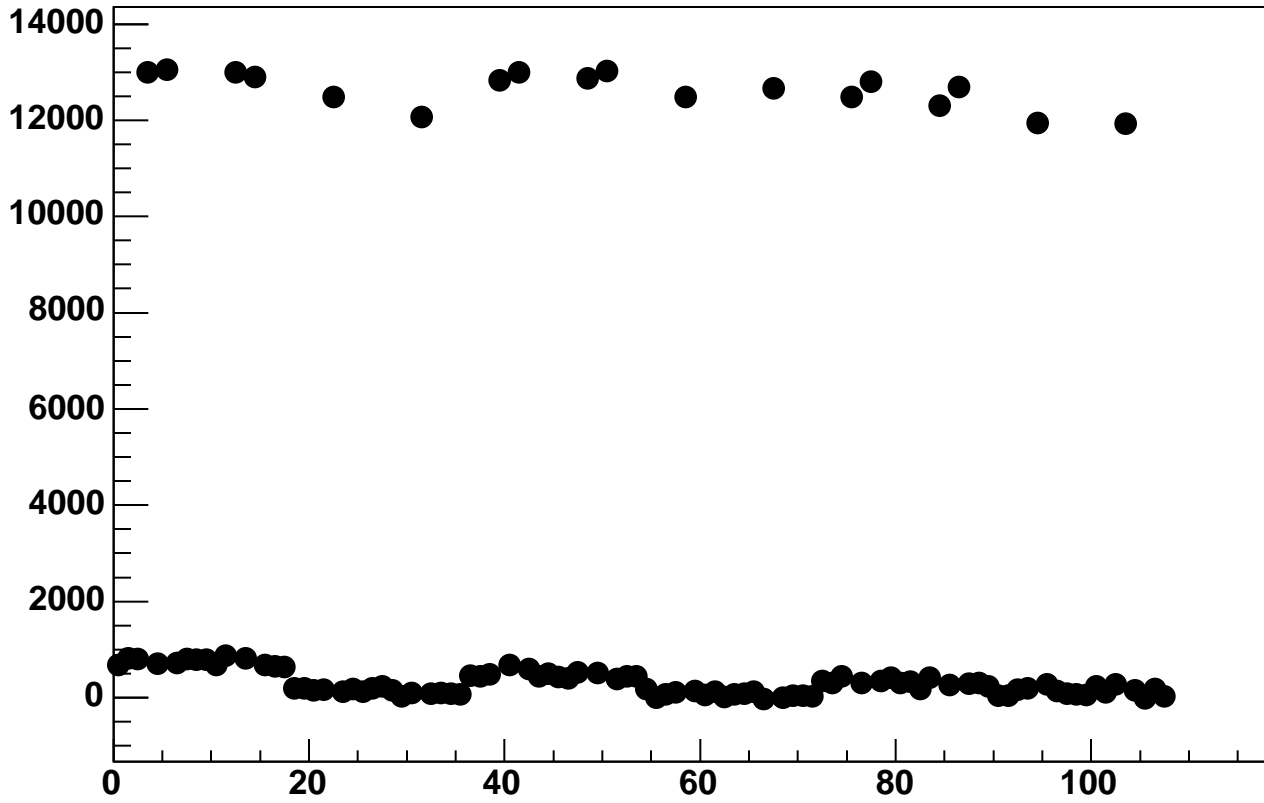
Enable 1, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



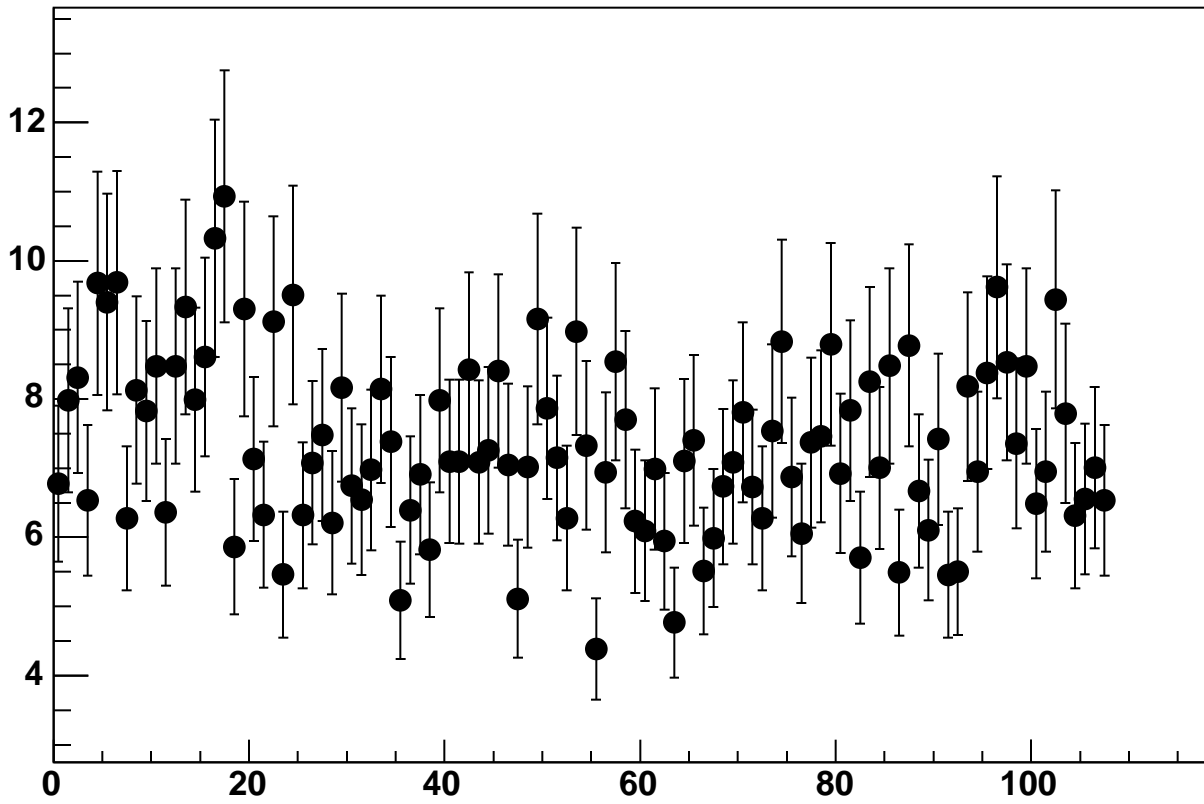
Enable 1, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



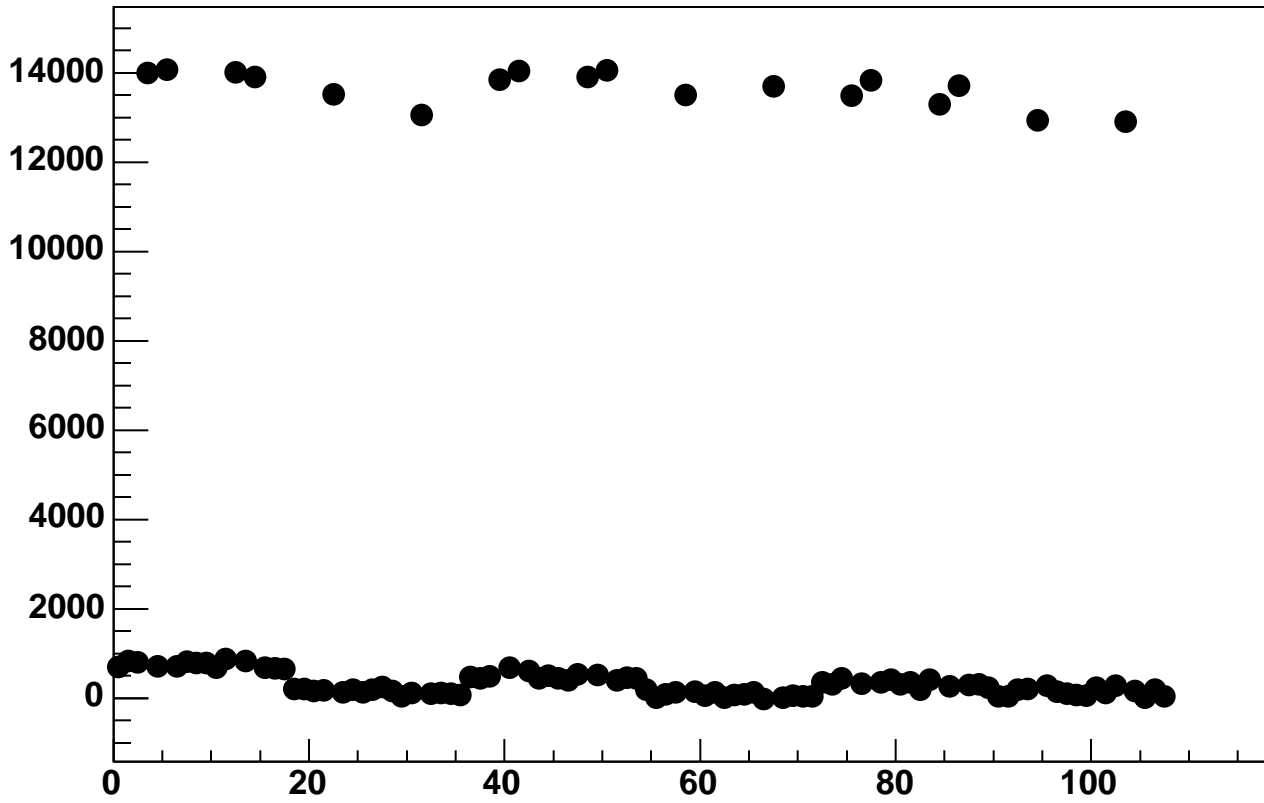
Enable 1, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



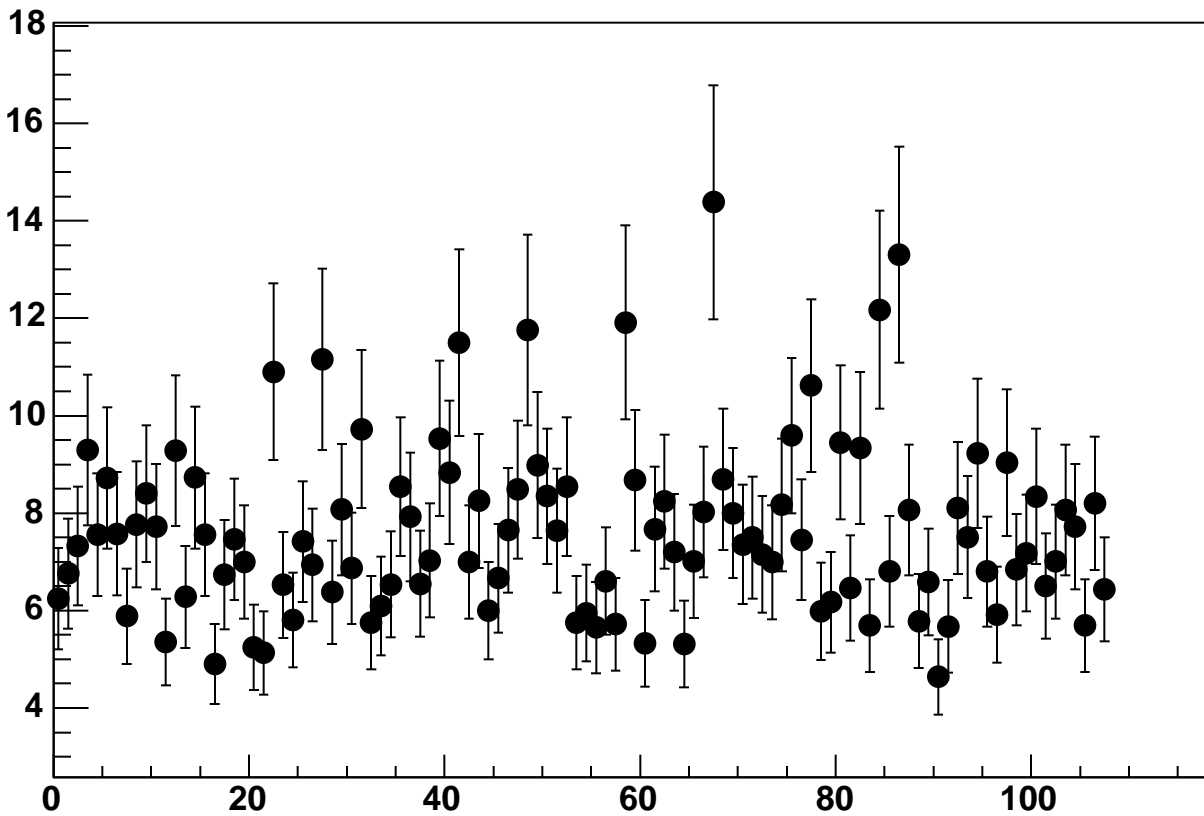
Enable 1, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



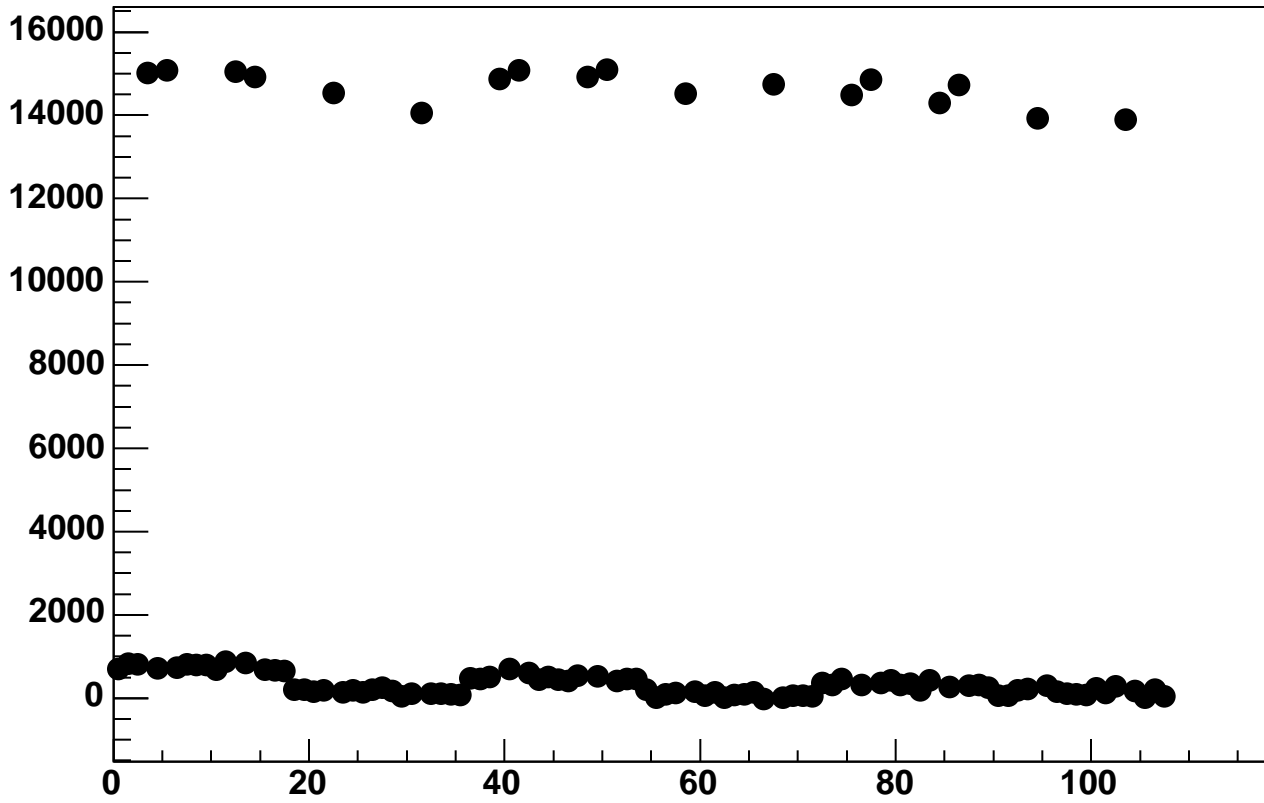
Enable 1, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



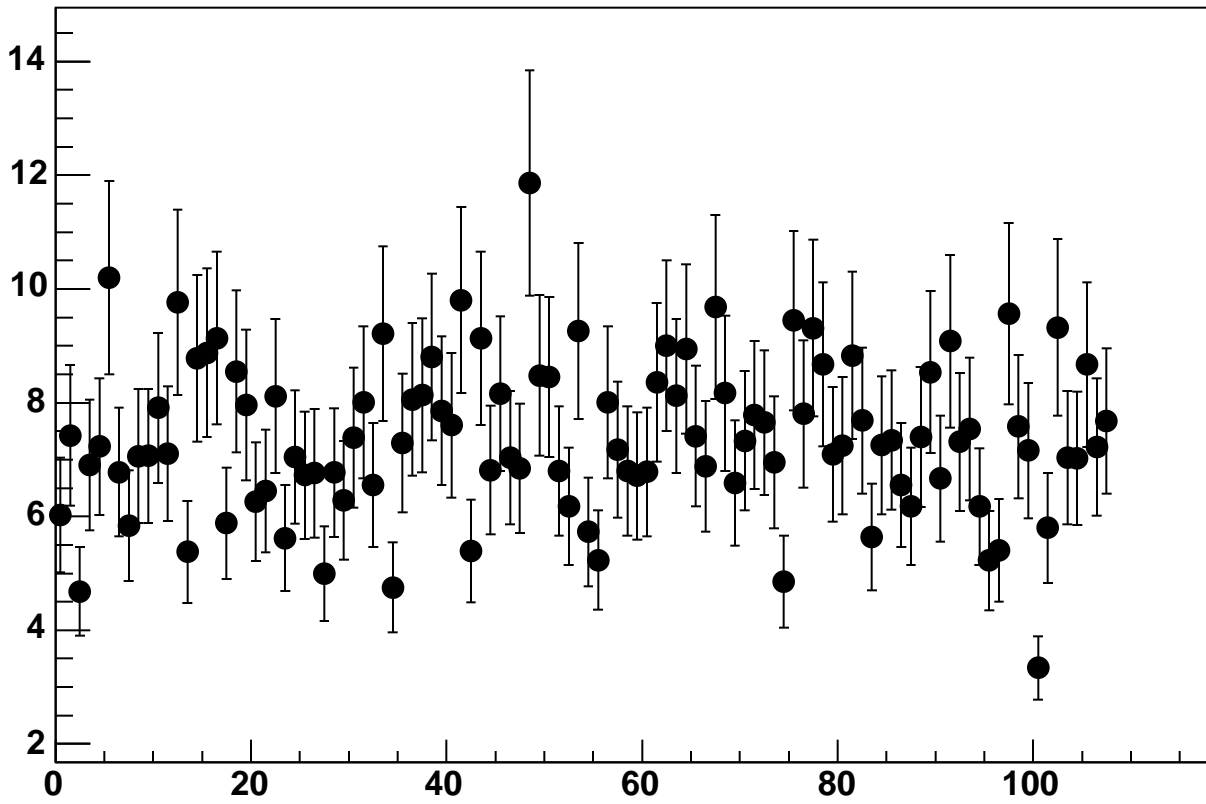
Enable 1, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



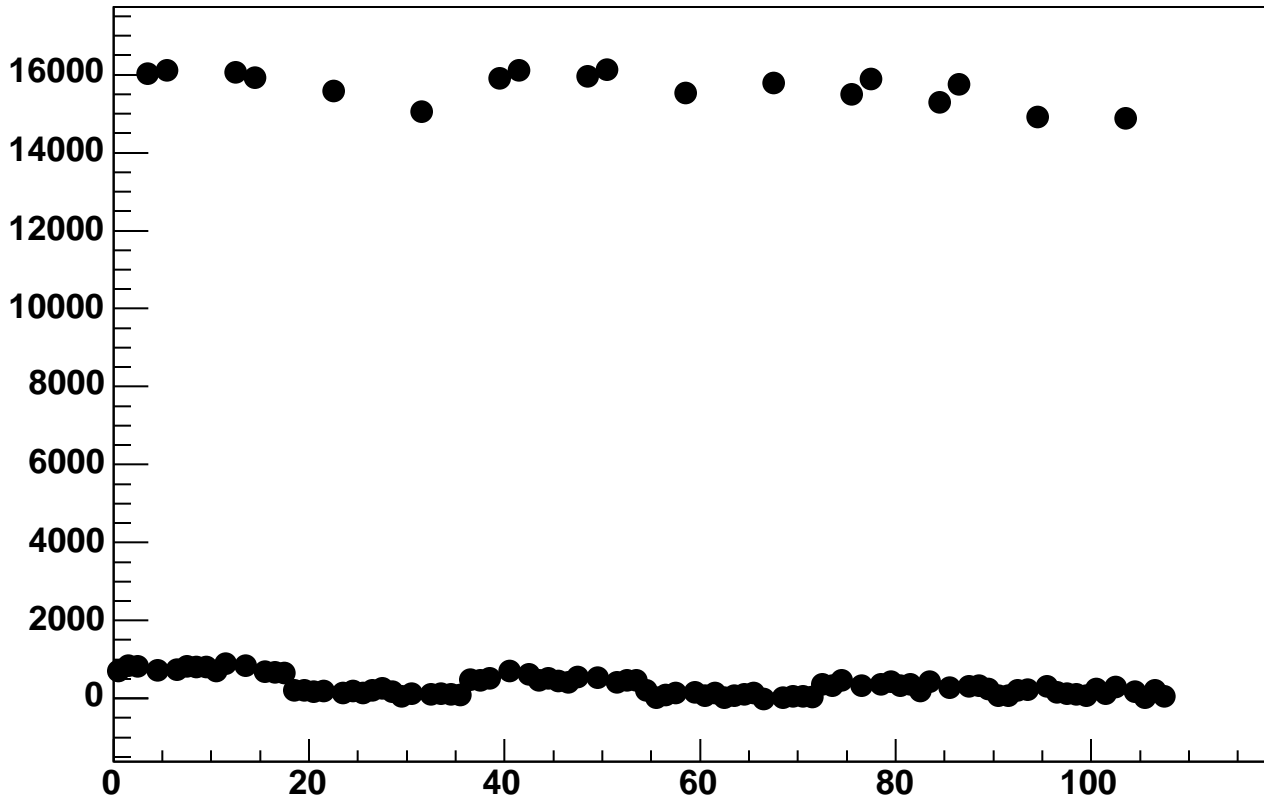
Enable 1, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



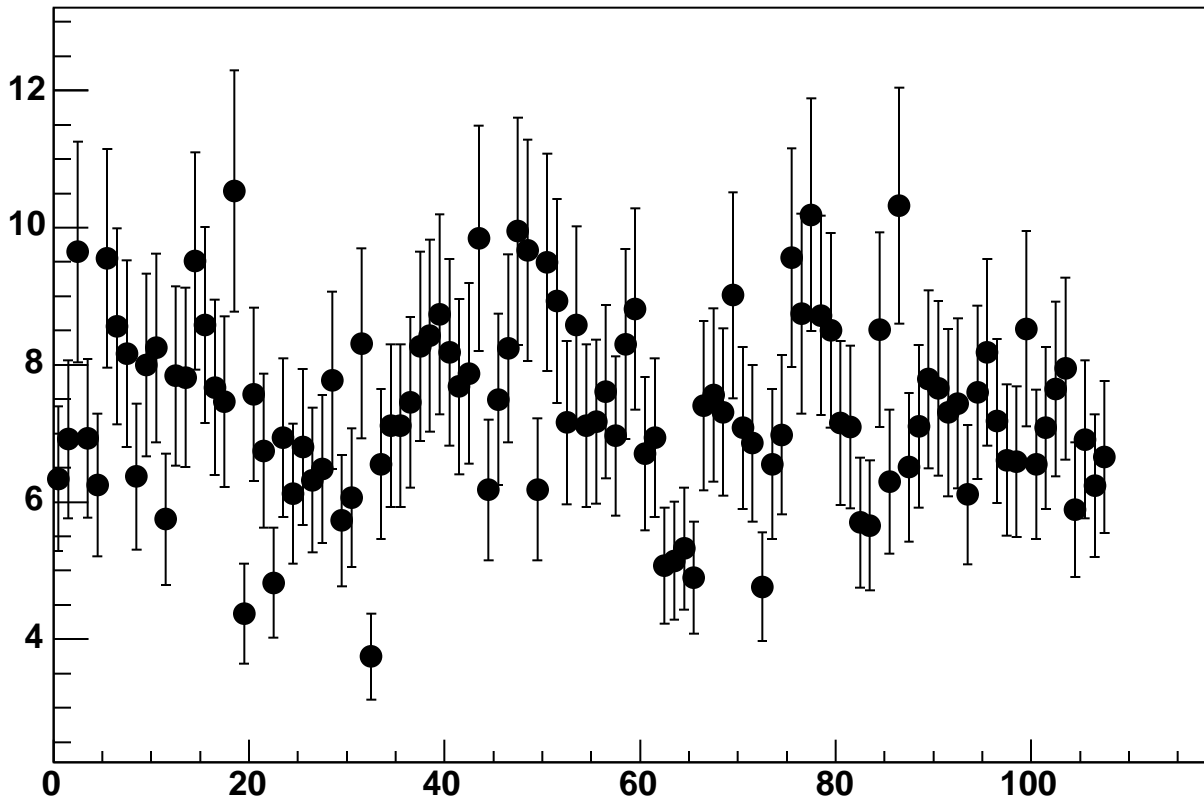
Enable 1, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



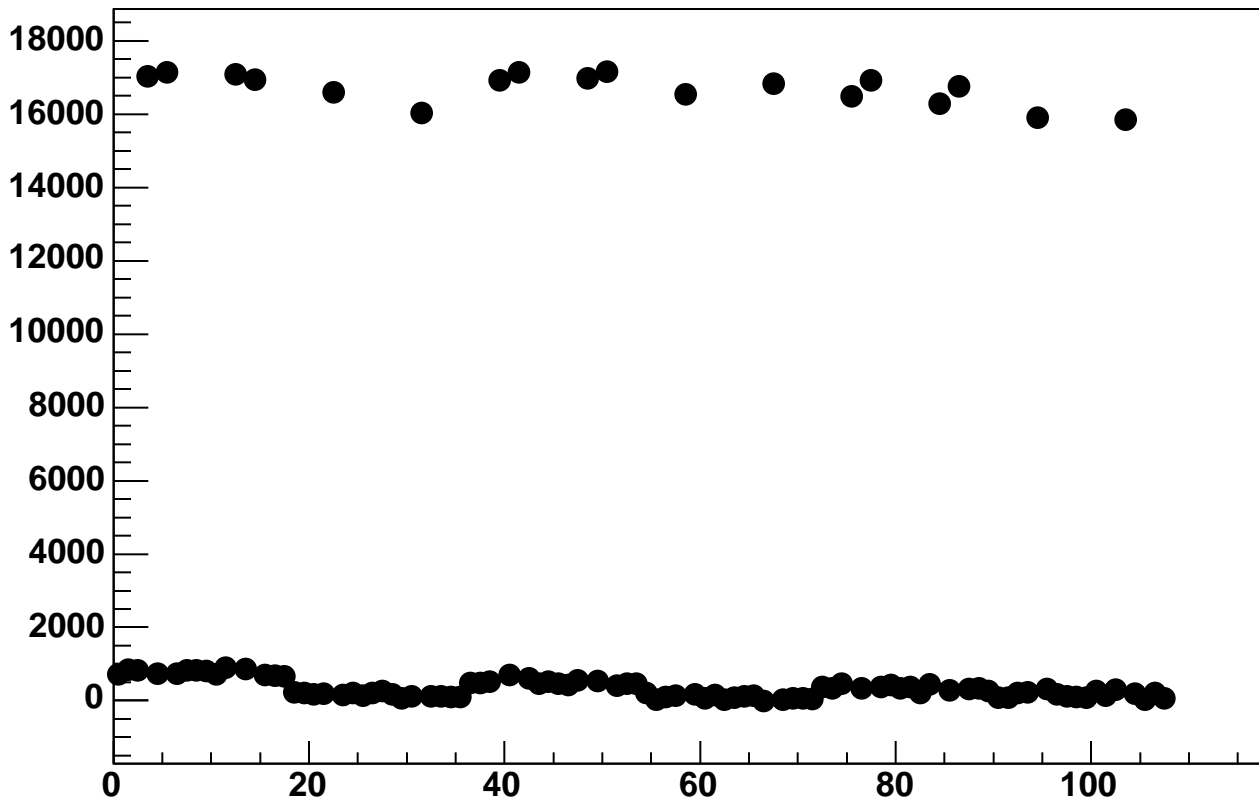
Enable 1, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



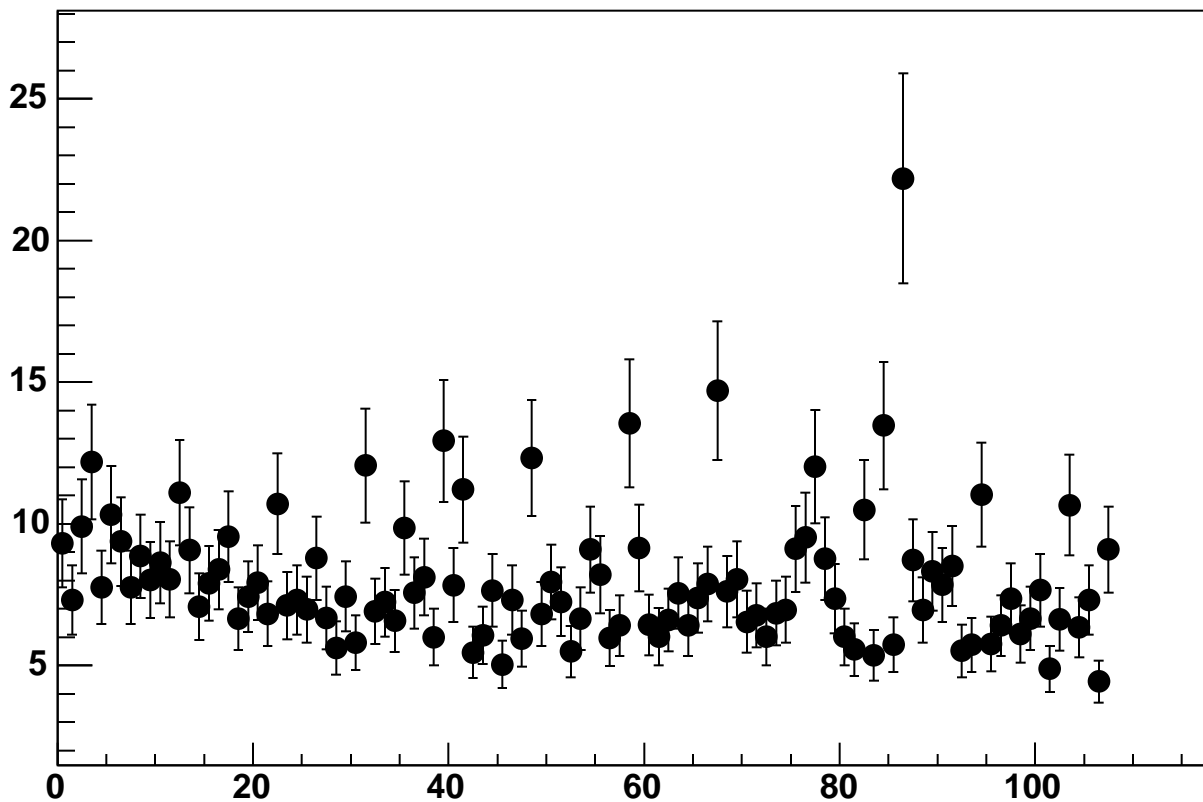
Enable 1, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



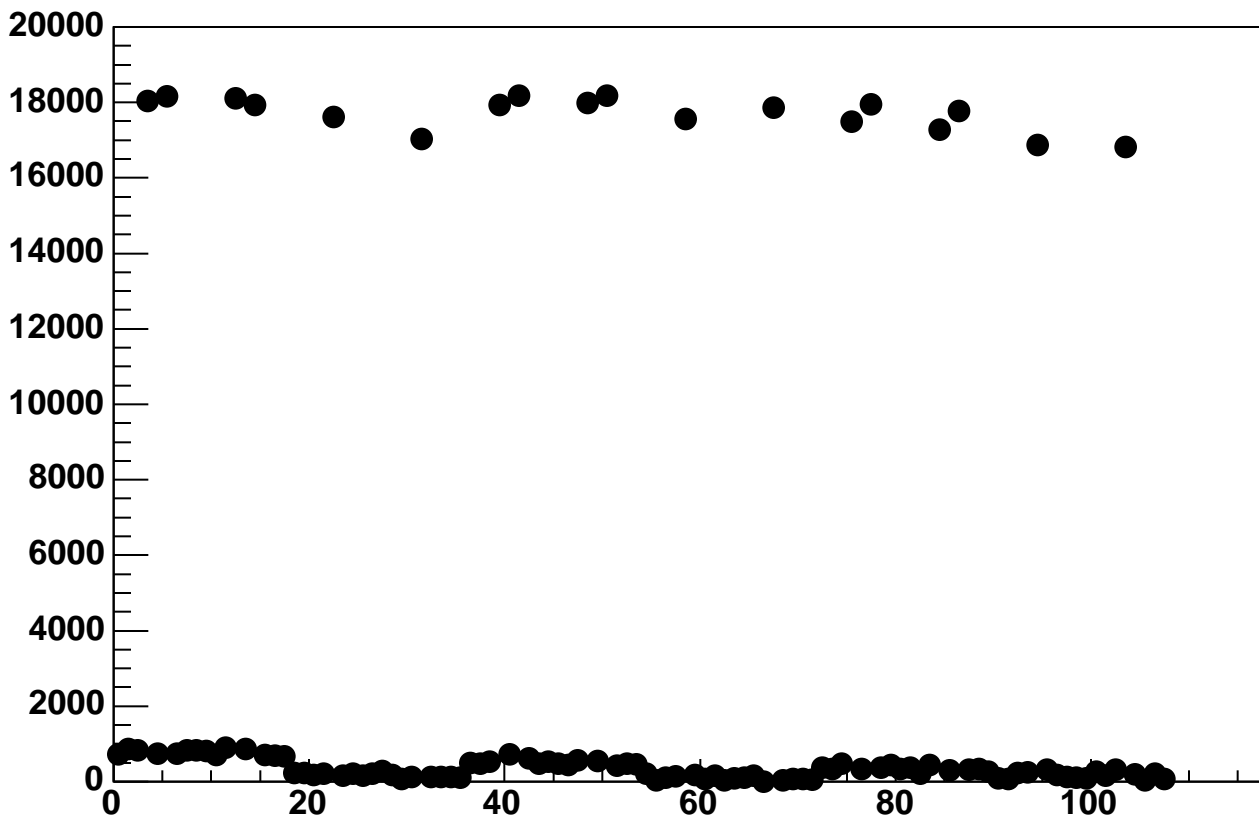
Enable 1, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



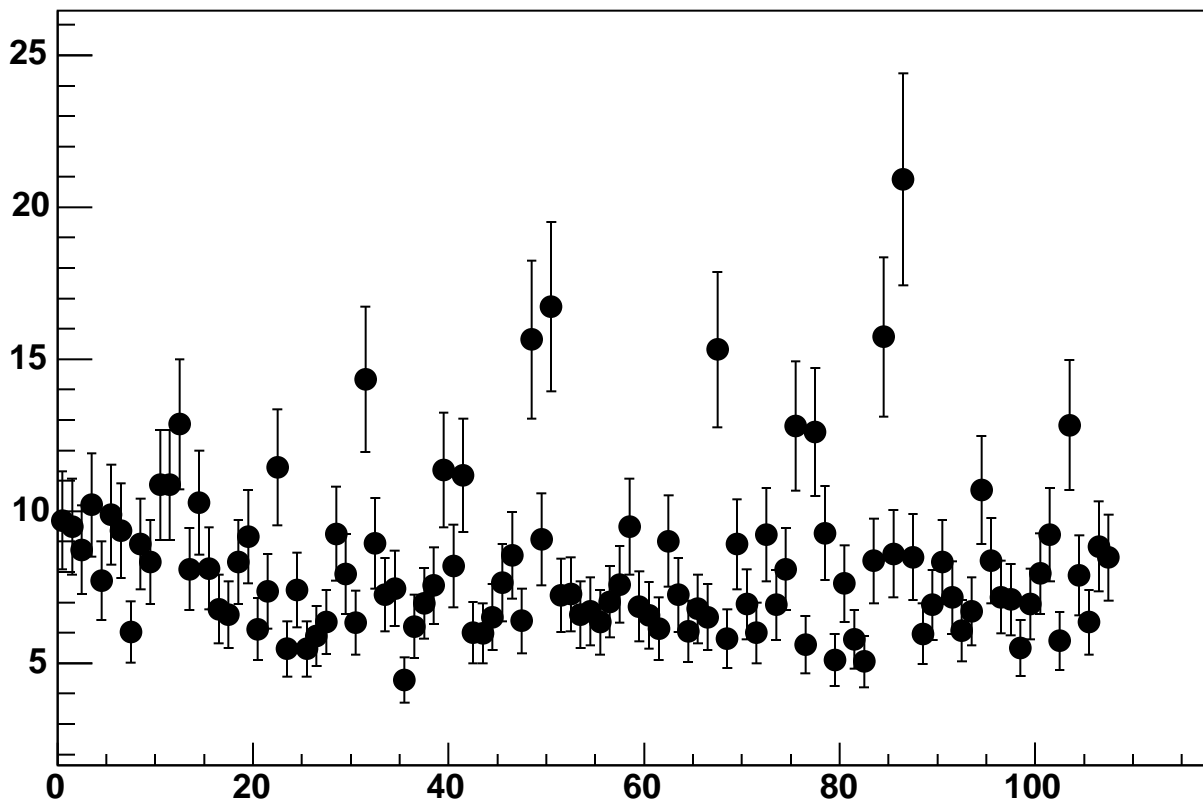
Enable 1, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



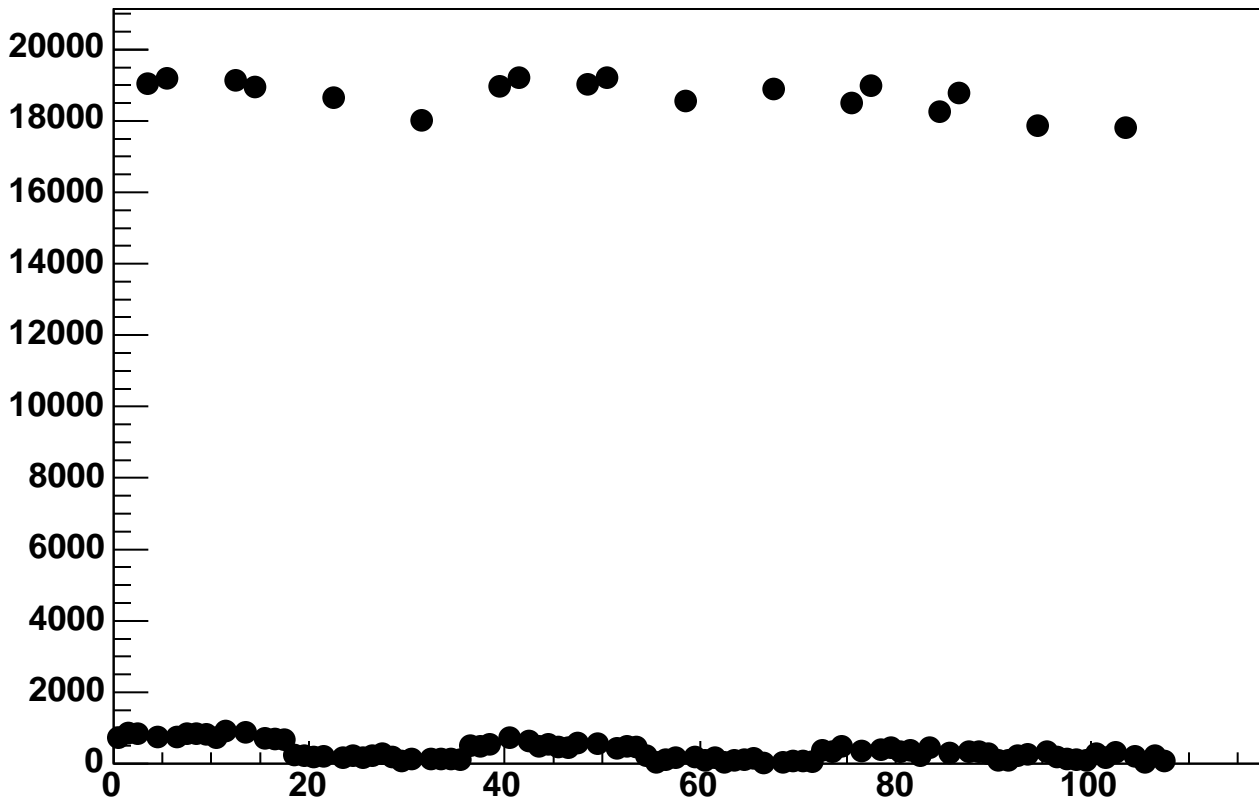
Enable 1, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



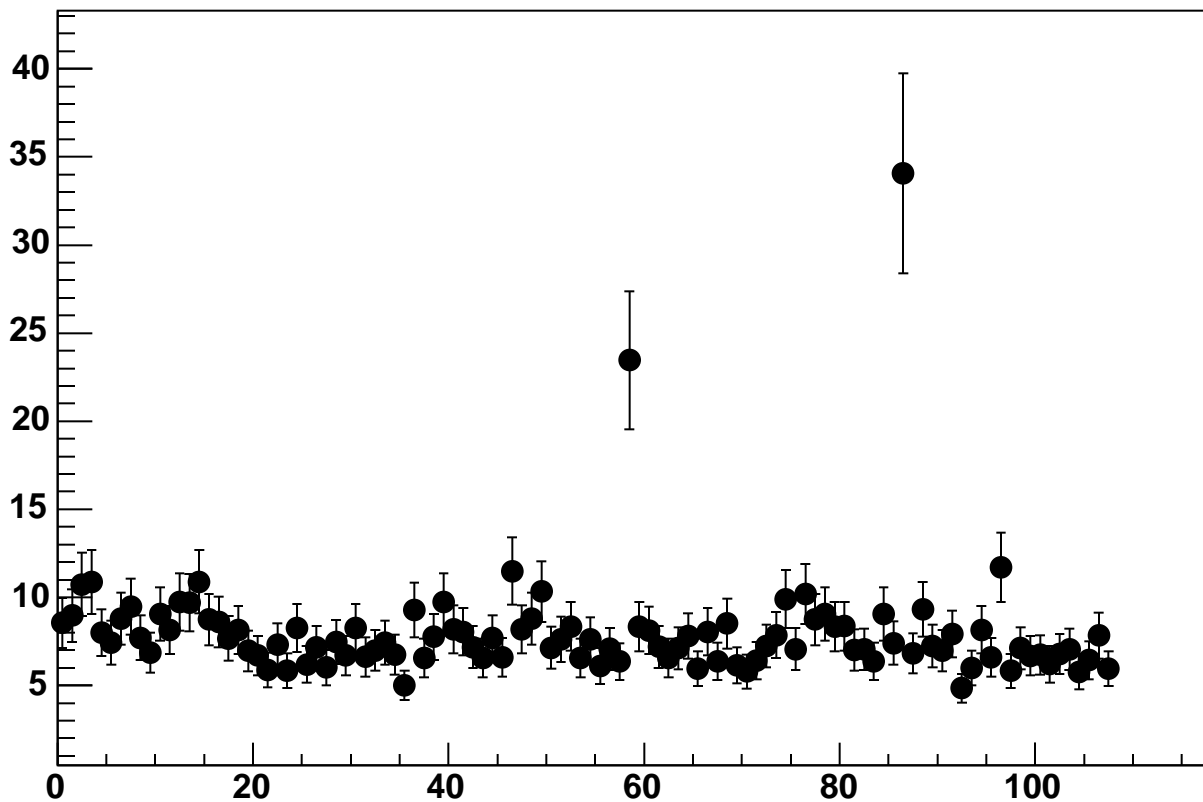
Enable 1, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



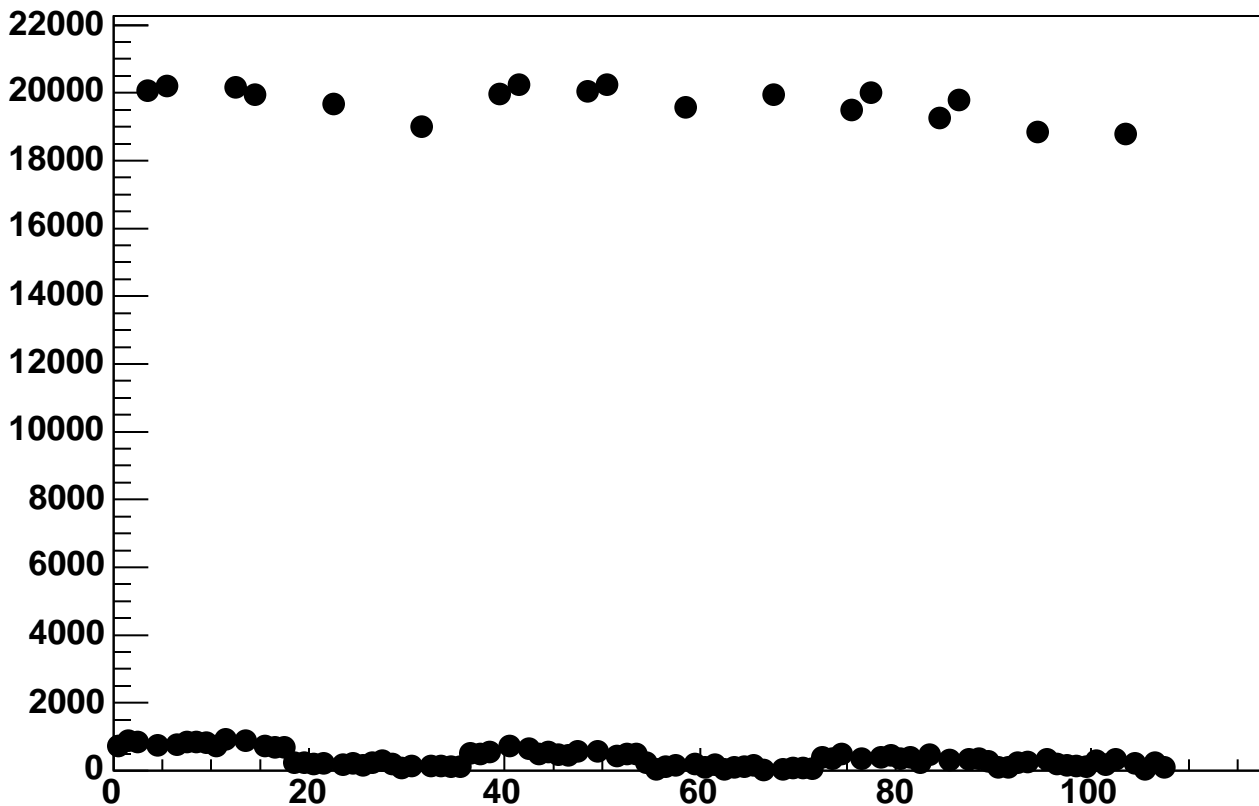
Enable 1, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



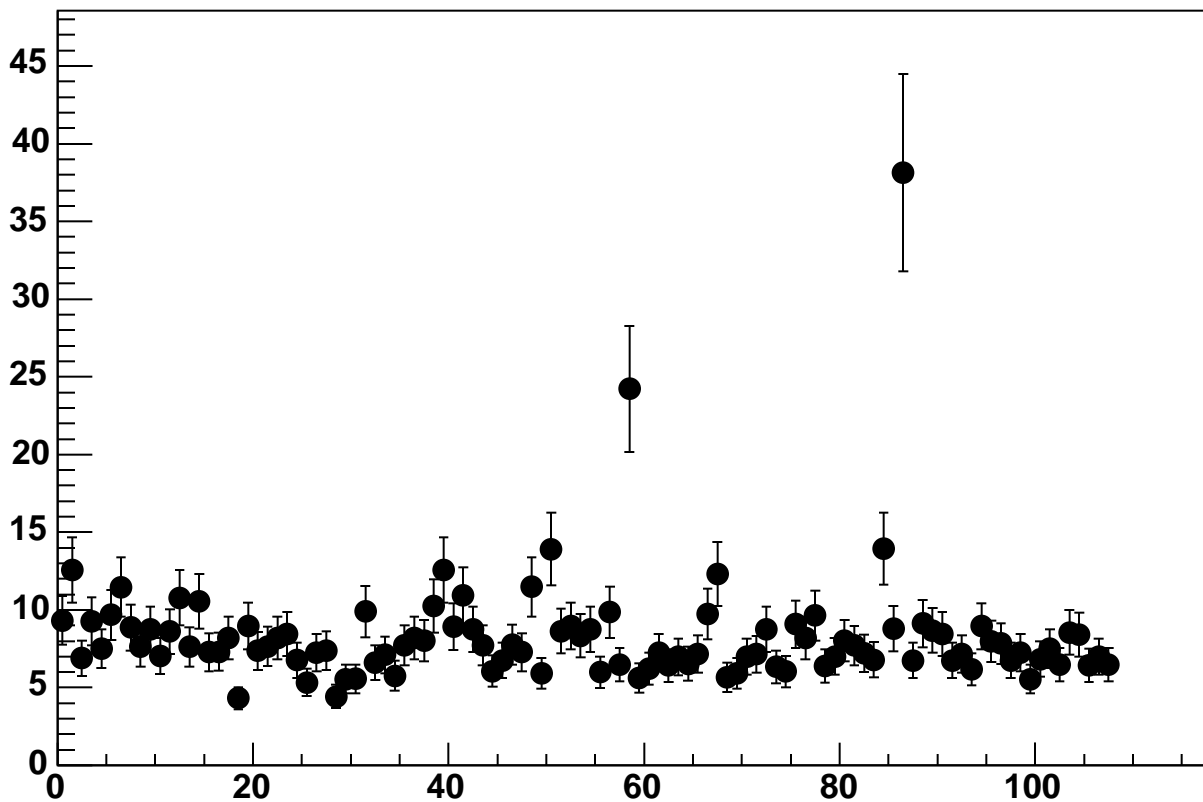
Enable 1, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



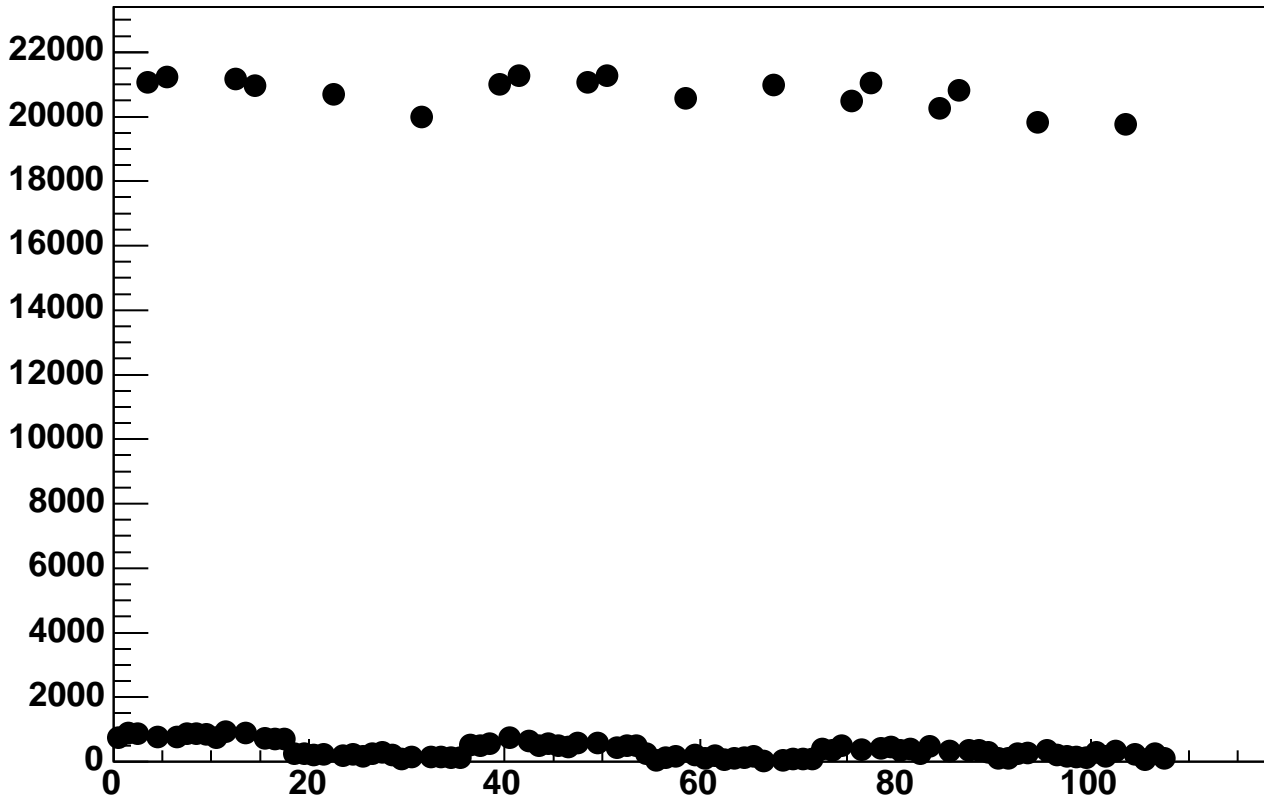
Enable 1, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



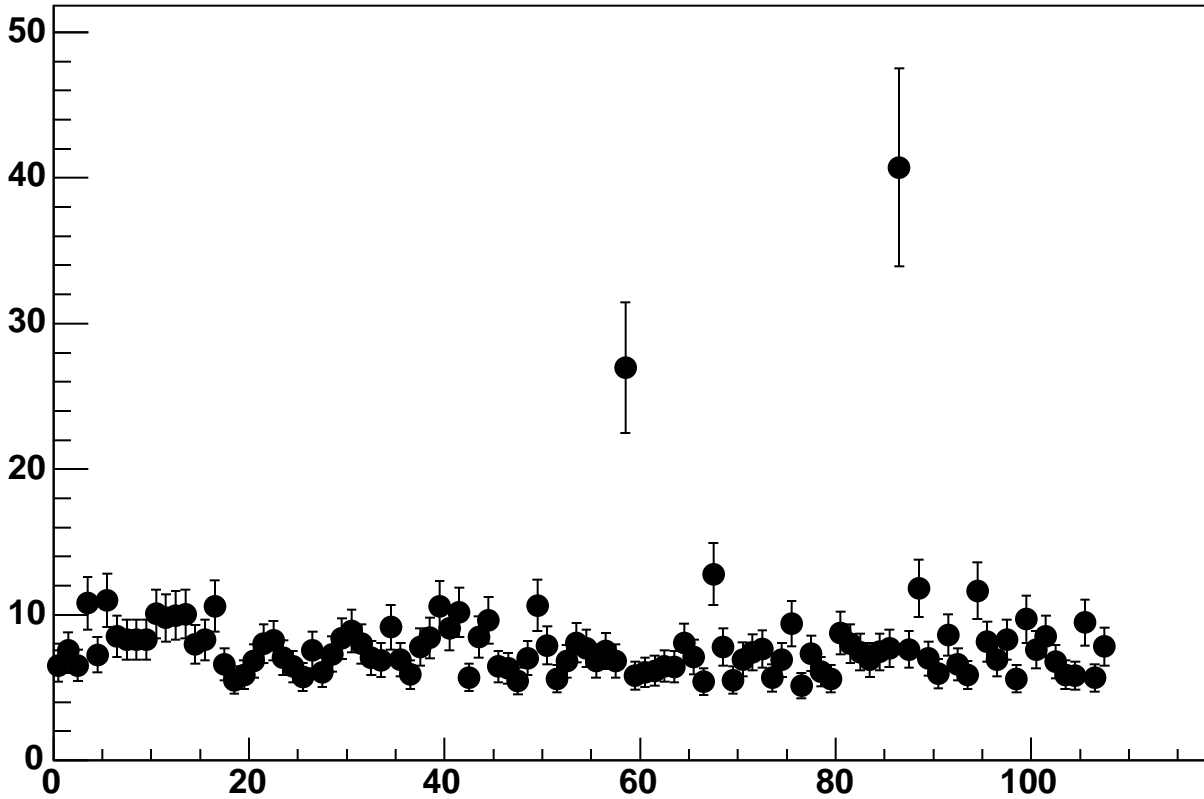
Enable 1, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



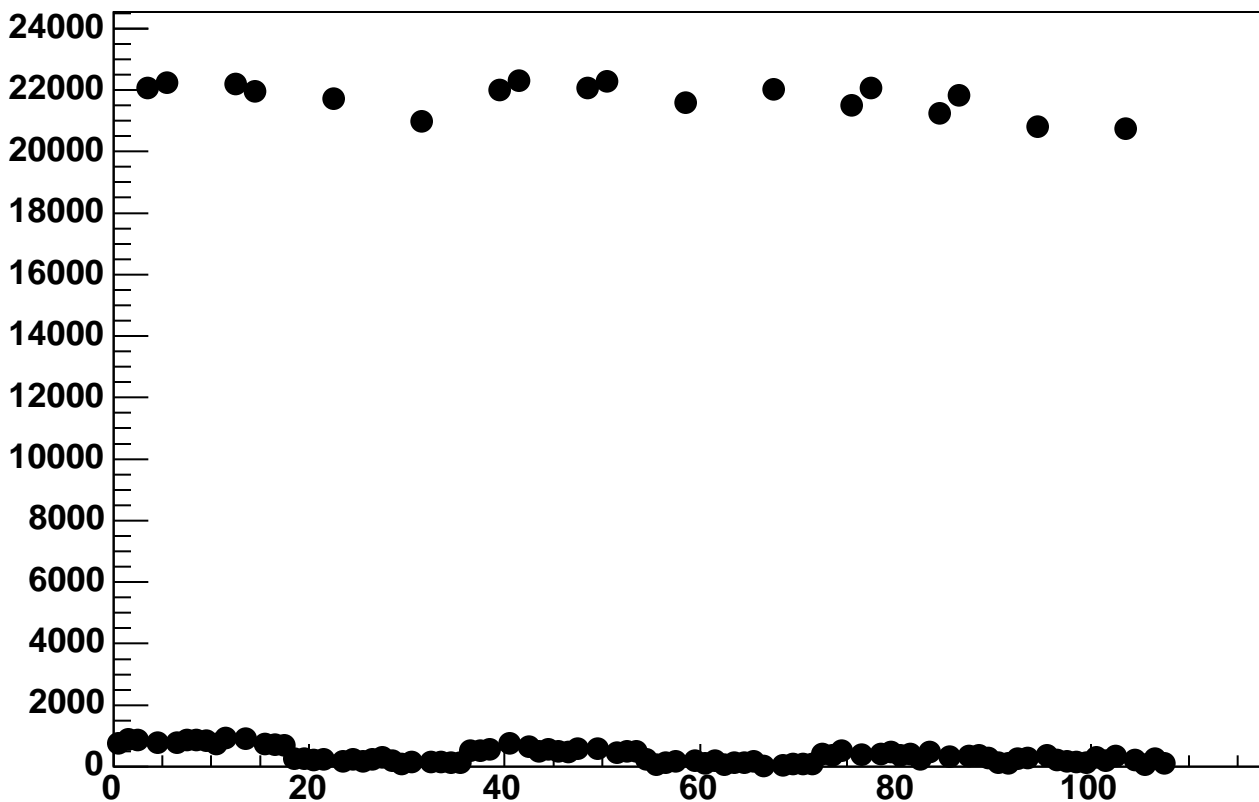
Enable 1, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



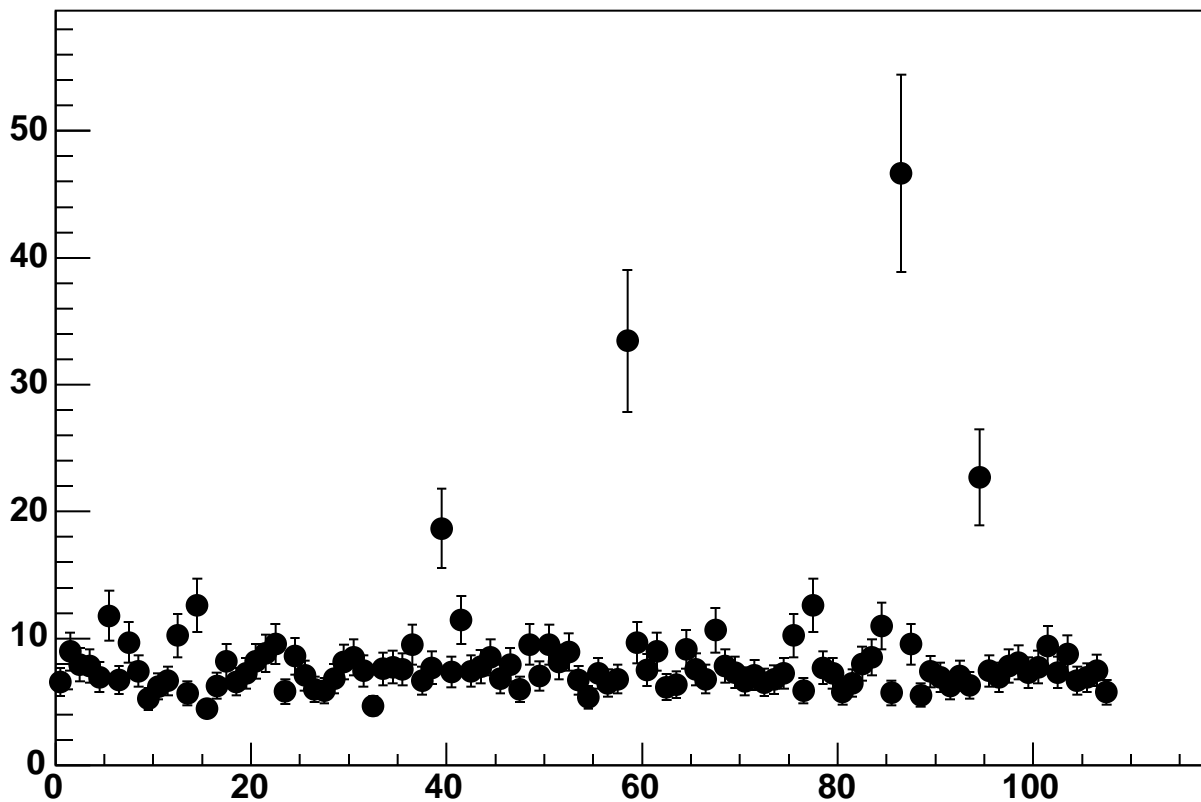
Enable 1, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



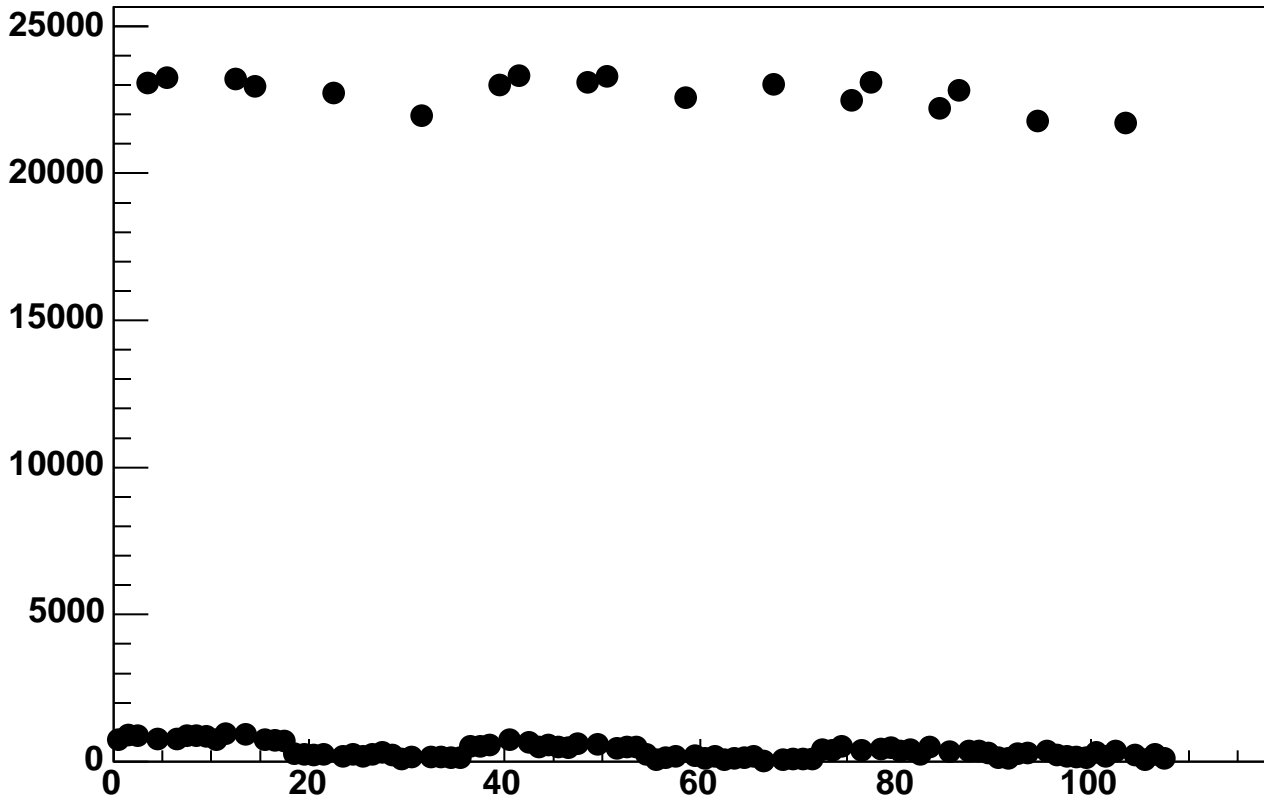
Enable 1, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



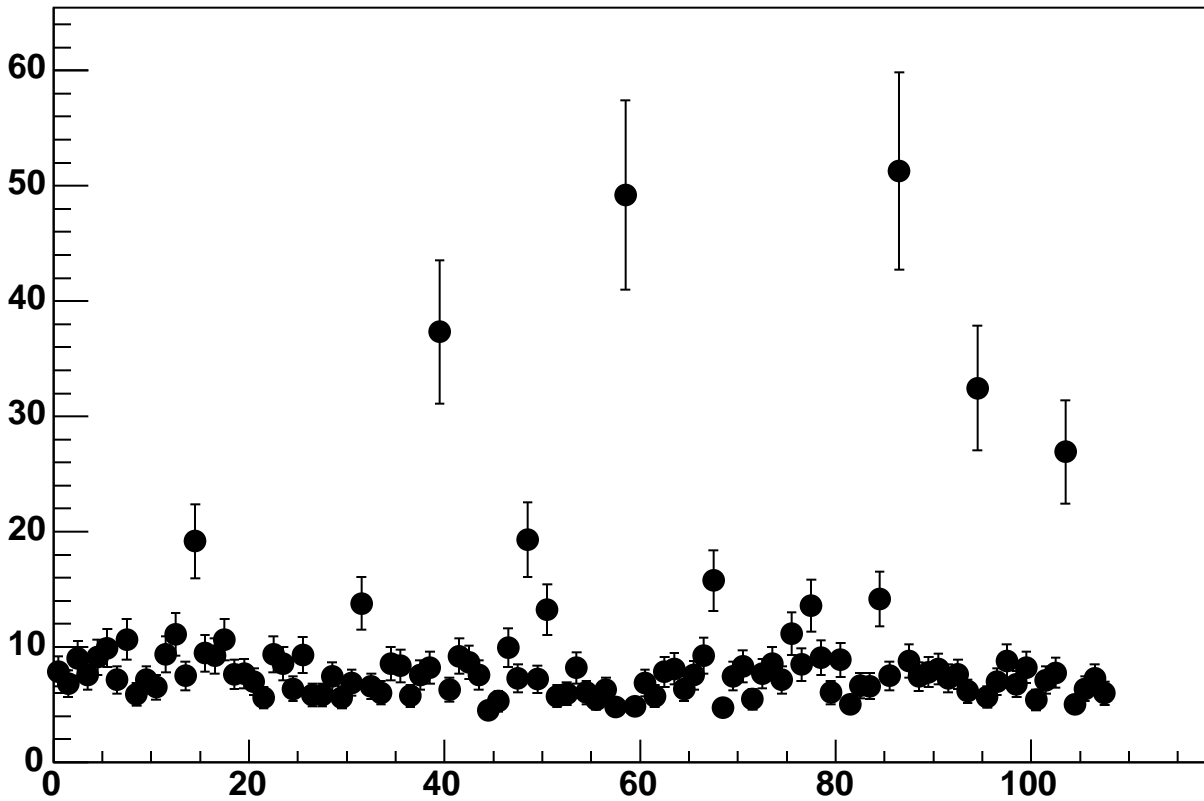
Enable 1, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



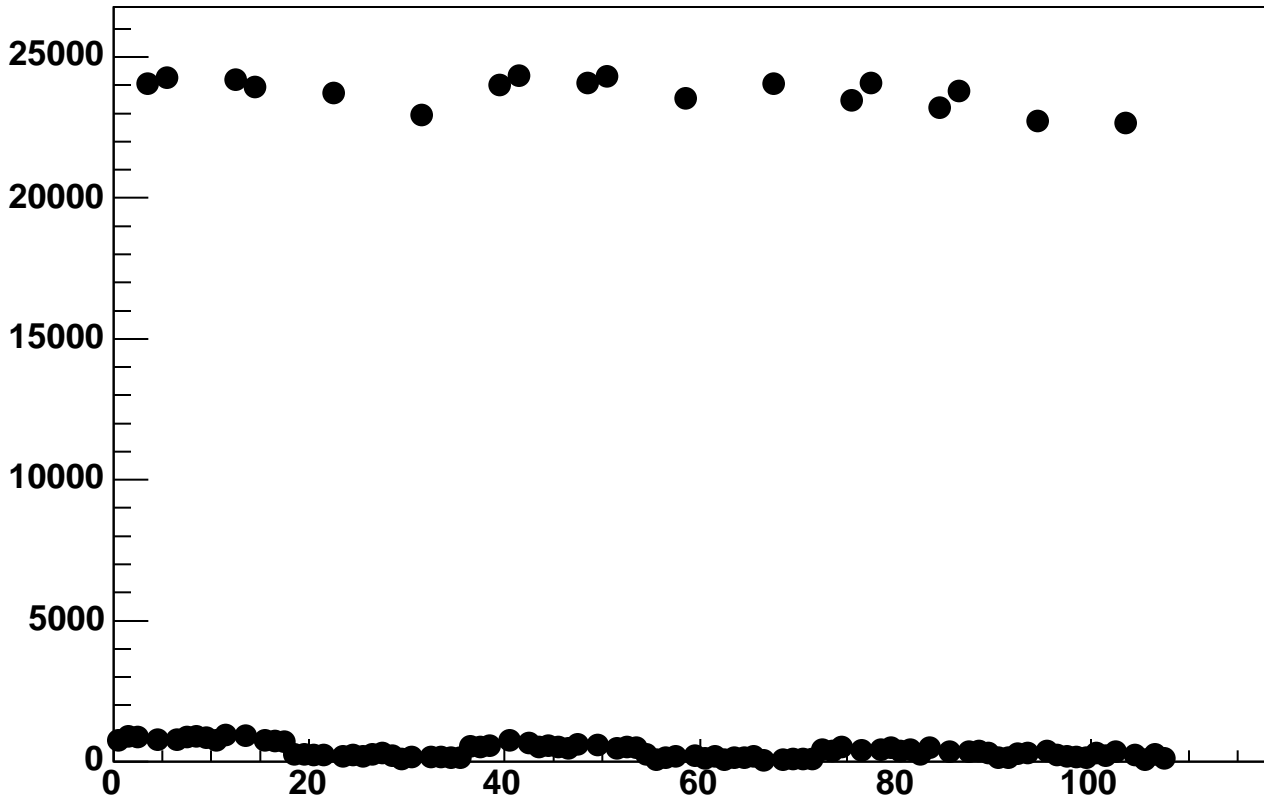
Enable 1, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



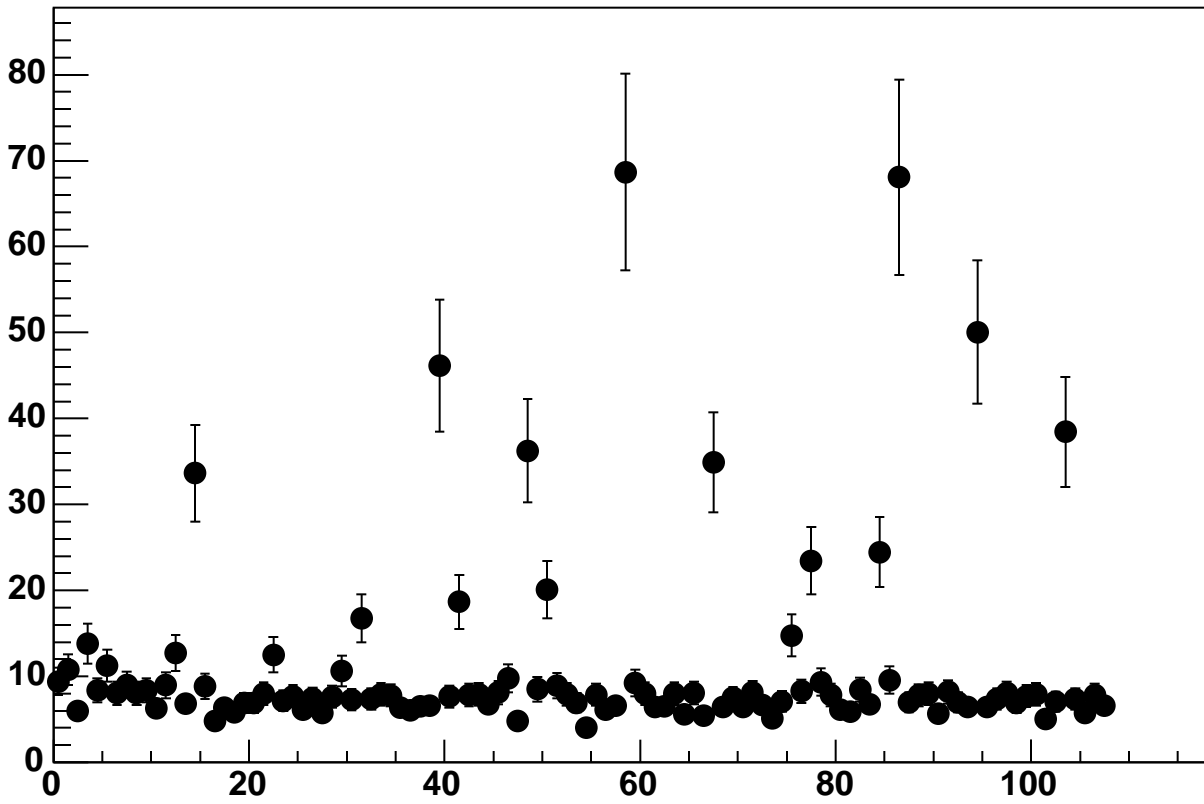
Enable 1, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



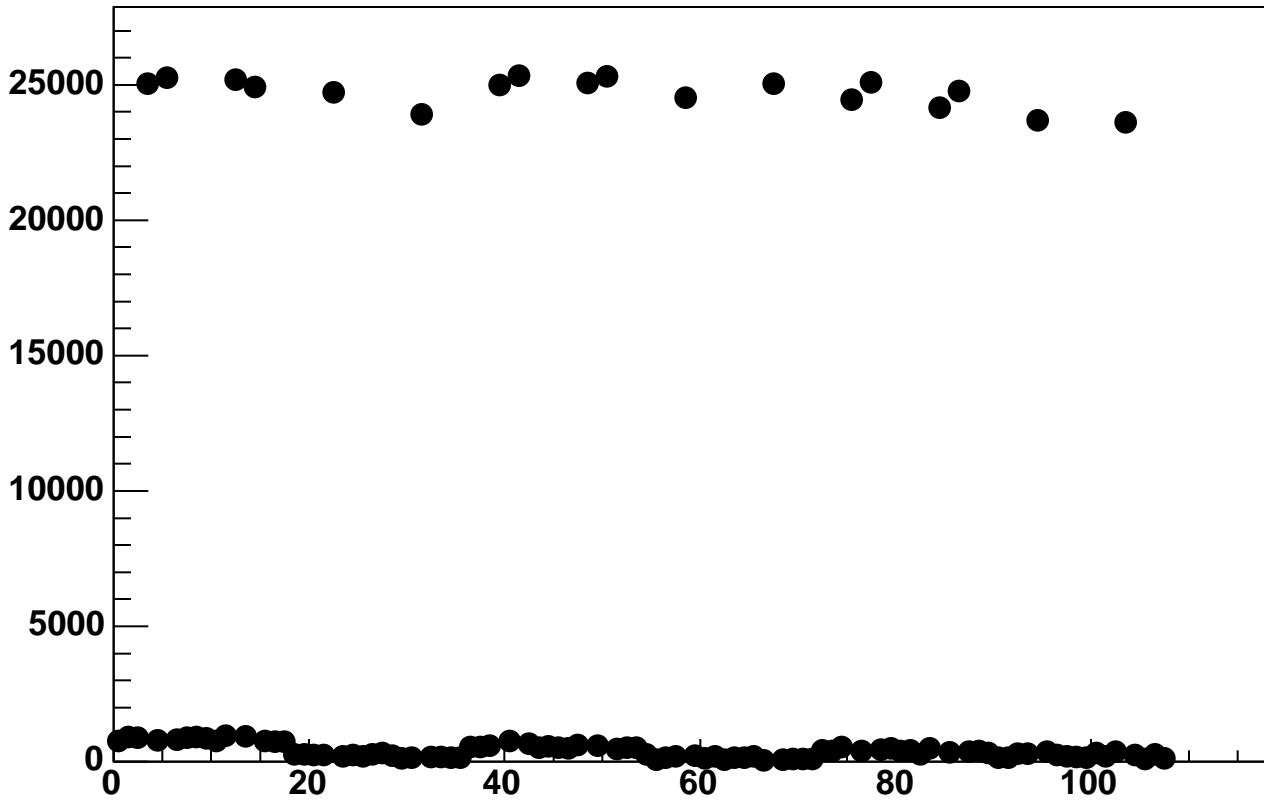
Enable 1, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



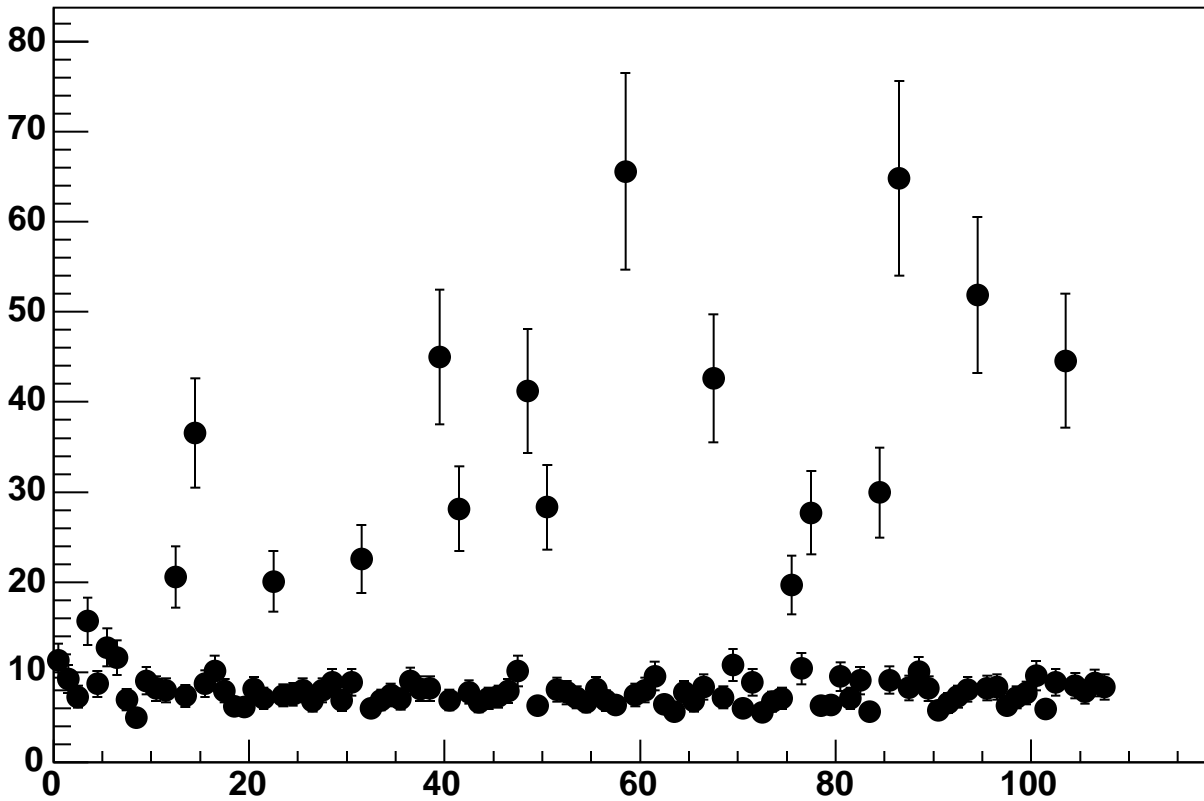
Enable 1, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



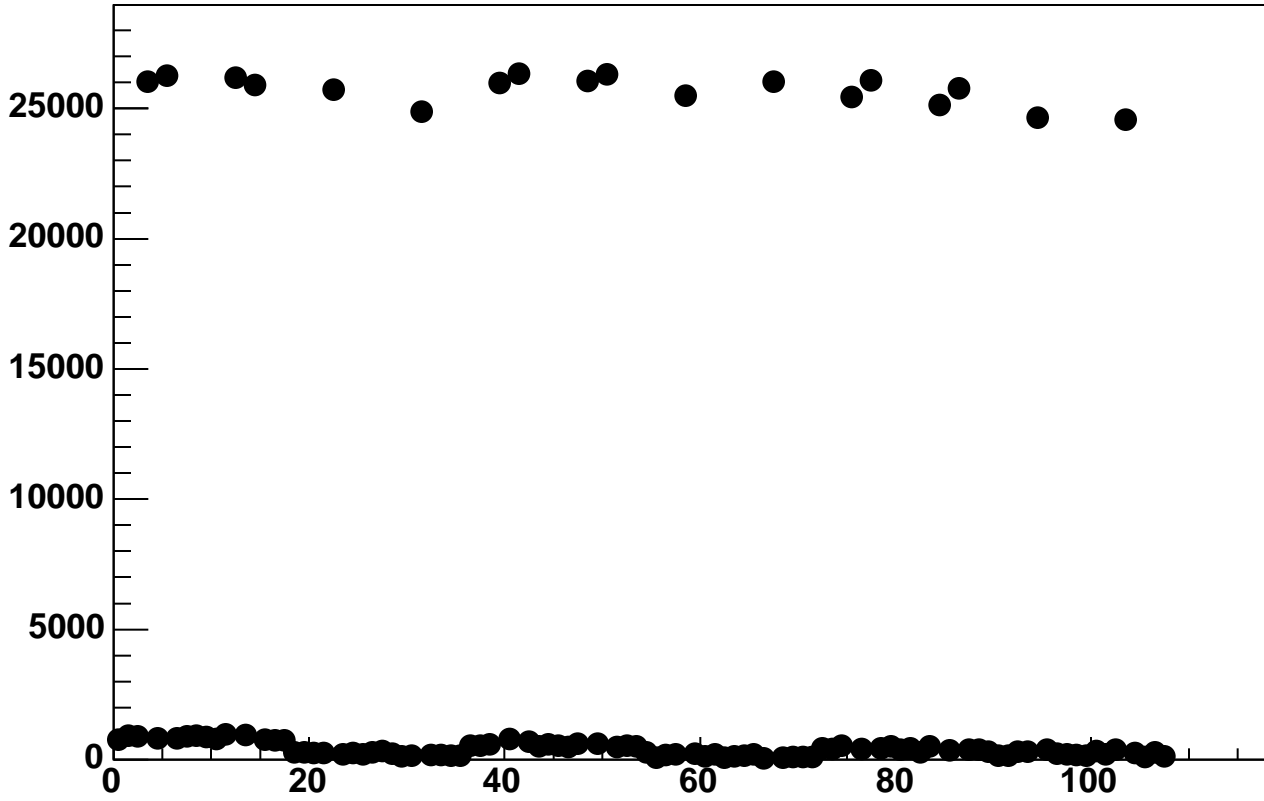
Enable 1, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



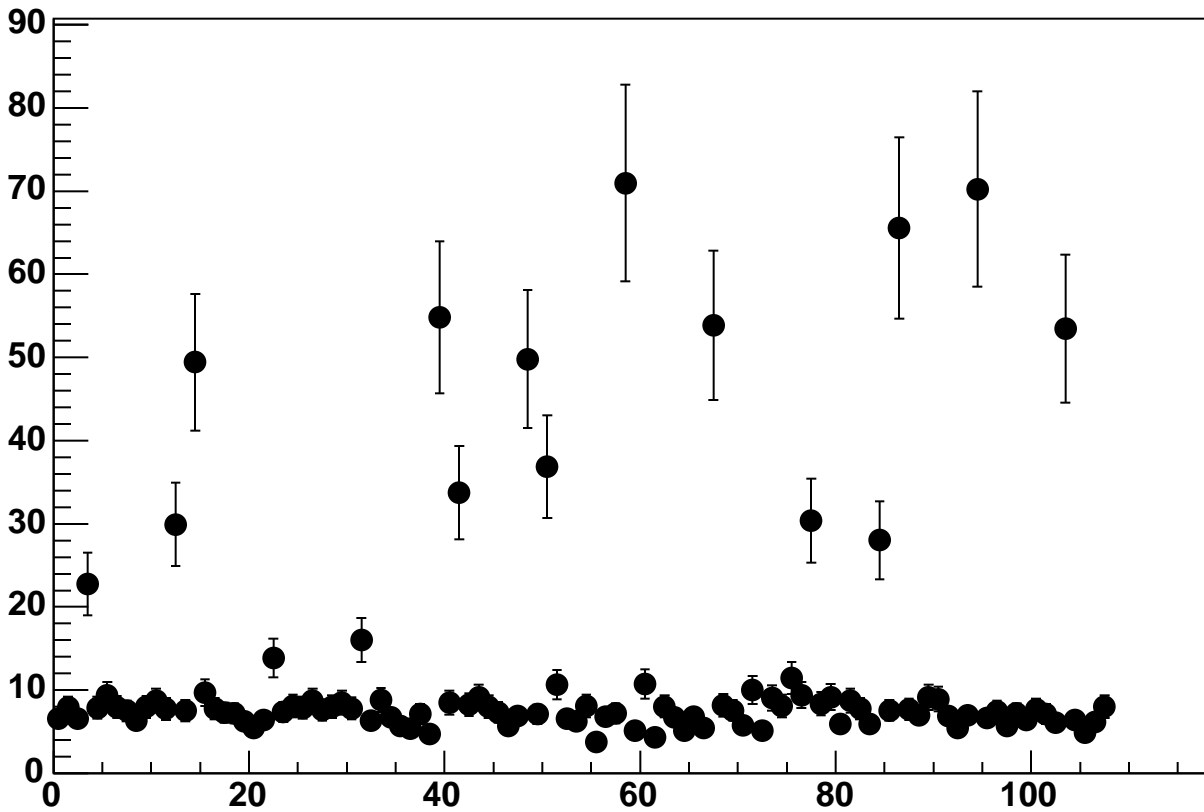
Enable 1, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



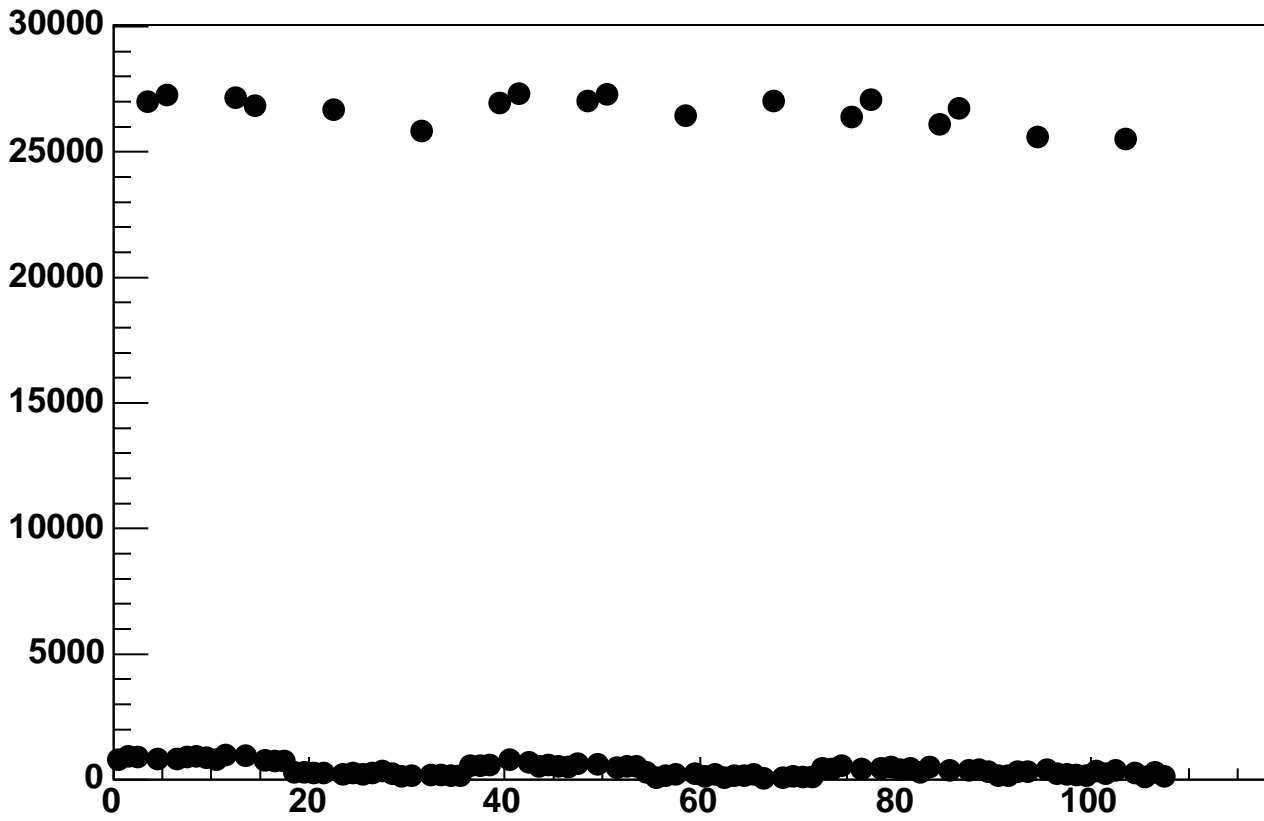
Enable 1, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



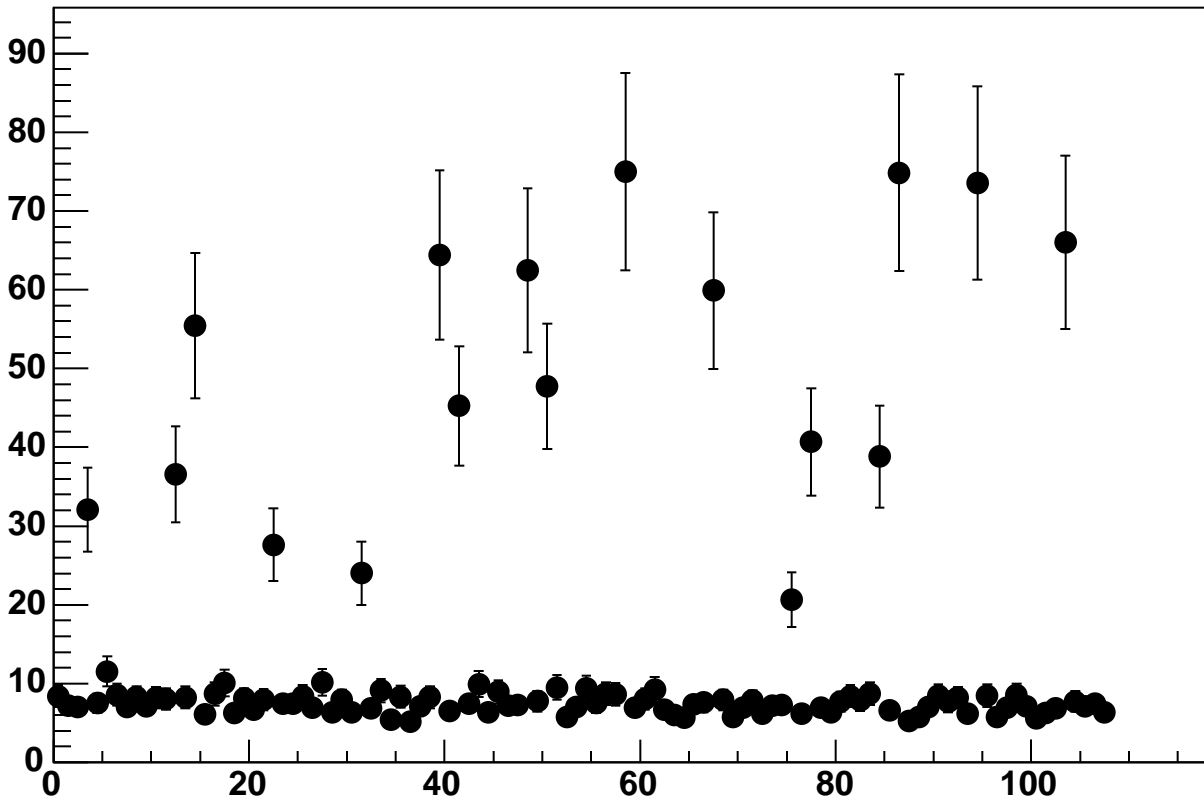
Enable 1, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



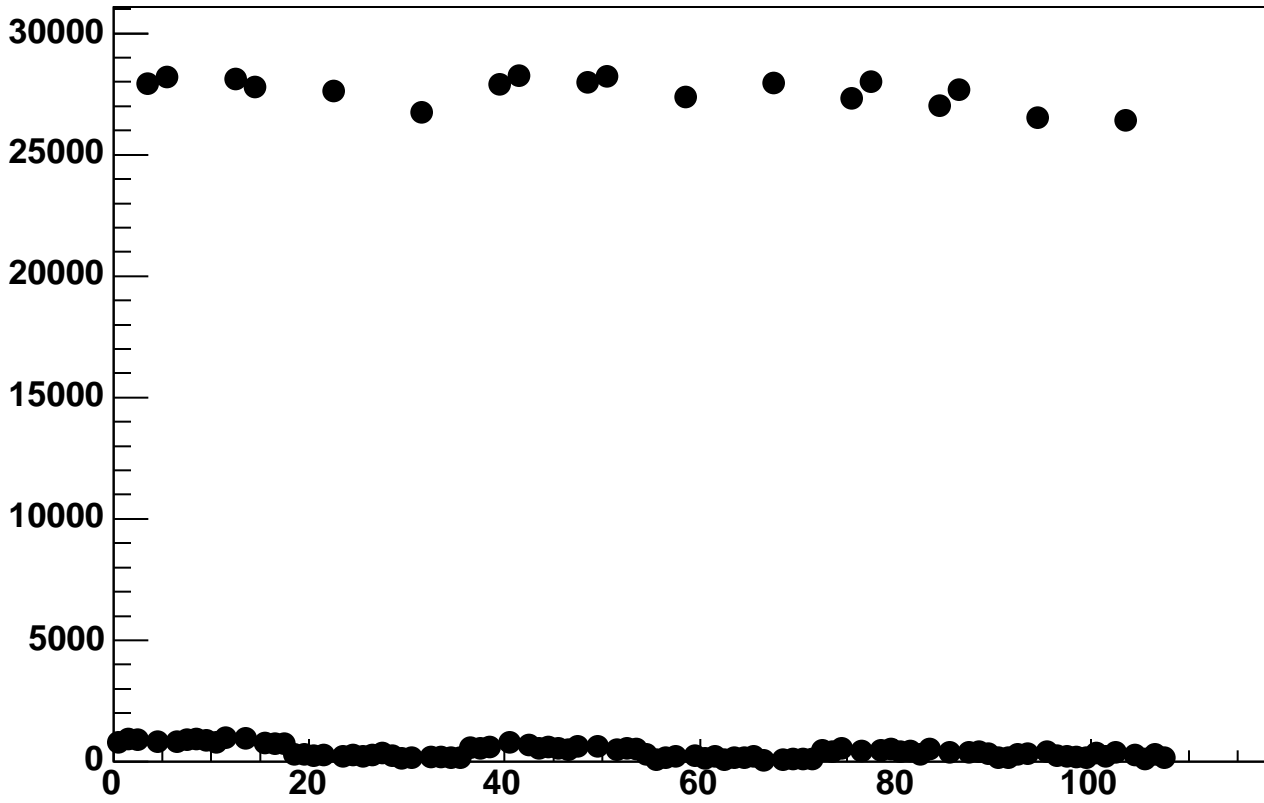
Enable 1, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



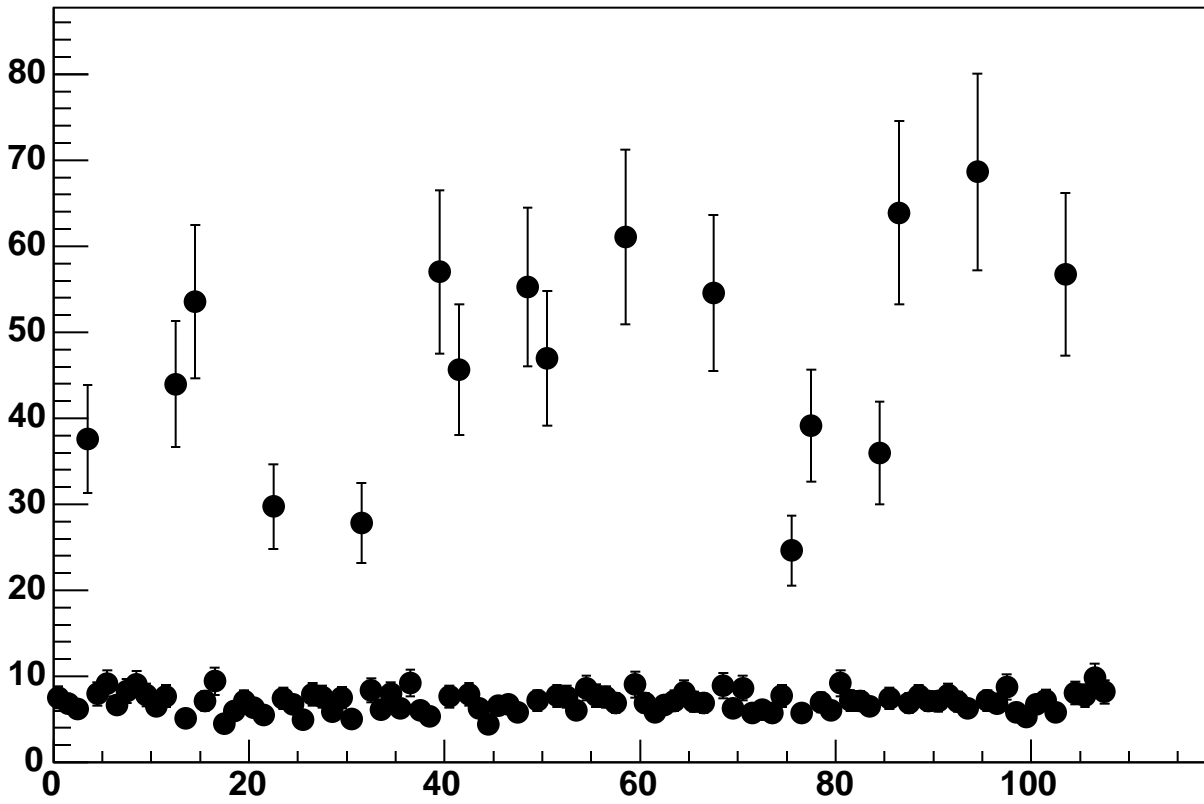
Enable 1, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



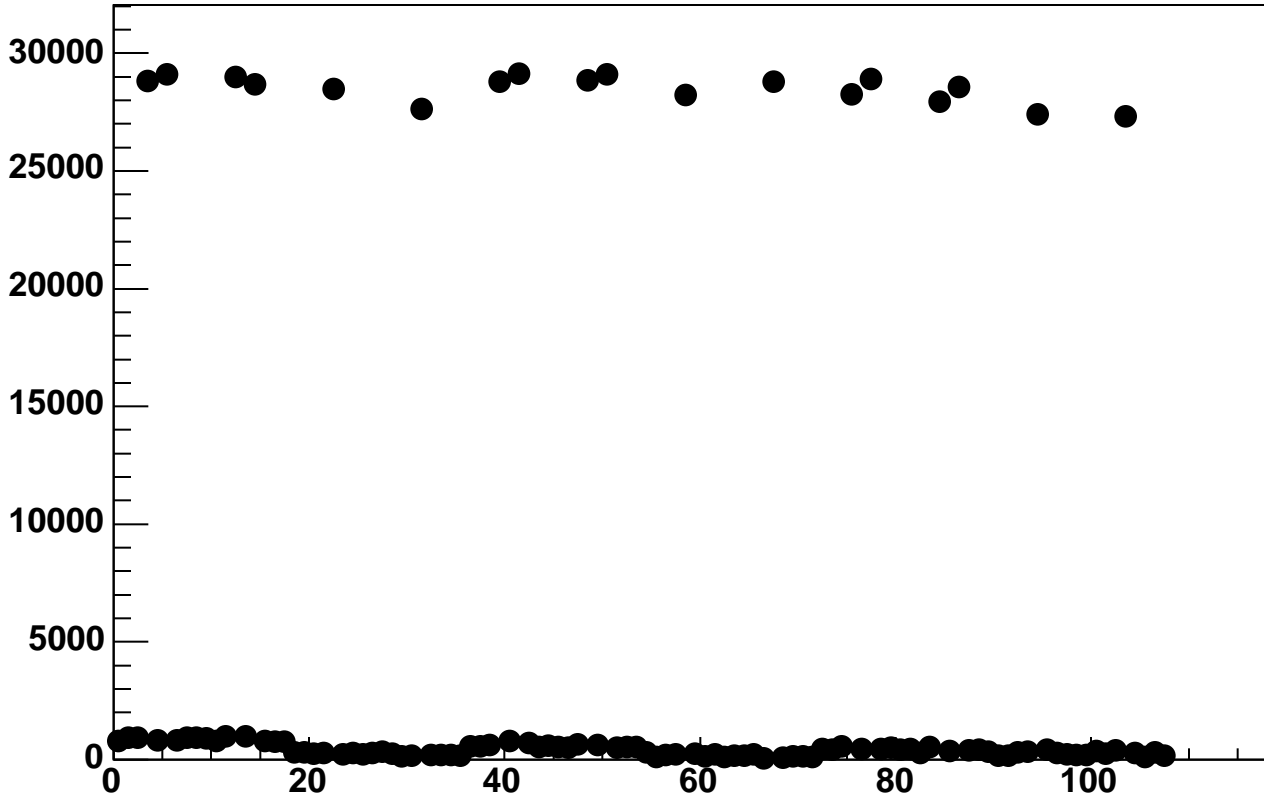
Enable 1, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



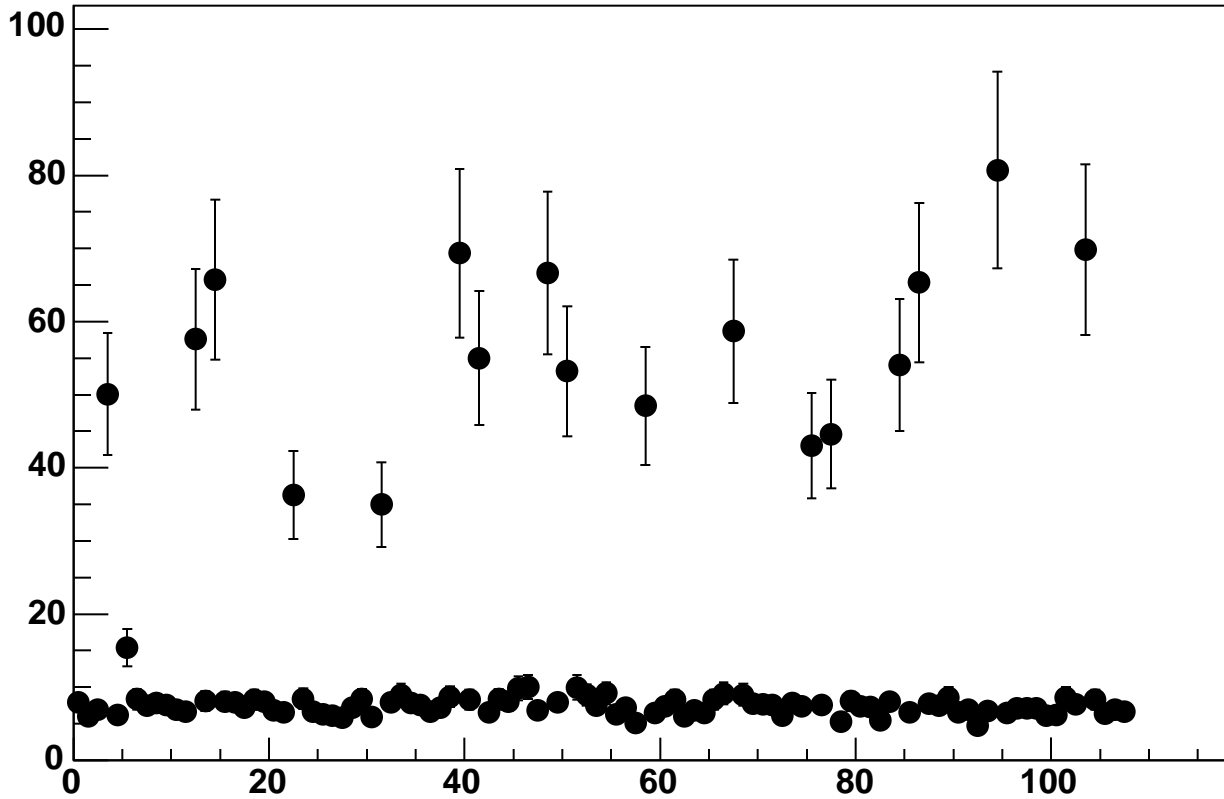
Enable 1, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



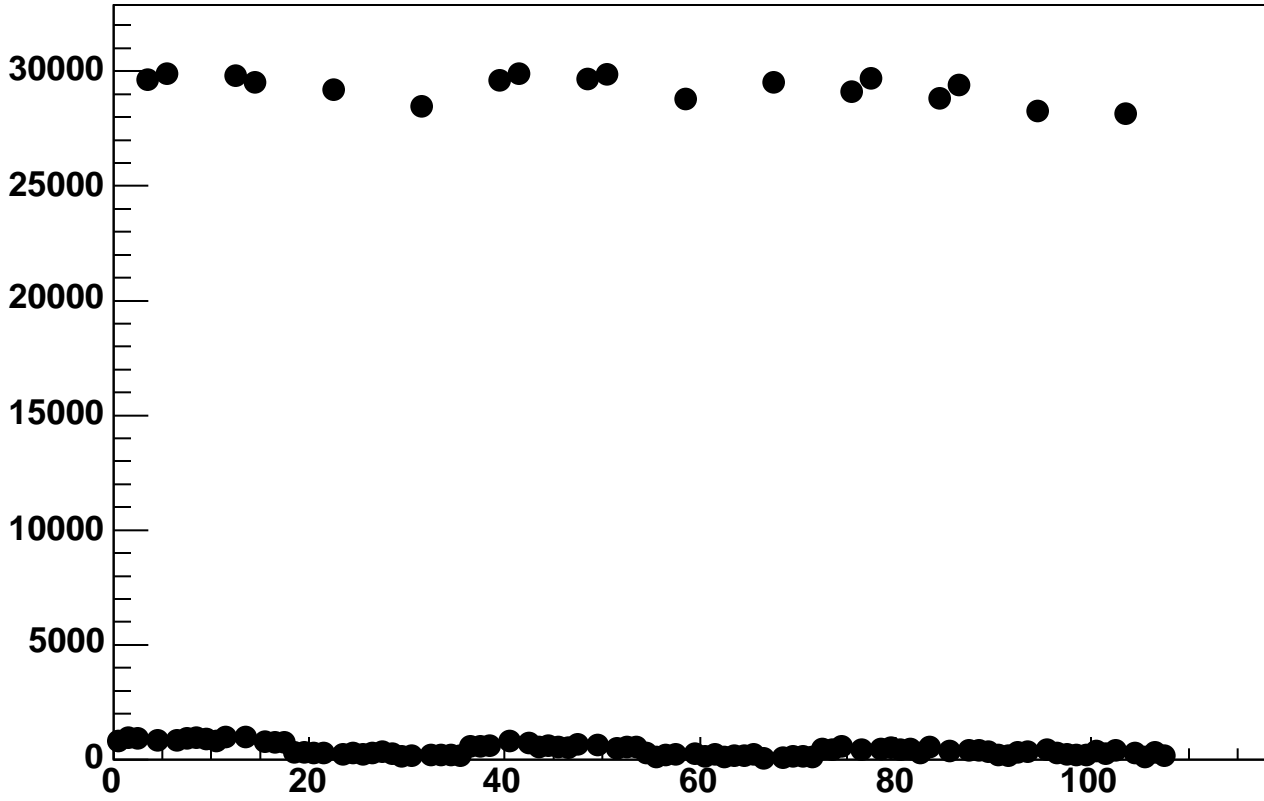
Enable 1, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



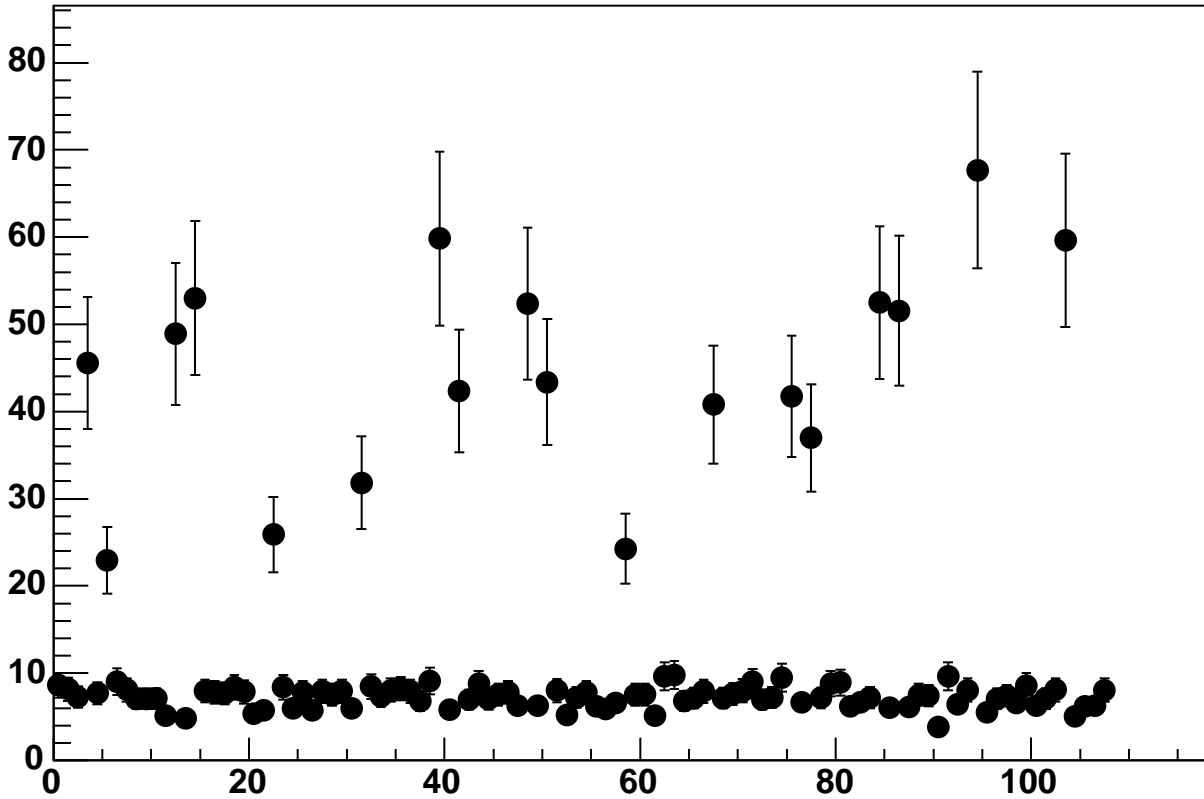
Enable 1, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



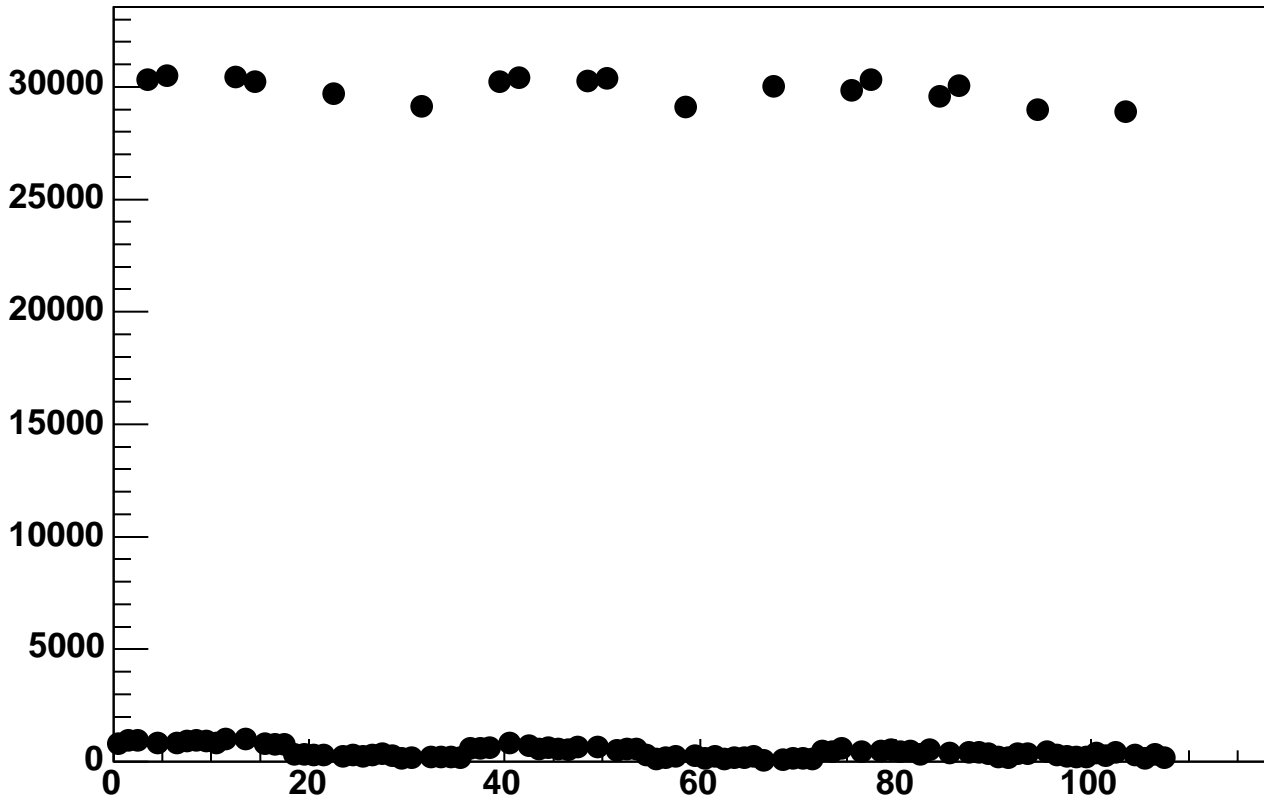
Enable 1, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



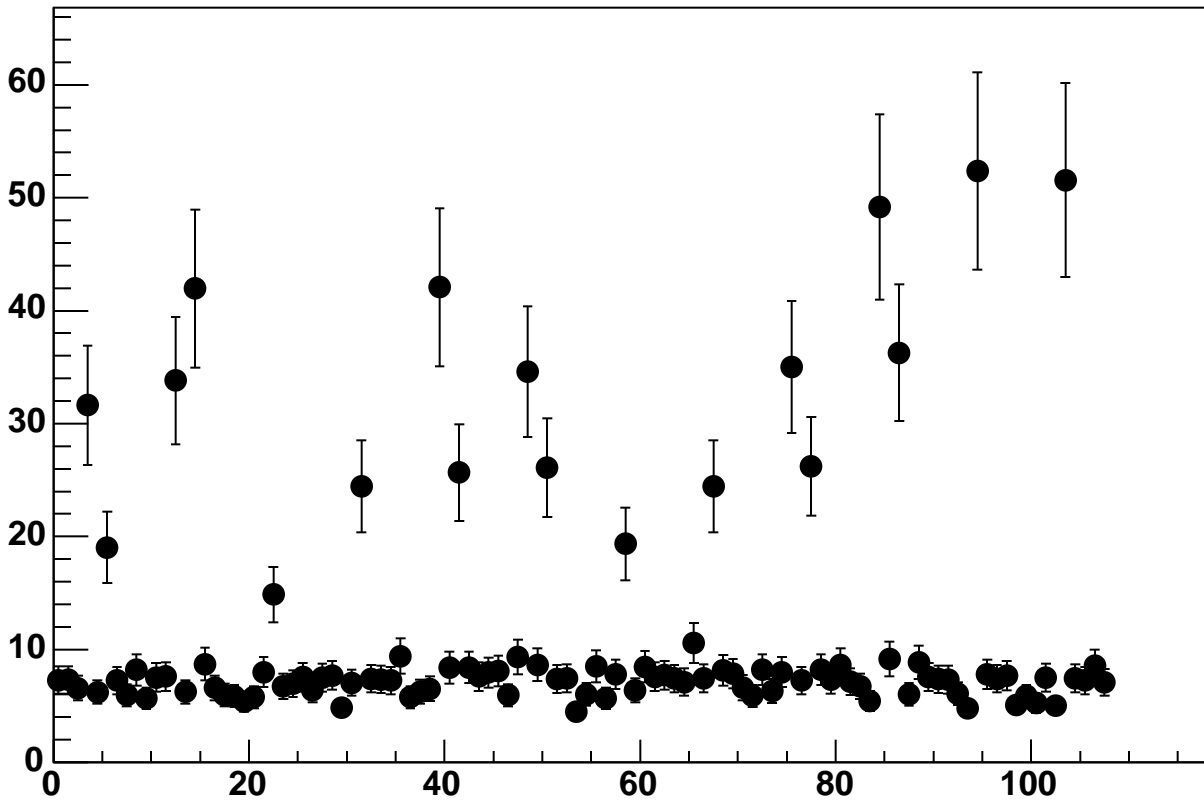
Enable 1, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



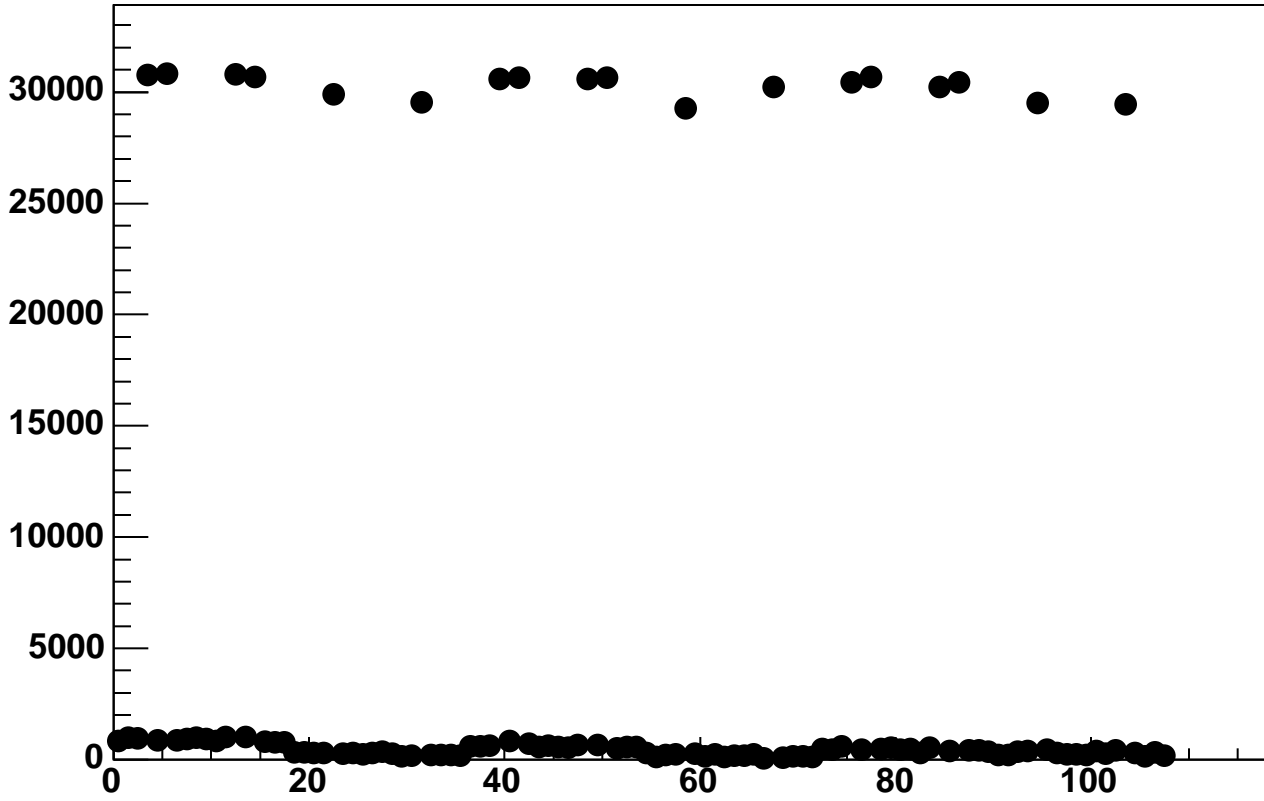
Enable 1, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



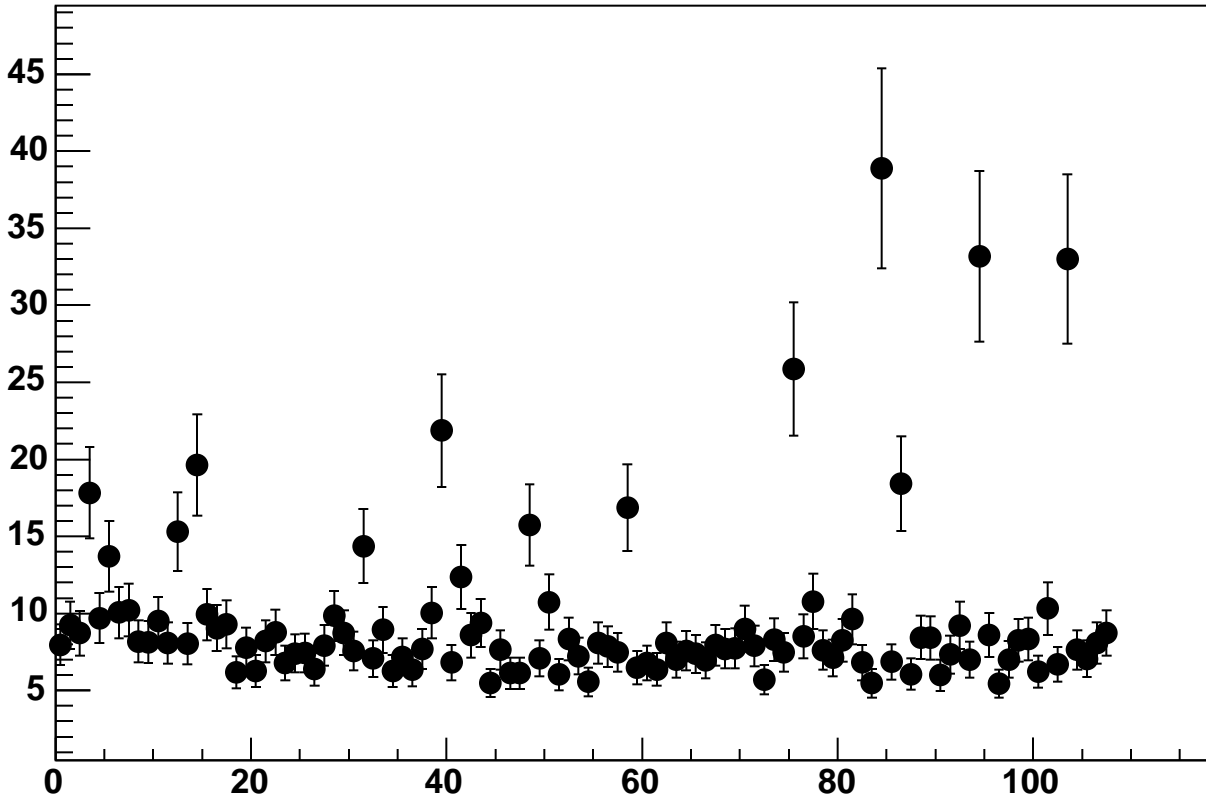
Enable 1, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



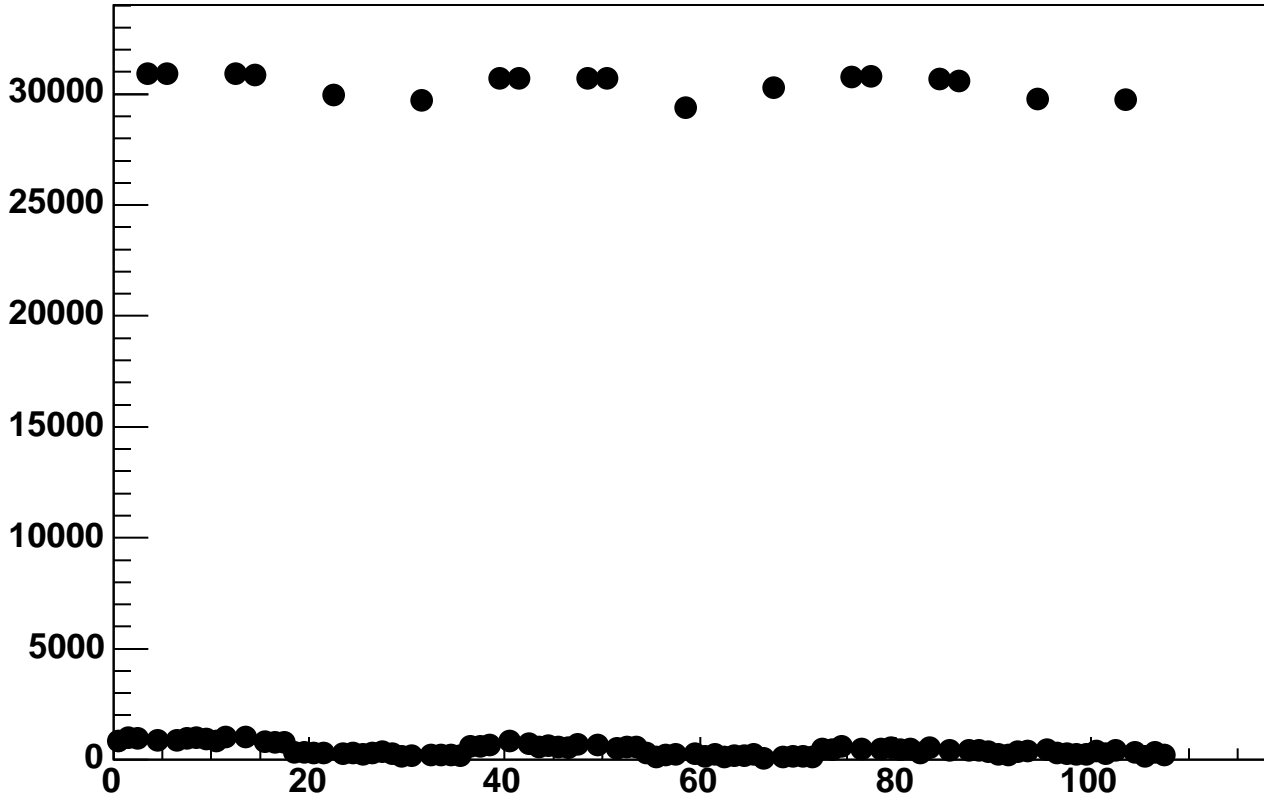
Enable 1, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



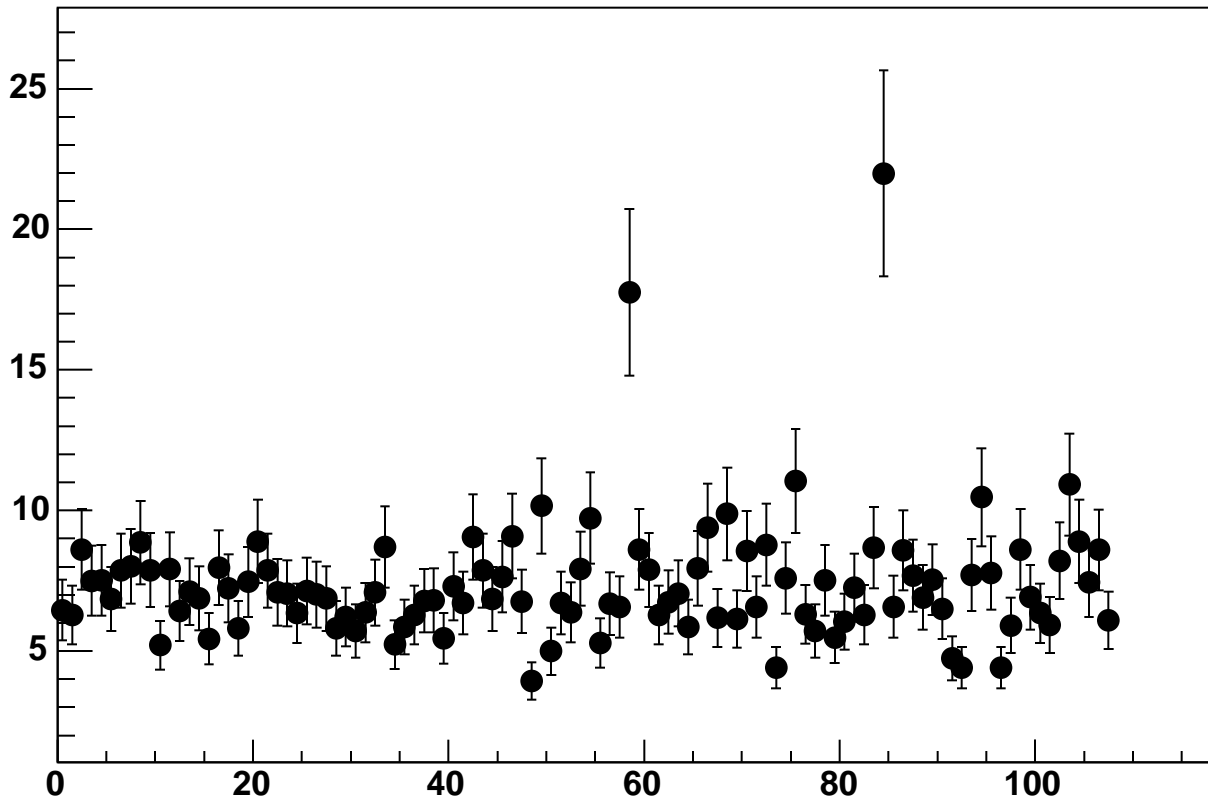
Enable 1, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



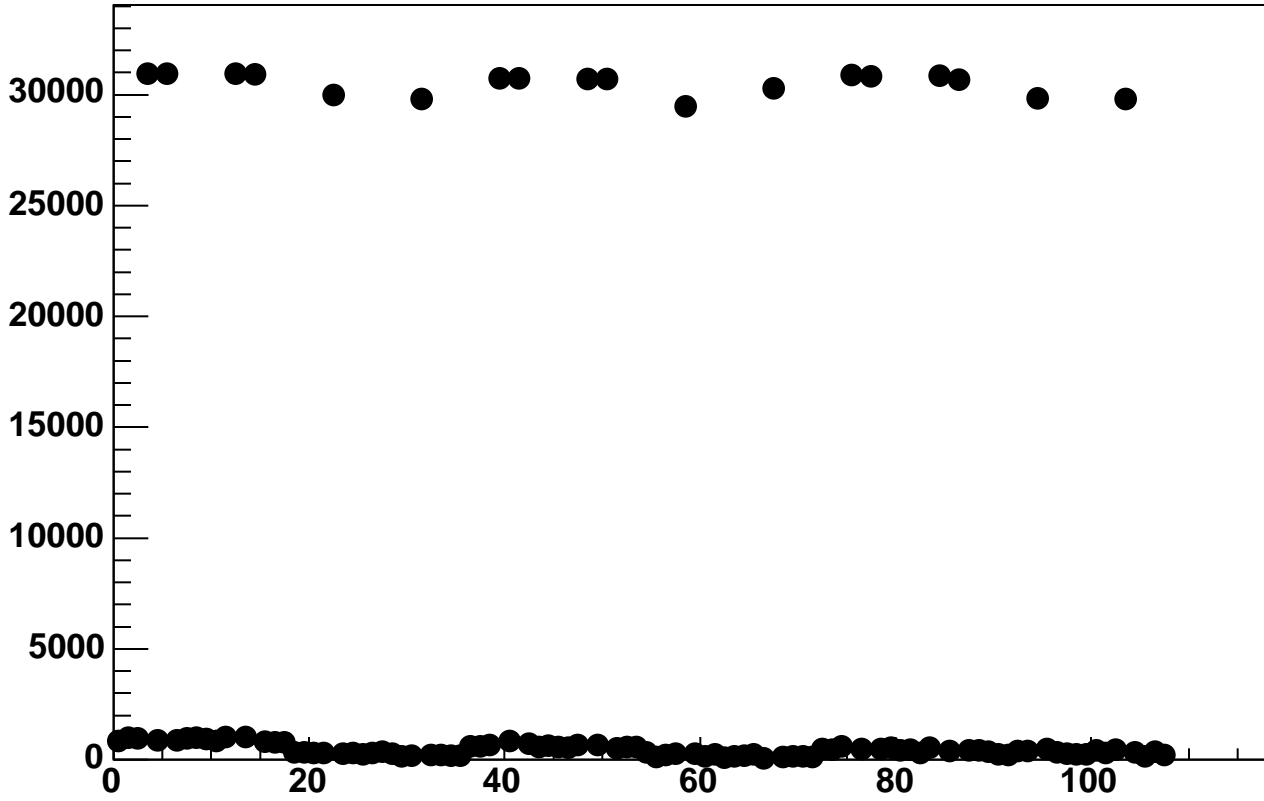
Enable 1, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



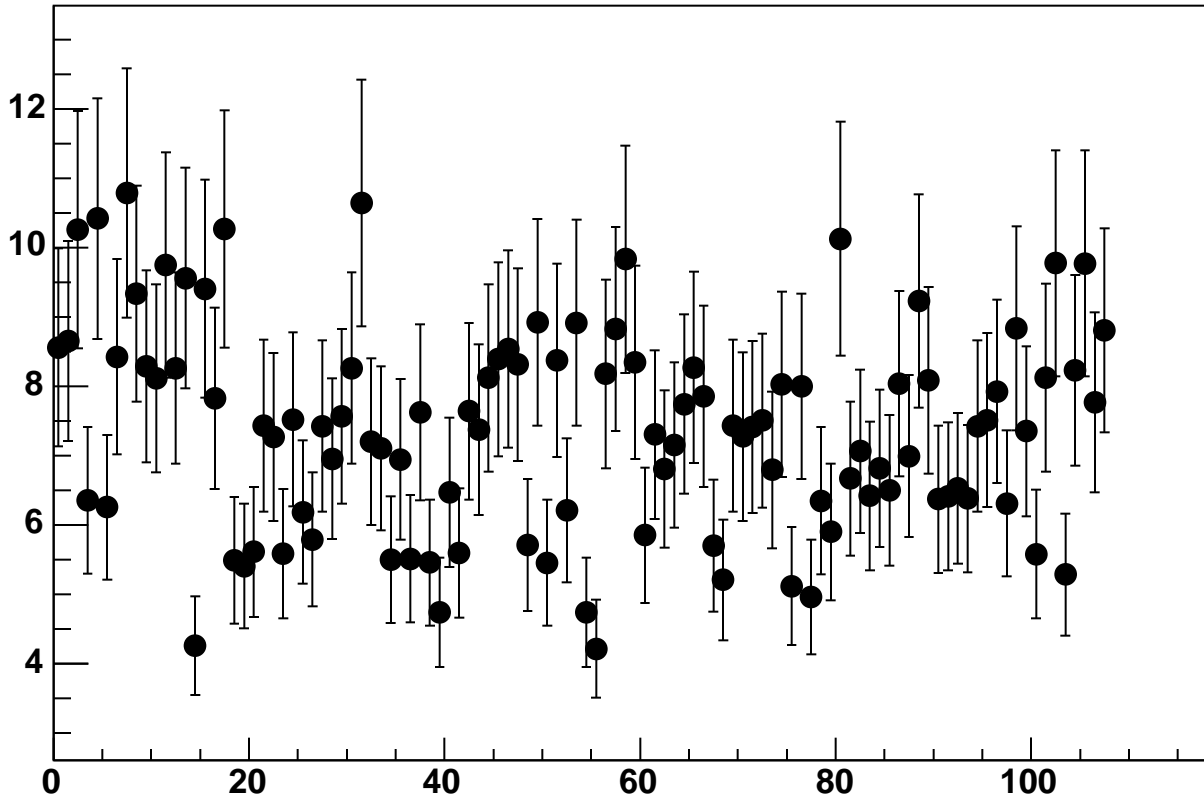
Enable 1, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



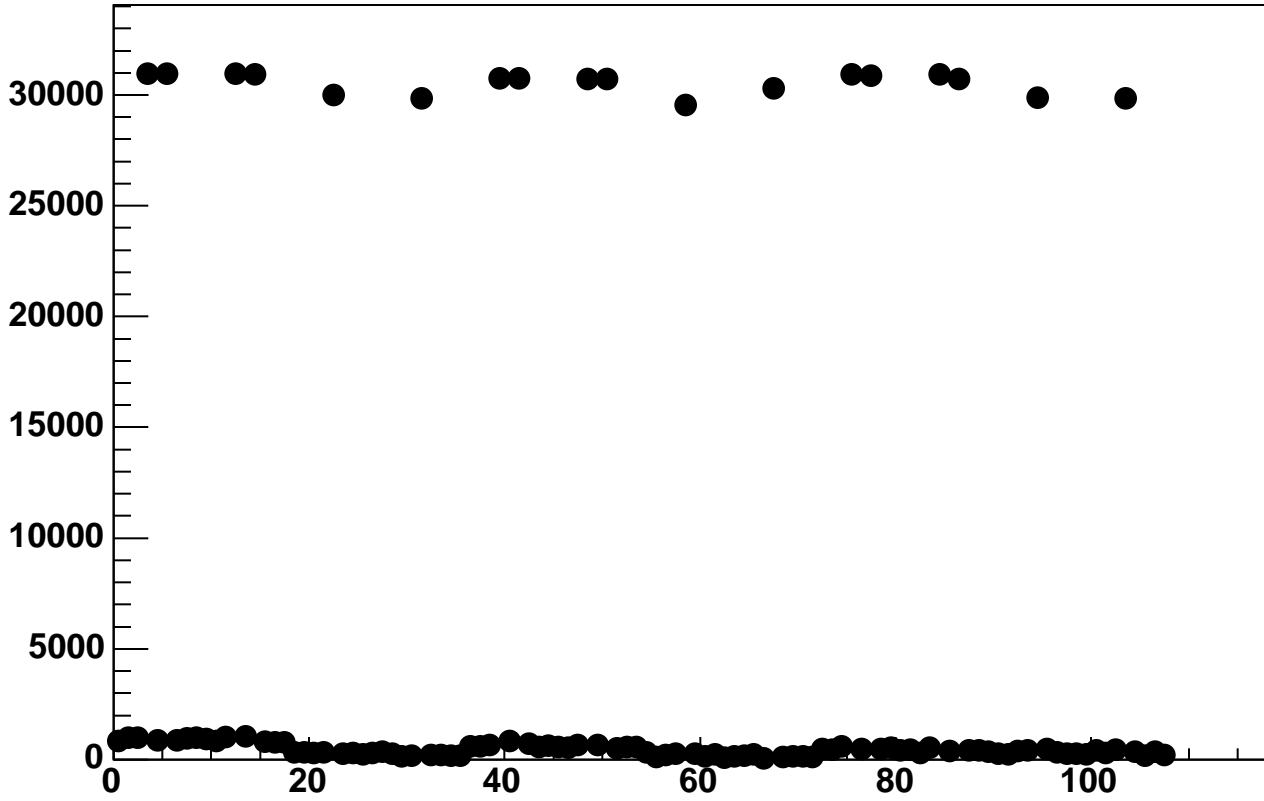
Enable 1, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



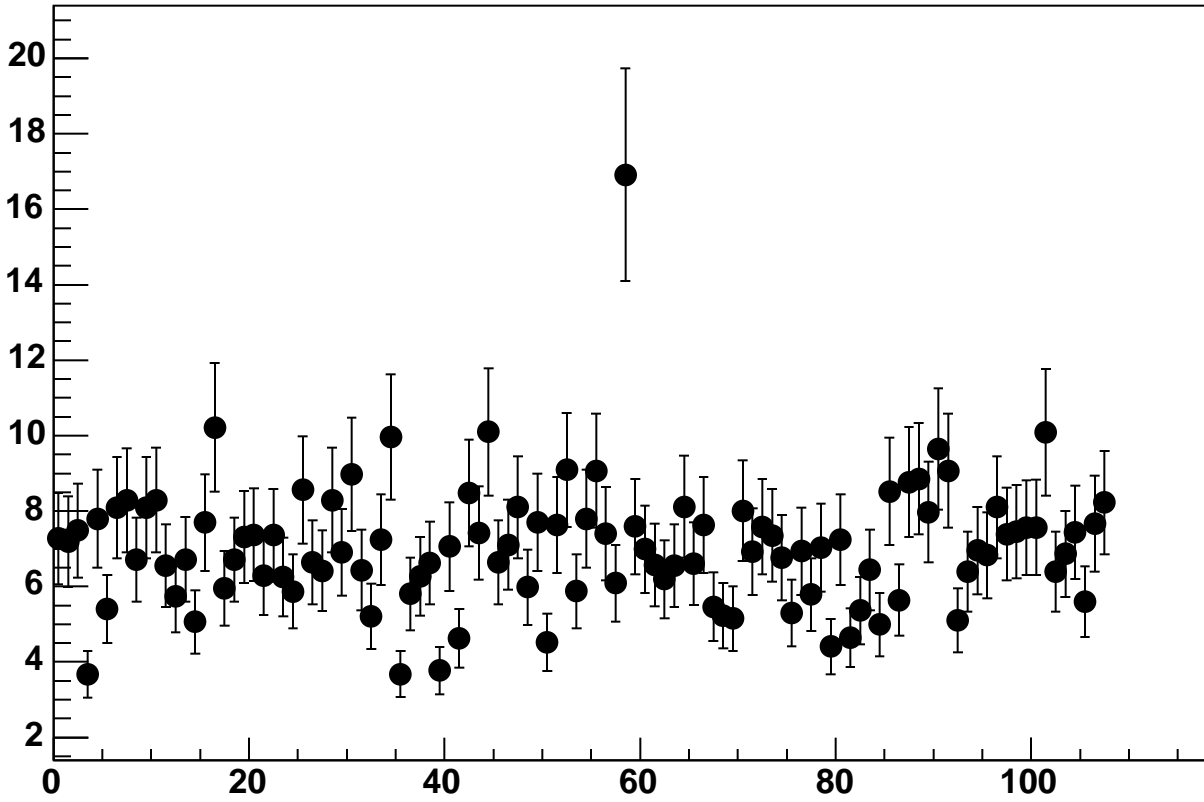
Enable 1, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



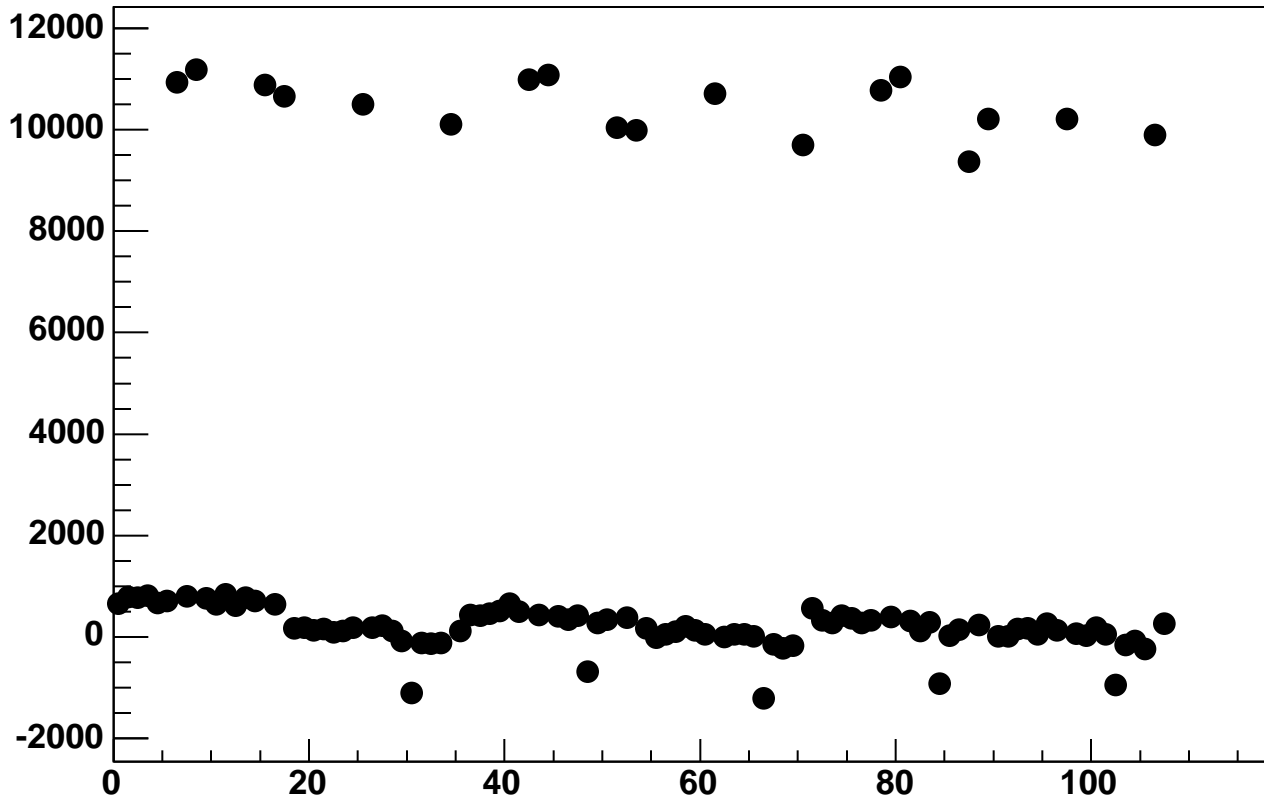
Enable 1, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



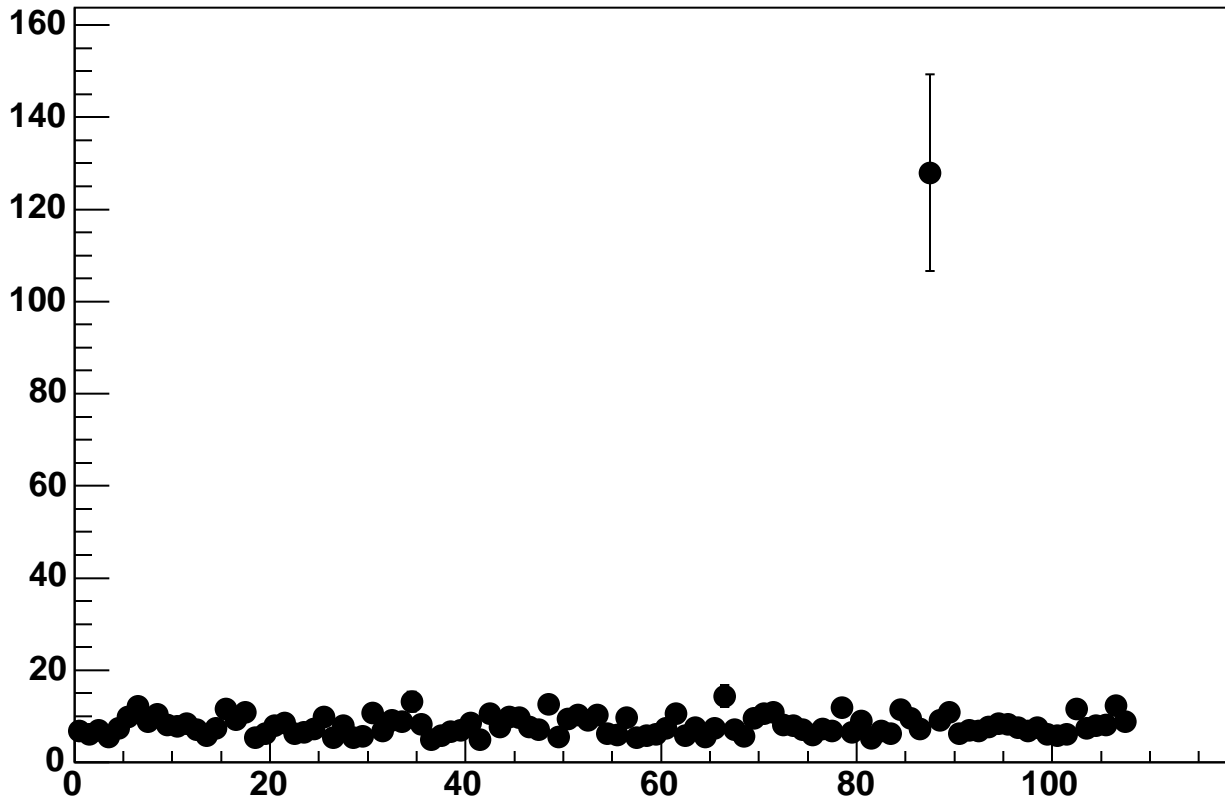
Enable 1, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



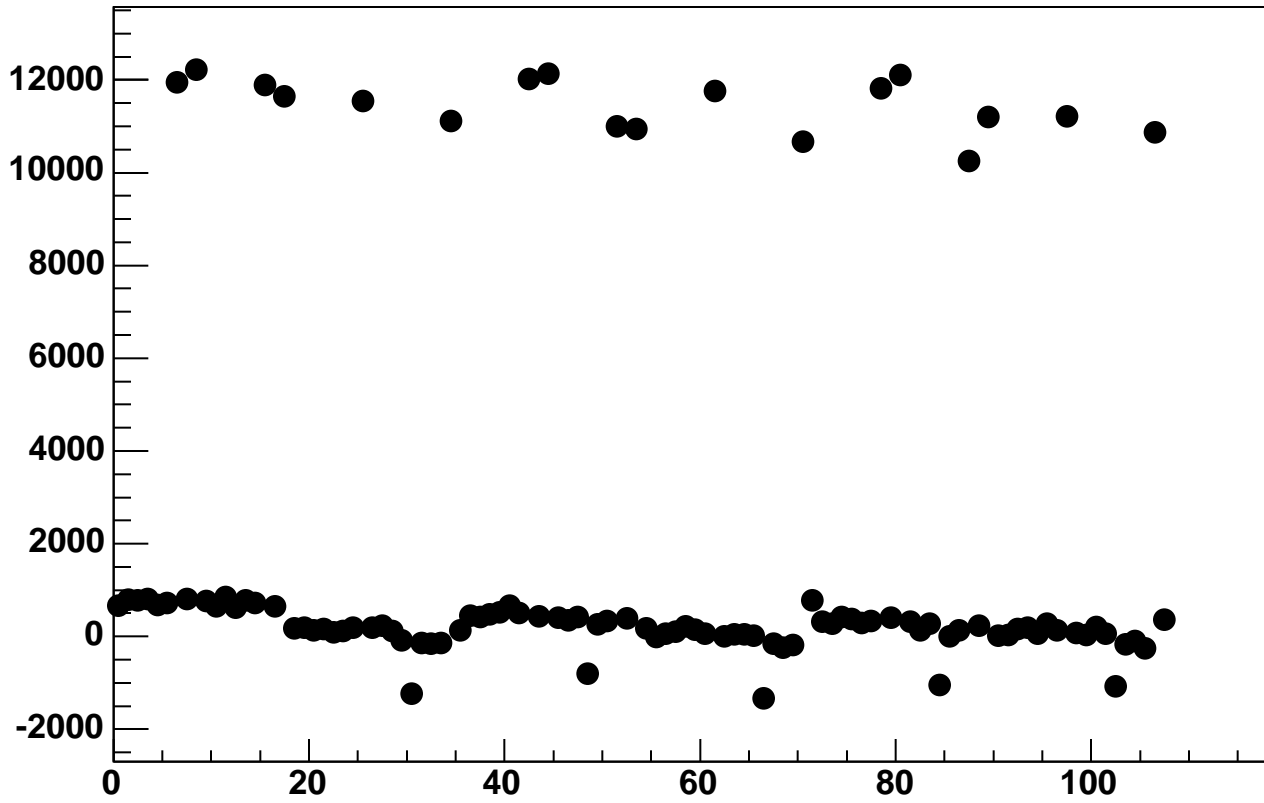
Enable 2, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



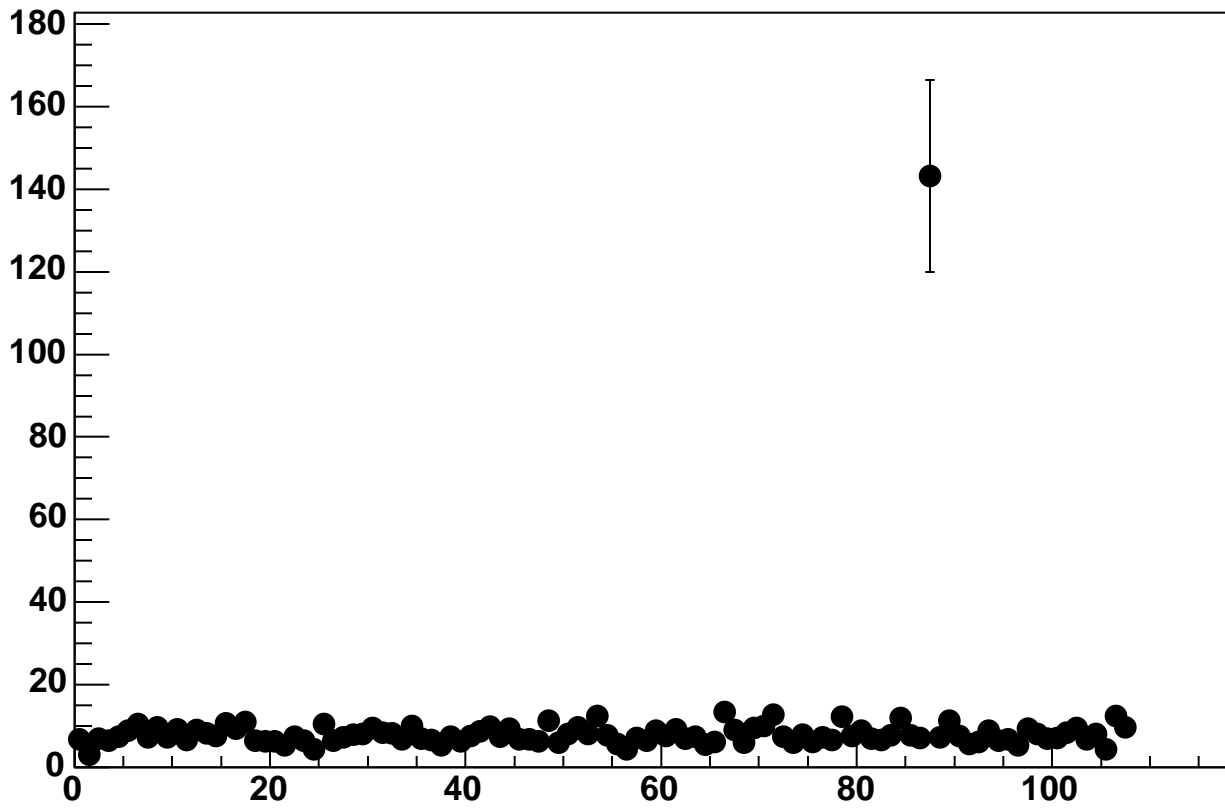
Enable 2, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



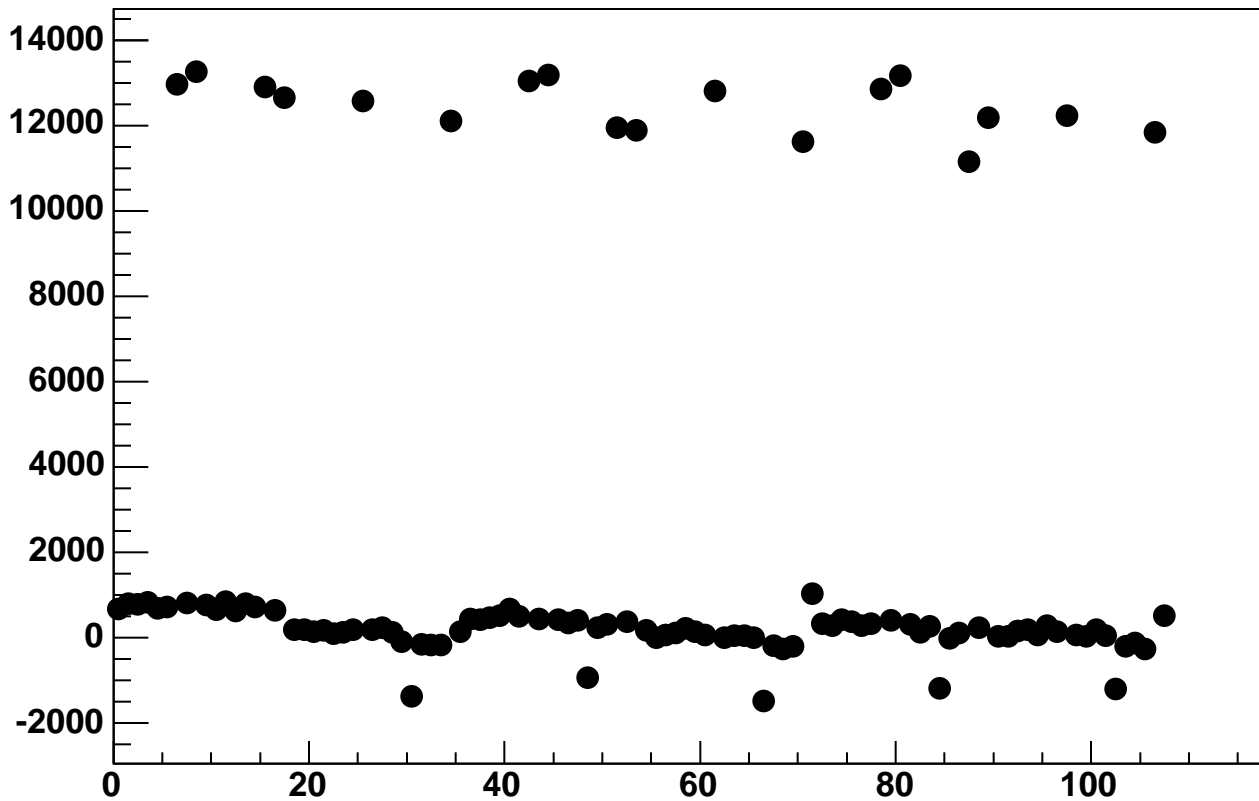
Enable 2, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



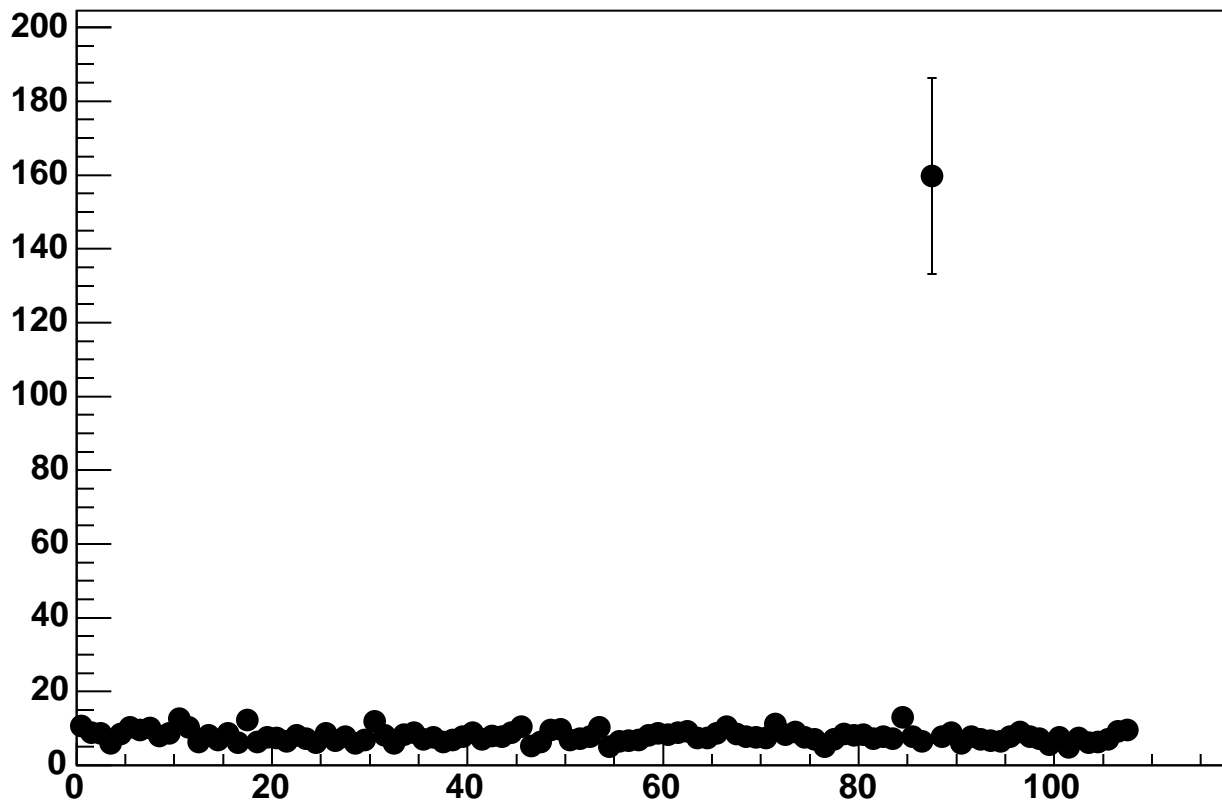
Enable 2, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



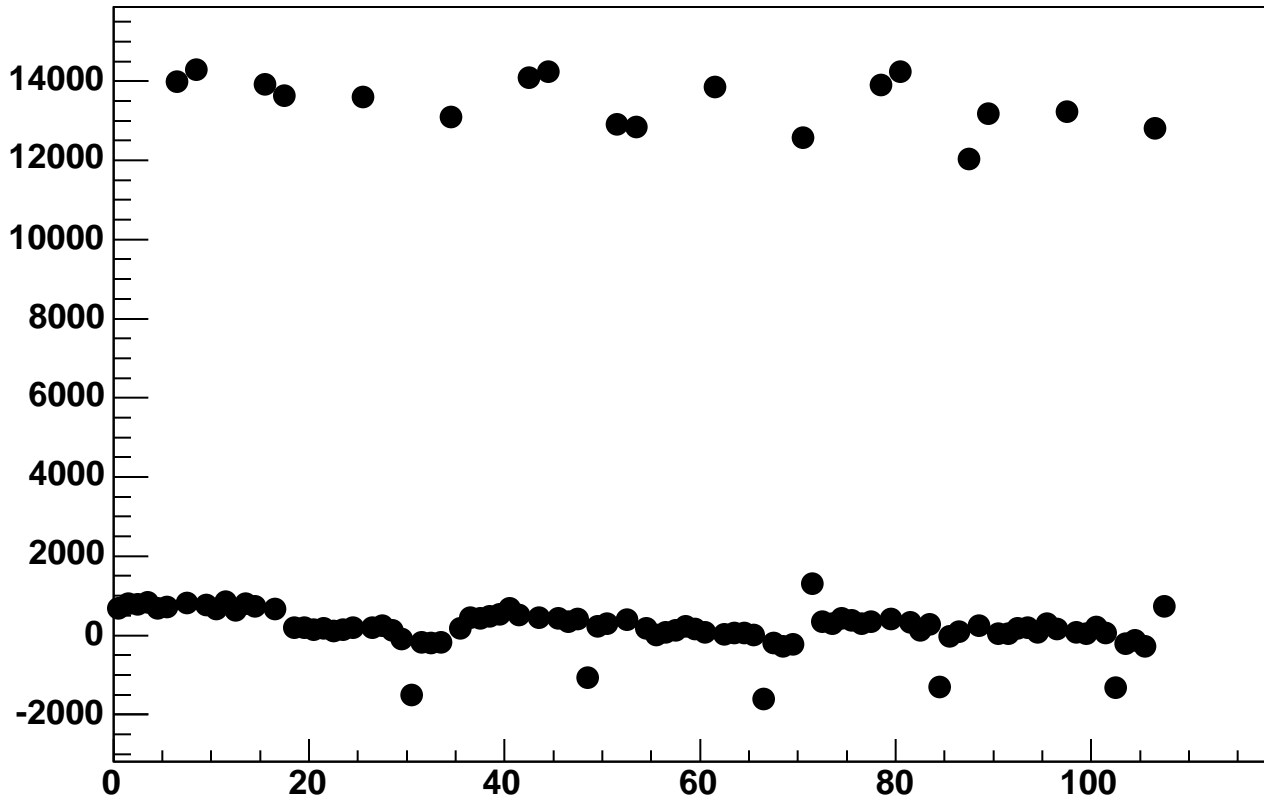
Enable 2, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



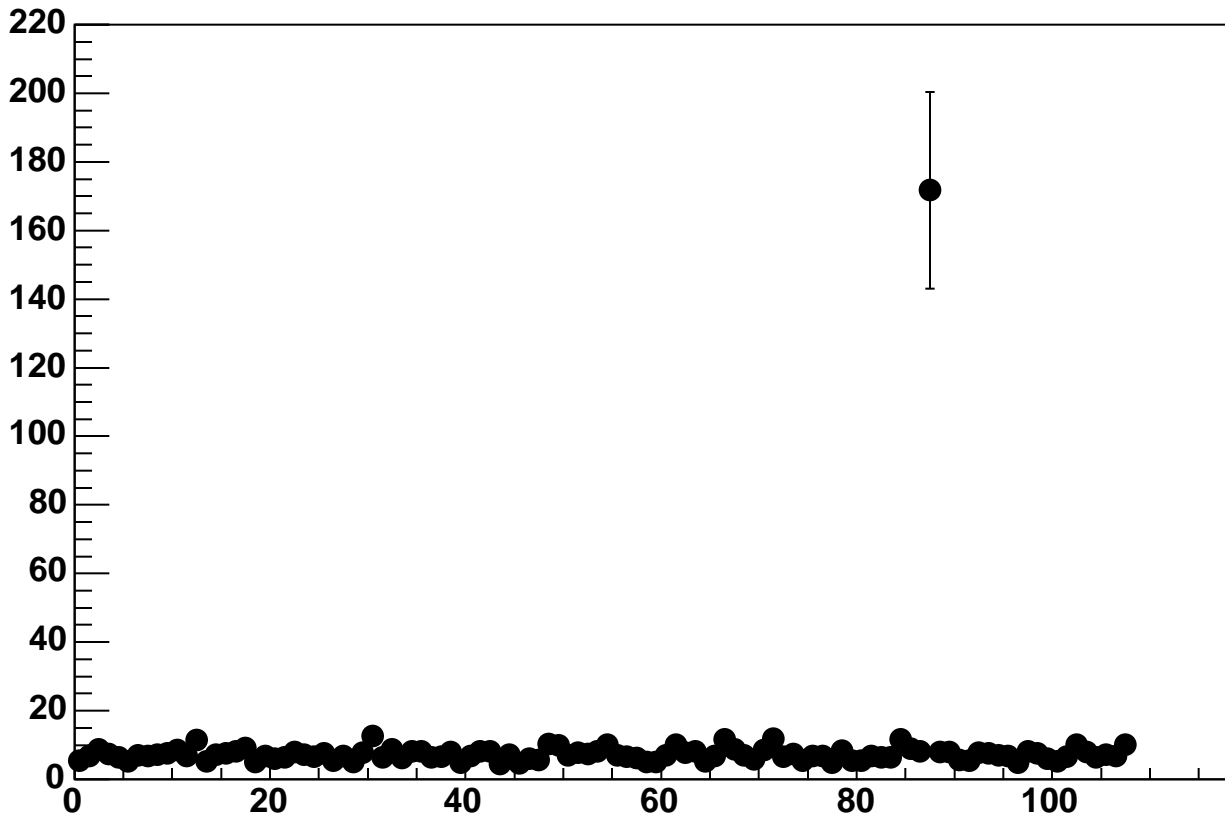
Enable 2, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



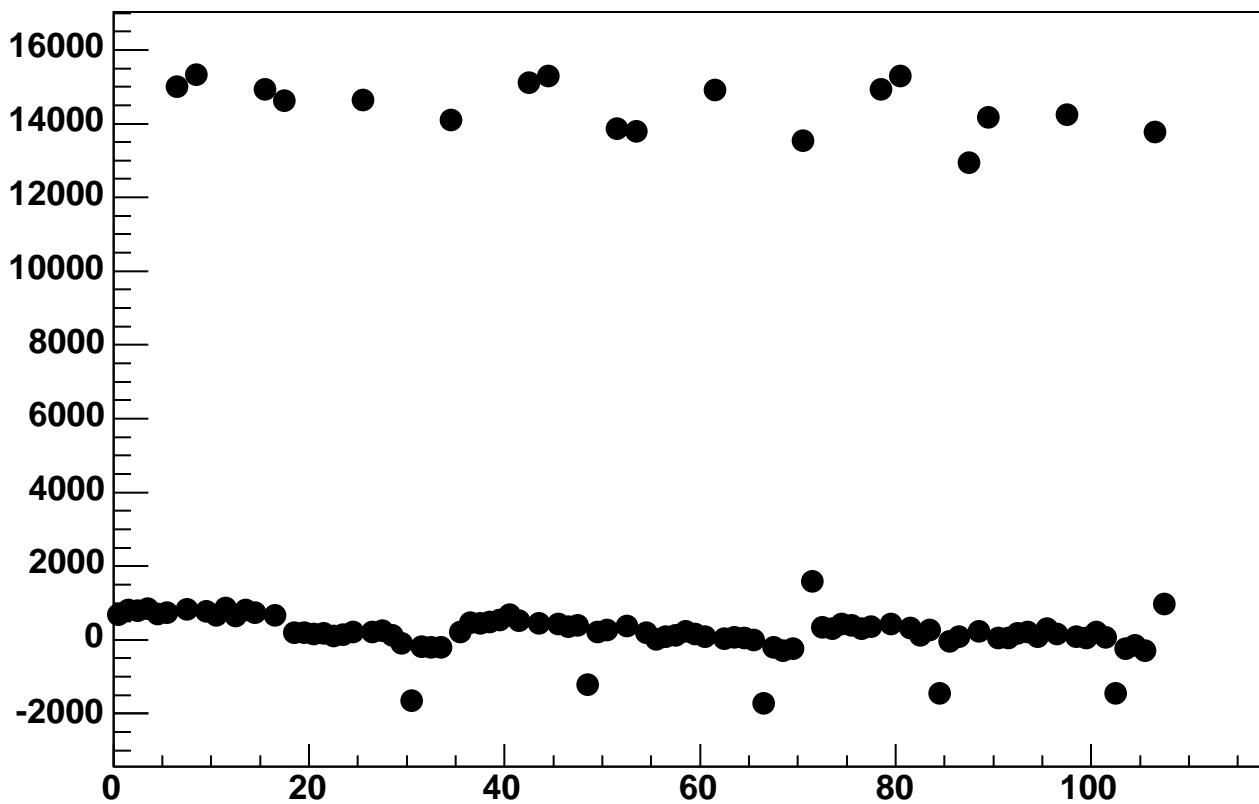
Enable 2, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



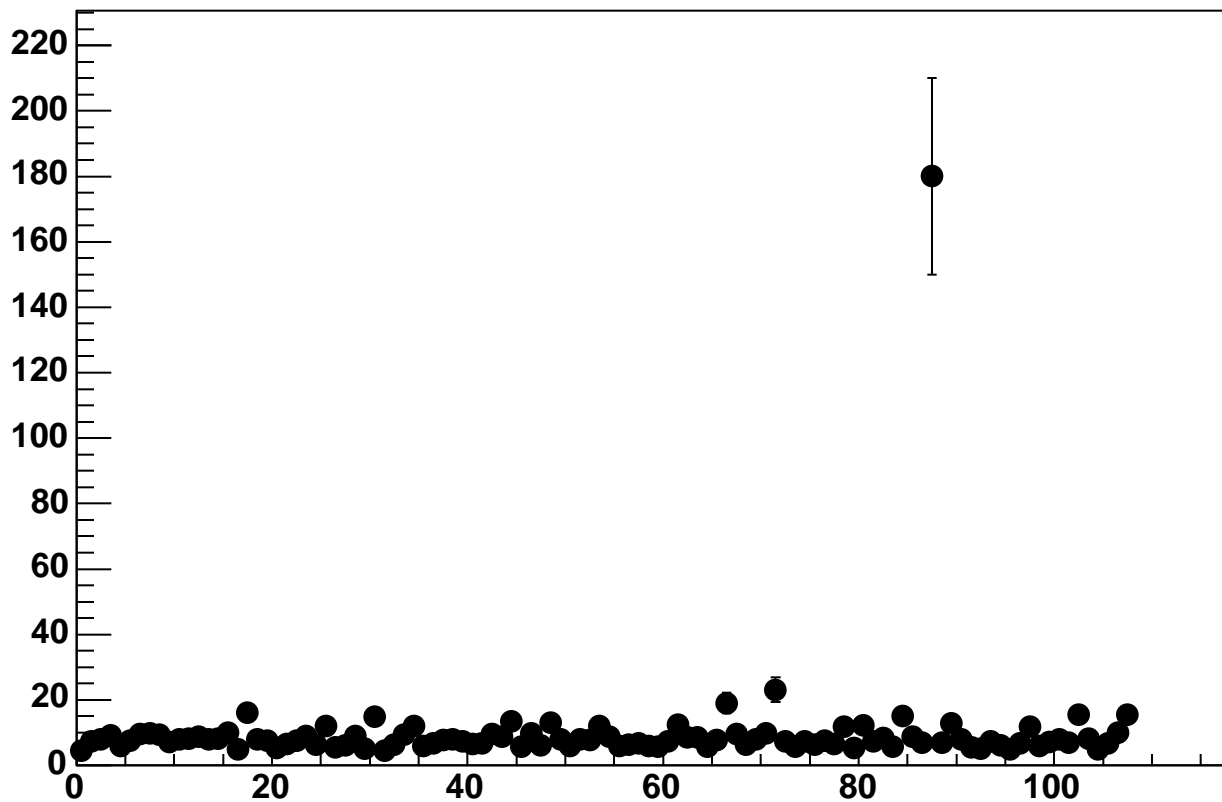
Enable 2, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



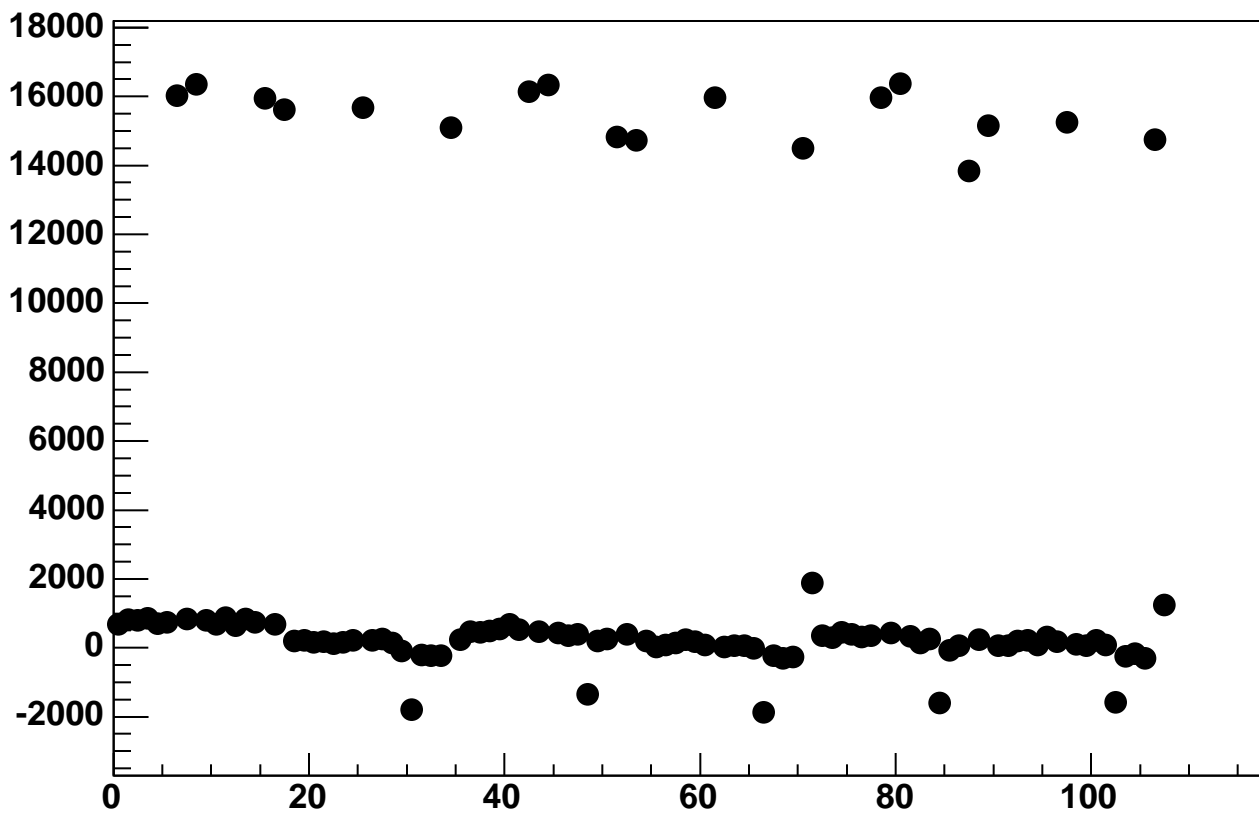
Enable 2, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



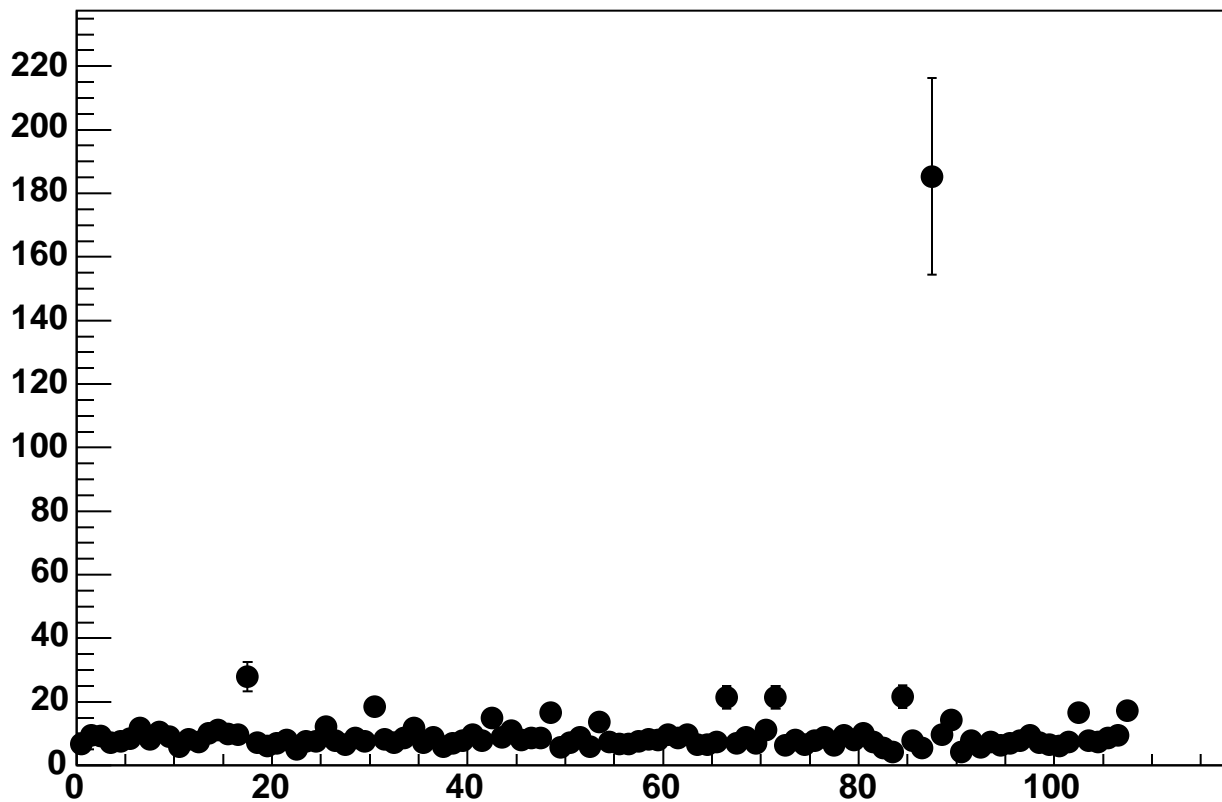
Enable 2, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



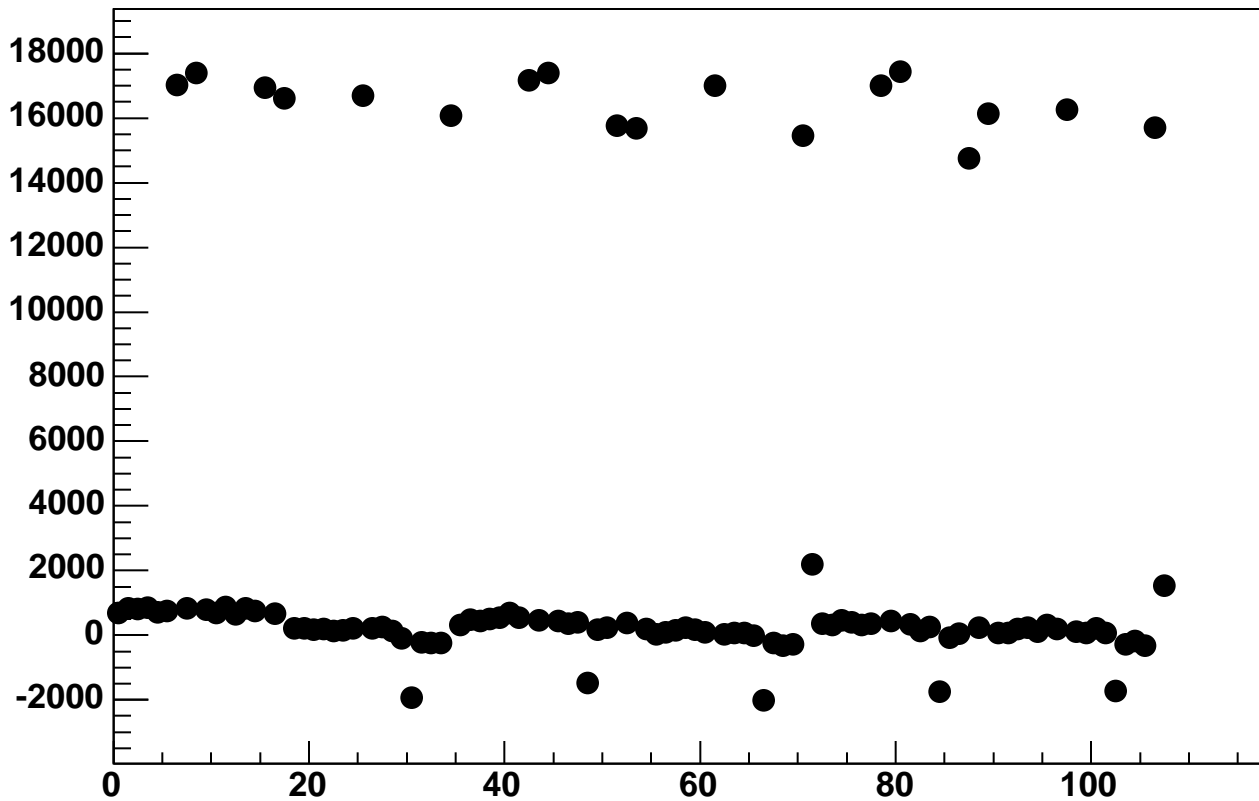
Enable 2, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



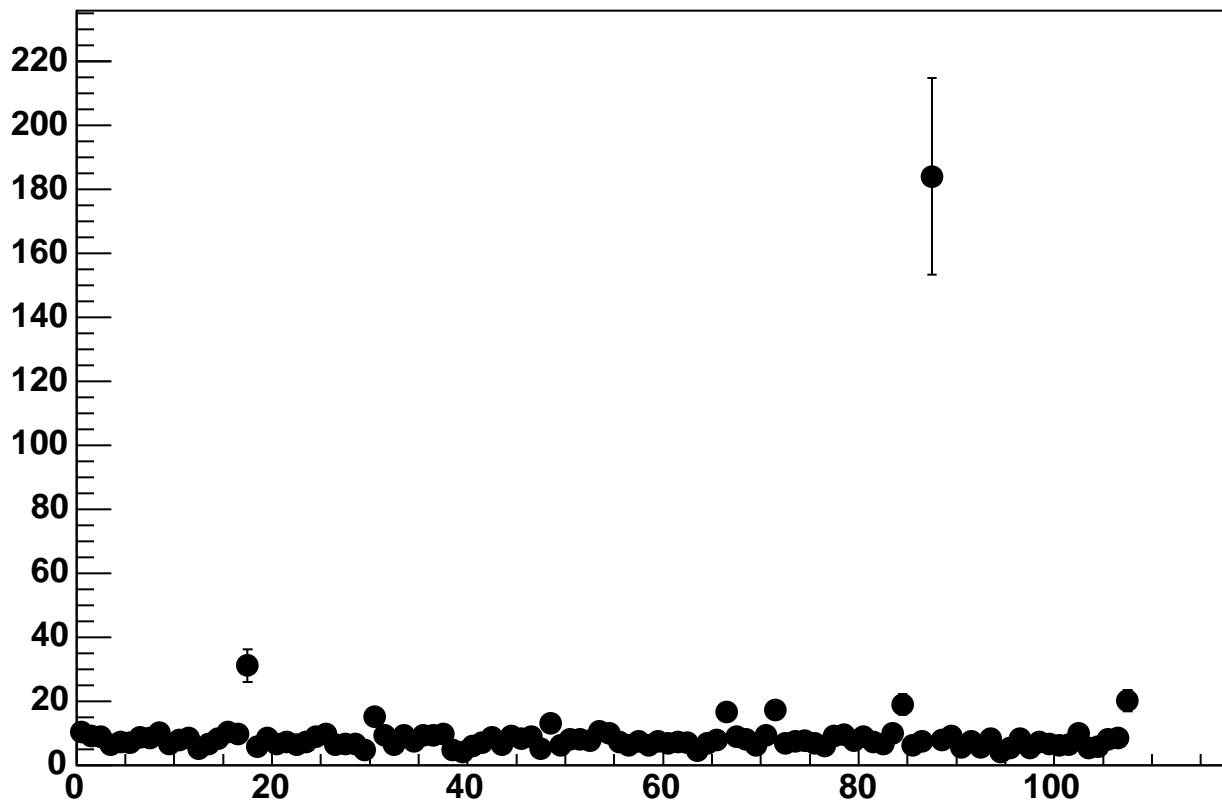
Enable 2, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



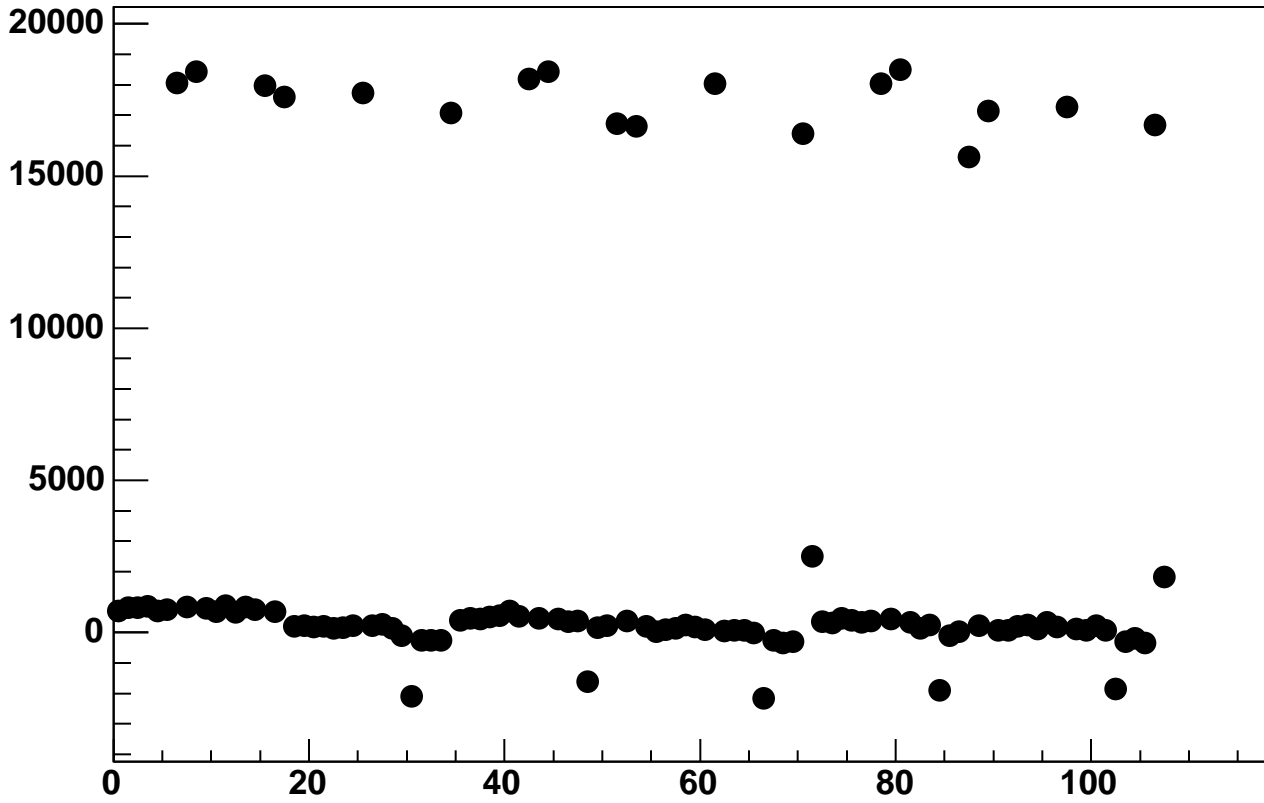
Enable 2, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



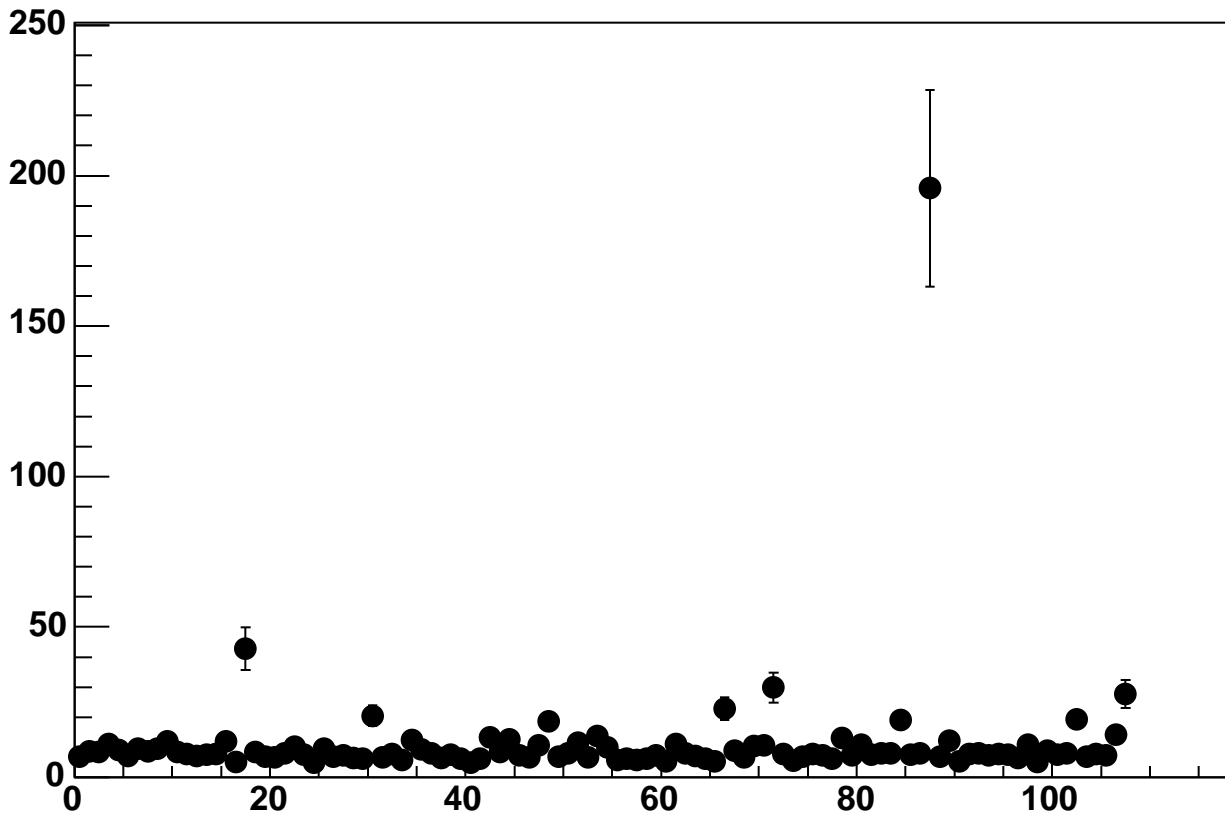
Enable 2, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



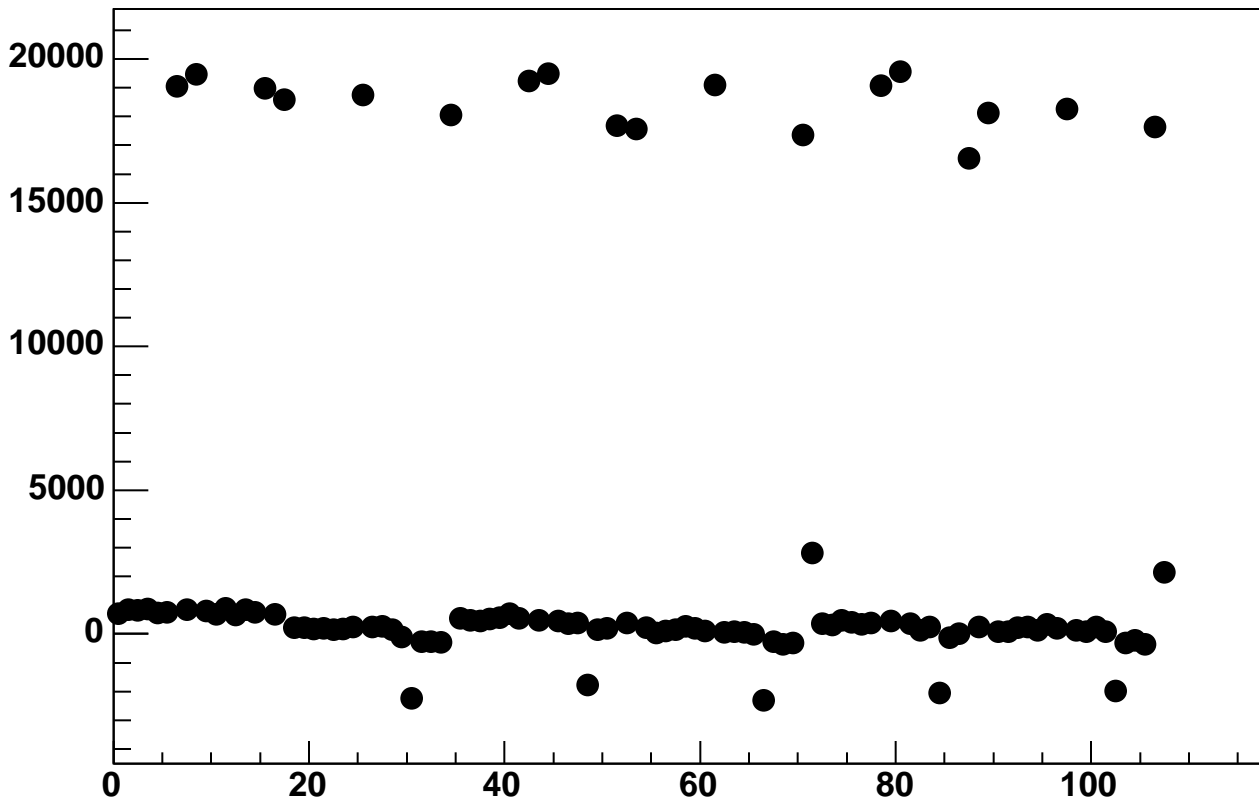
Enable 2, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



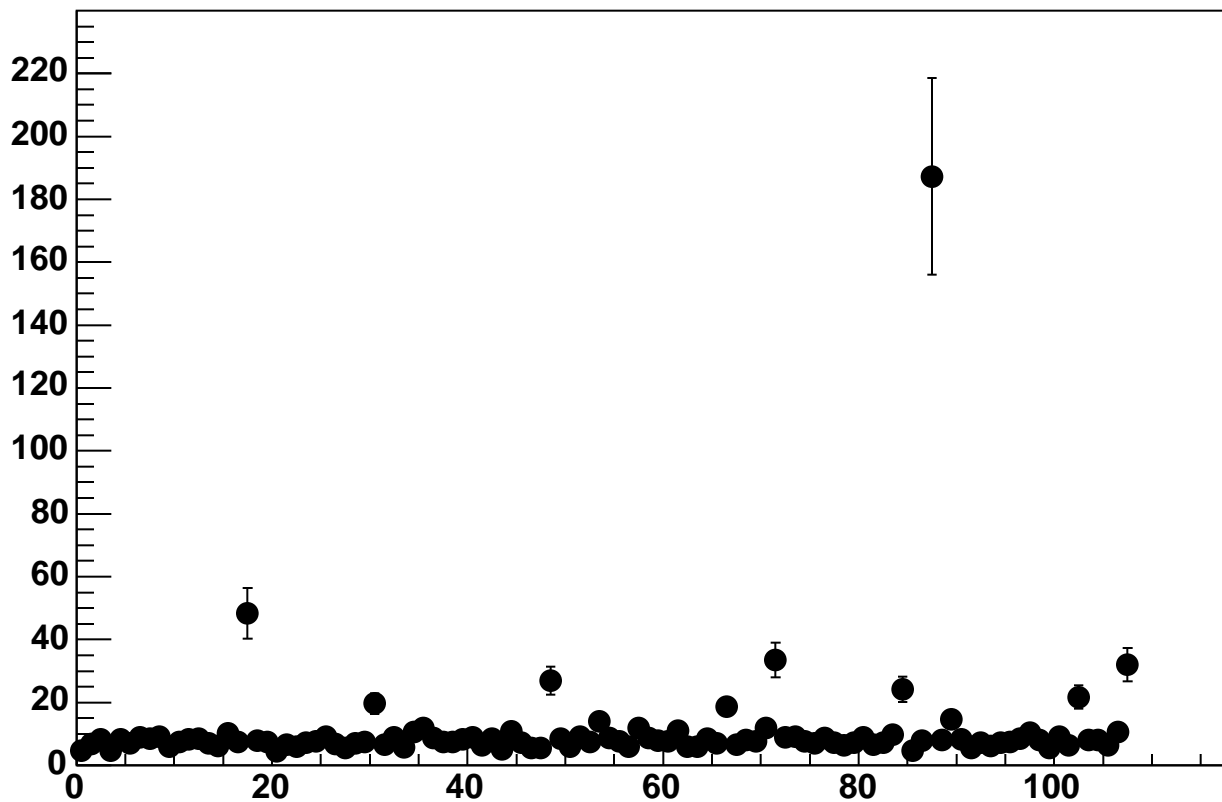
Enable 2, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



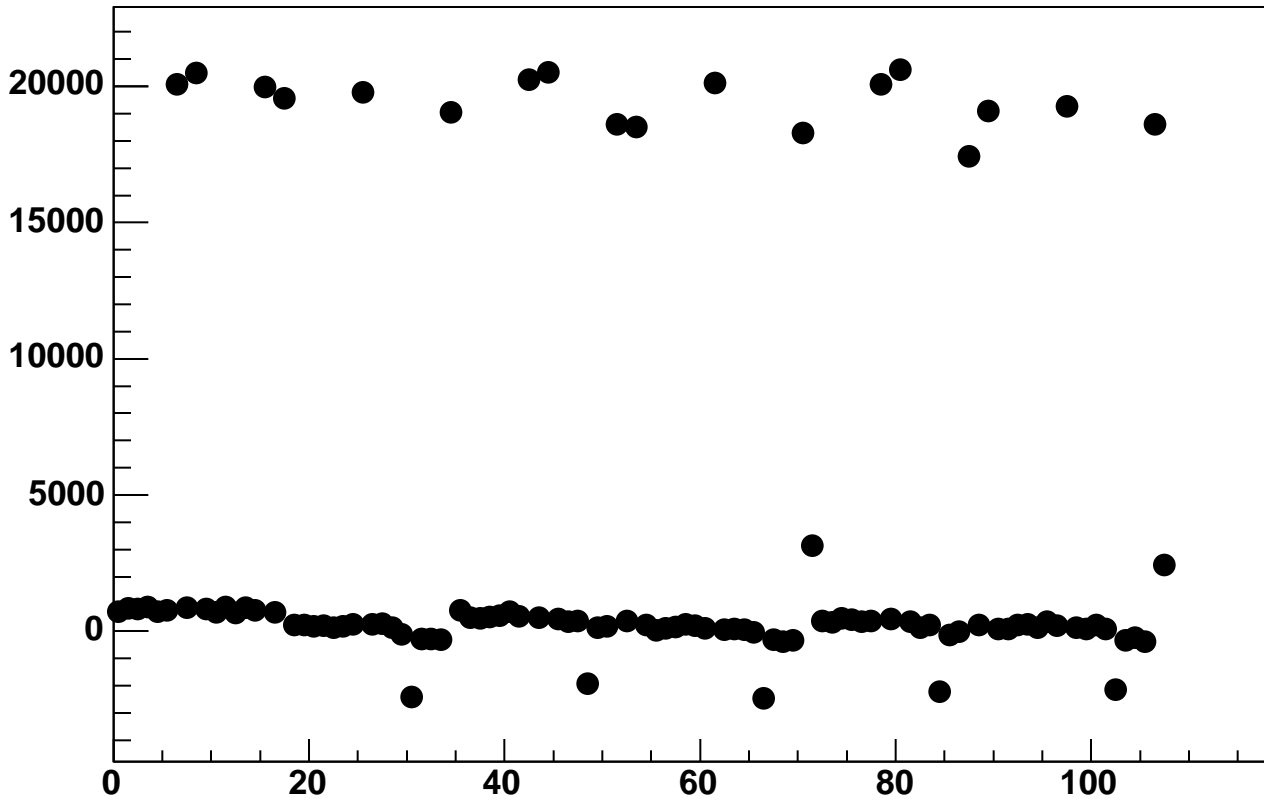
Enable 2, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



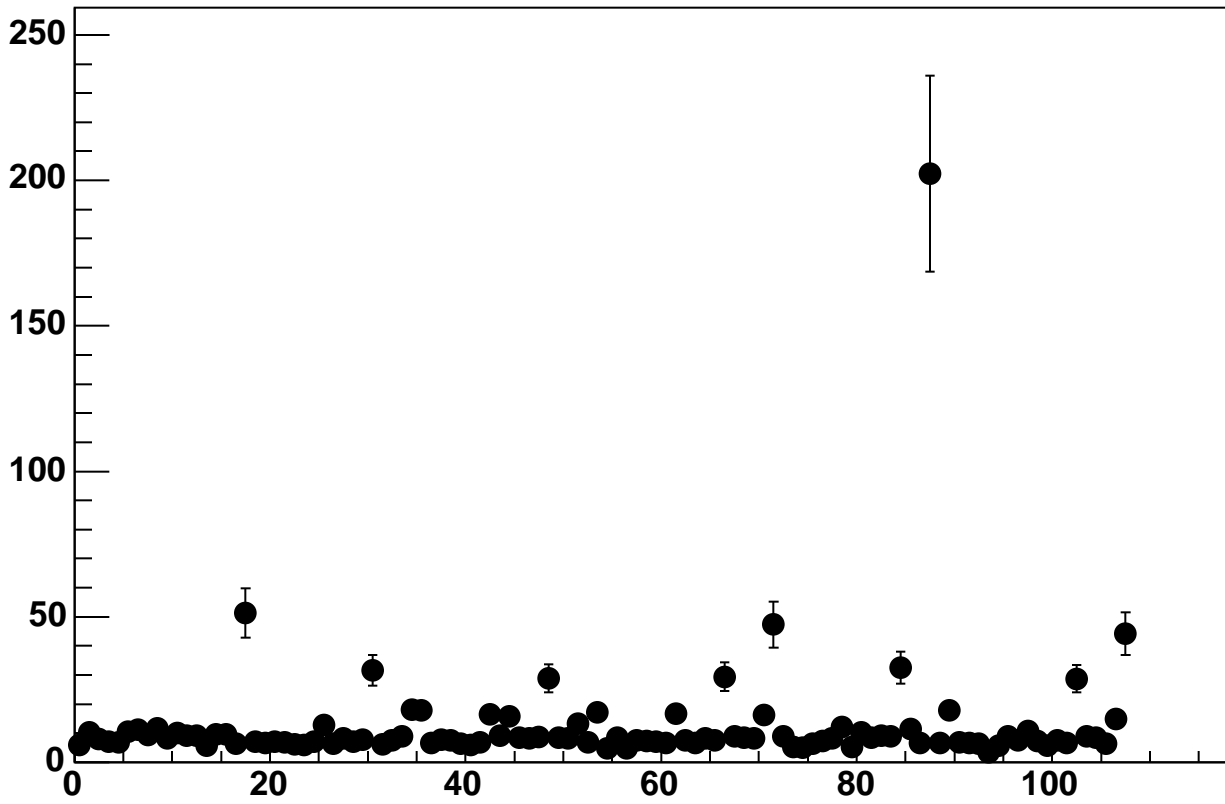
Enable 2, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



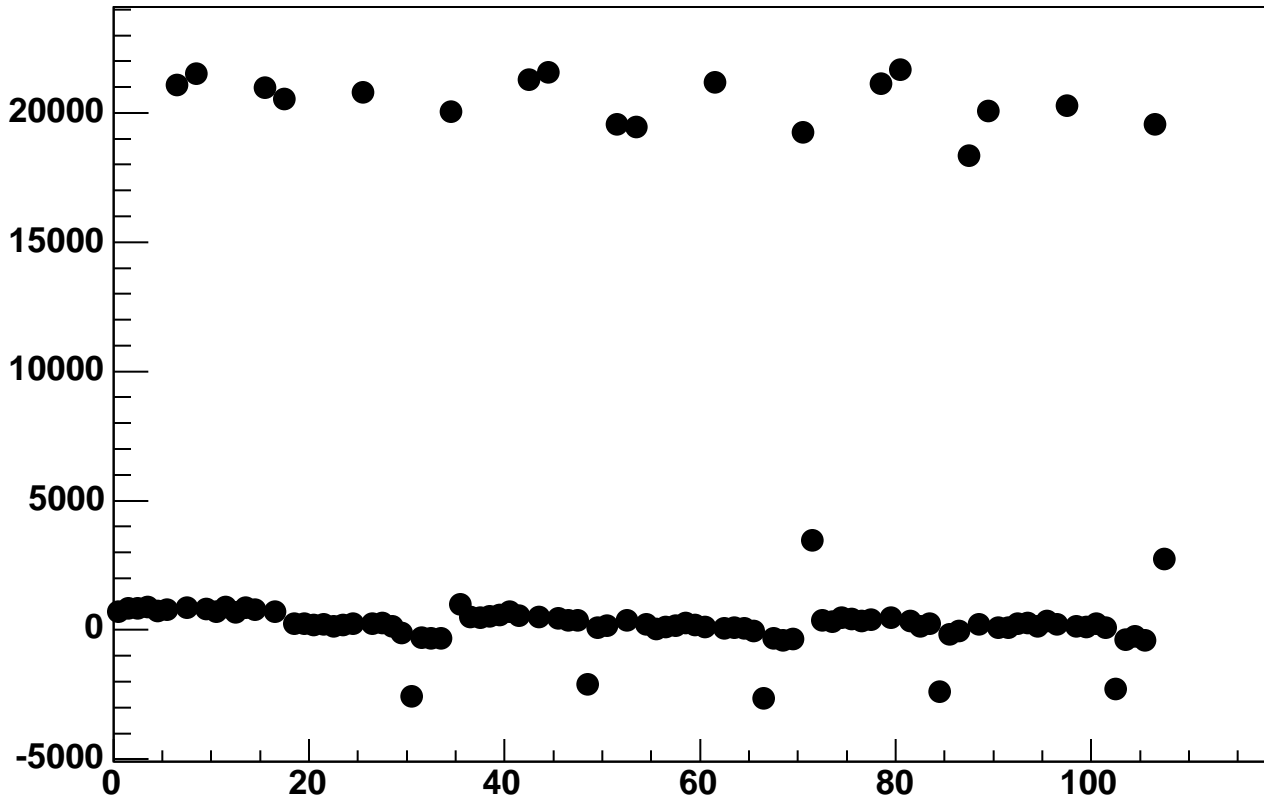
Enable 2, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



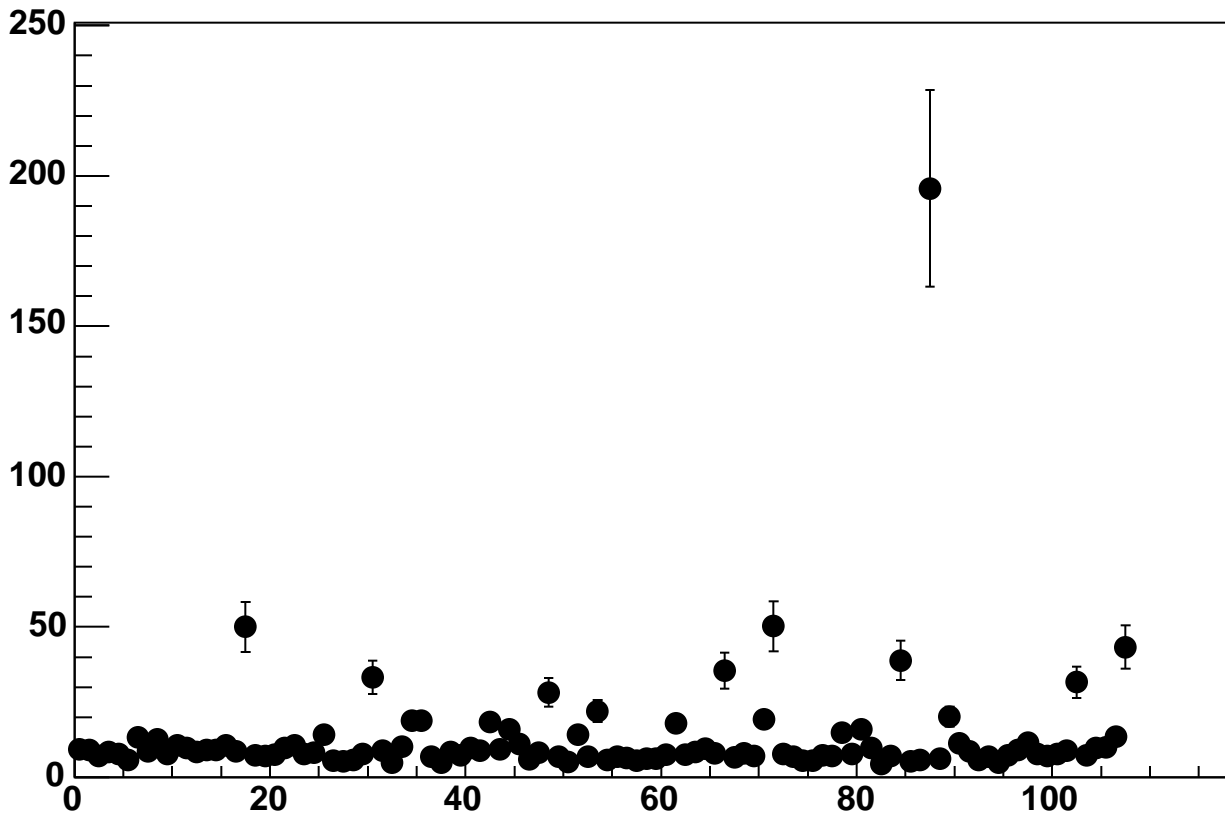
Enable 2, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



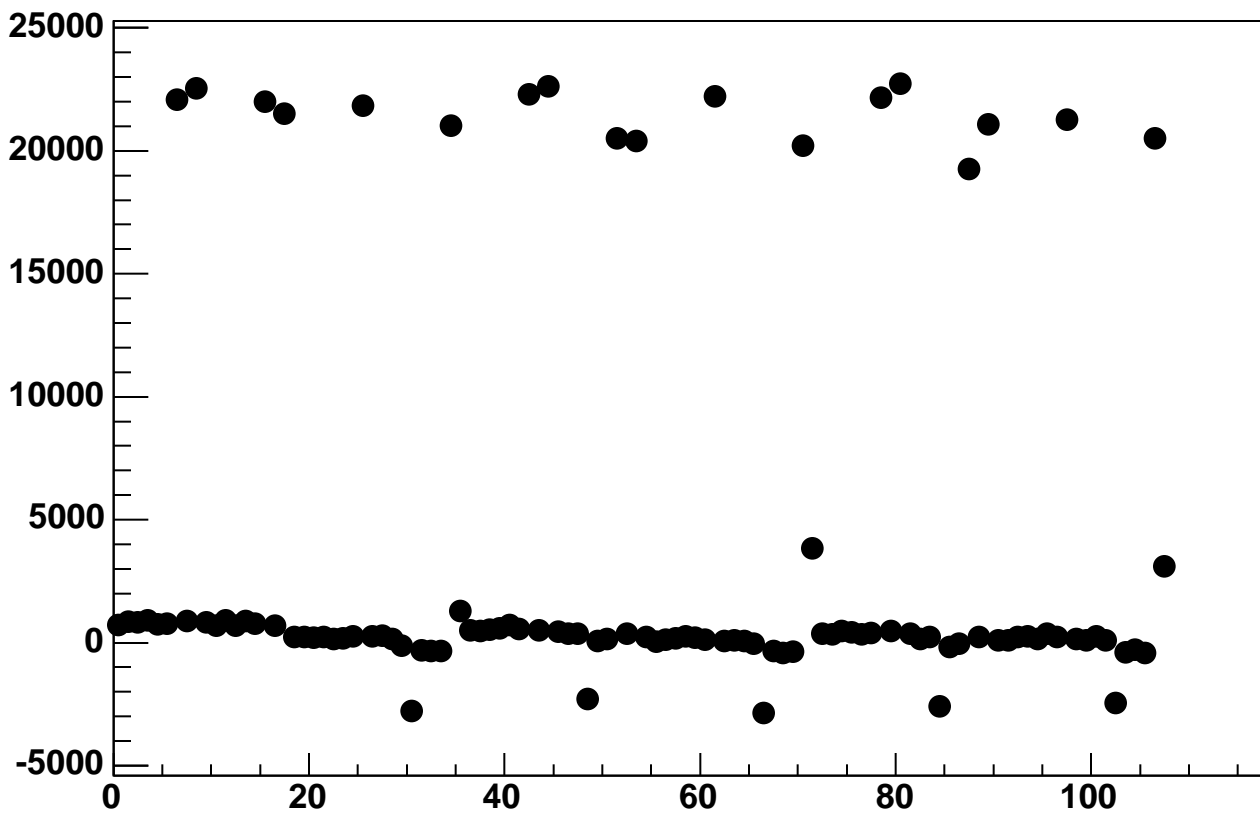
Enable 2, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



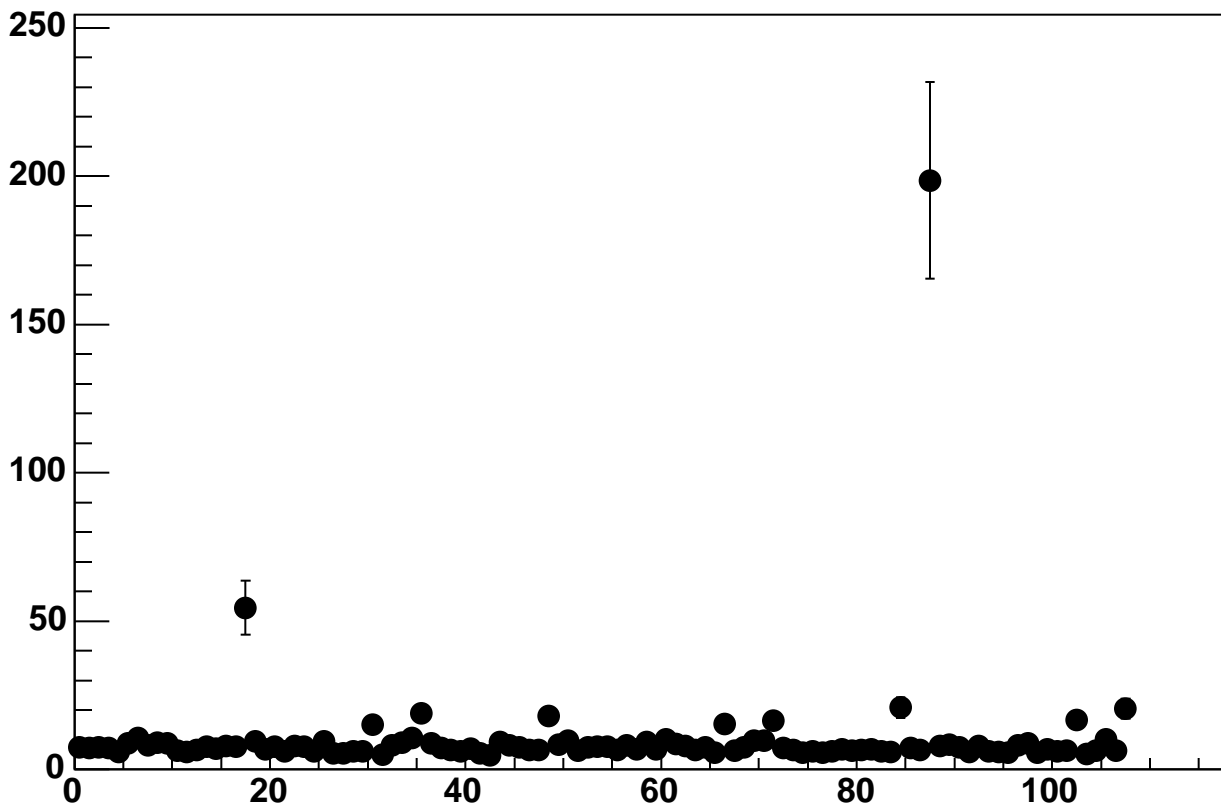
Enable 2, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



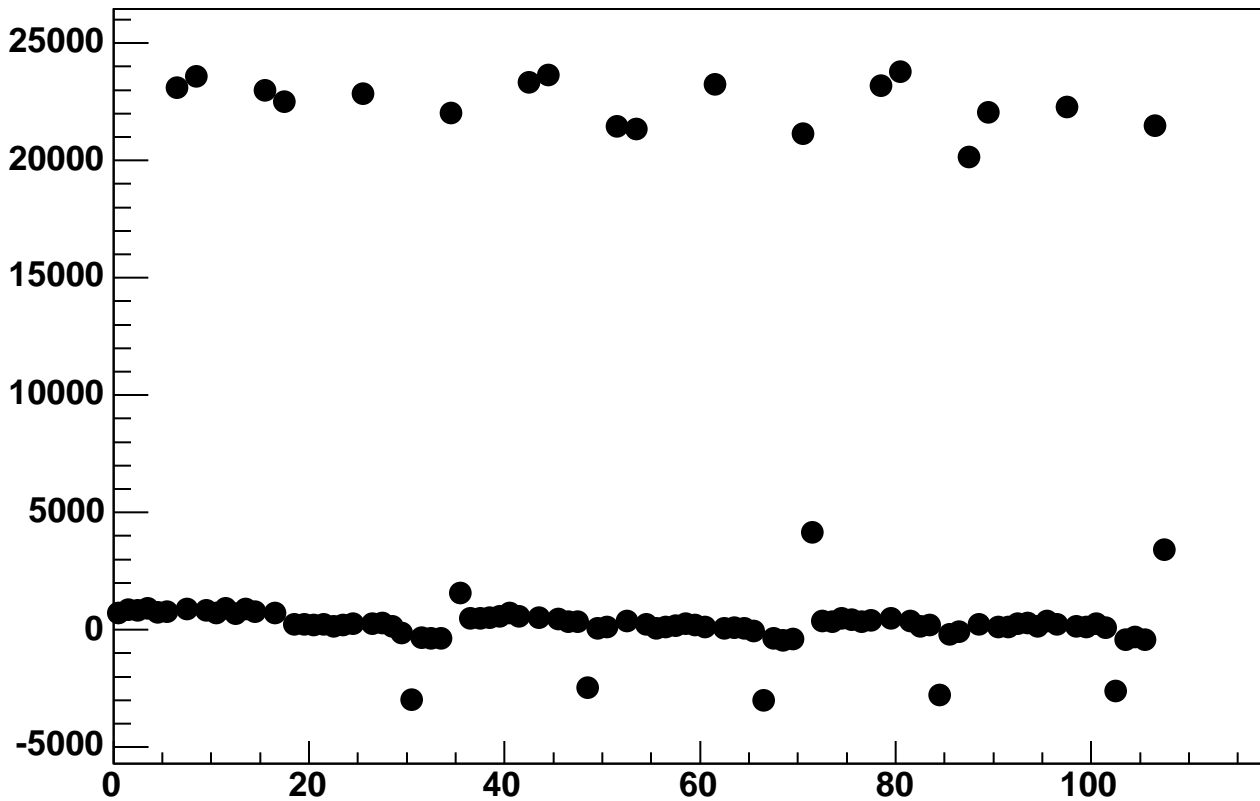
Enable 2, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



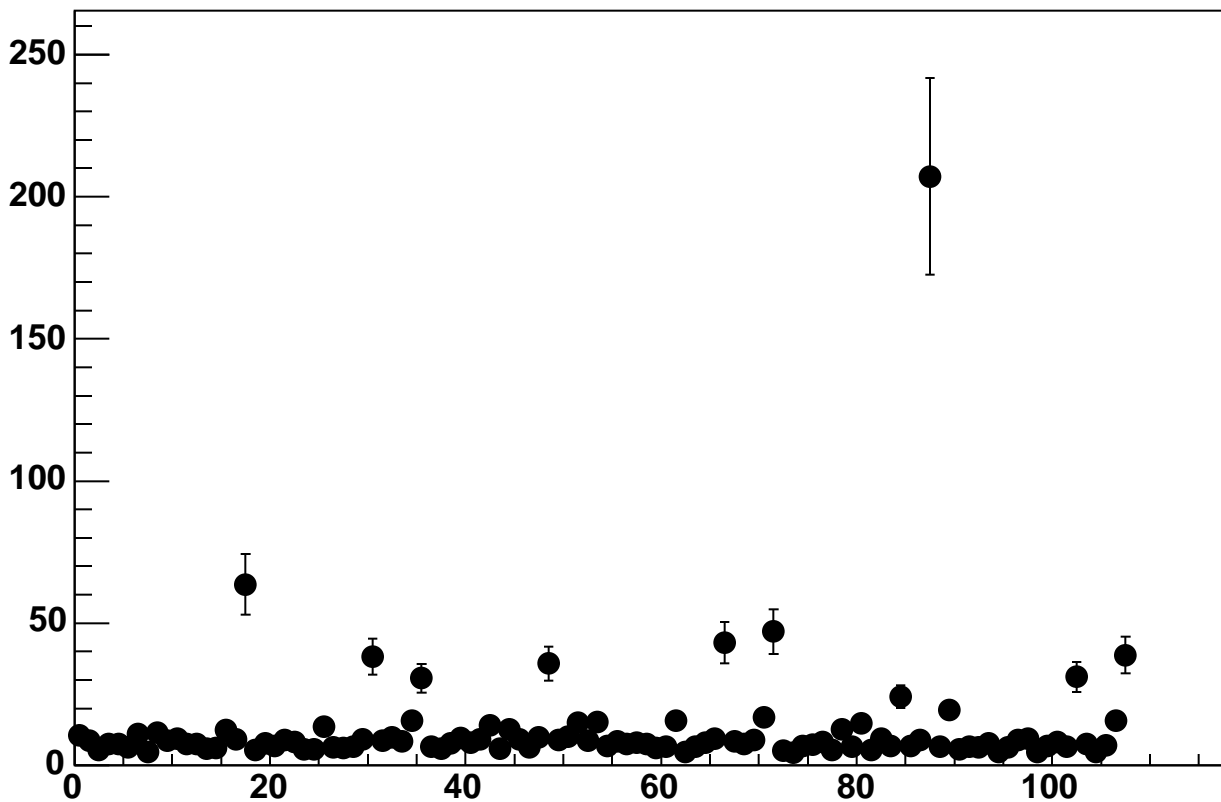
Enable 2, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



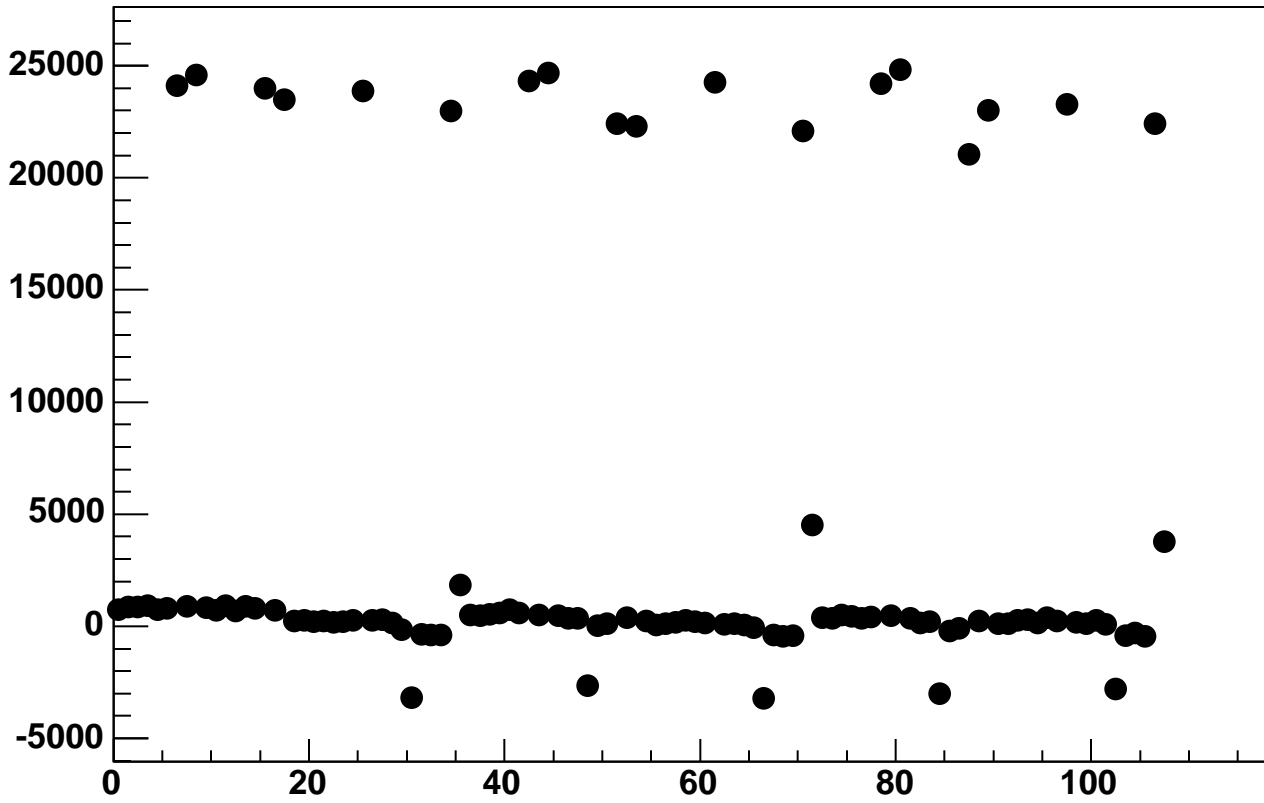
Enable 2, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



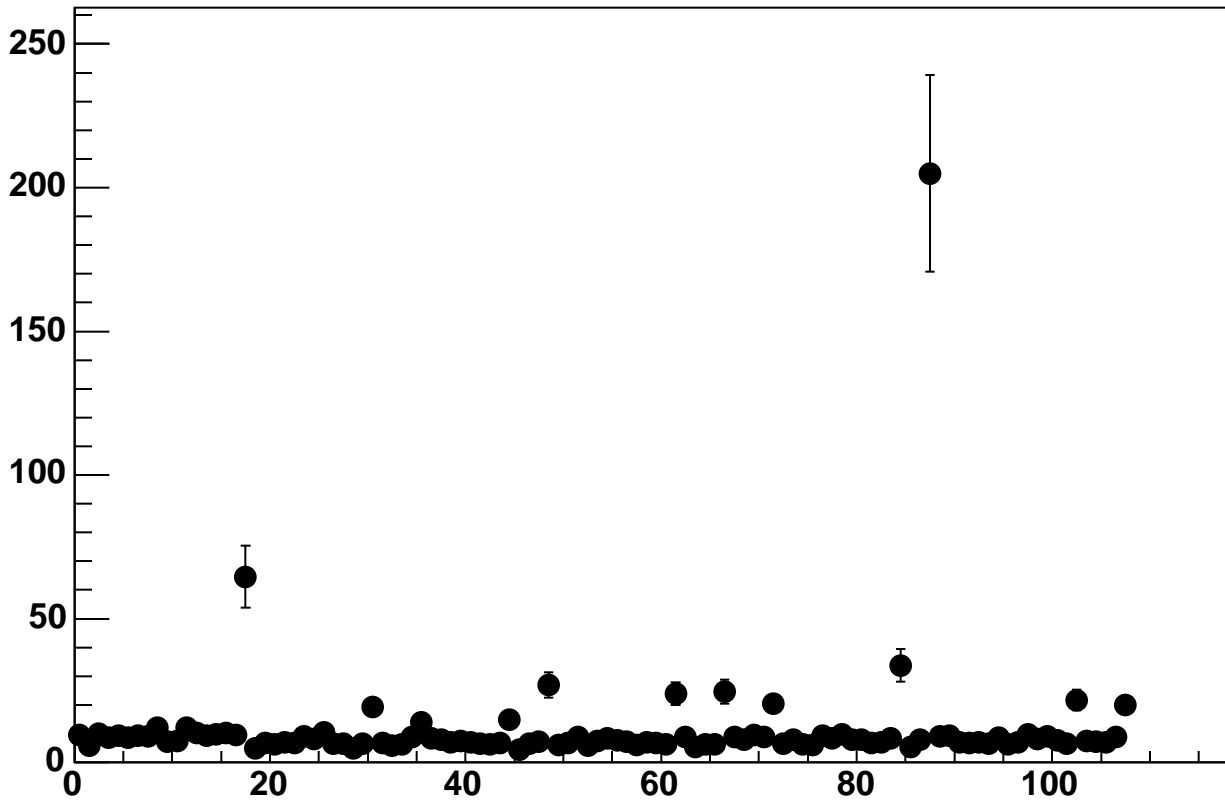
Enable 2, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



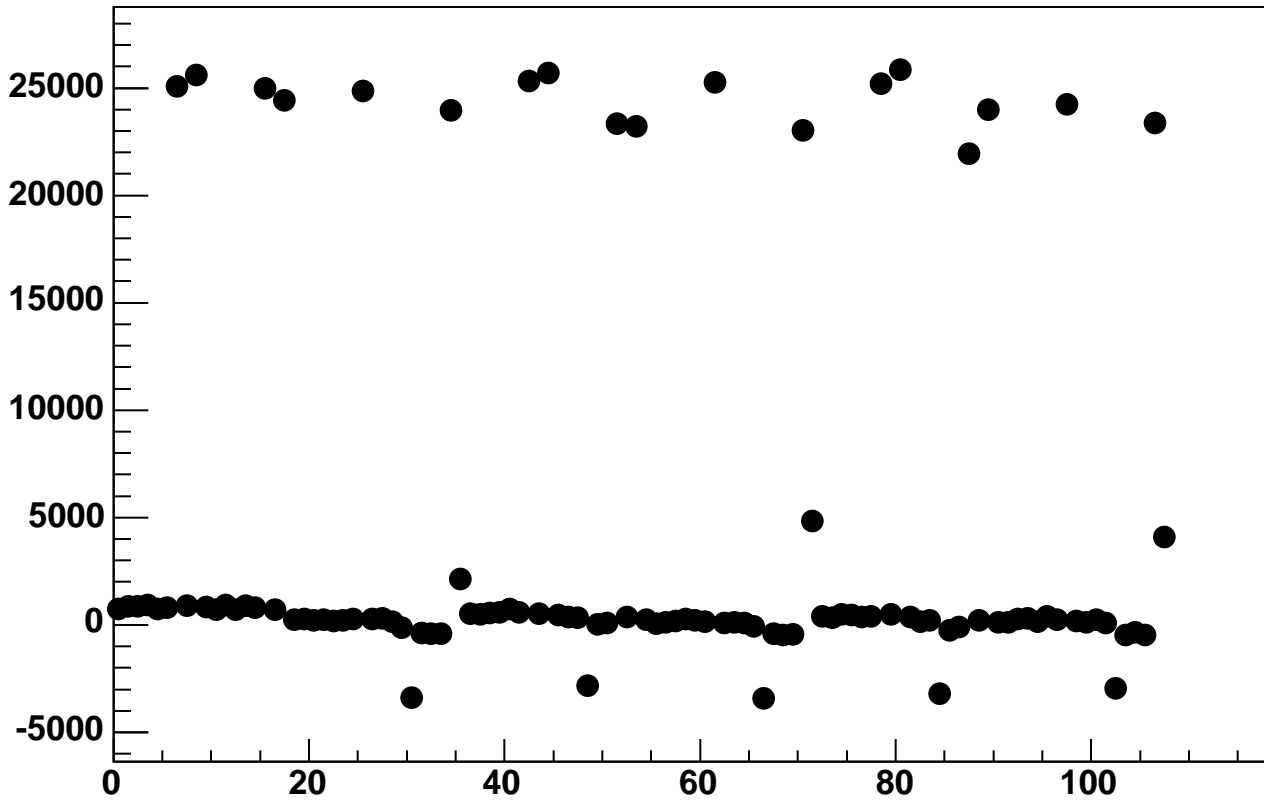
Enable 2, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



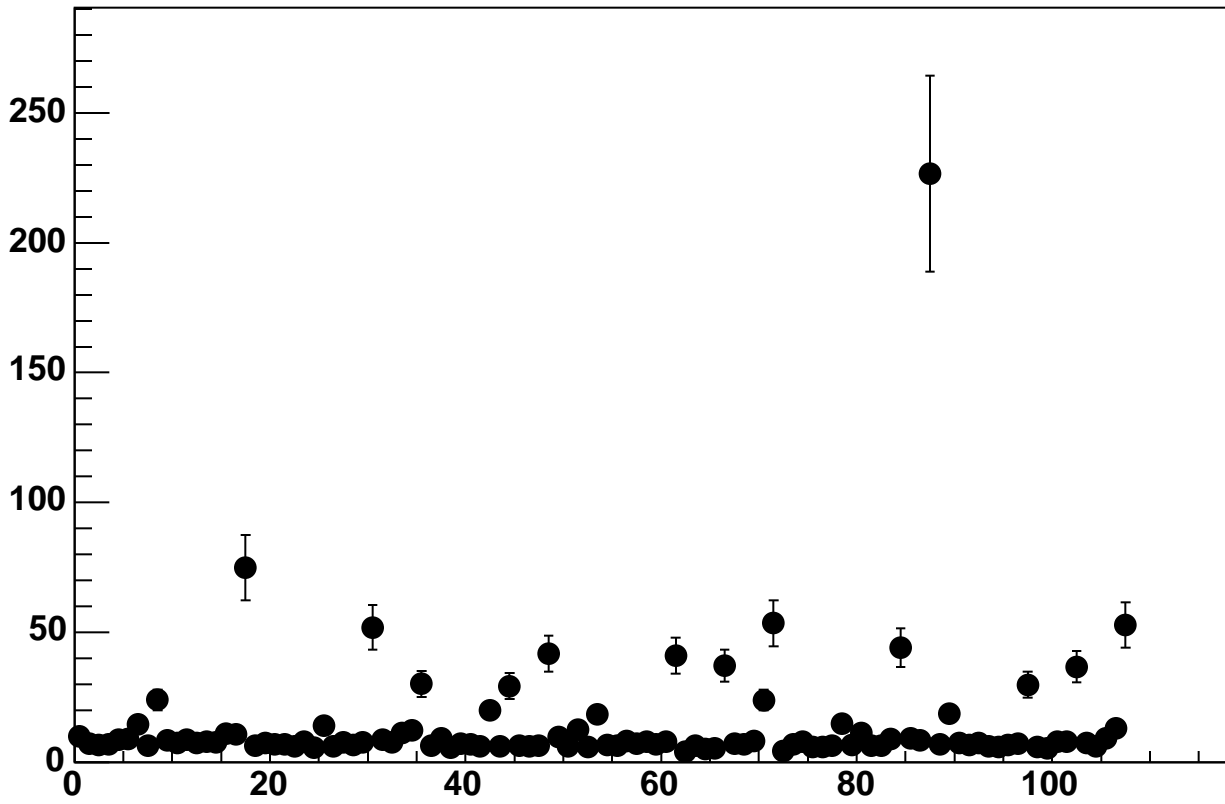
Enable 2, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



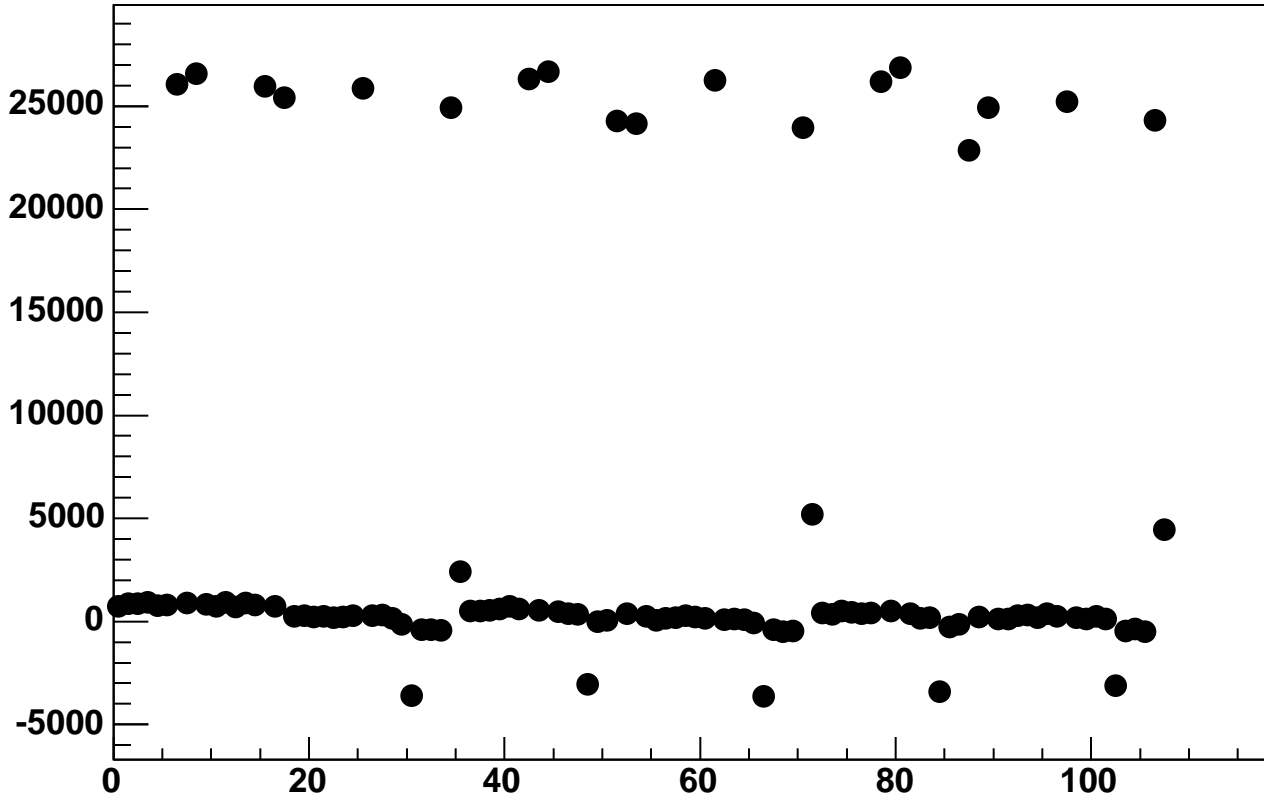
Enable 2, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



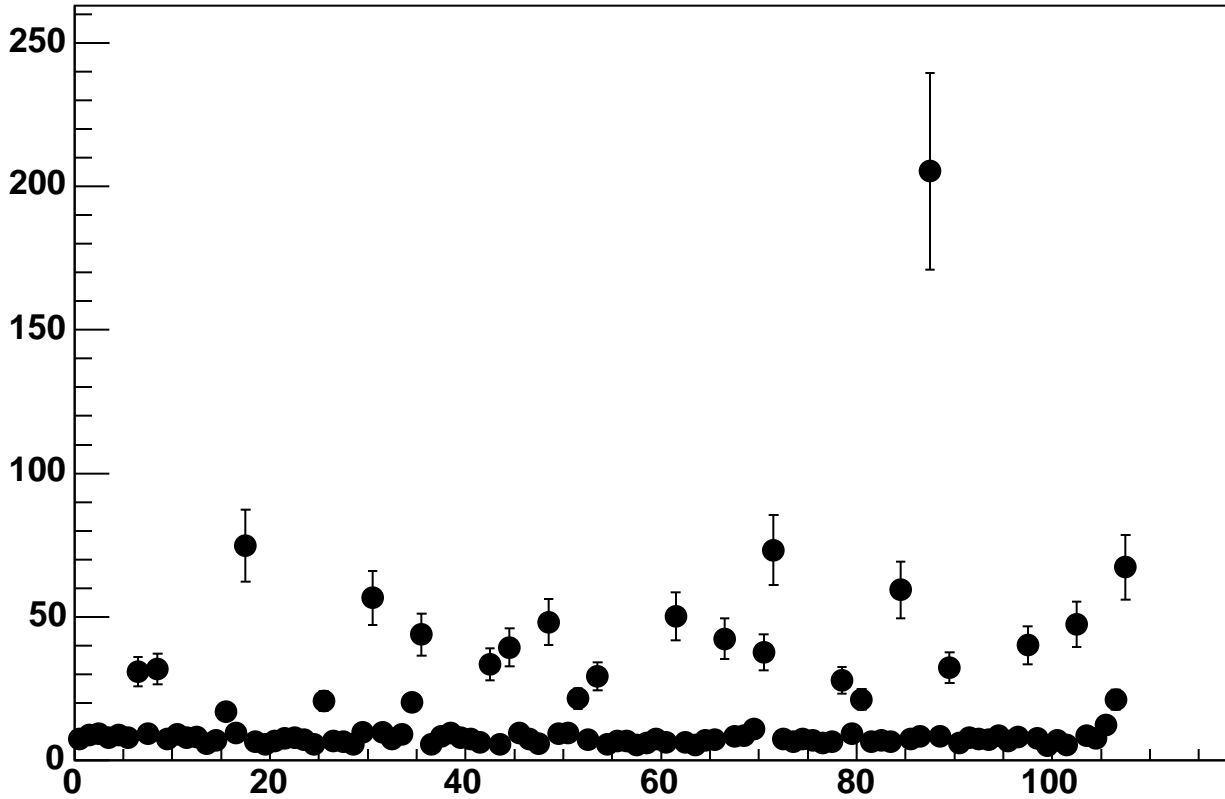
Enable 2, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



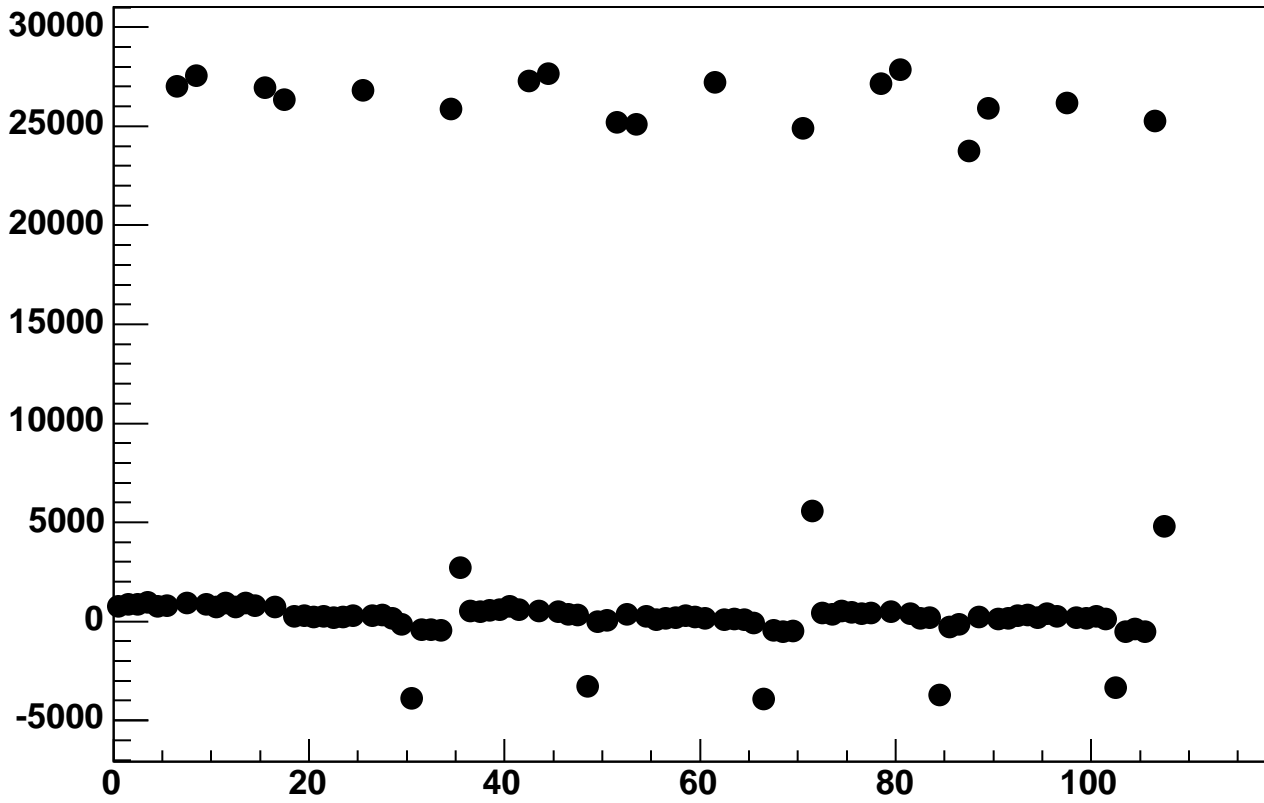
Enable 2, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



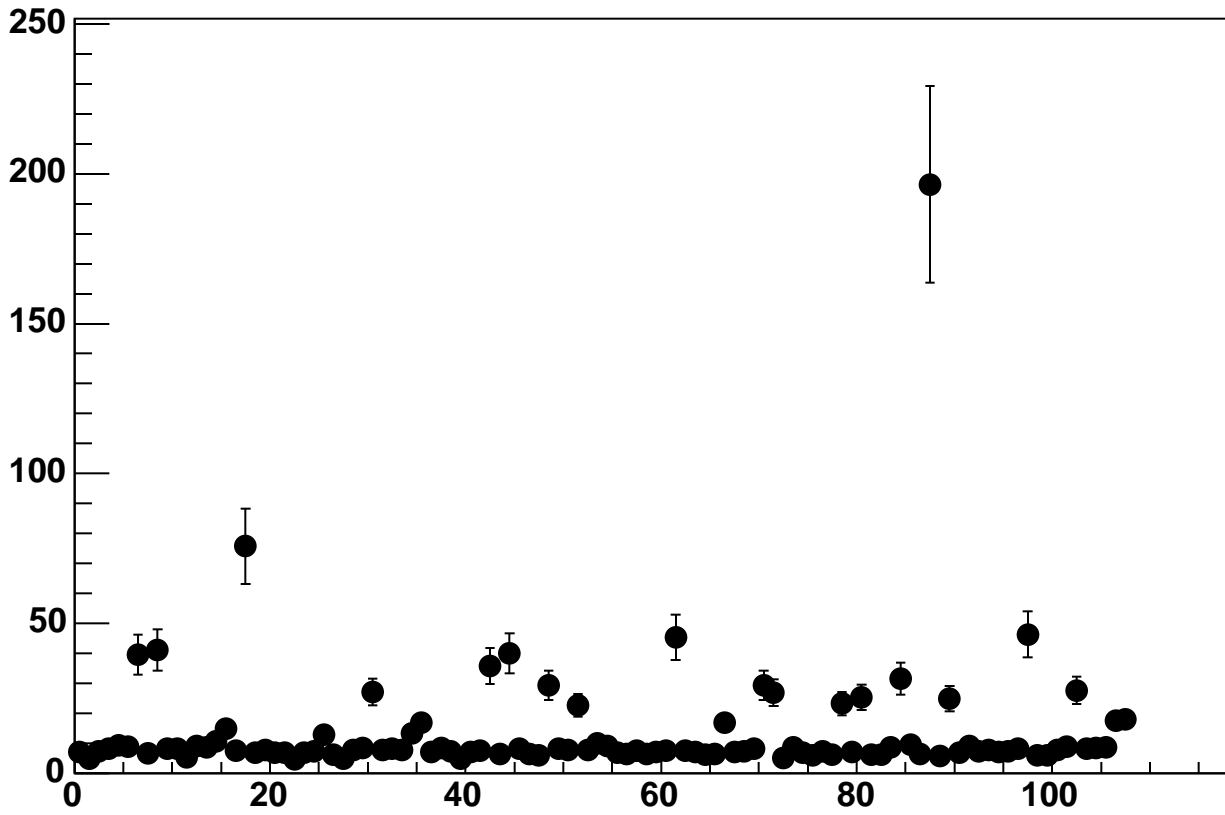
Enable 2, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



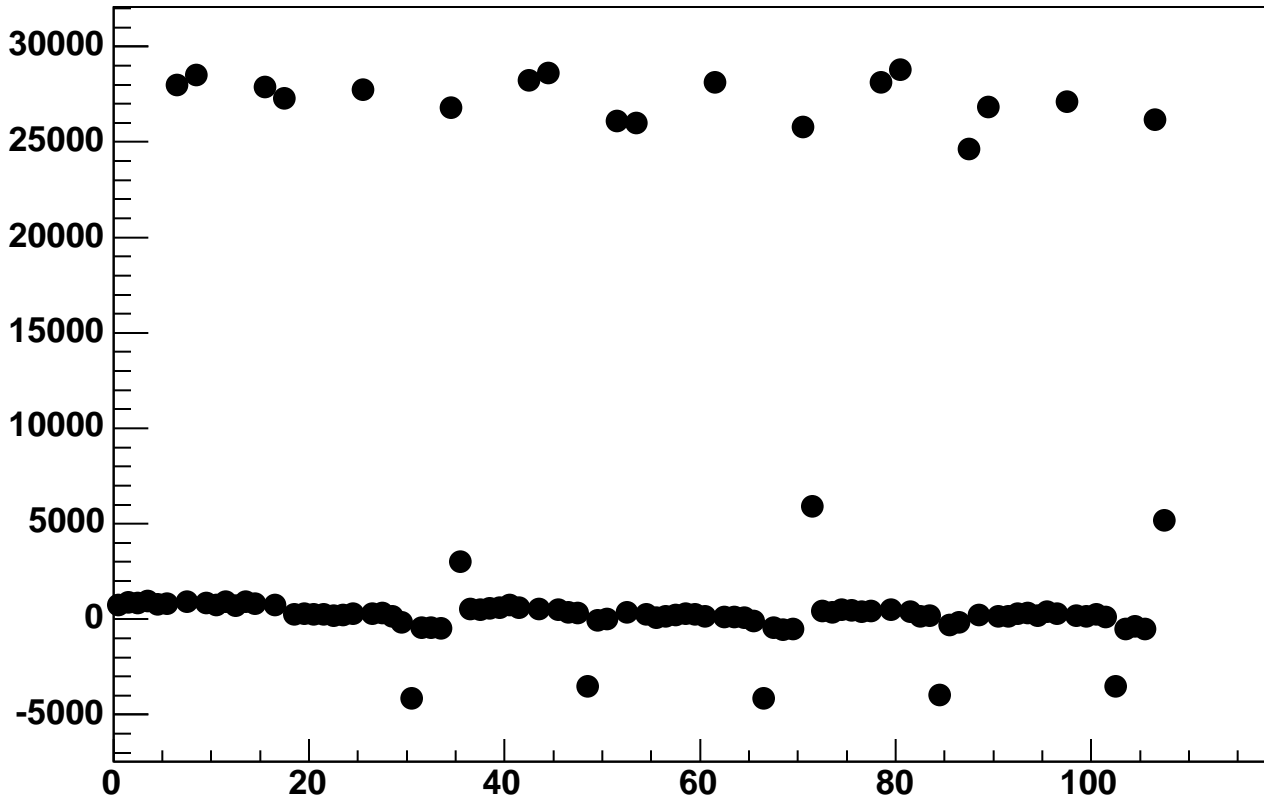
Enable 2, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



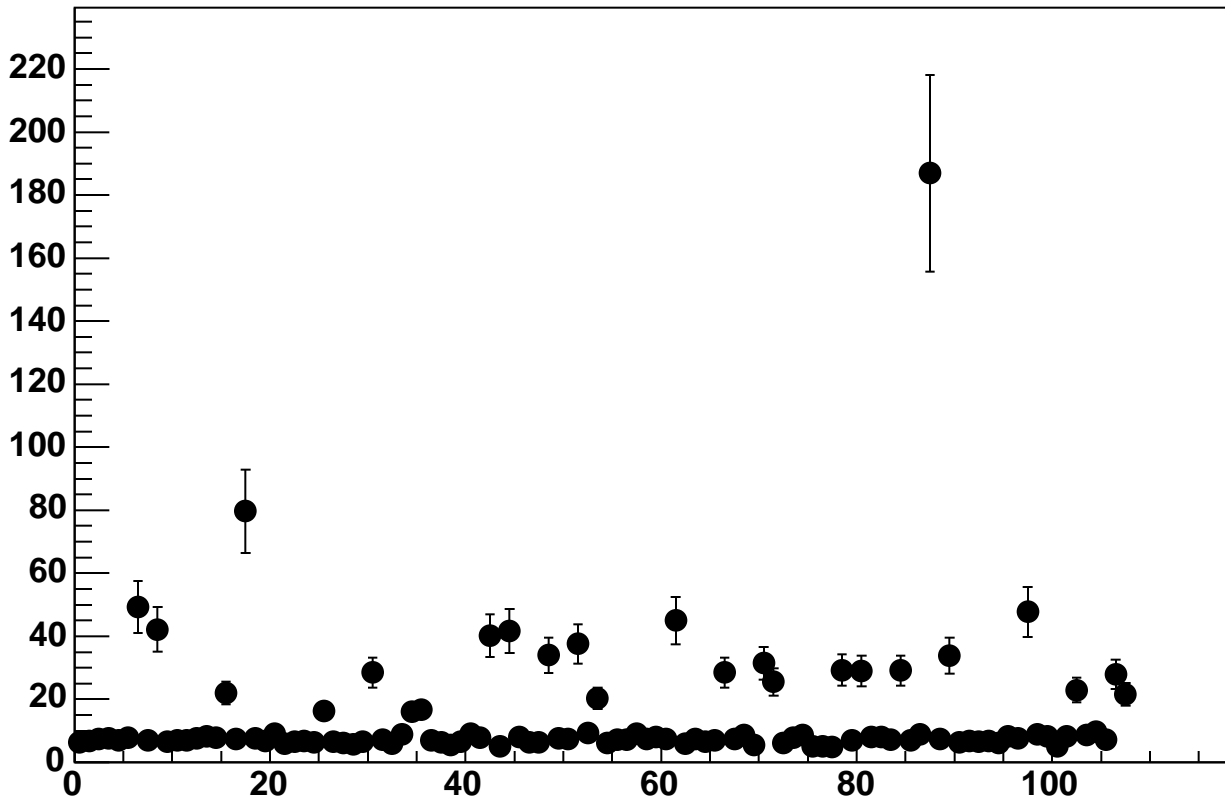
Enable 2, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



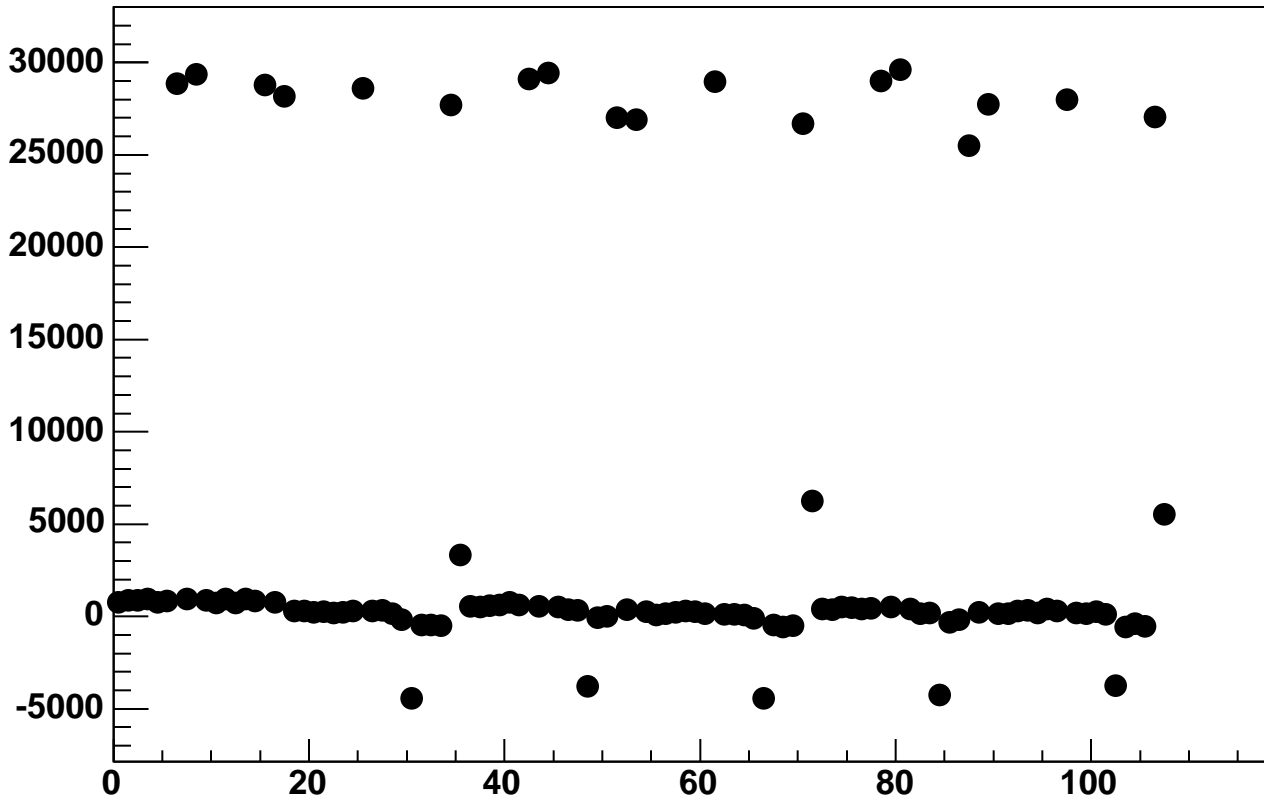
Enable 2, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



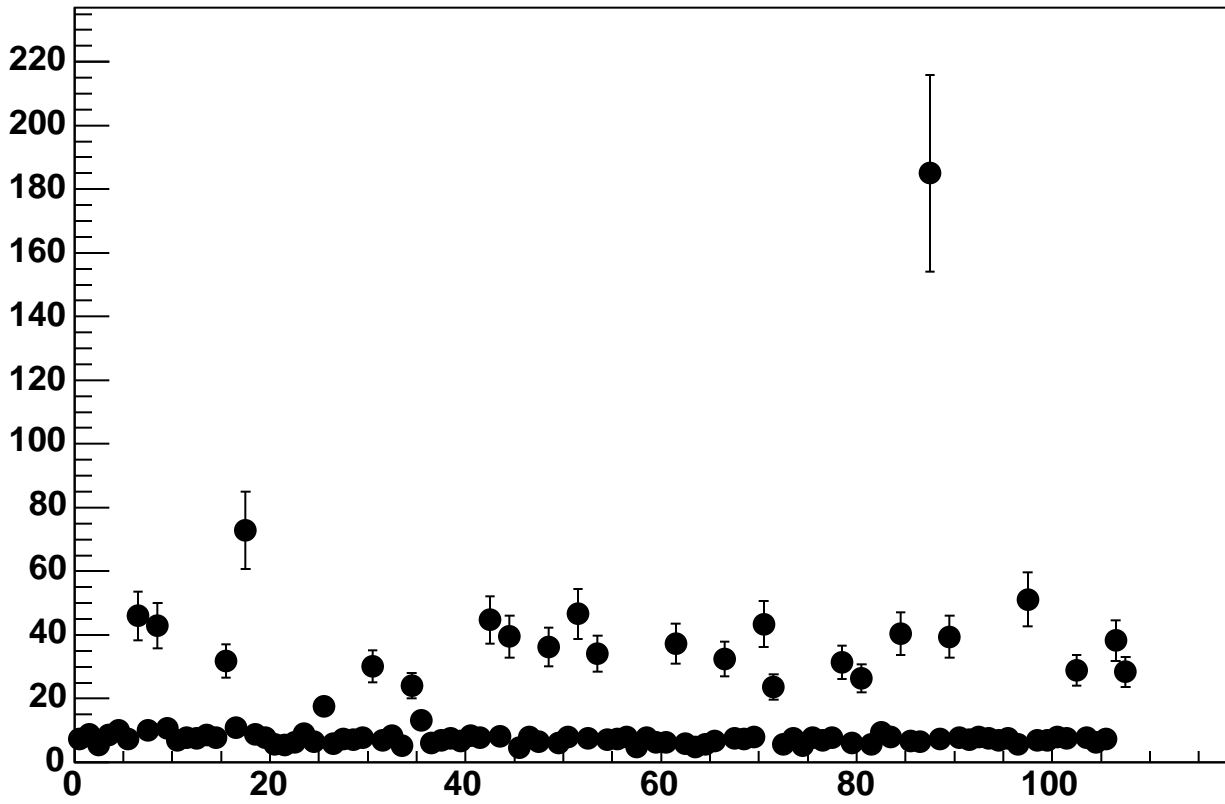
Enable 2, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



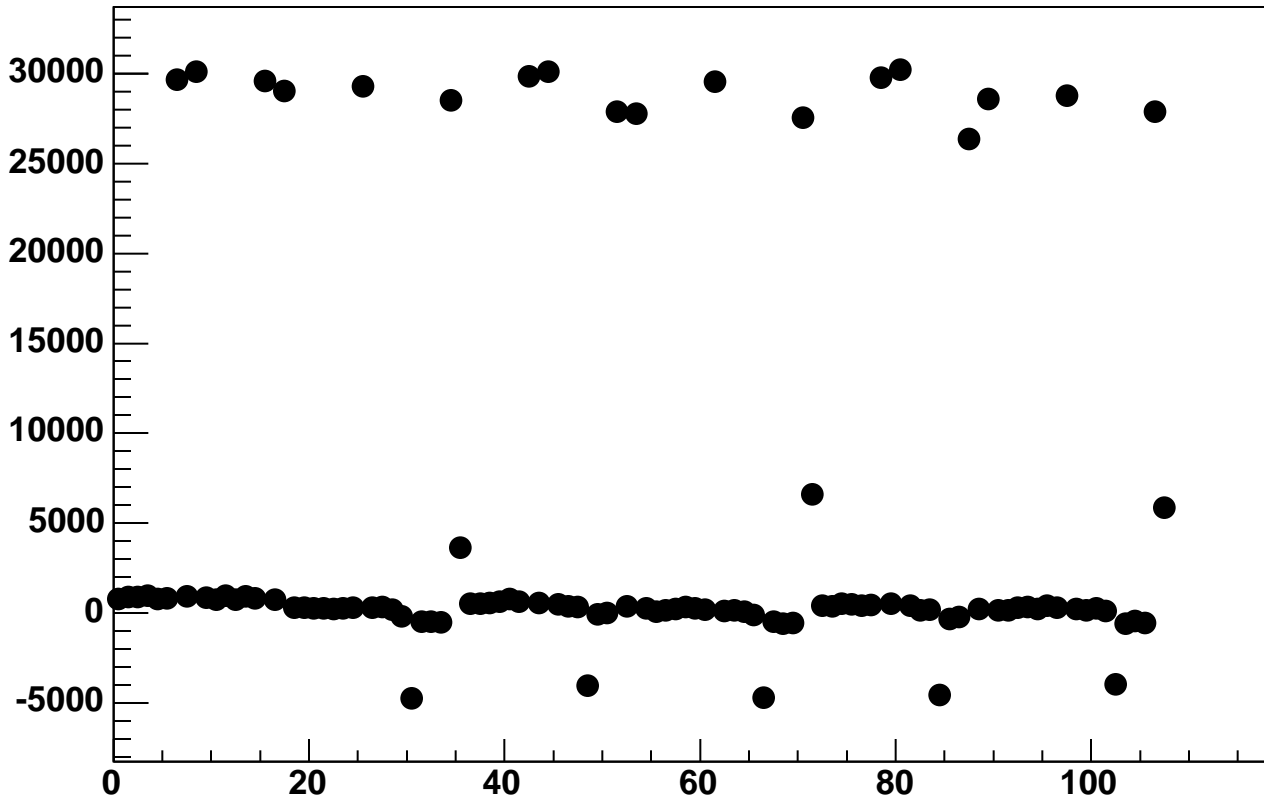
Enable 2, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



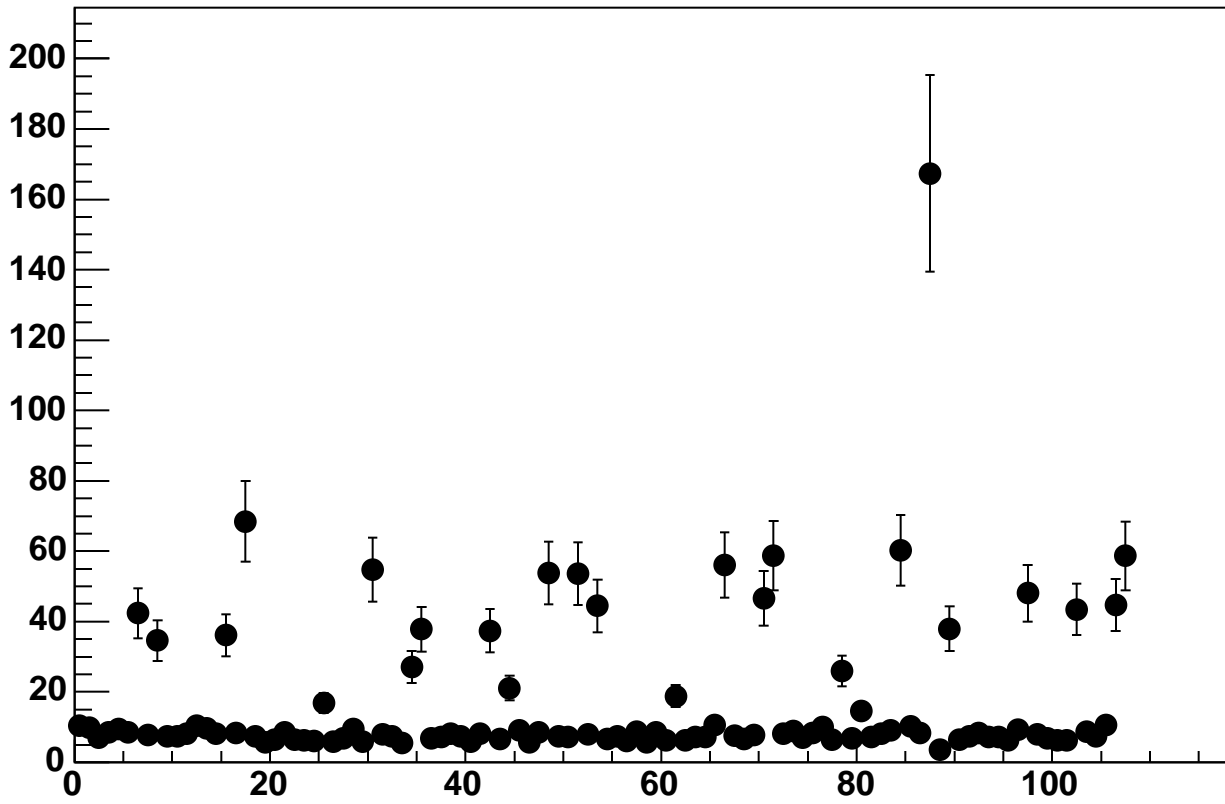
Enable 2, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



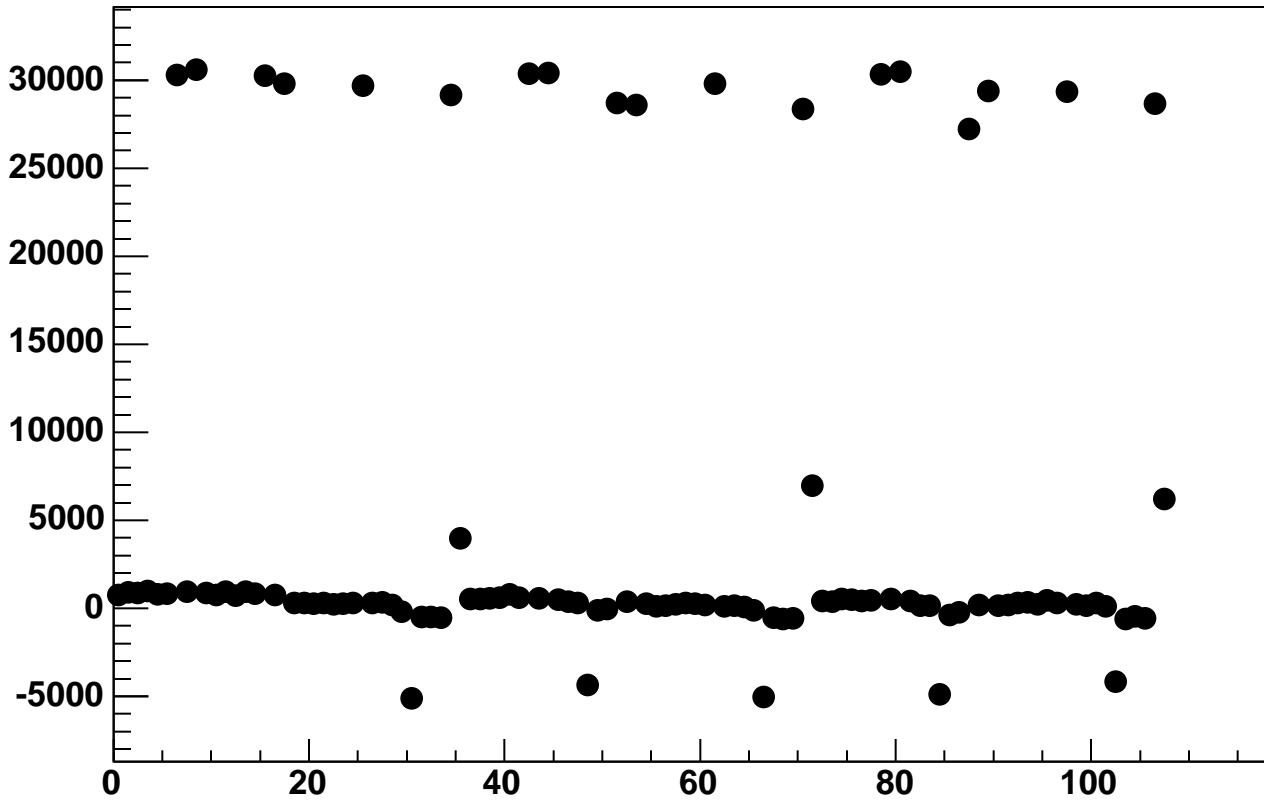
Enable 2, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



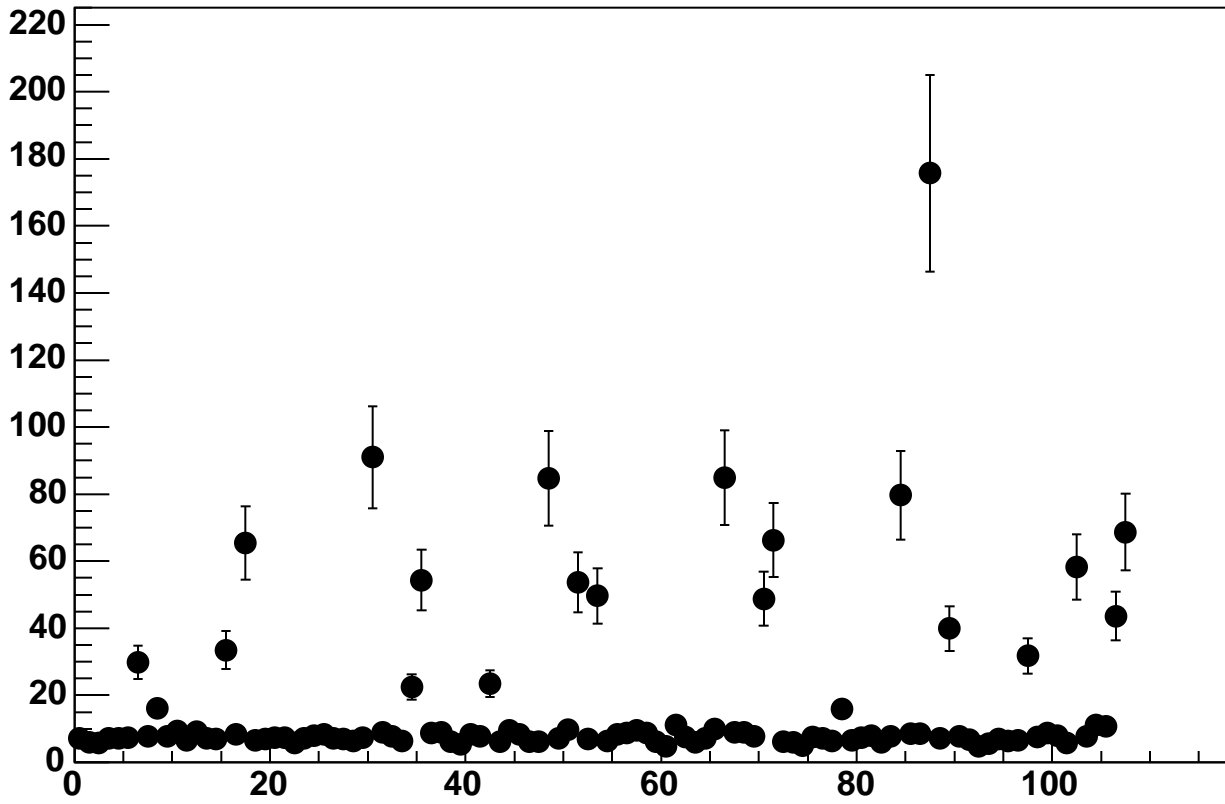
Enable 2, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



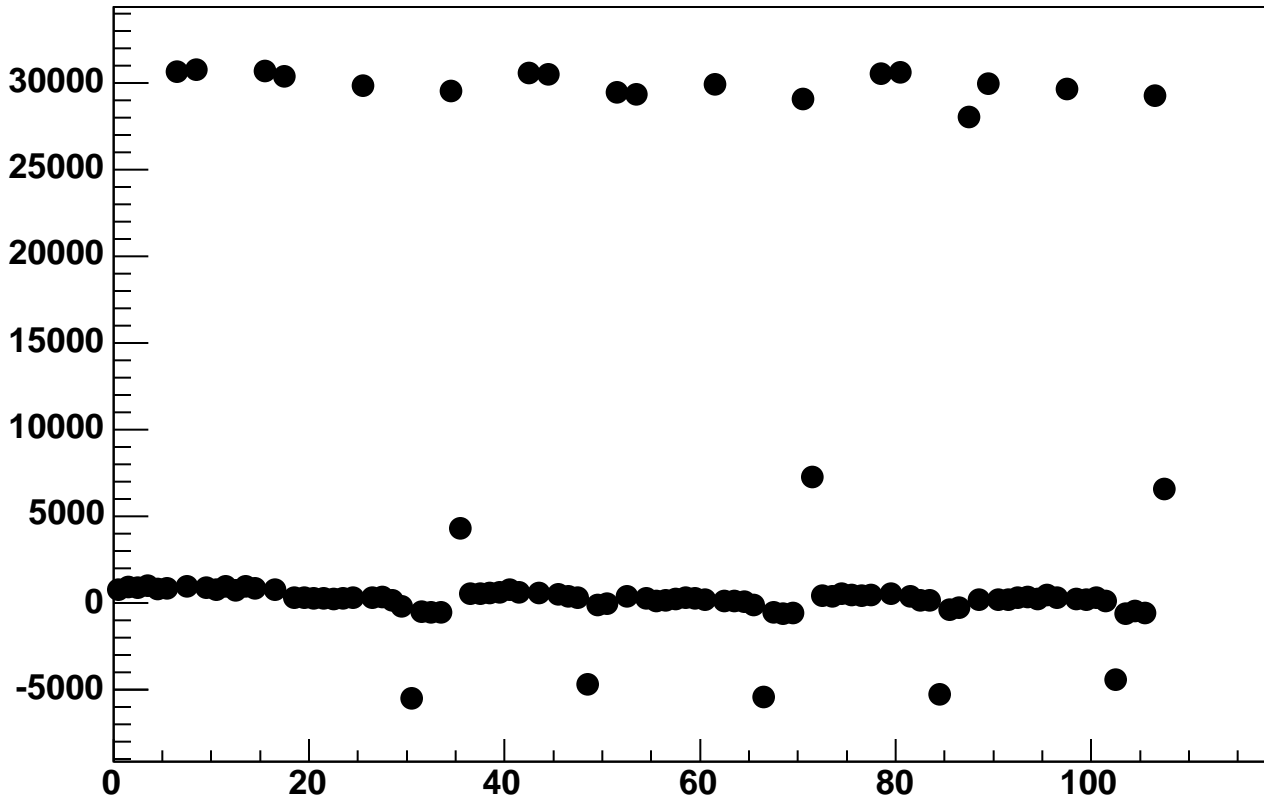
Enable 2, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



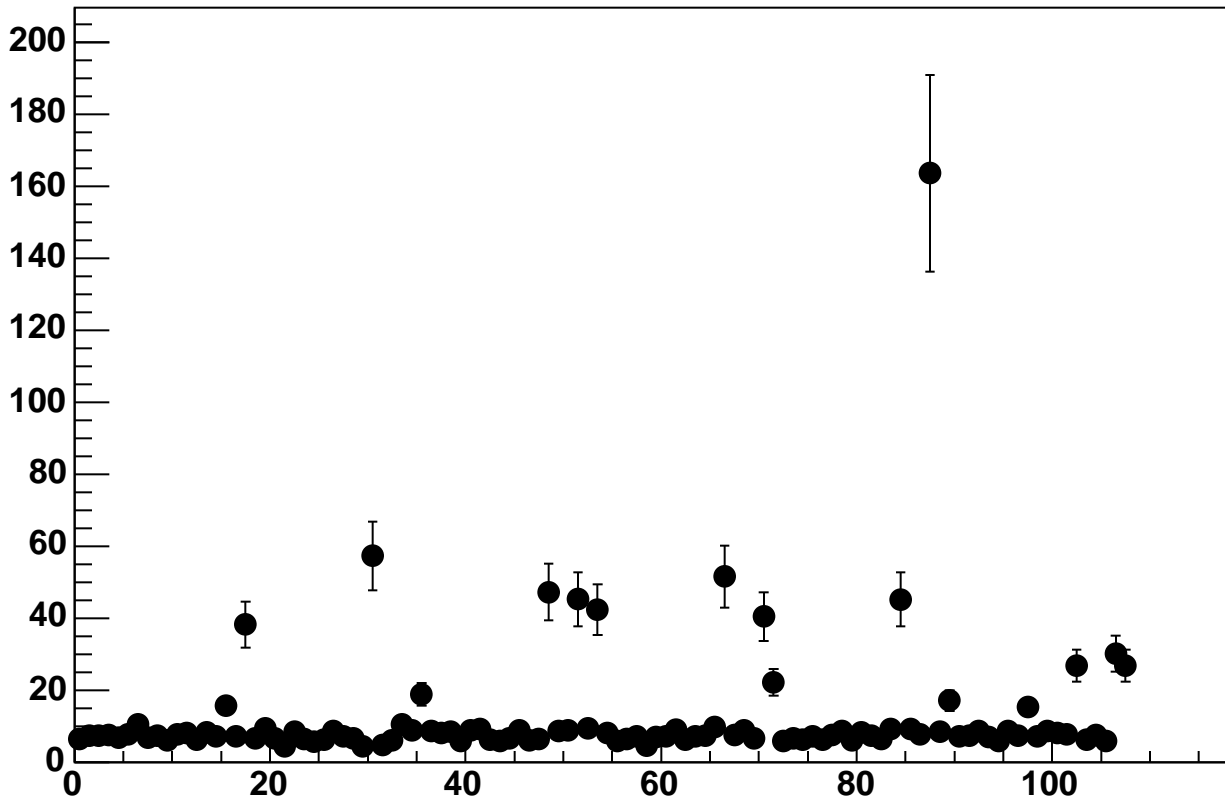
Enable 2, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



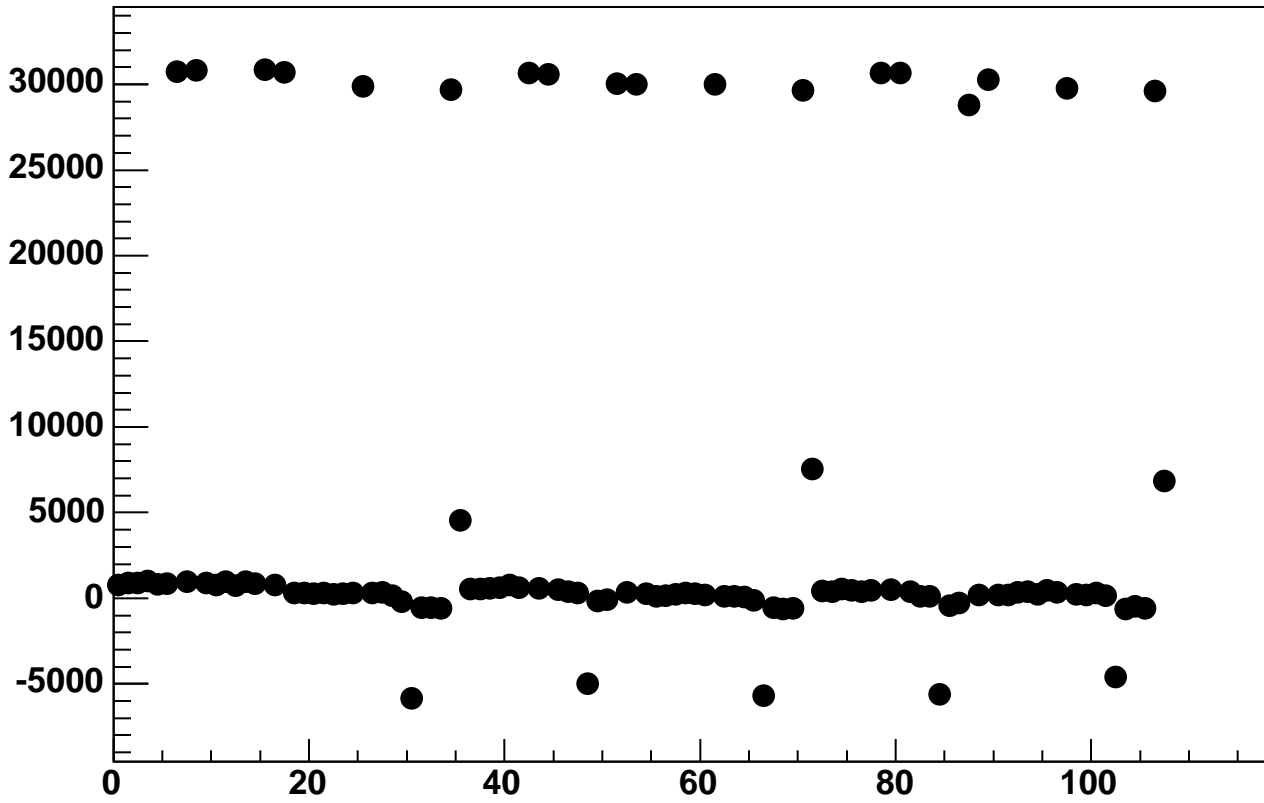
Enable 2, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



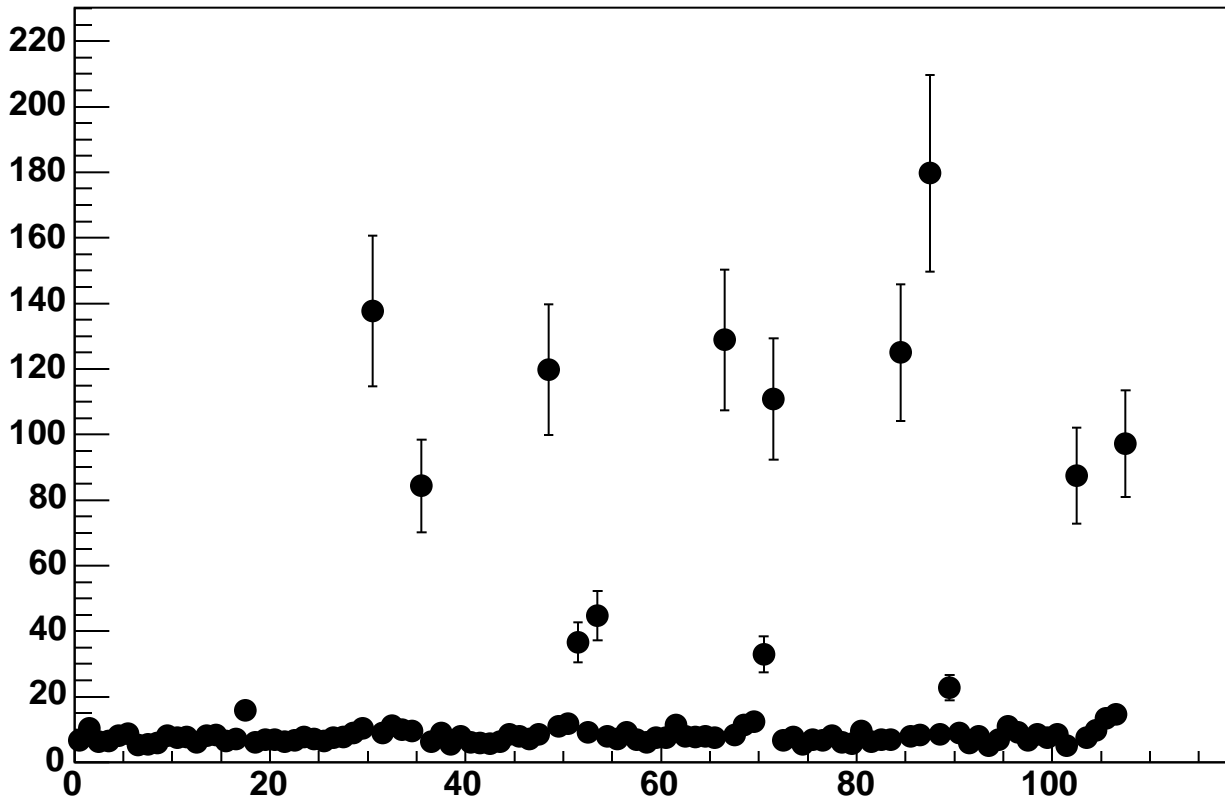
Enable 2, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



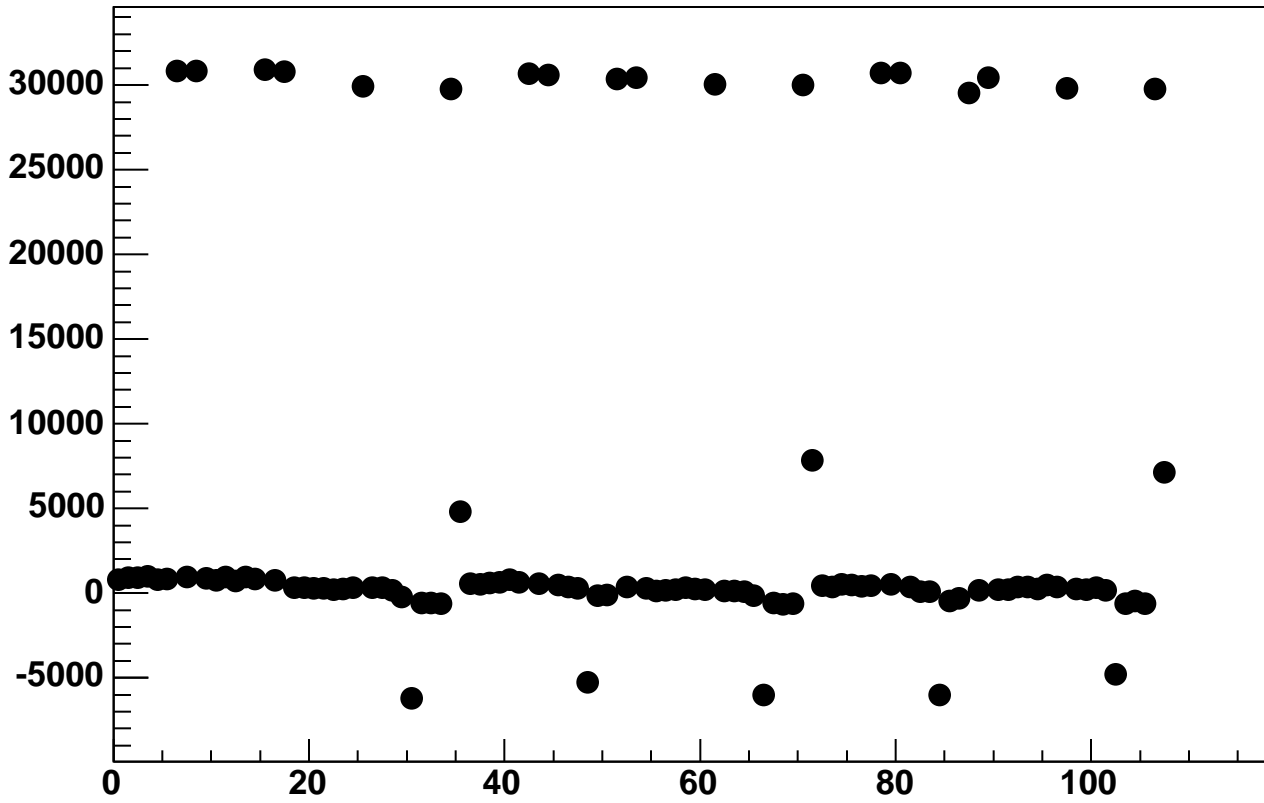
Enable 2, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



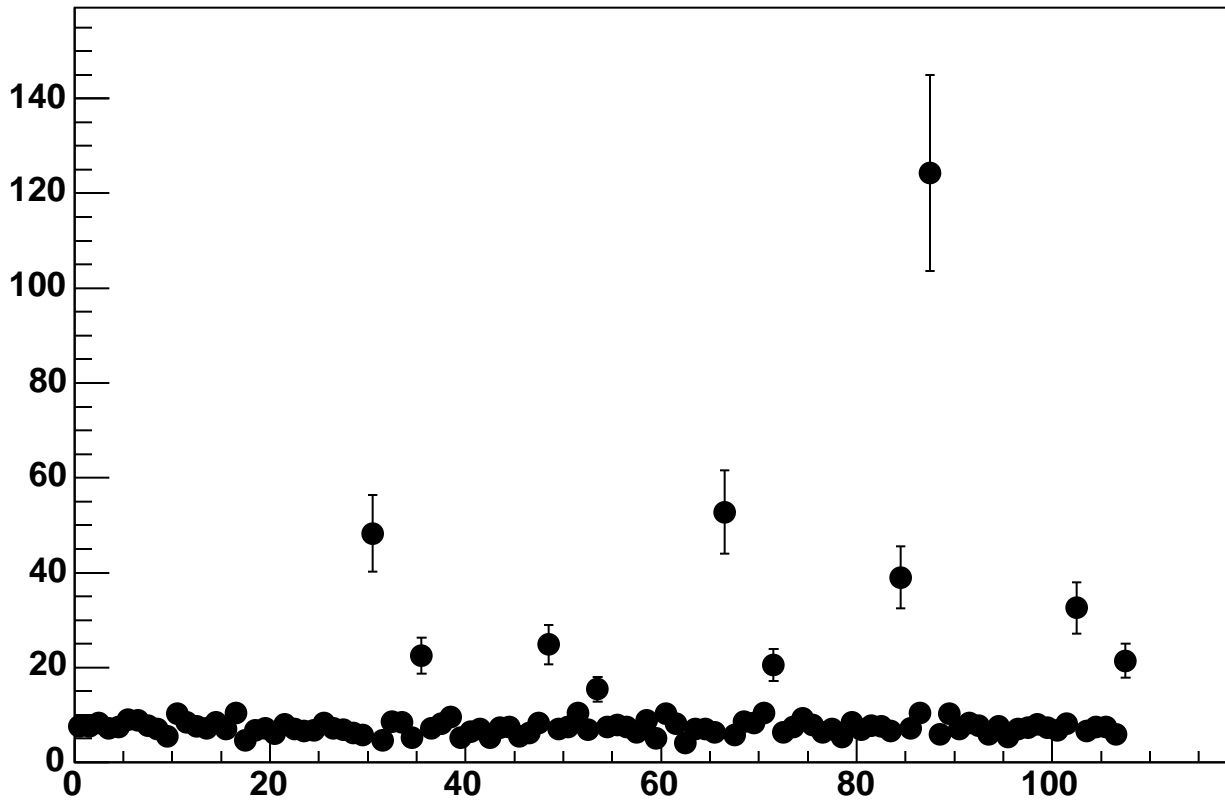
Enable 2, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



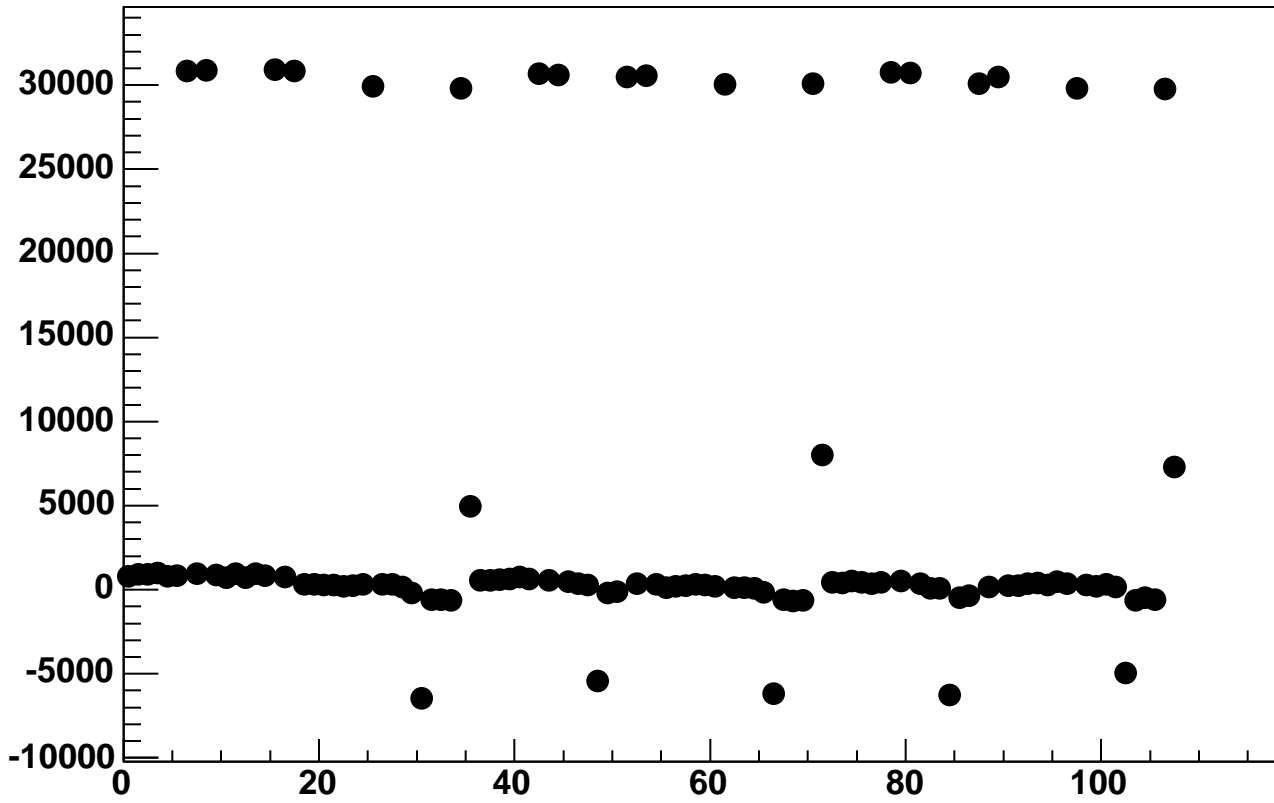
Enable 2, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



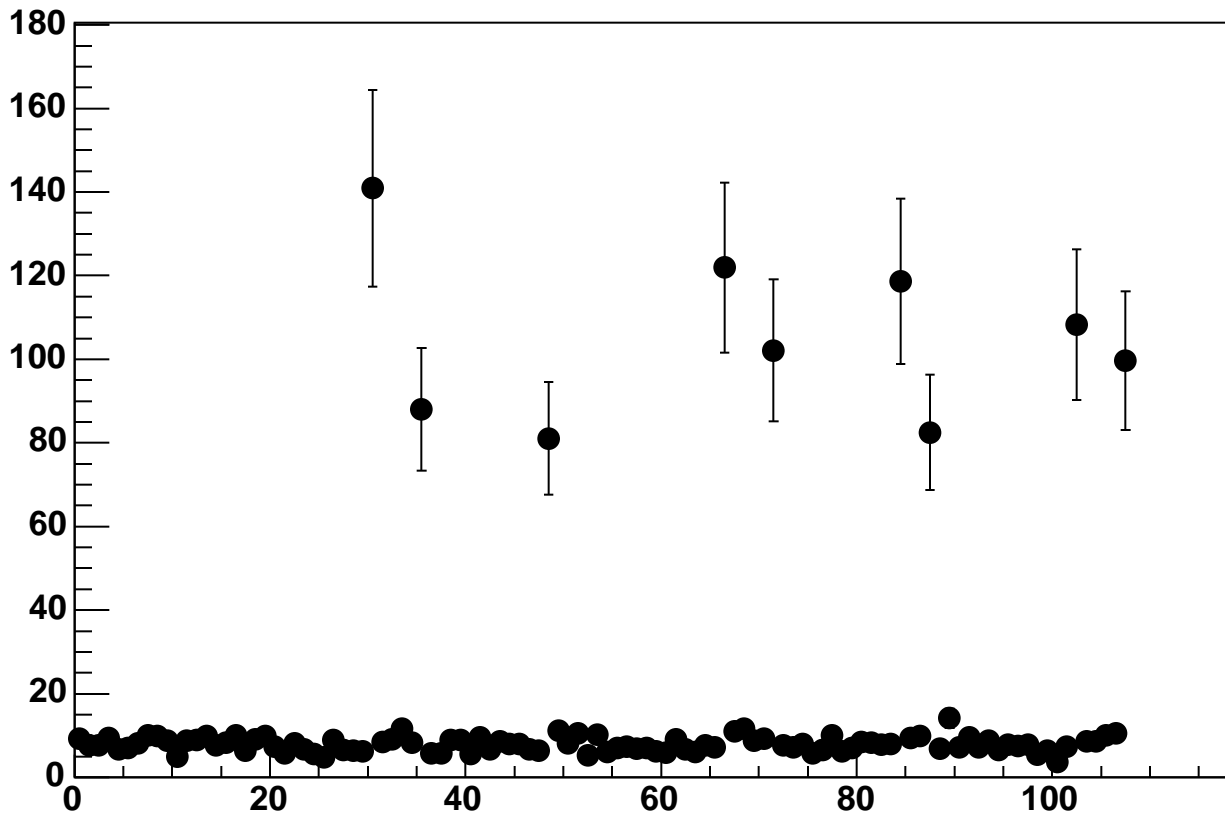
Enable 2, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



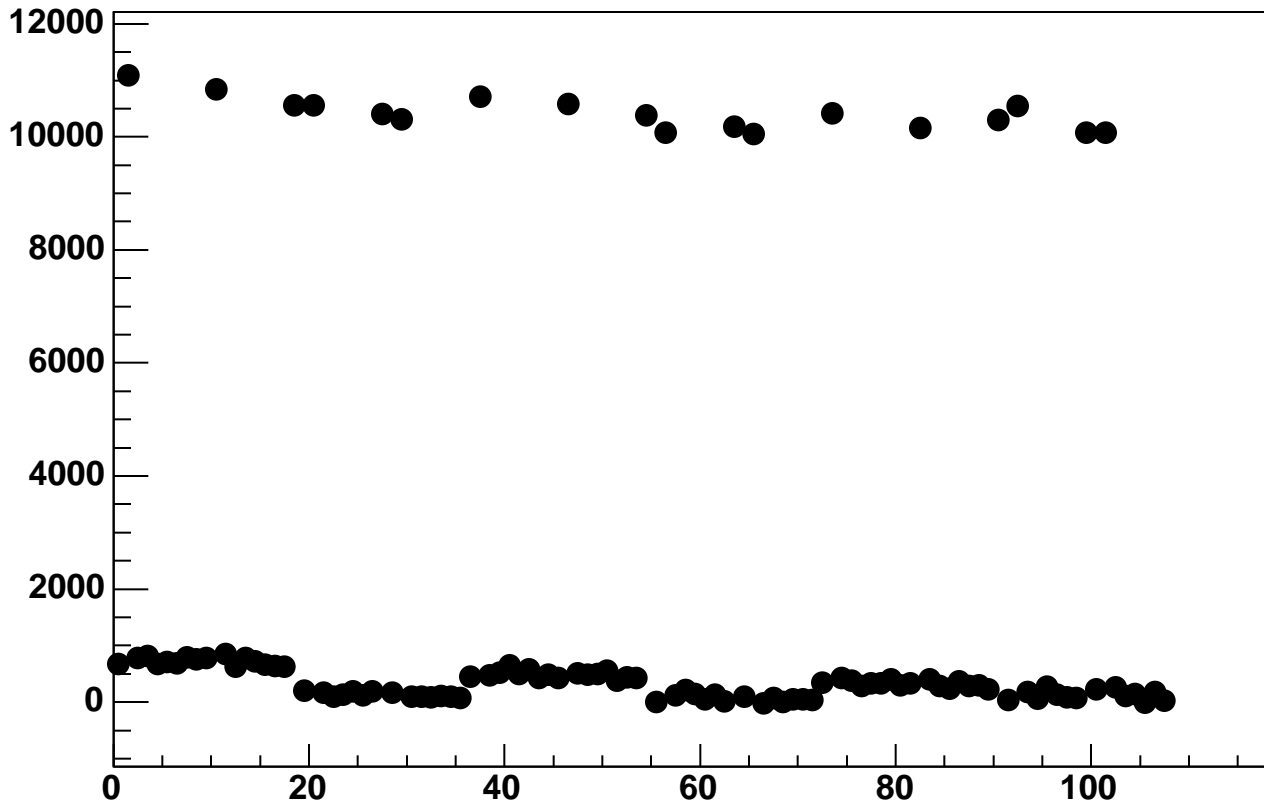
Enable 2, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



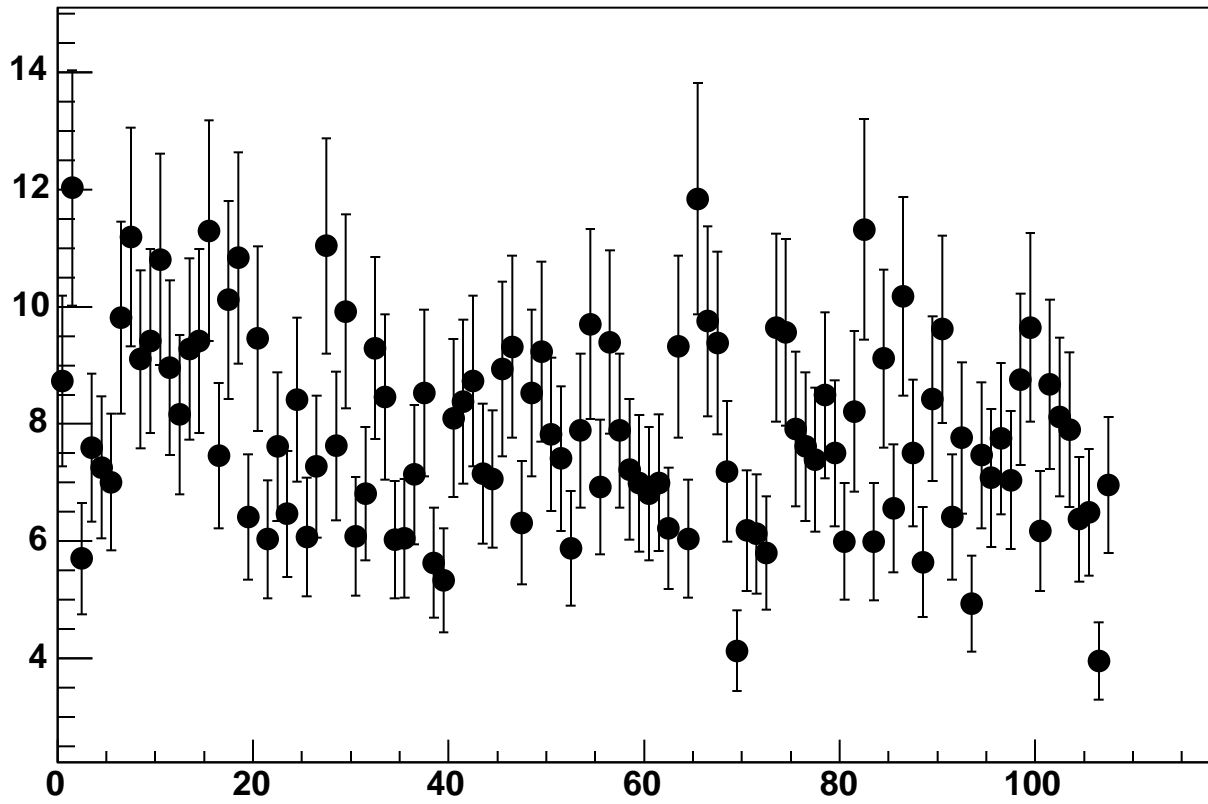
Enable 2, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



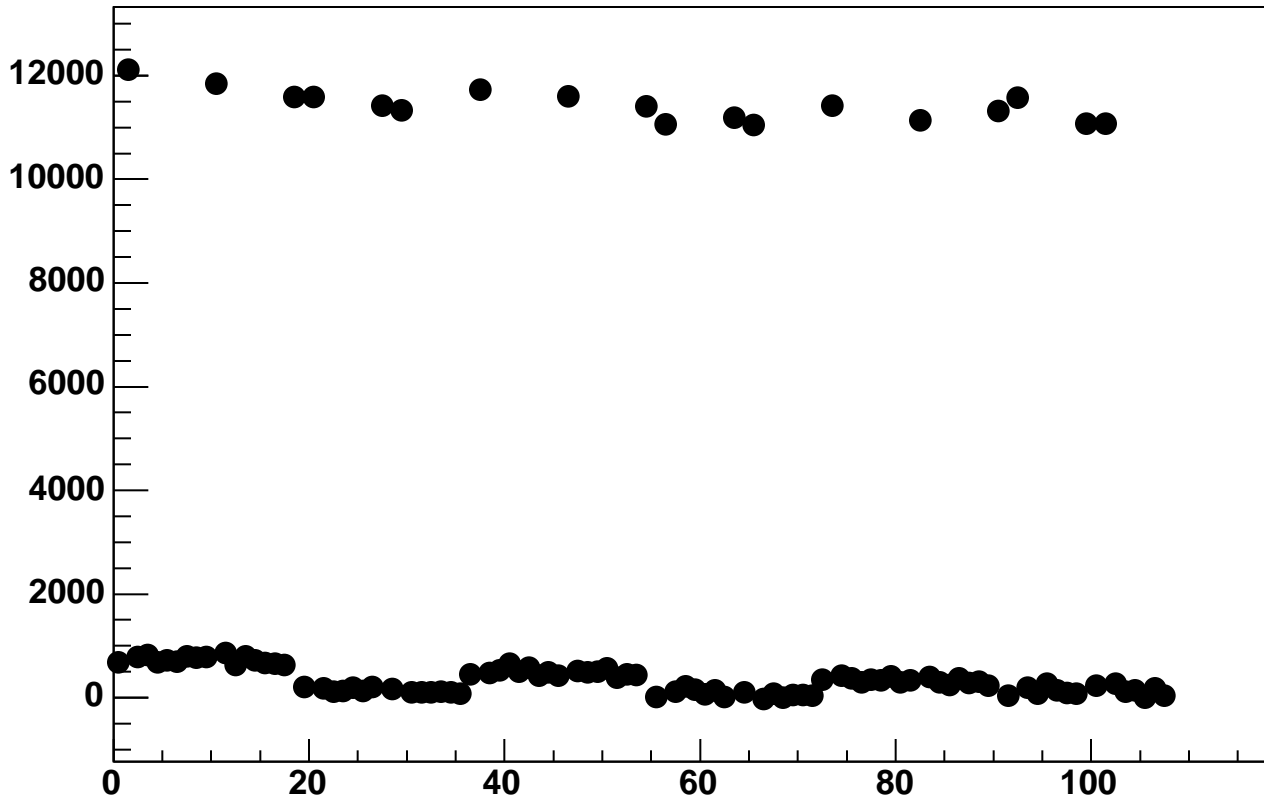
Enable 3, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



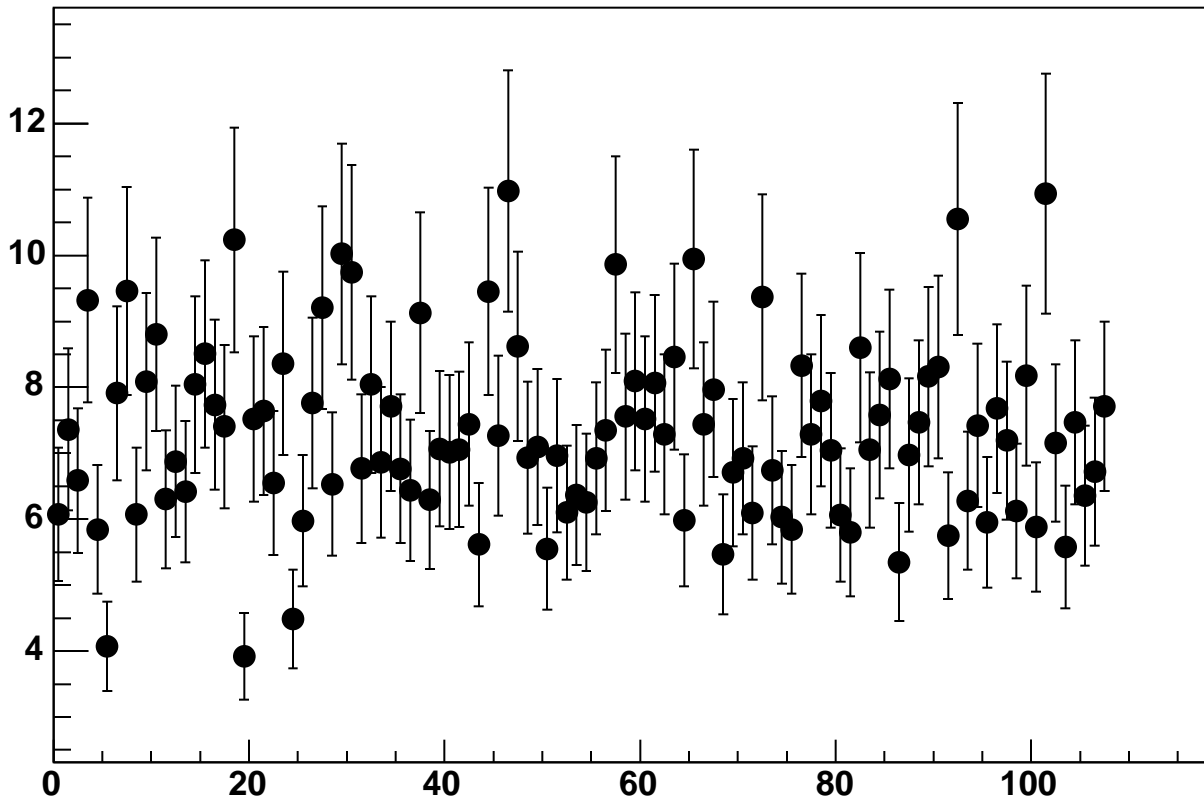
Enable 3, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



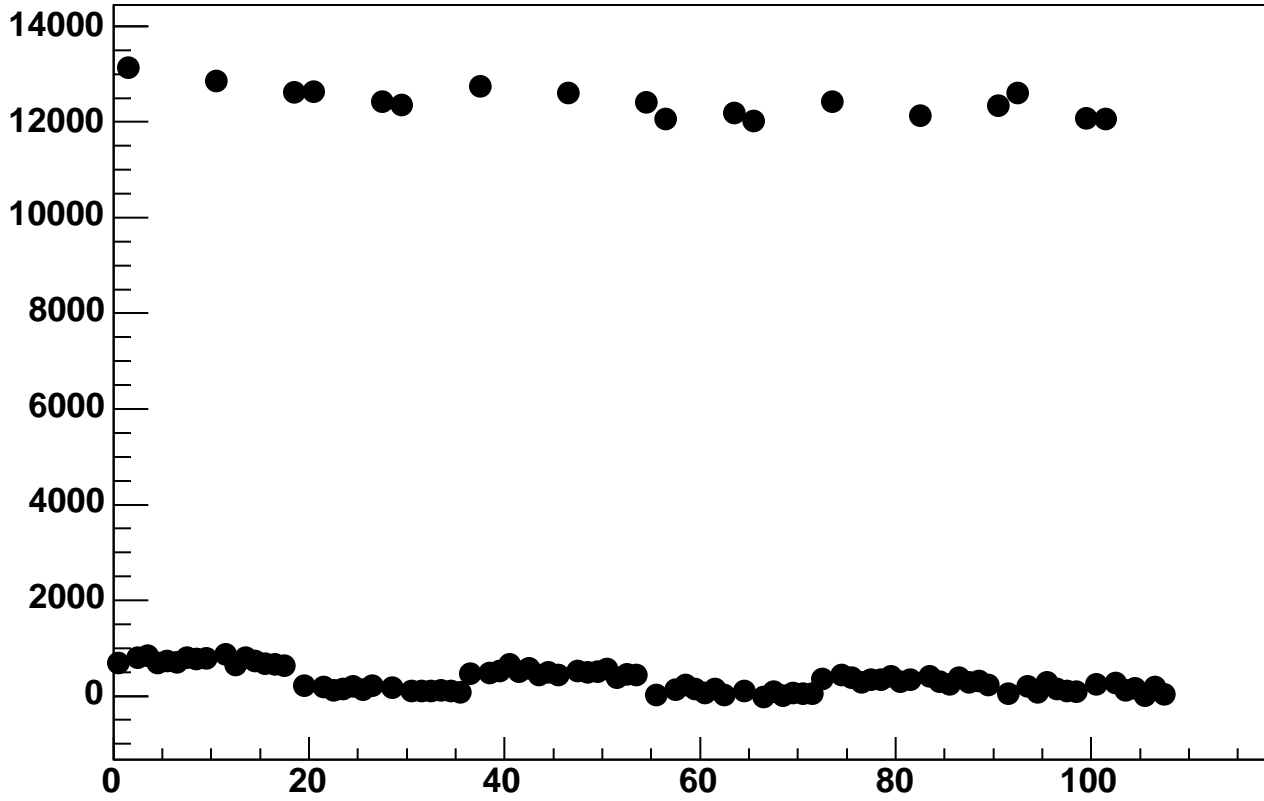
Enable 3, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



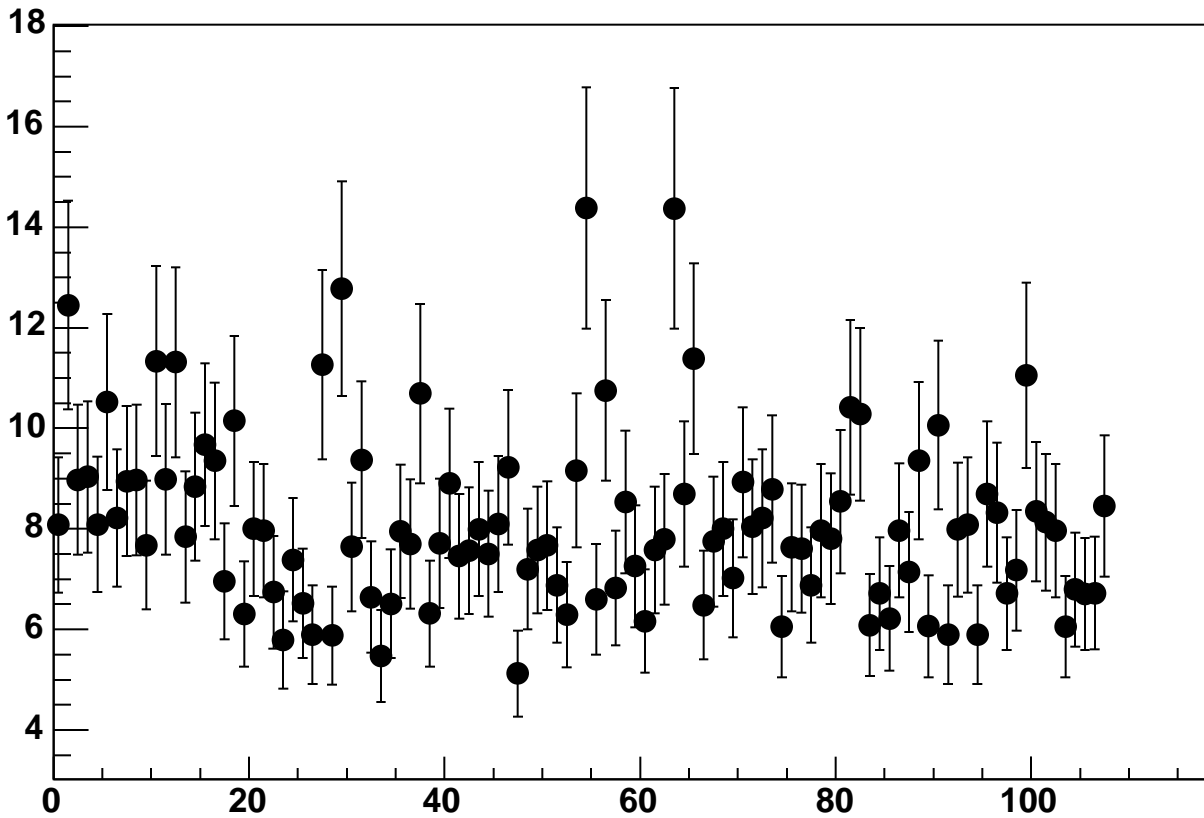
Enable 3, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



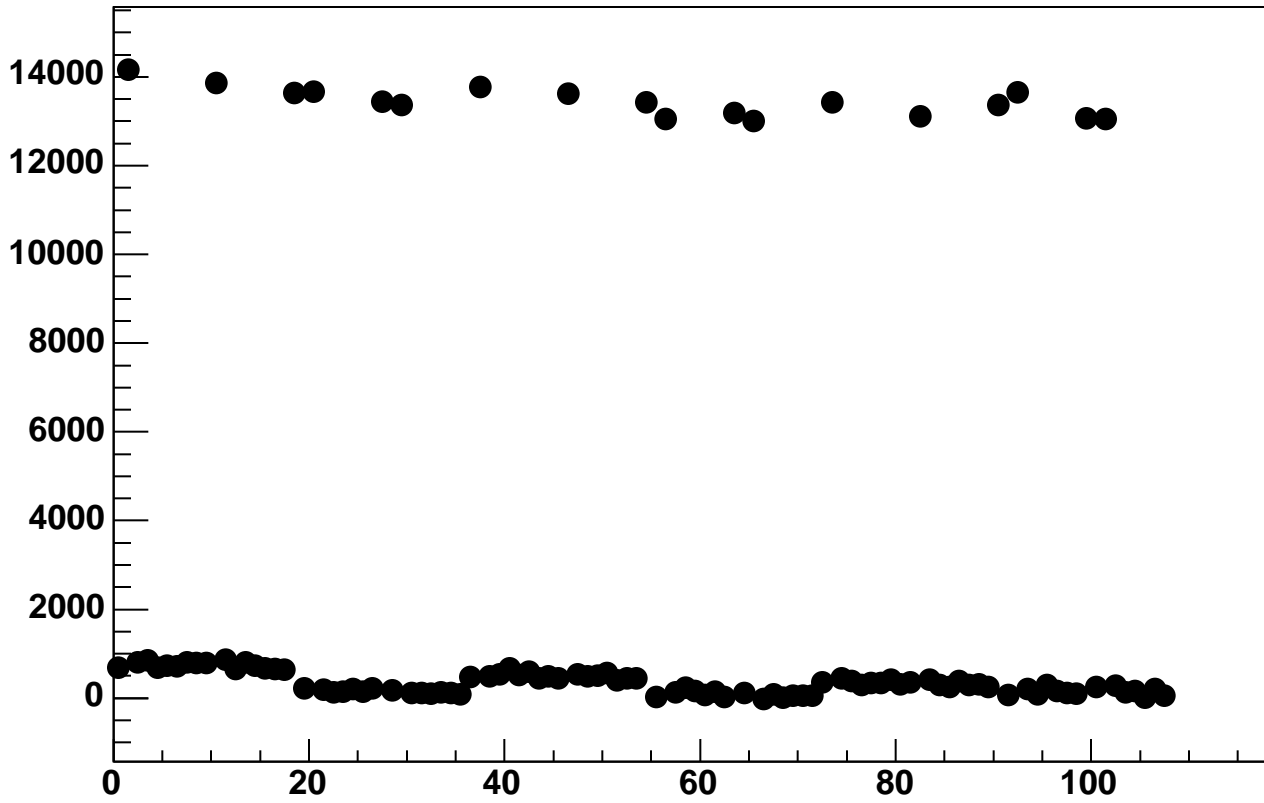
Enable 3, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



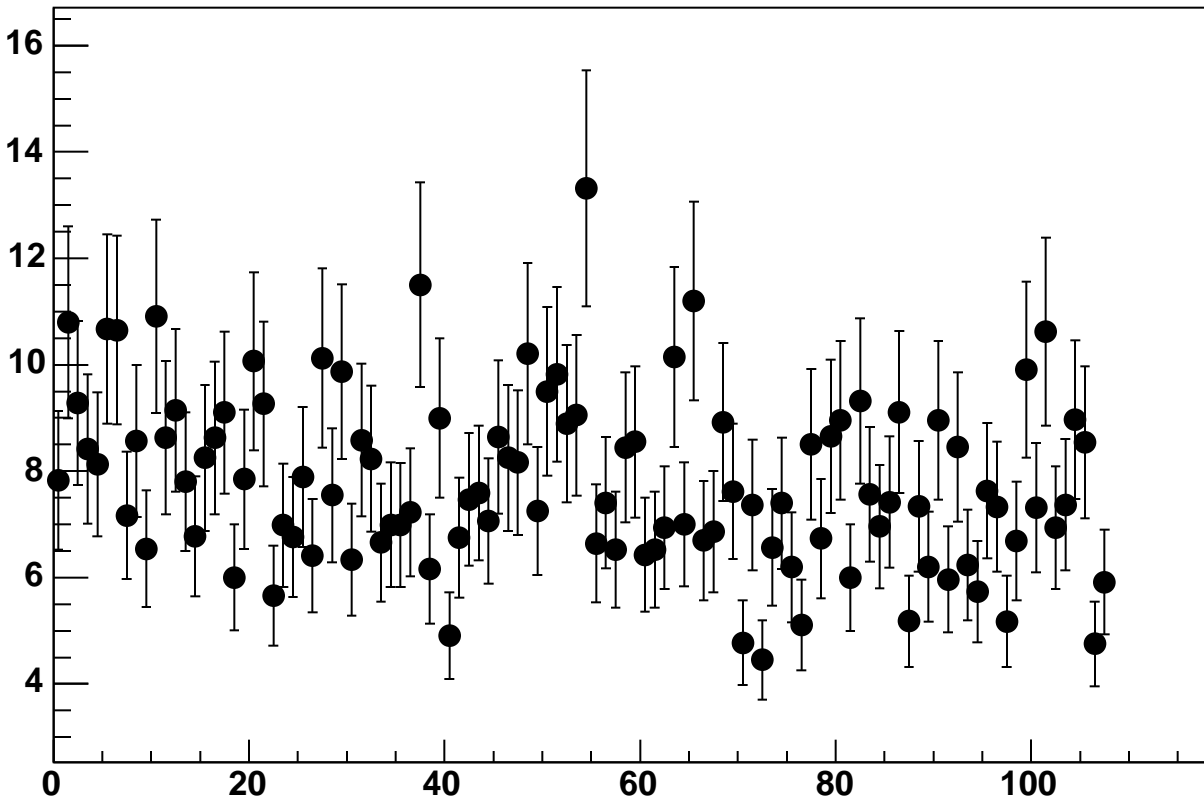
Enable 3, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



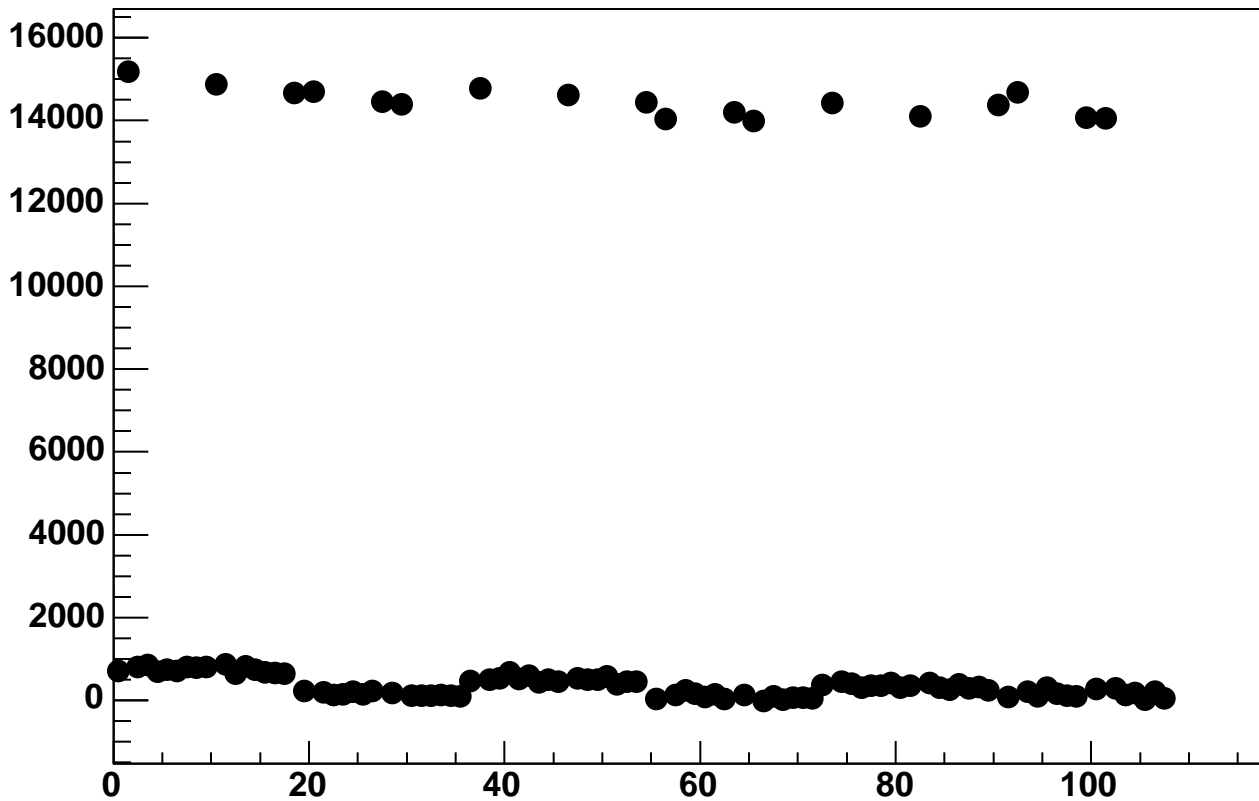
Enable 3, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



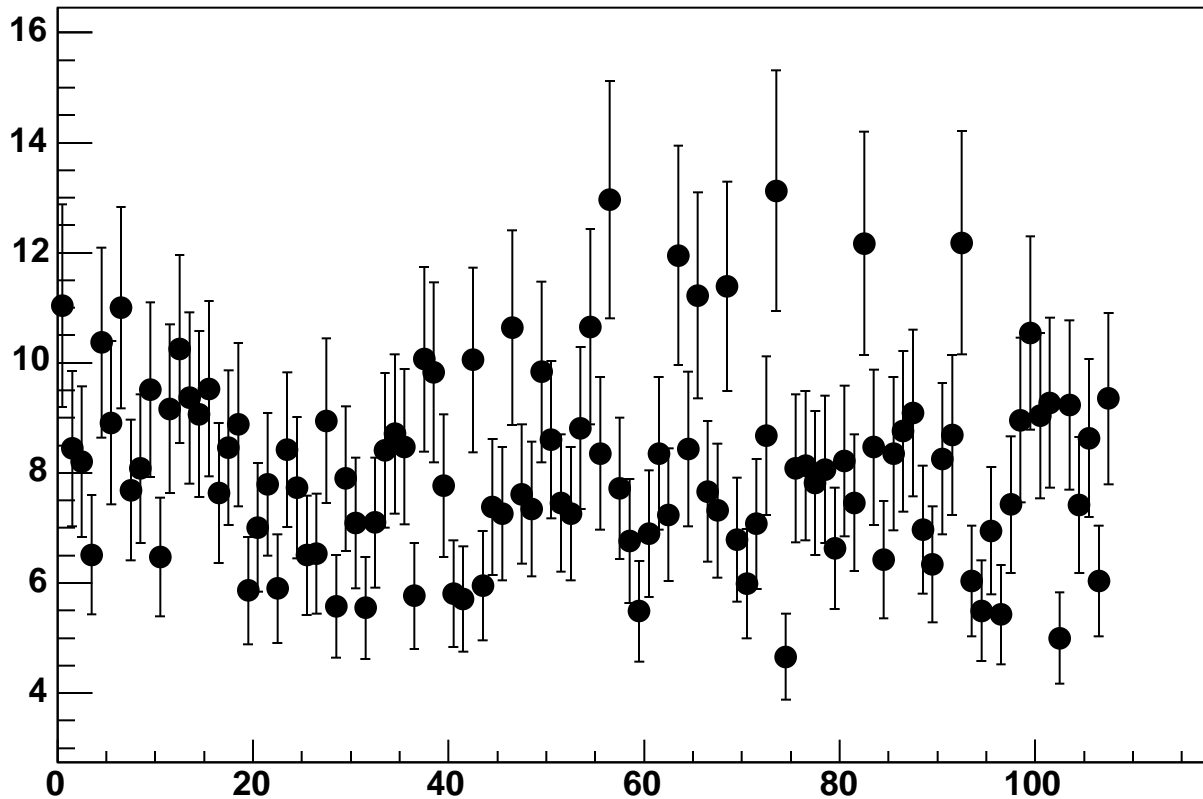
Enable 3, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



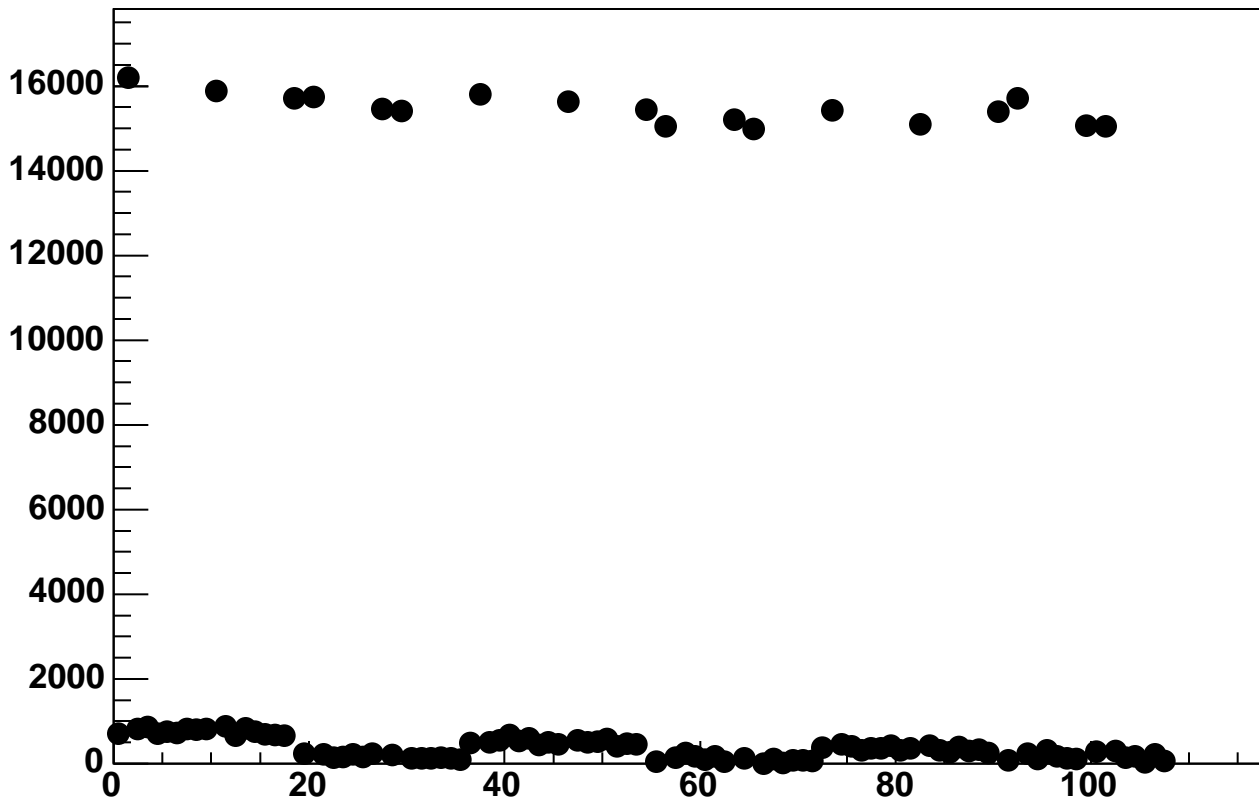
Enable 3, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



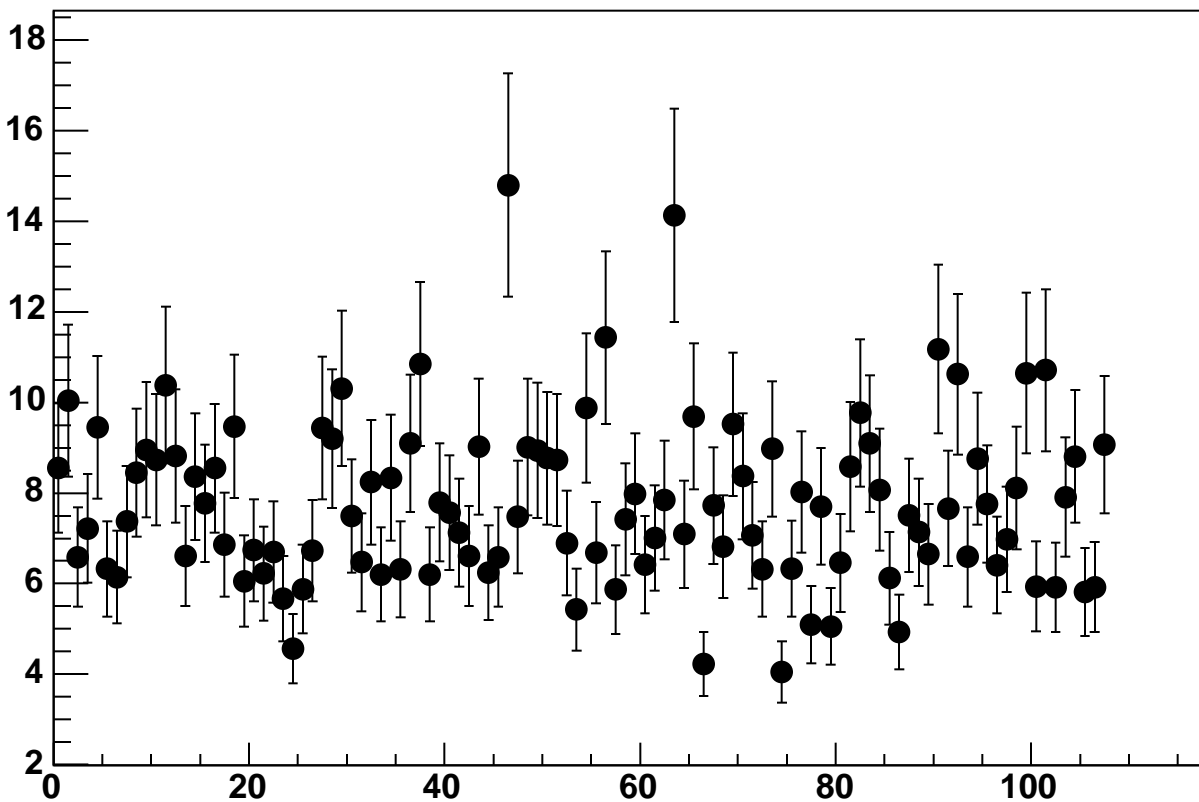
Enable 3, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



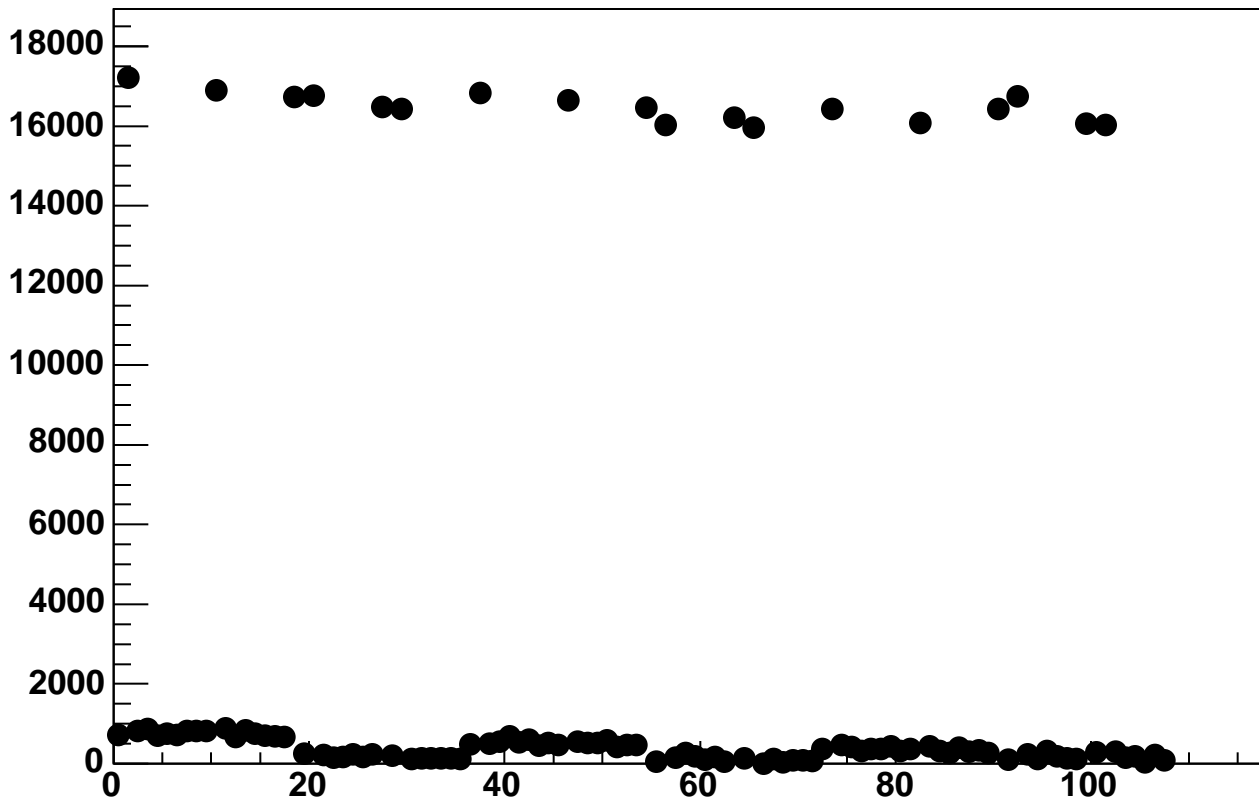
Enable 3, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



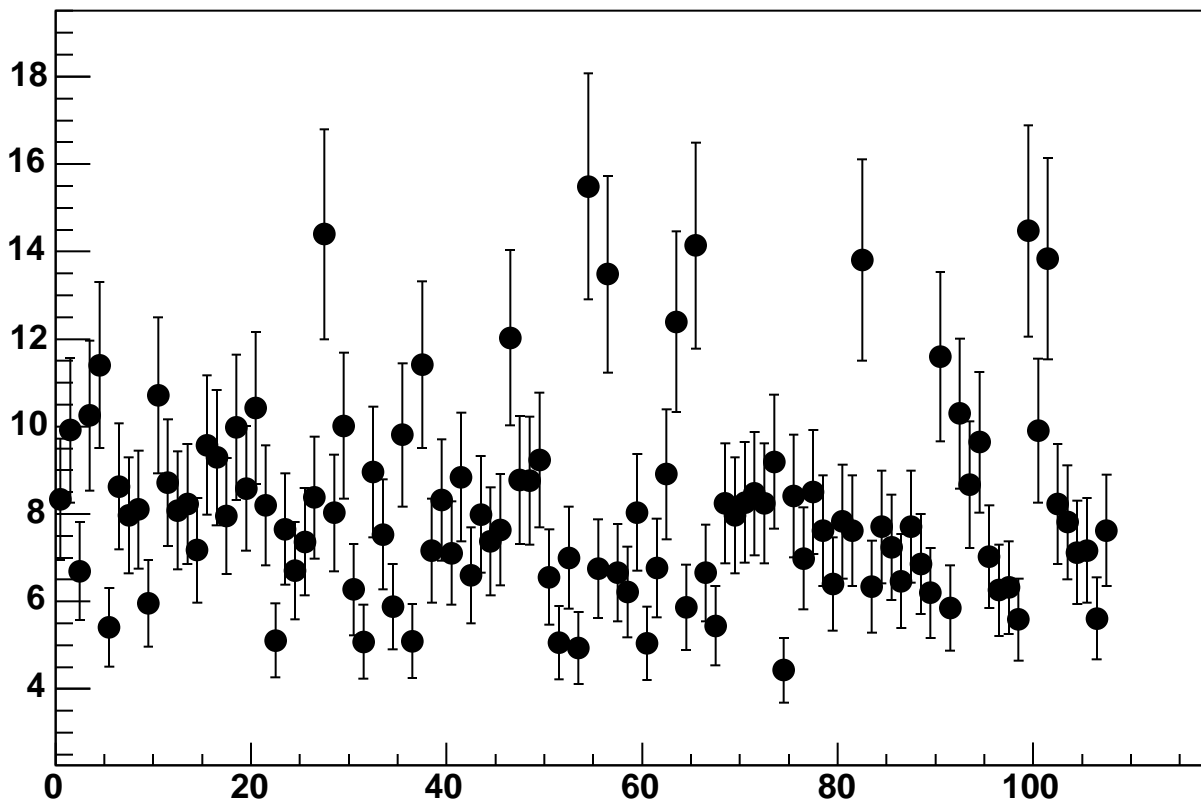
Enable 3, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



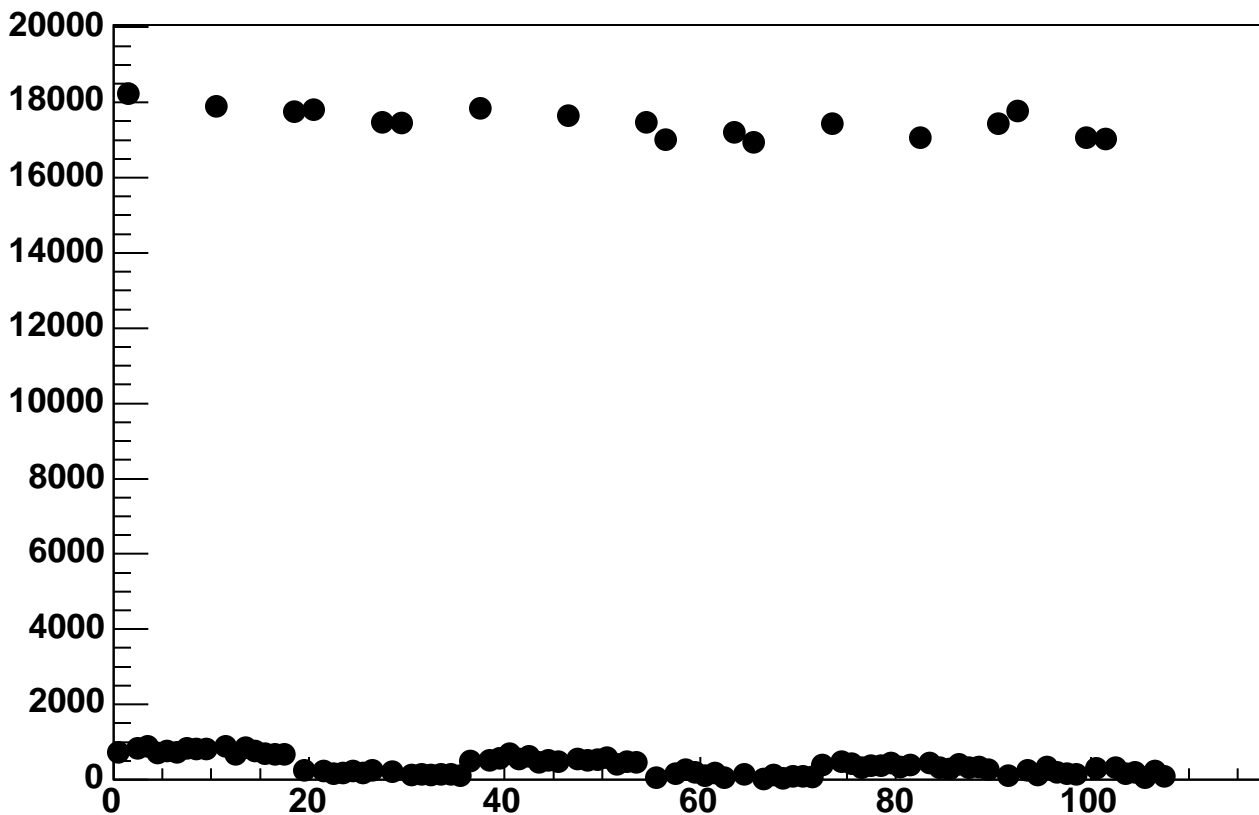
Enable 3, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



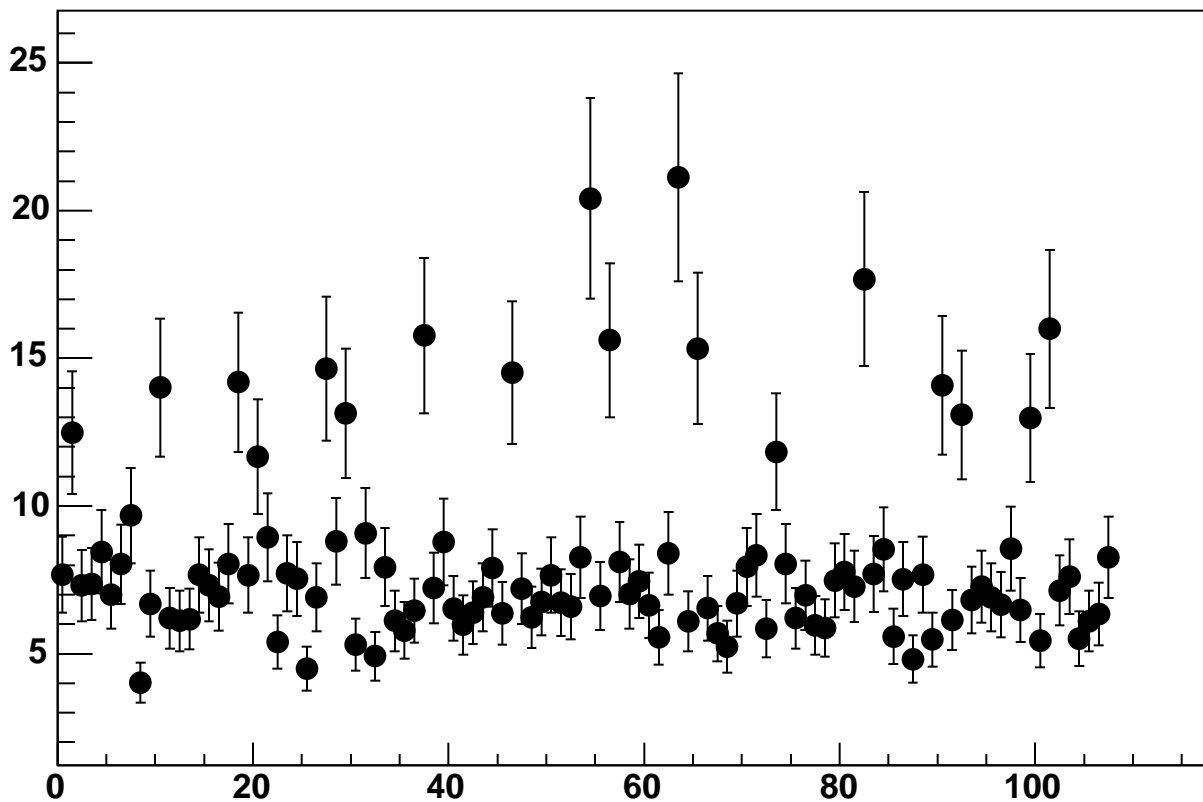
Enable 3, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



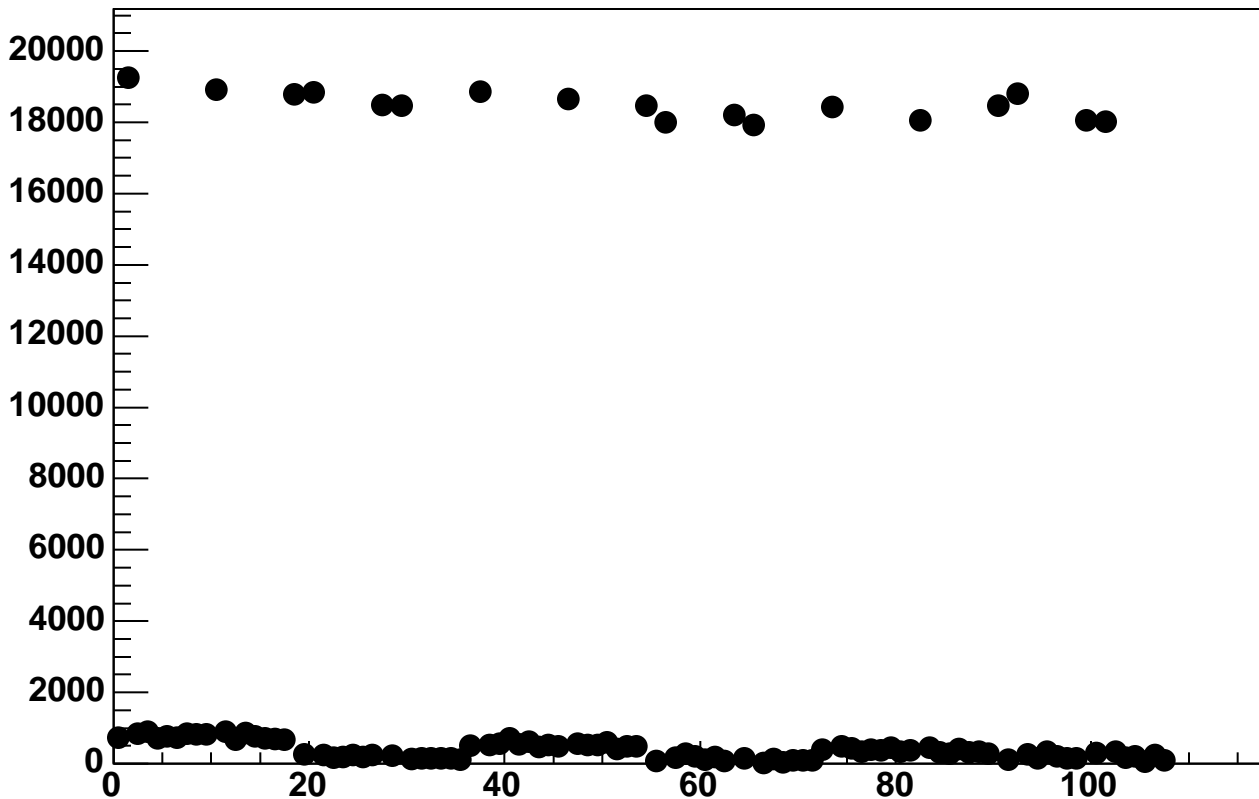
Enable 3, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



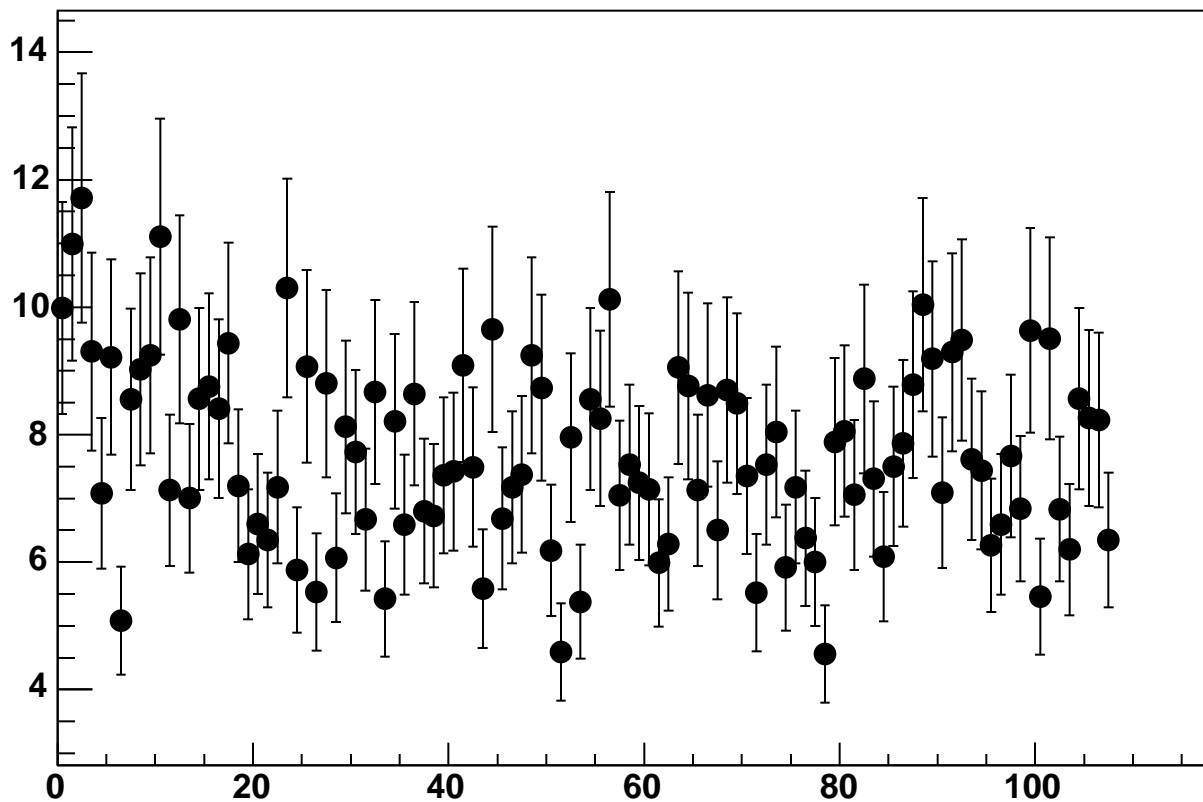
Enable 3, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



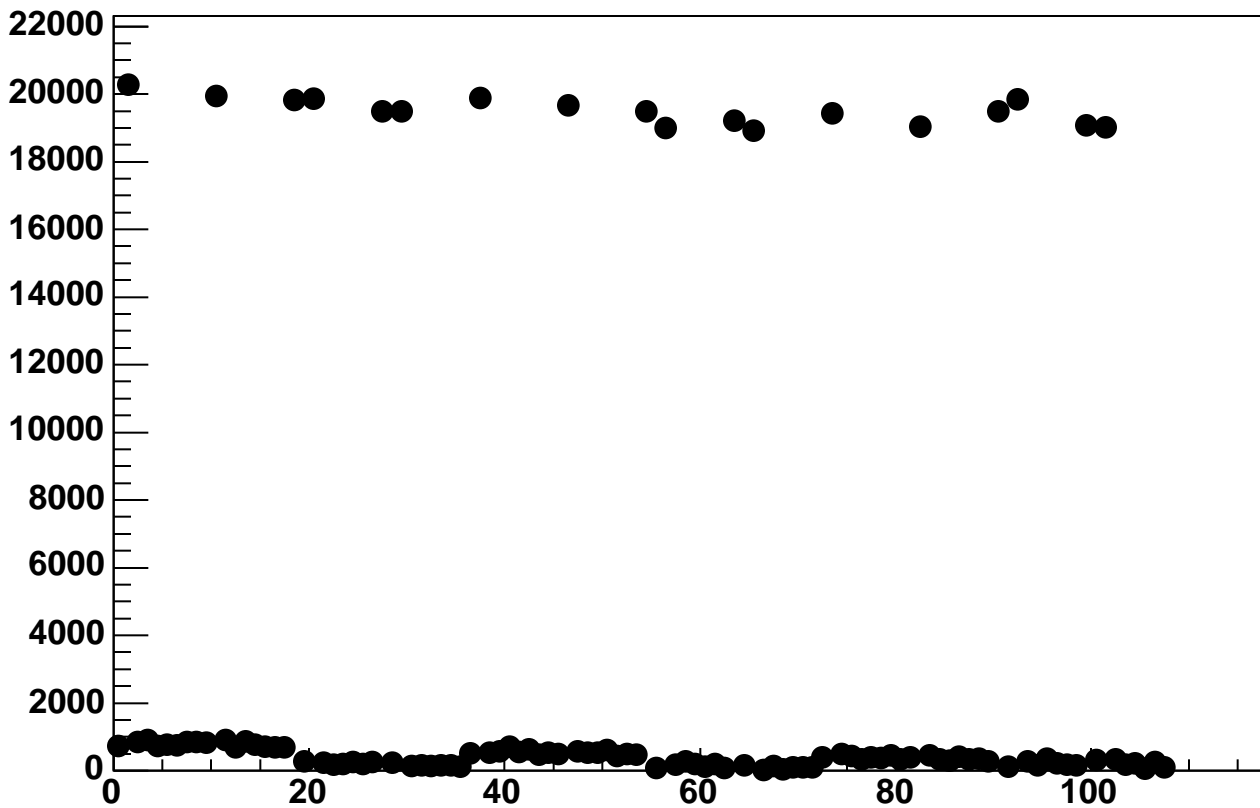
Enable 3, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



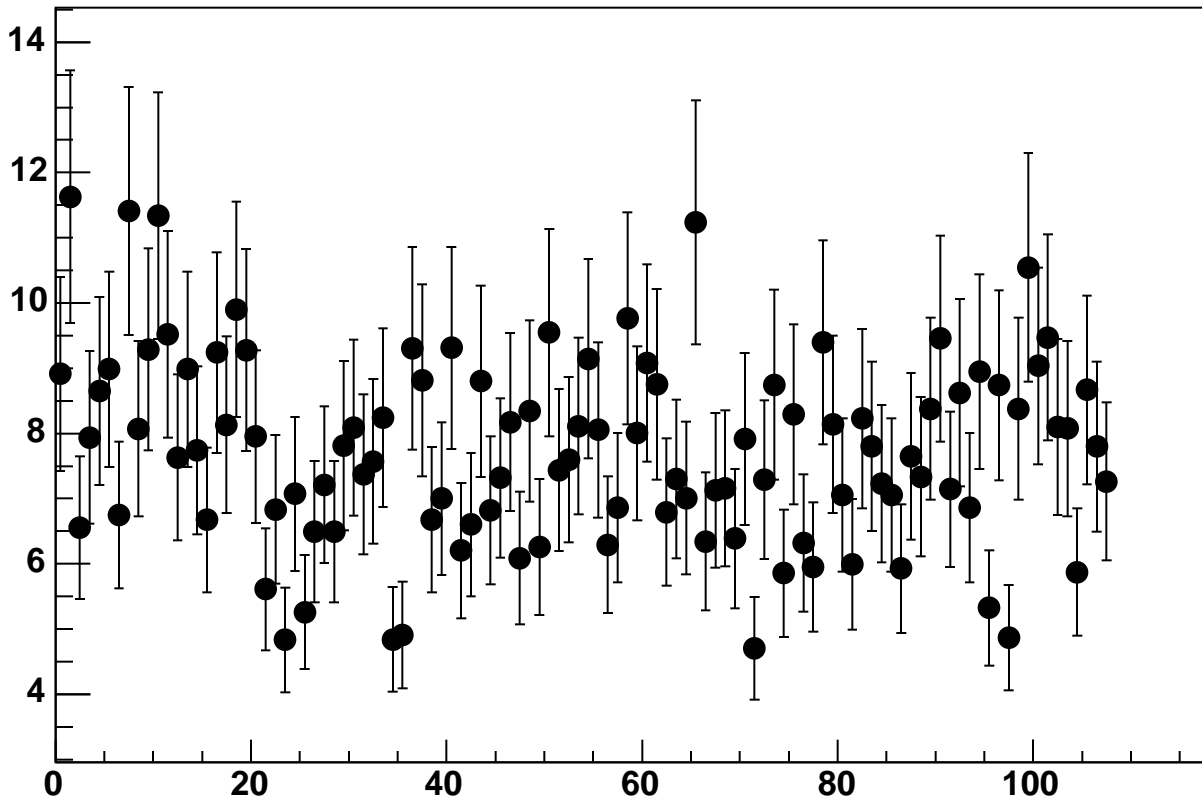
Enable 3, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



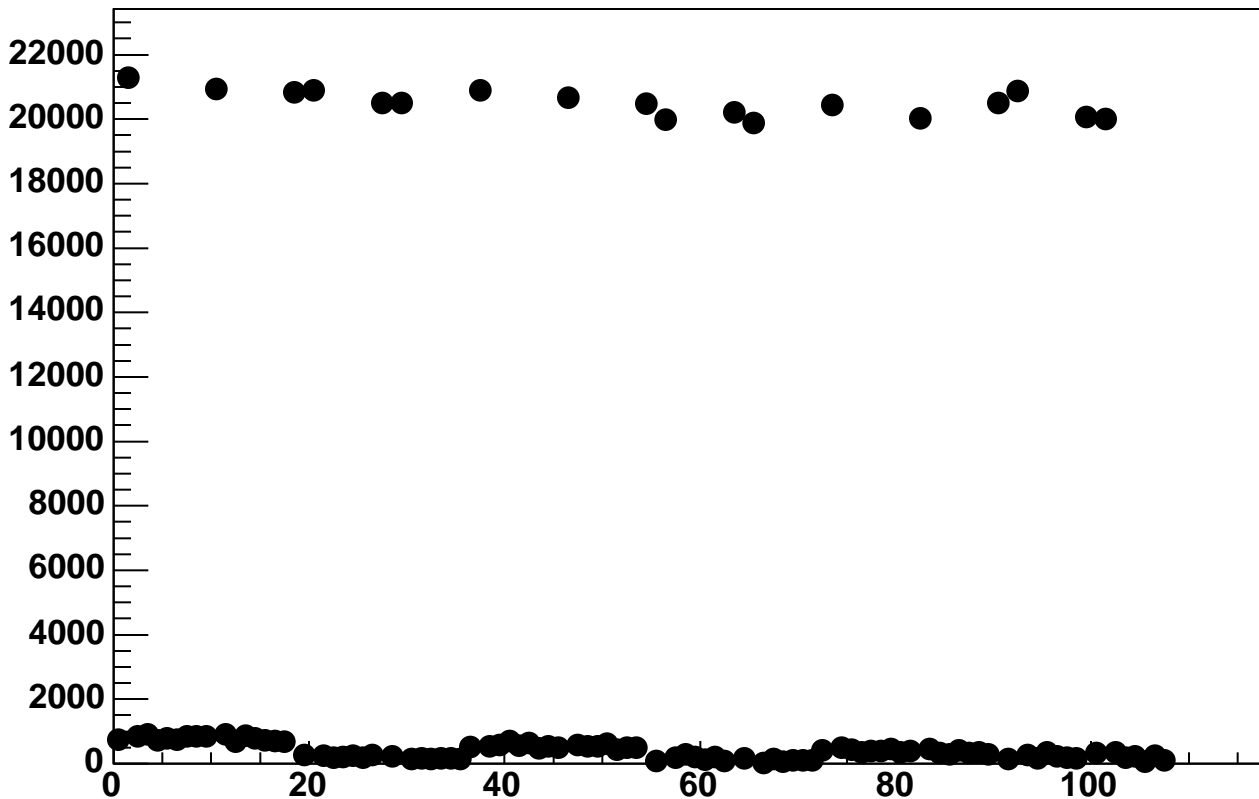
Enable 3, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



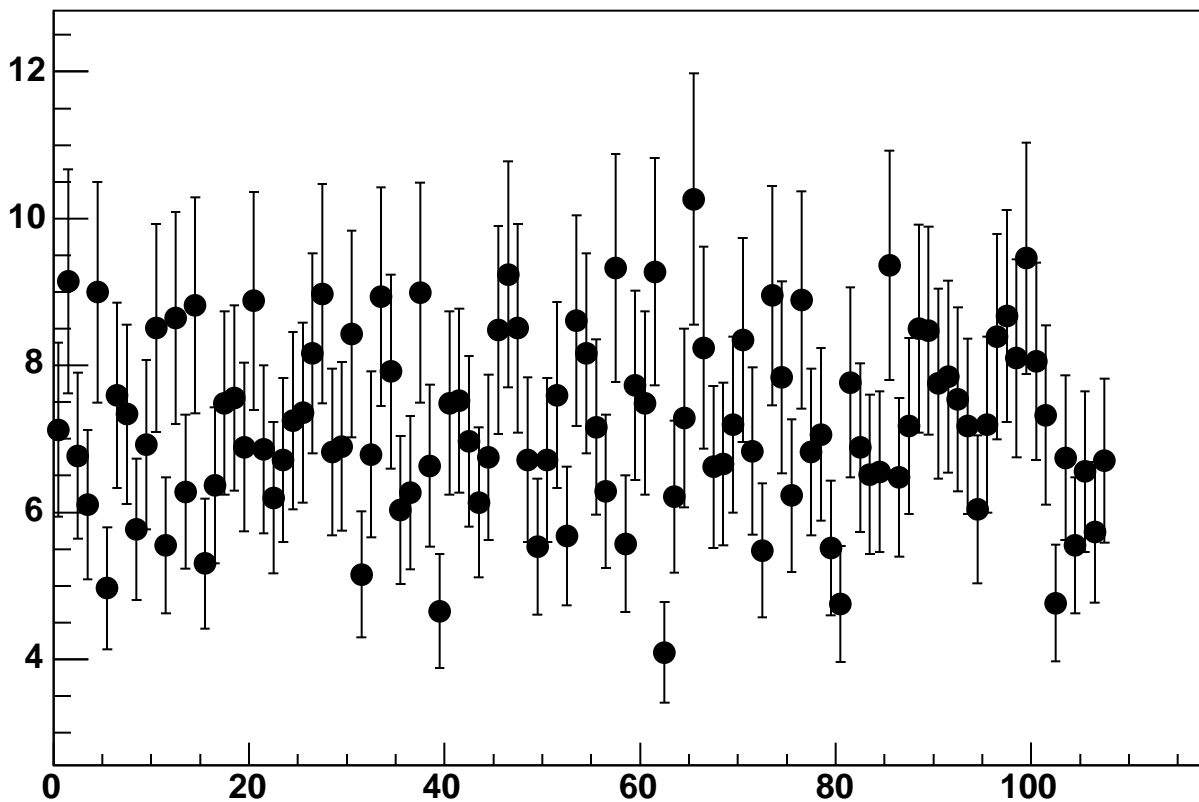
Enable 3, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



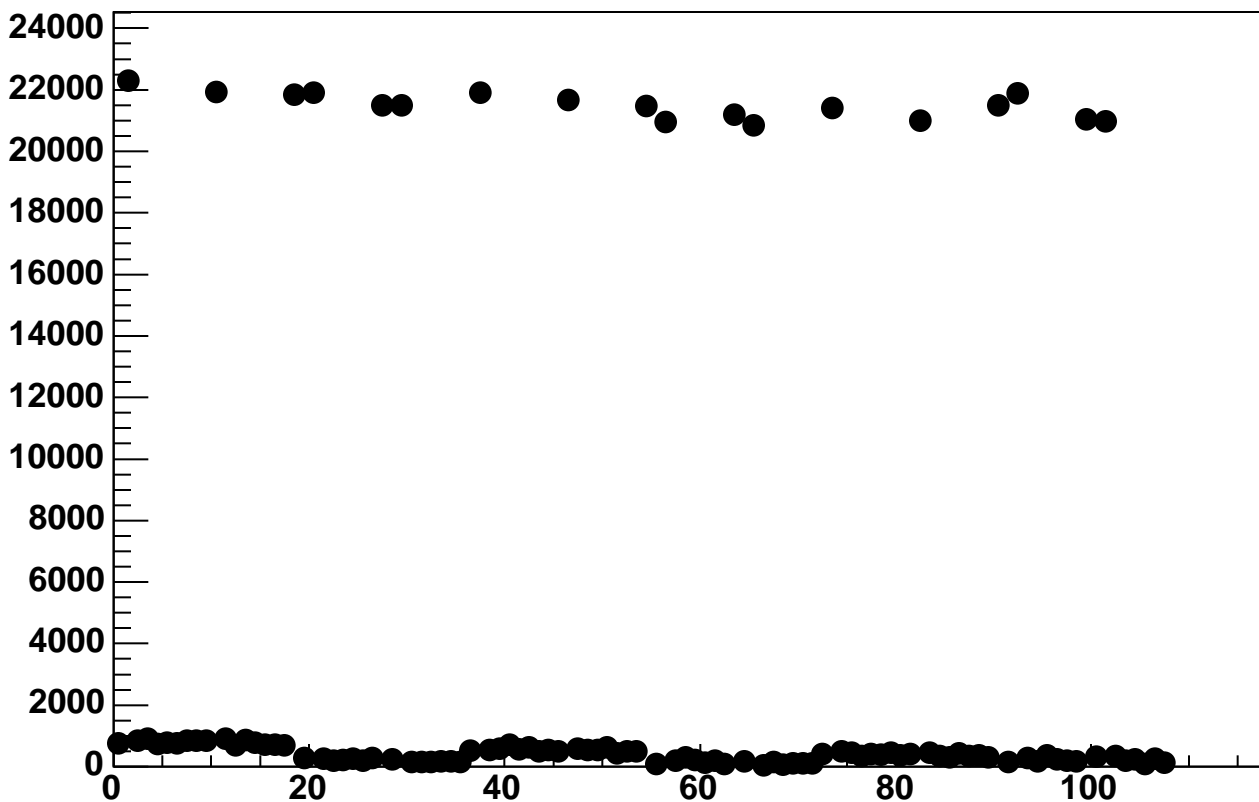
Enable 3, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



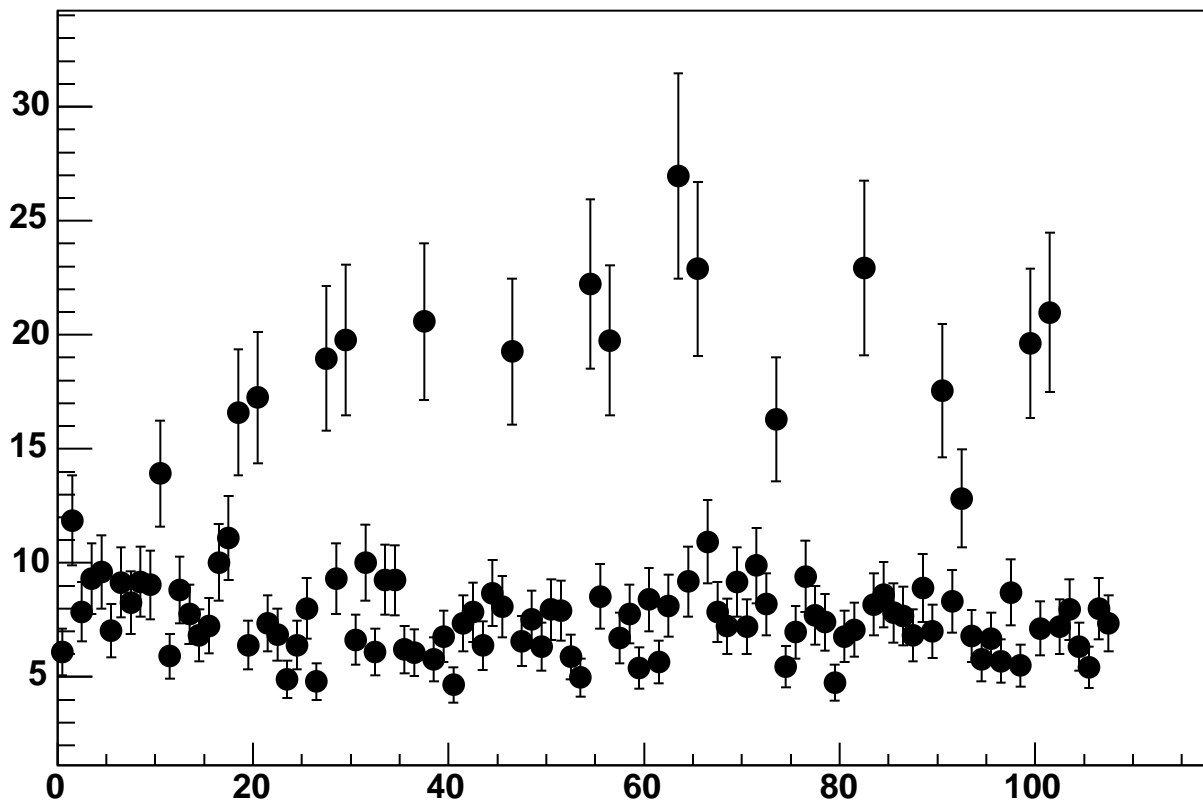
Enable 3, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



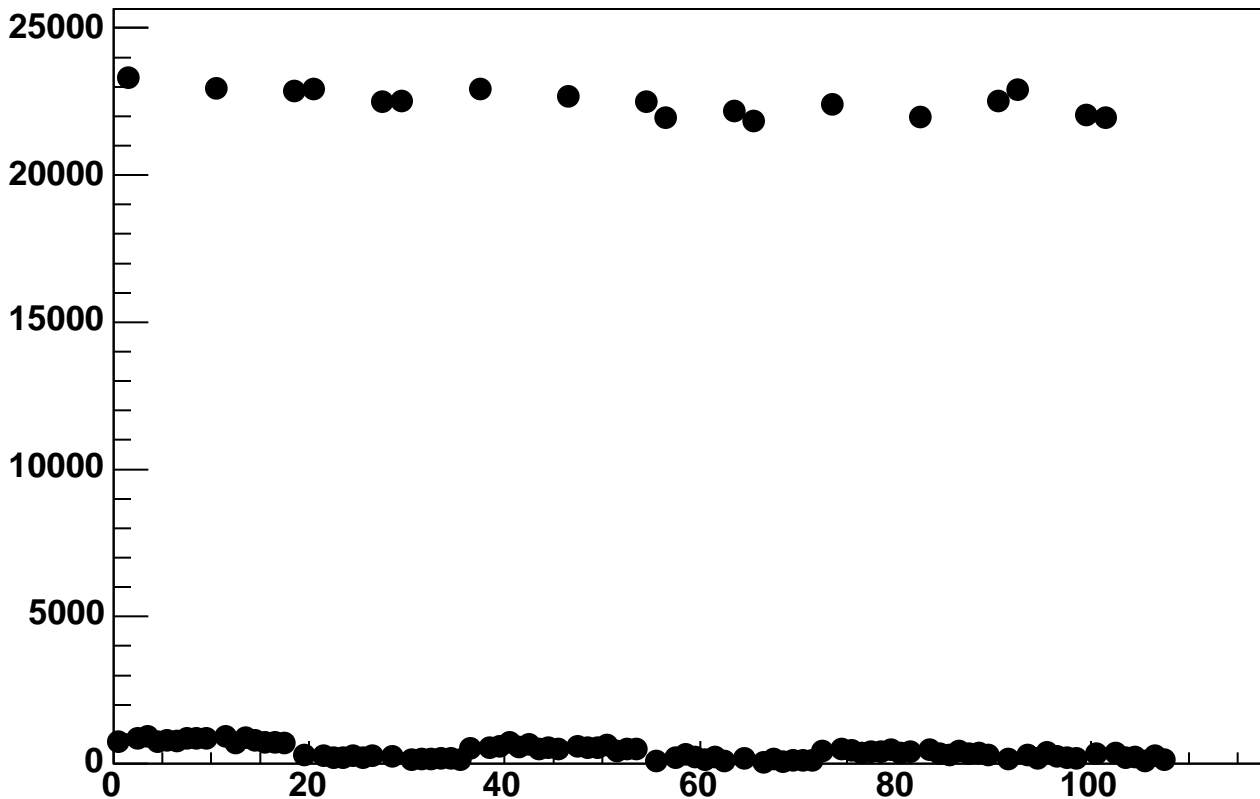
Enable 3, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



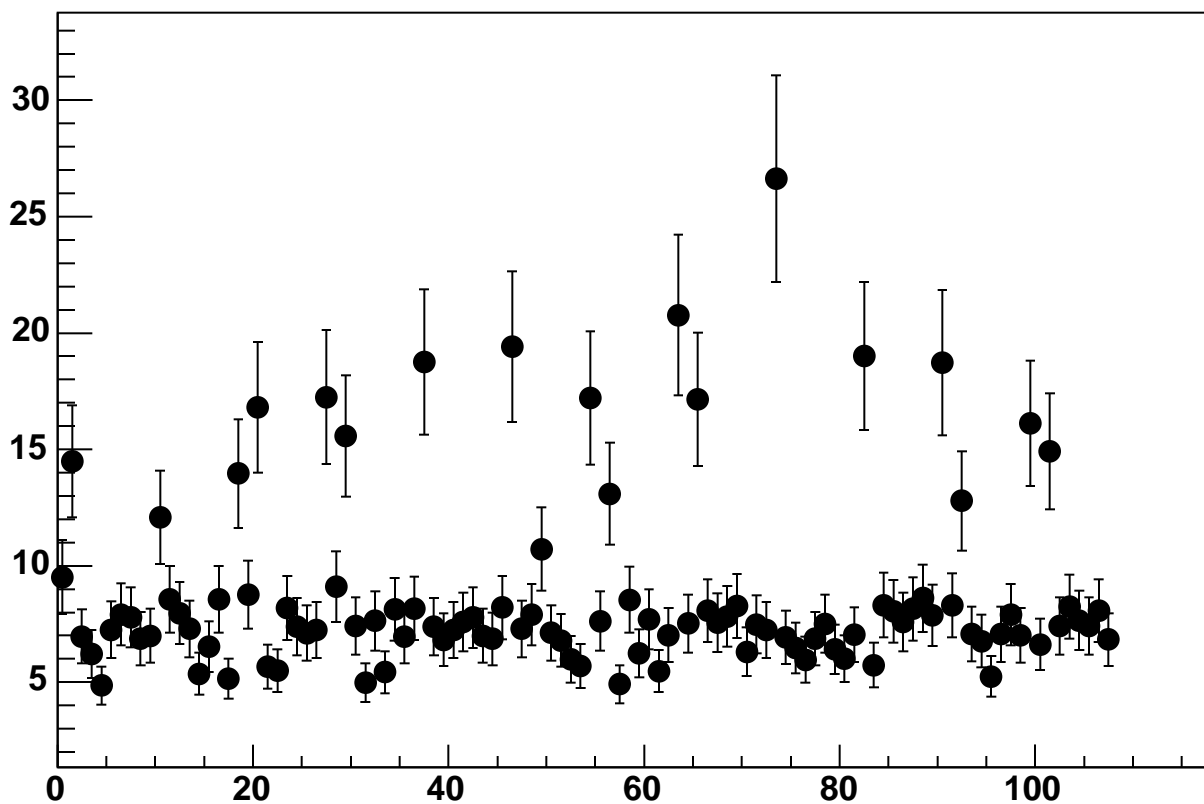
Enable 3, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



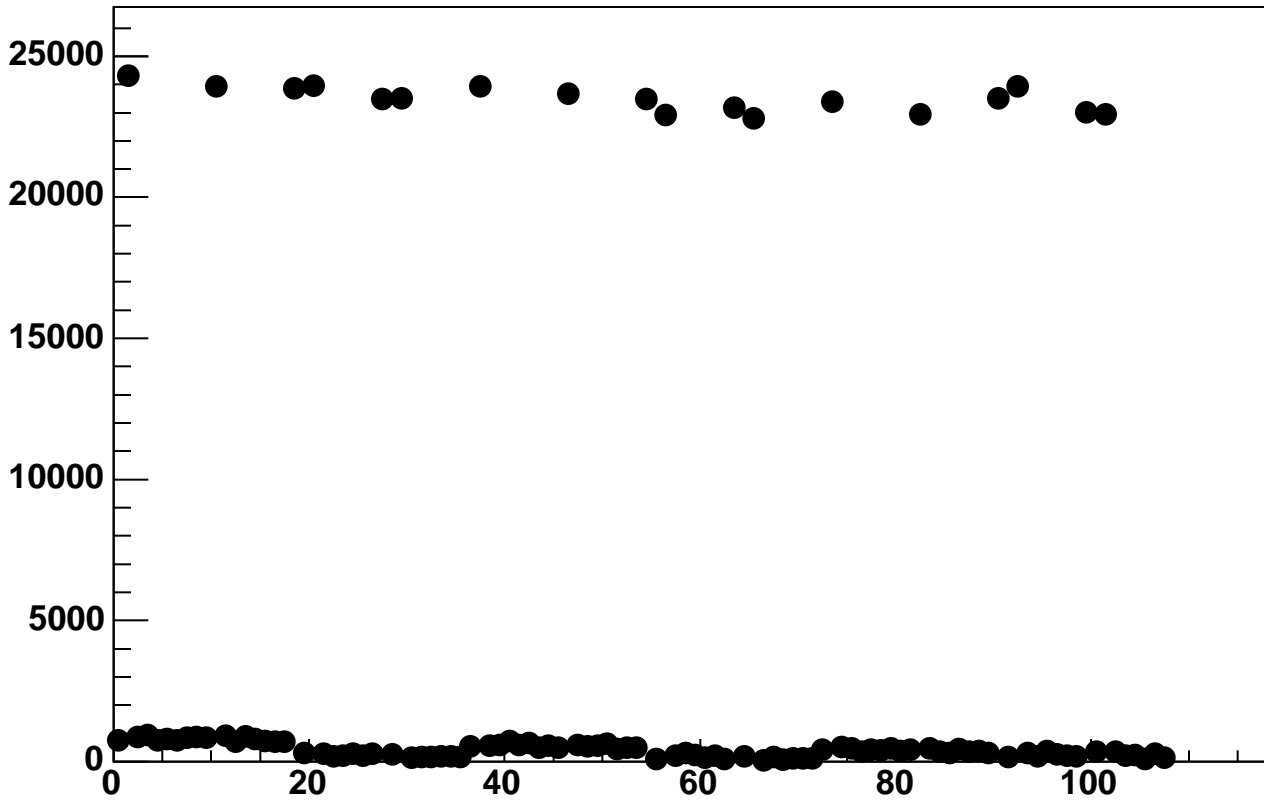
Enable 3, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



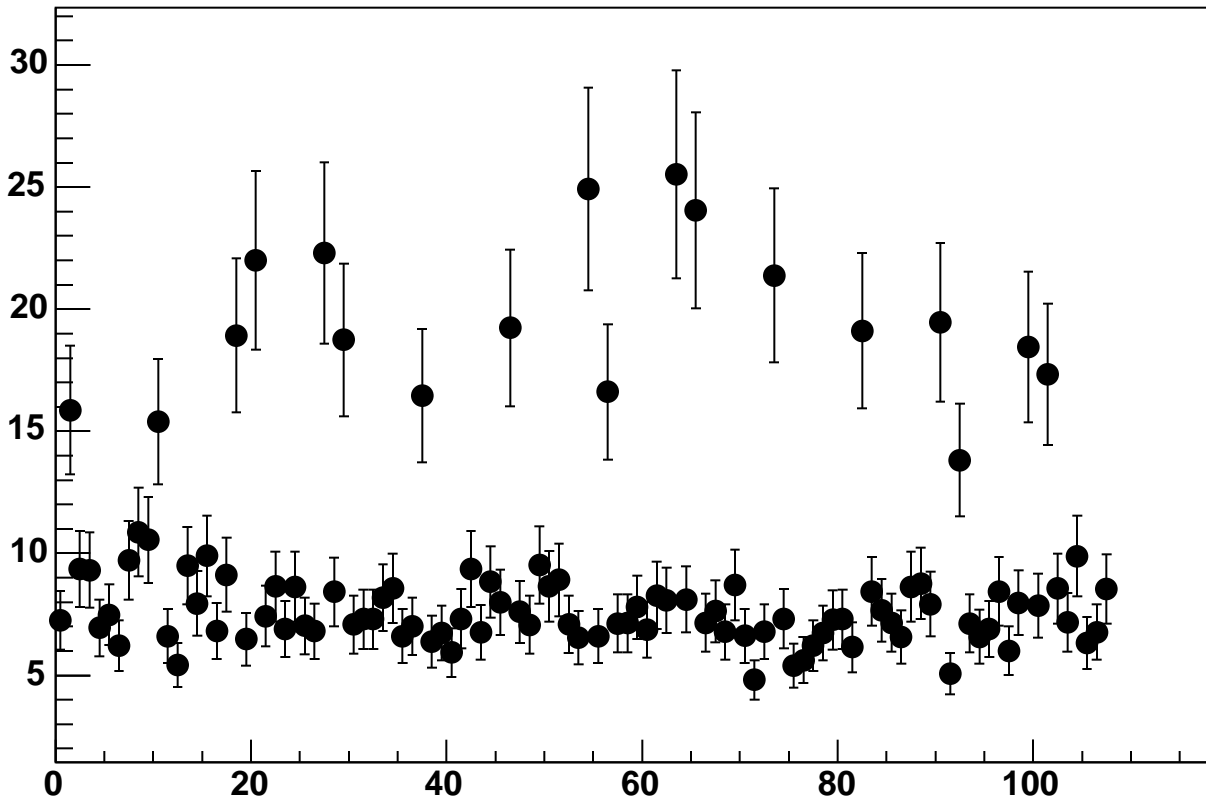
Enable 3, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



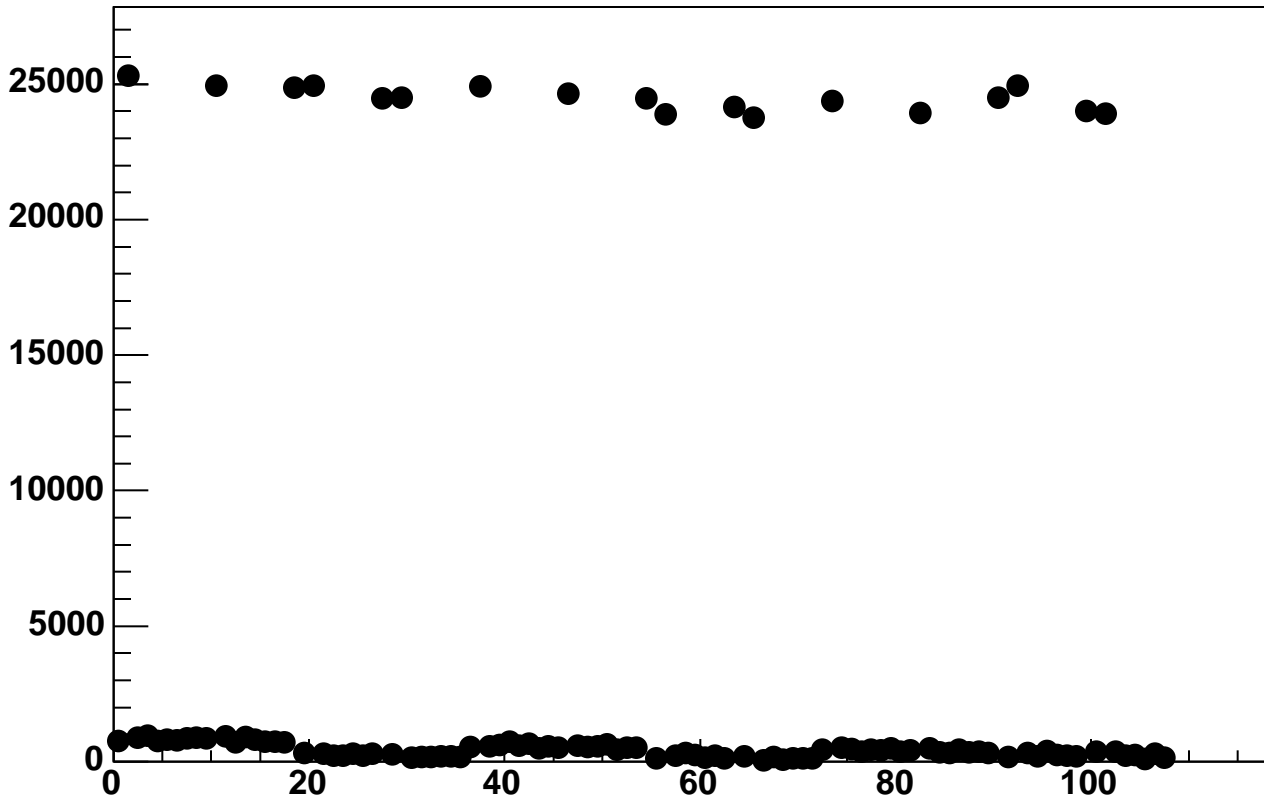
Enable 3, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



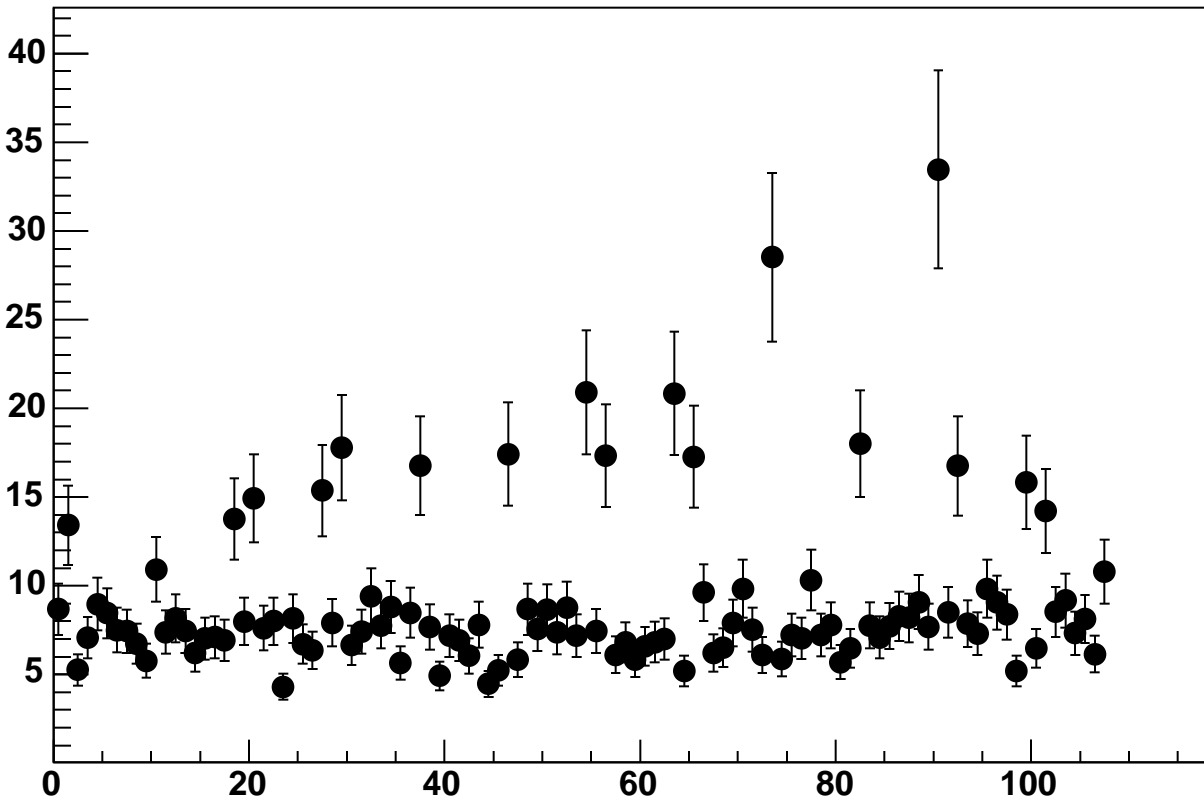
Enable 3, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



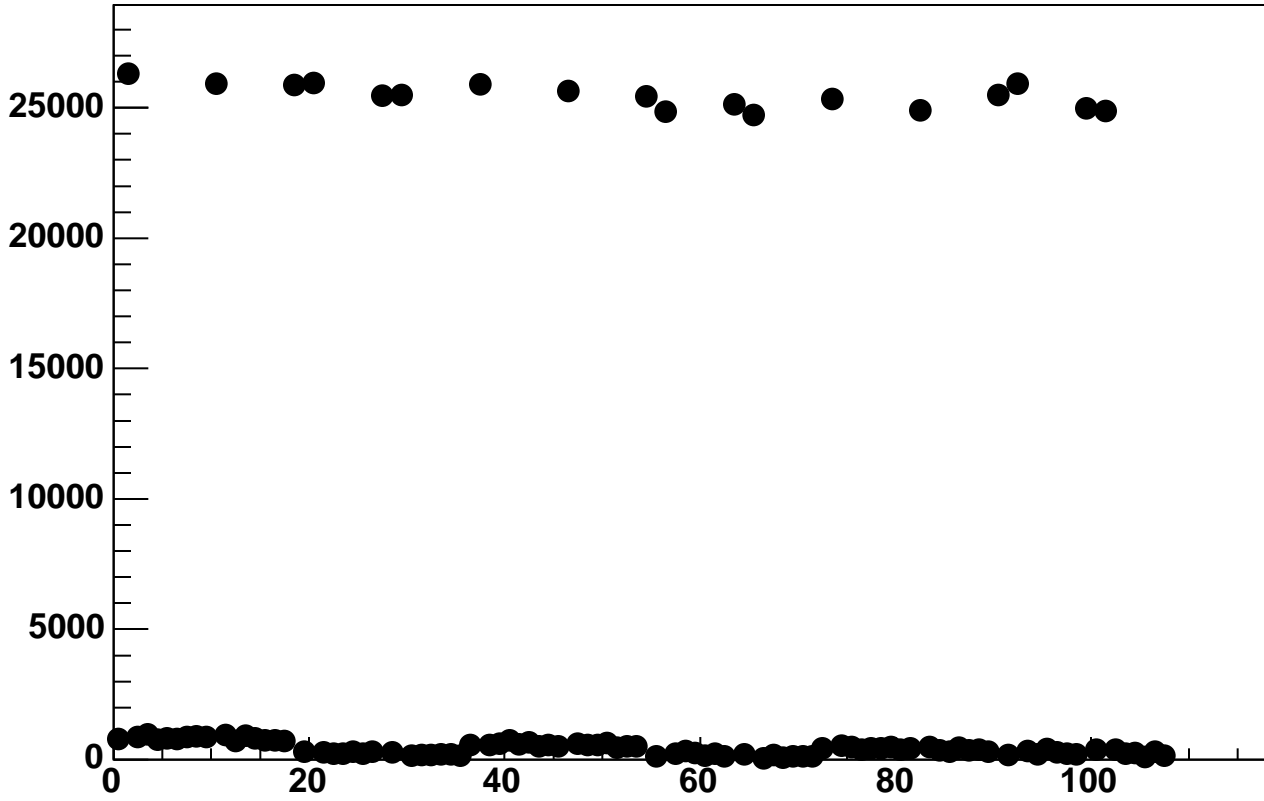
Enable 3, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



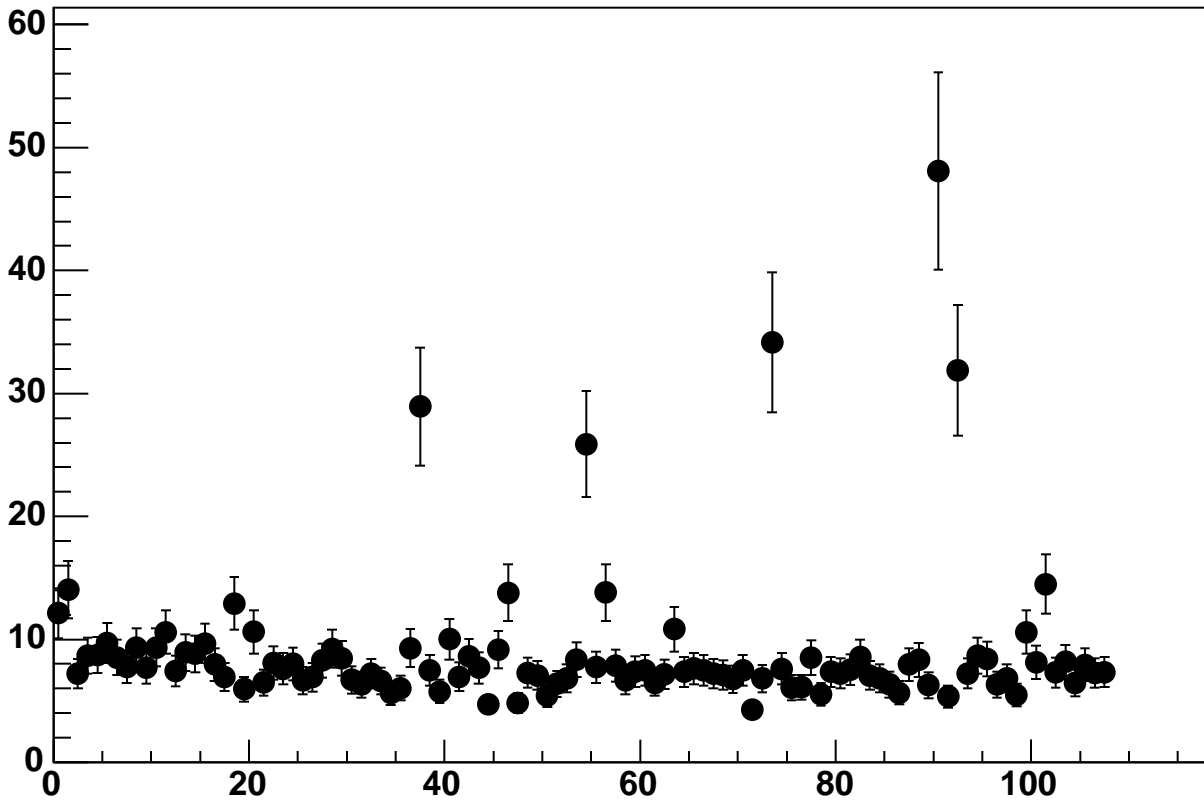
Enable 3, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



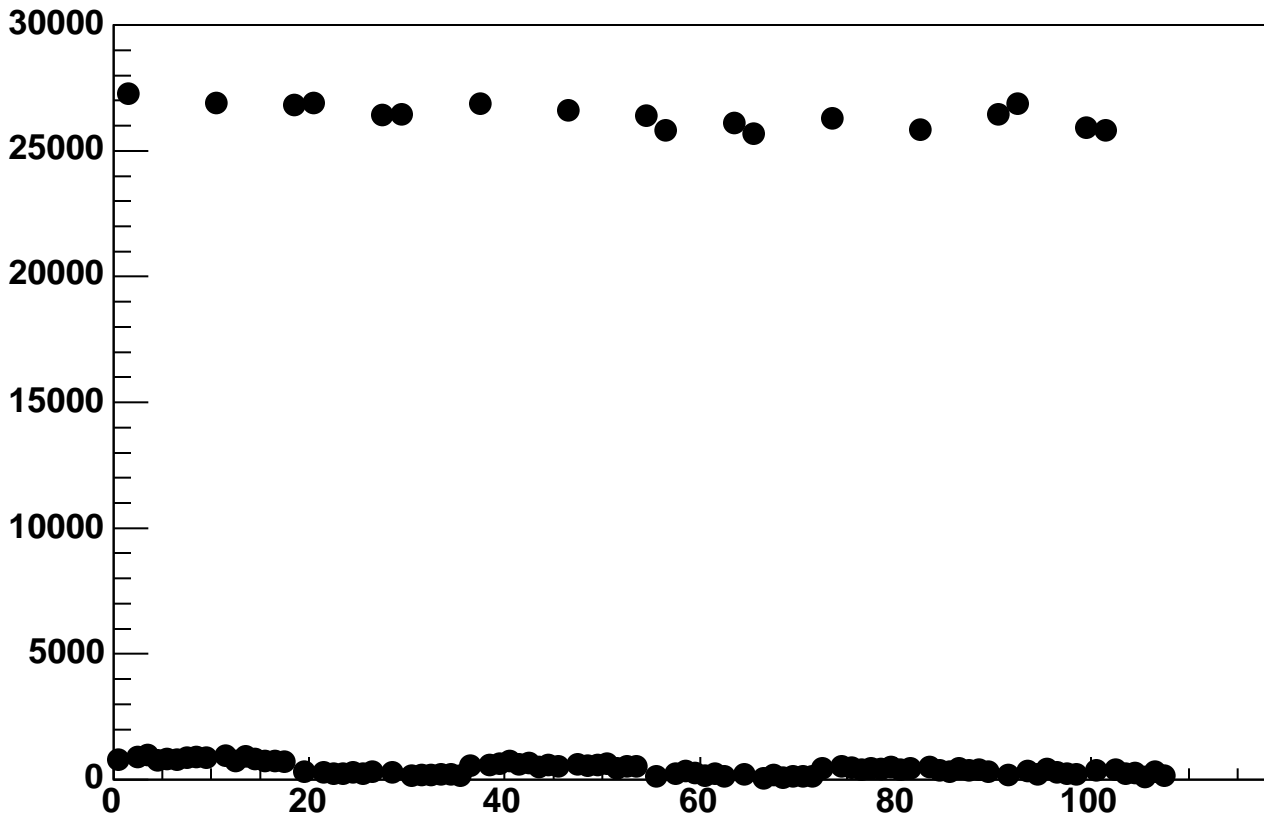
Enable 3, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



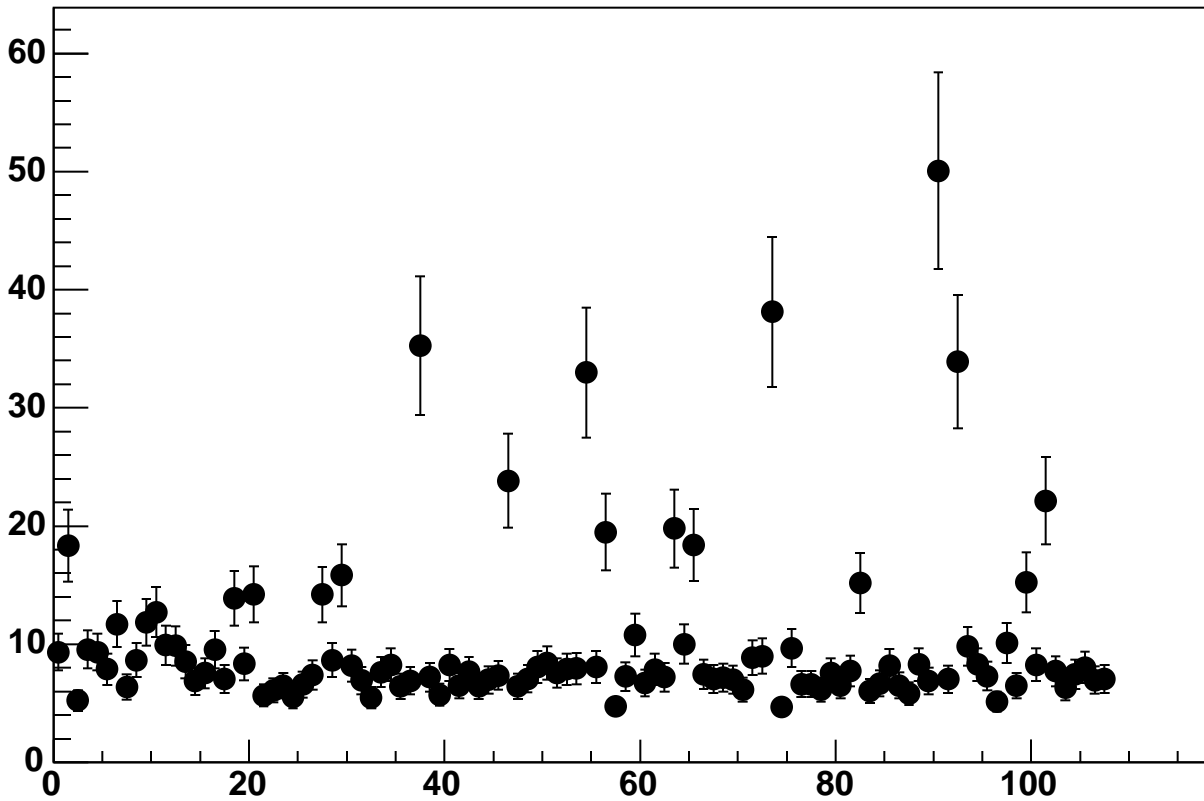
Enable 3, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



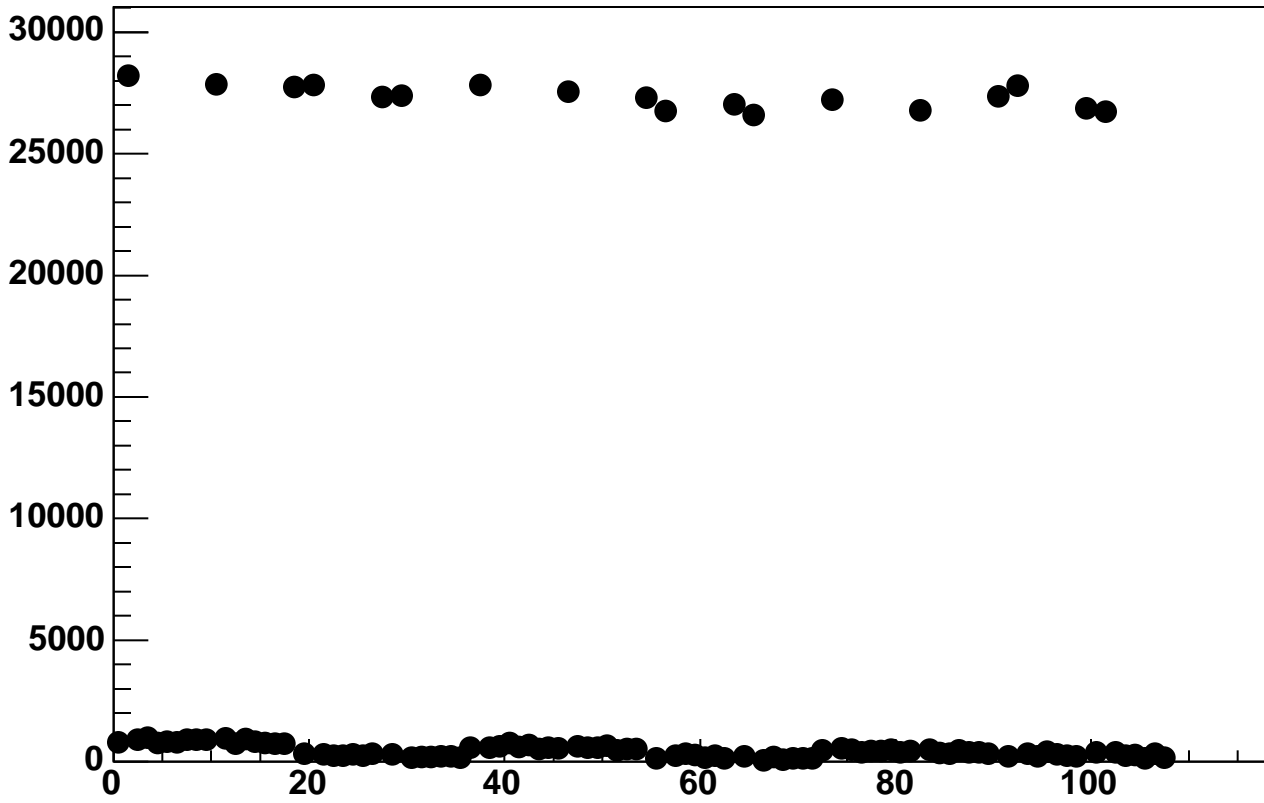
Enable 3, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



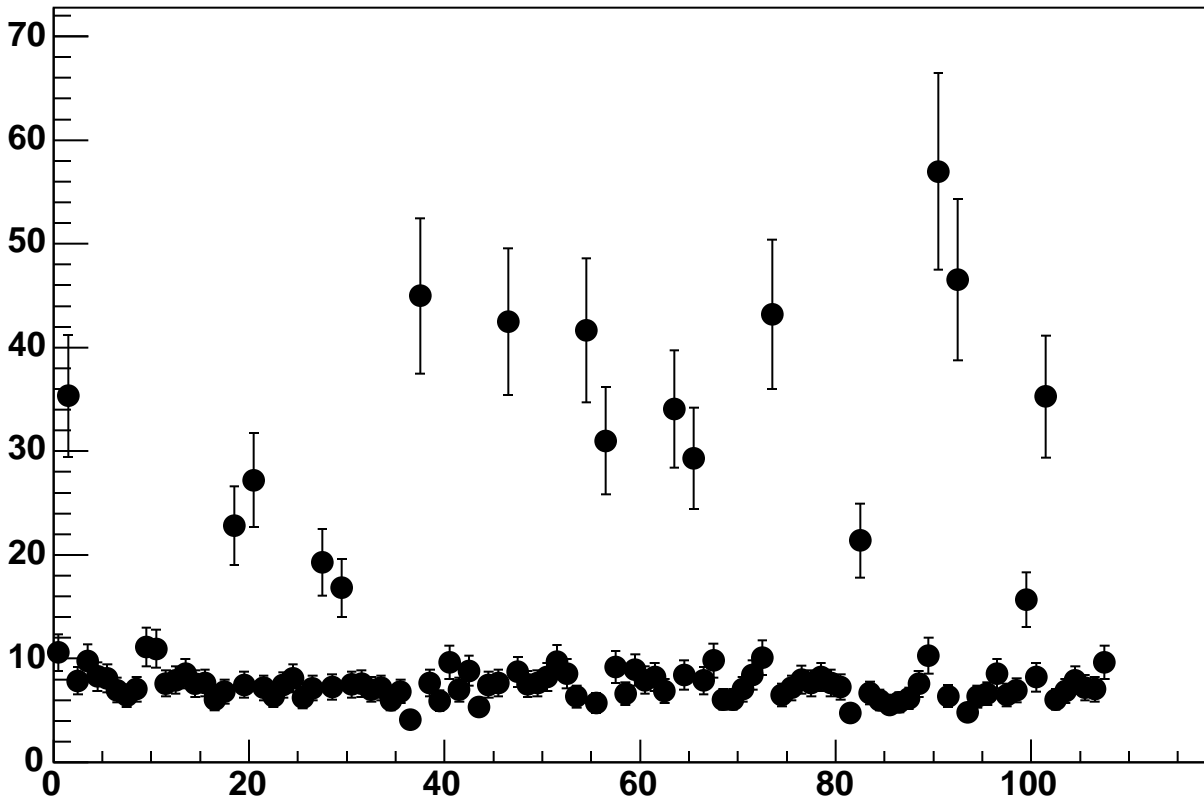
Enable 3, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



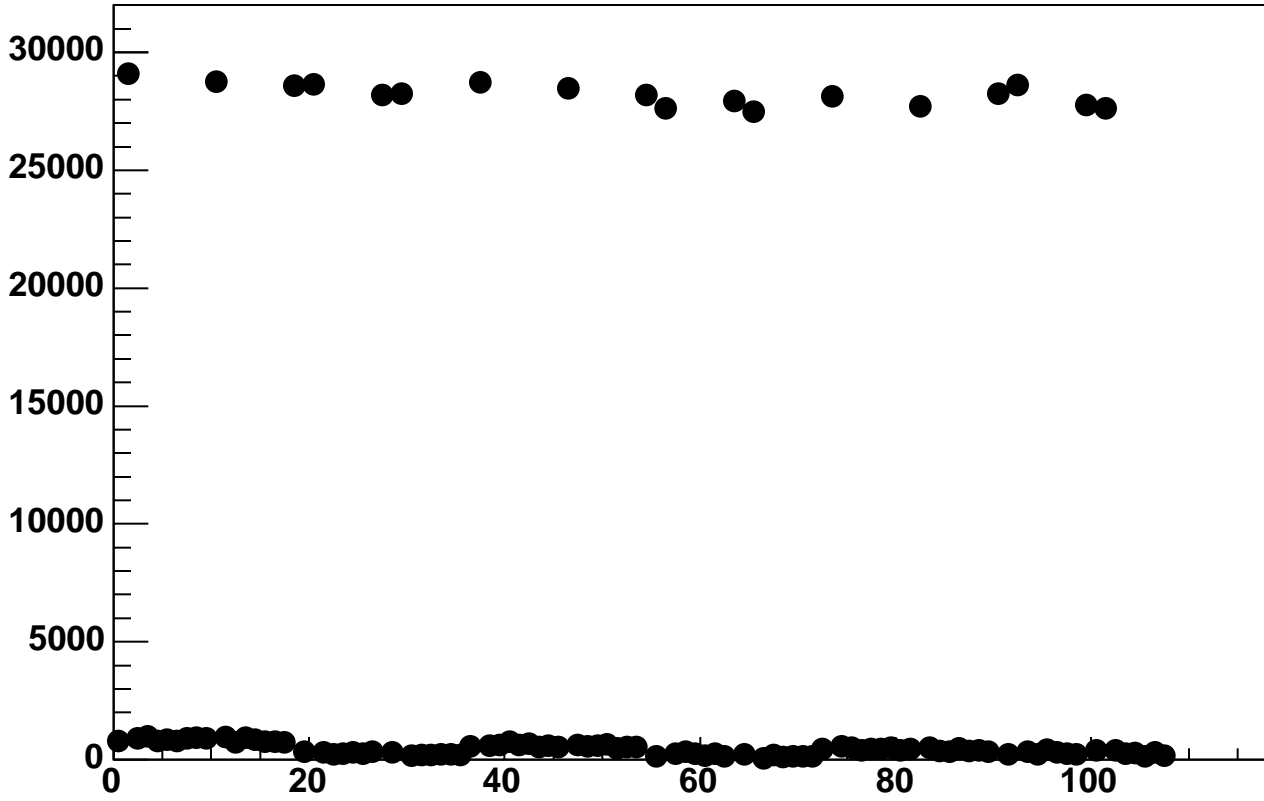
Enable 3, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



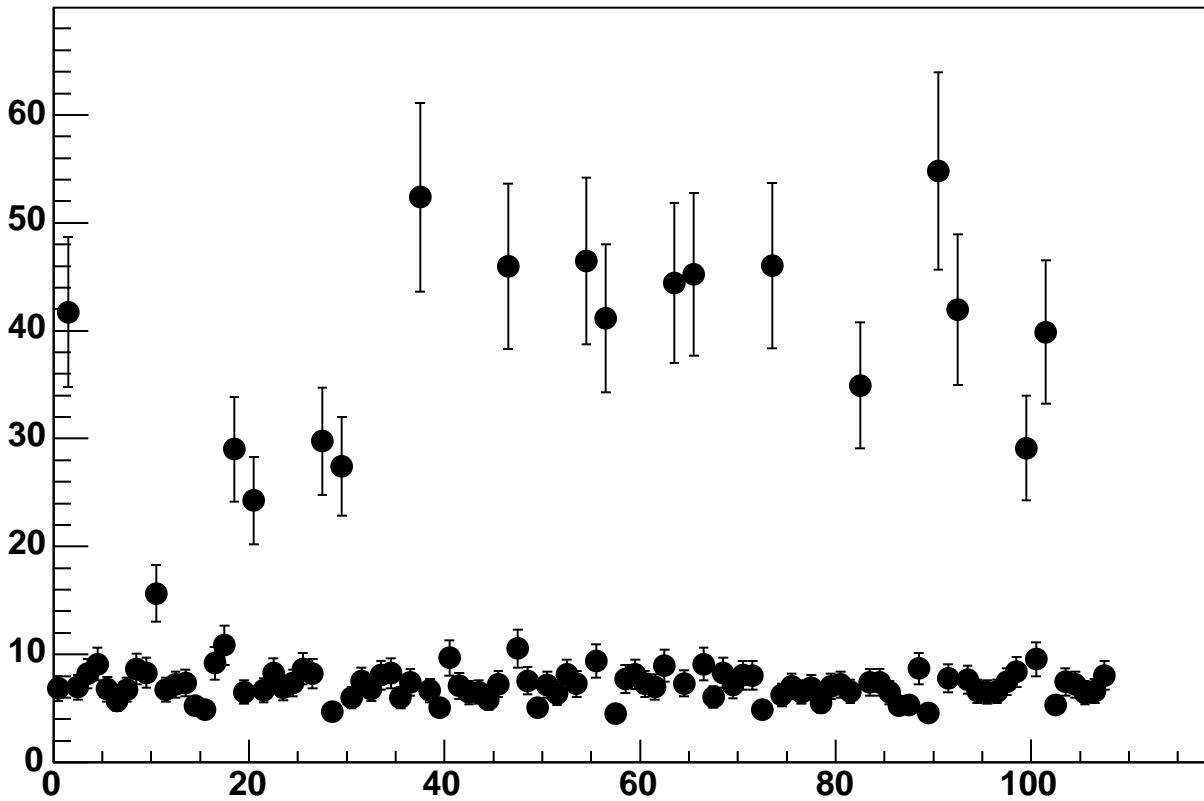
Enable 3, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



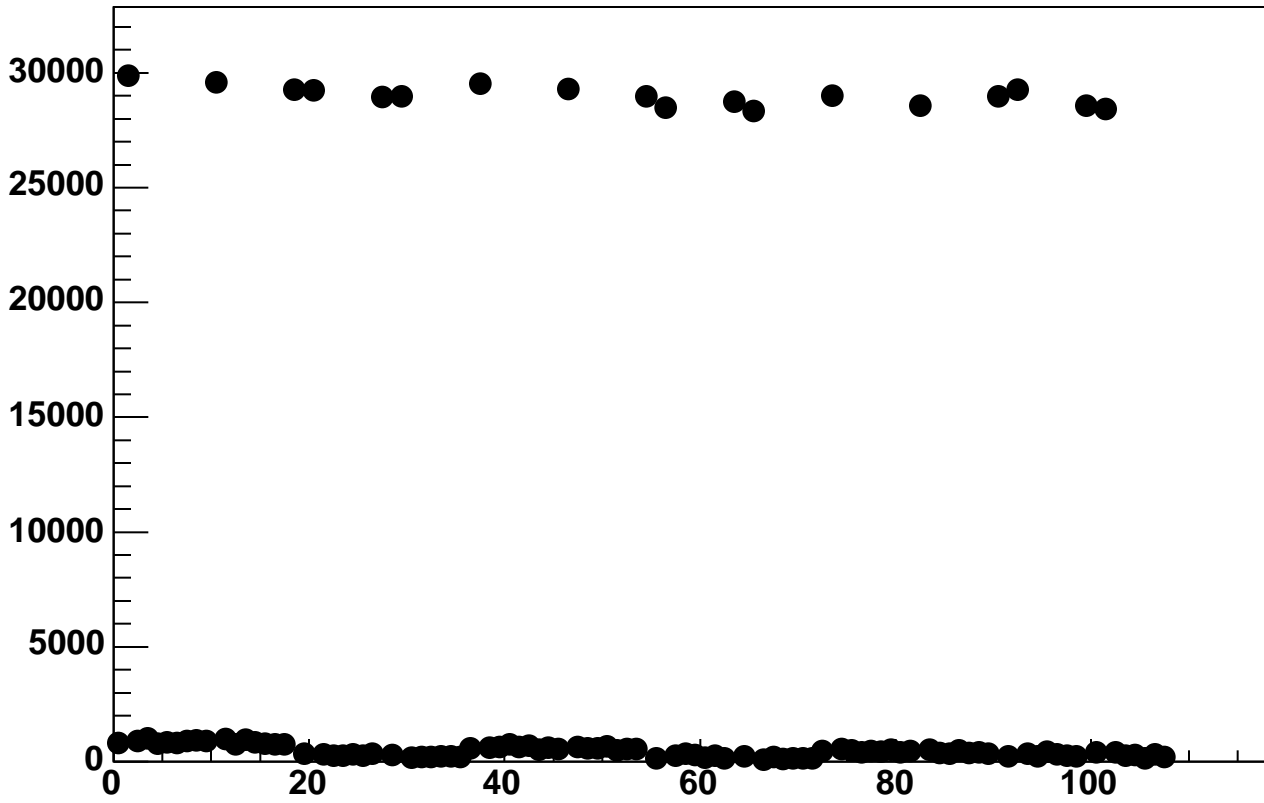
Enable 3, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



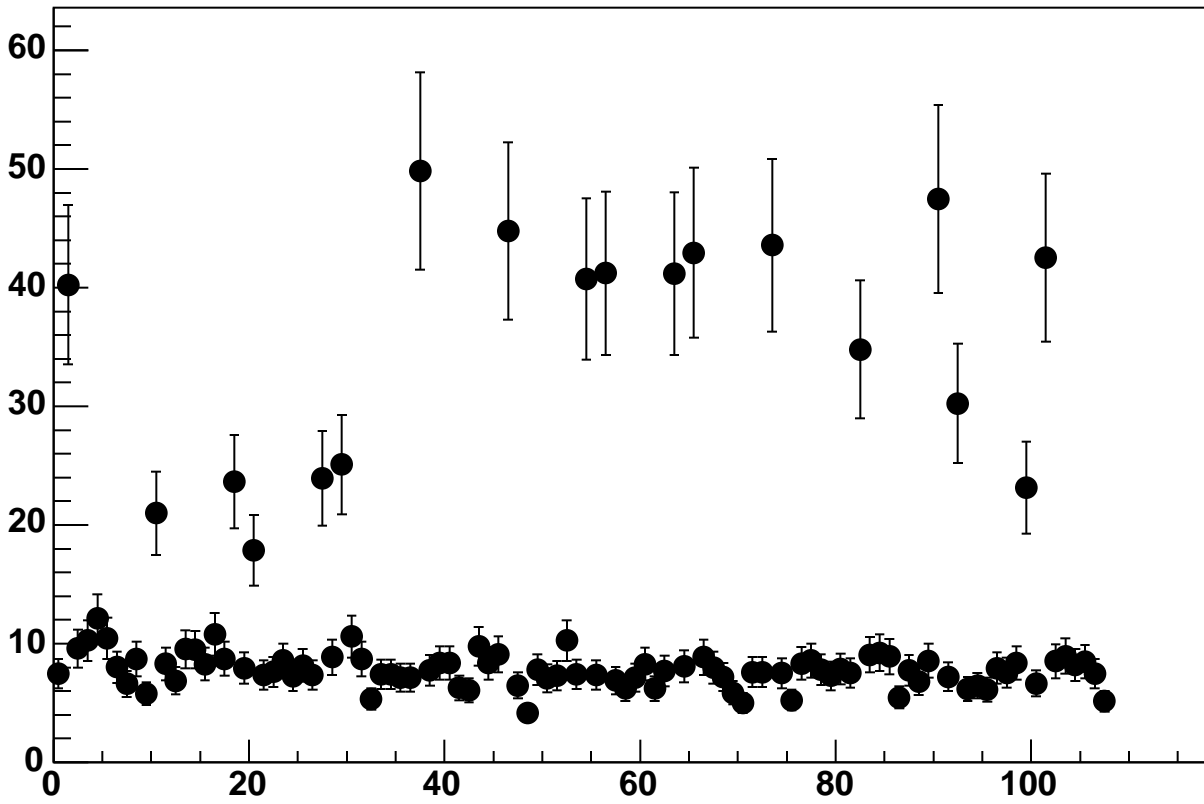
Enable 3, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



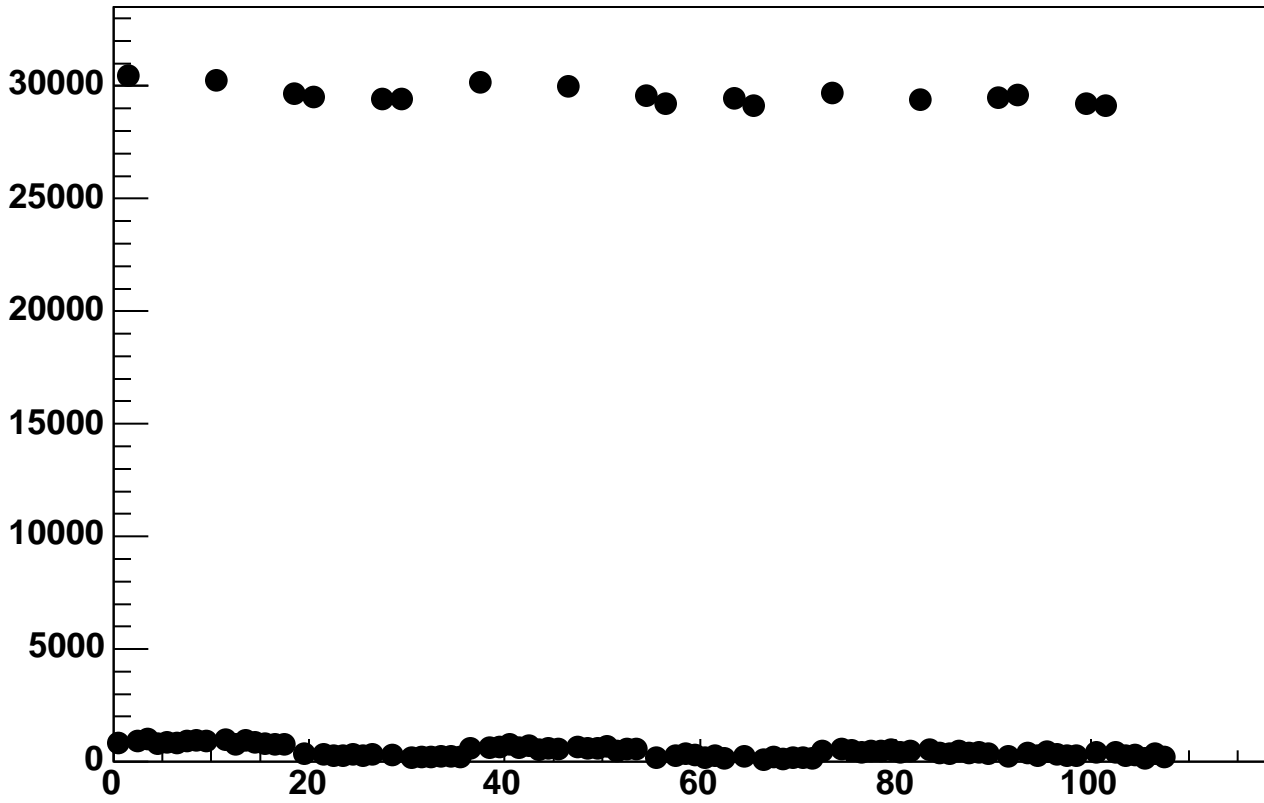
Enable 3, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



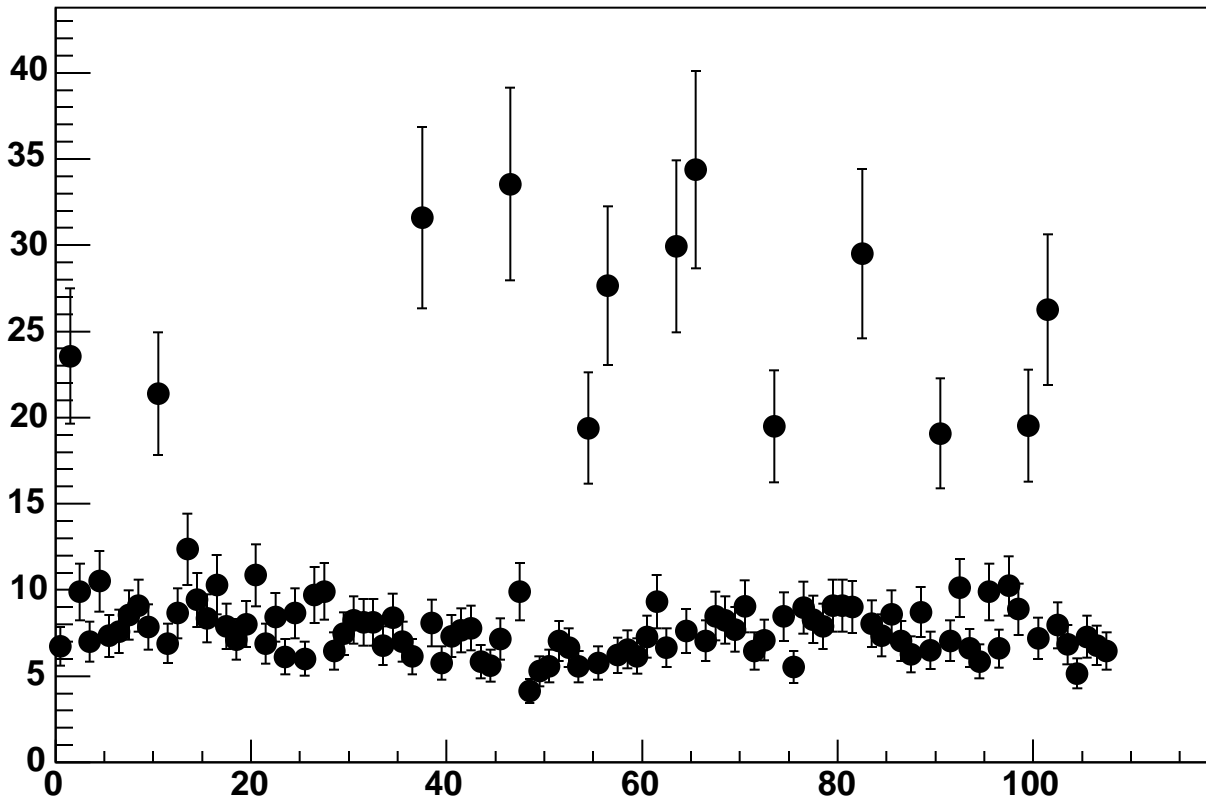
Enable 3, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



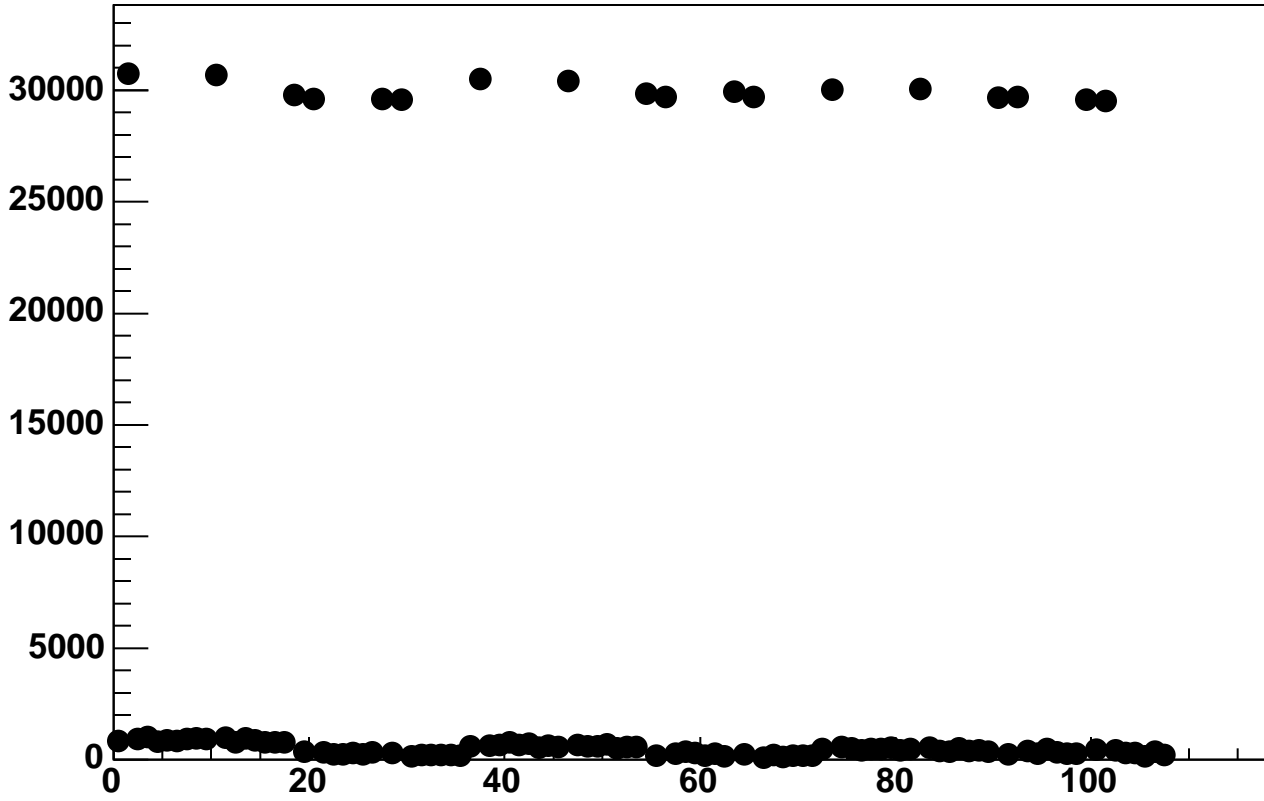
Enable 3, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



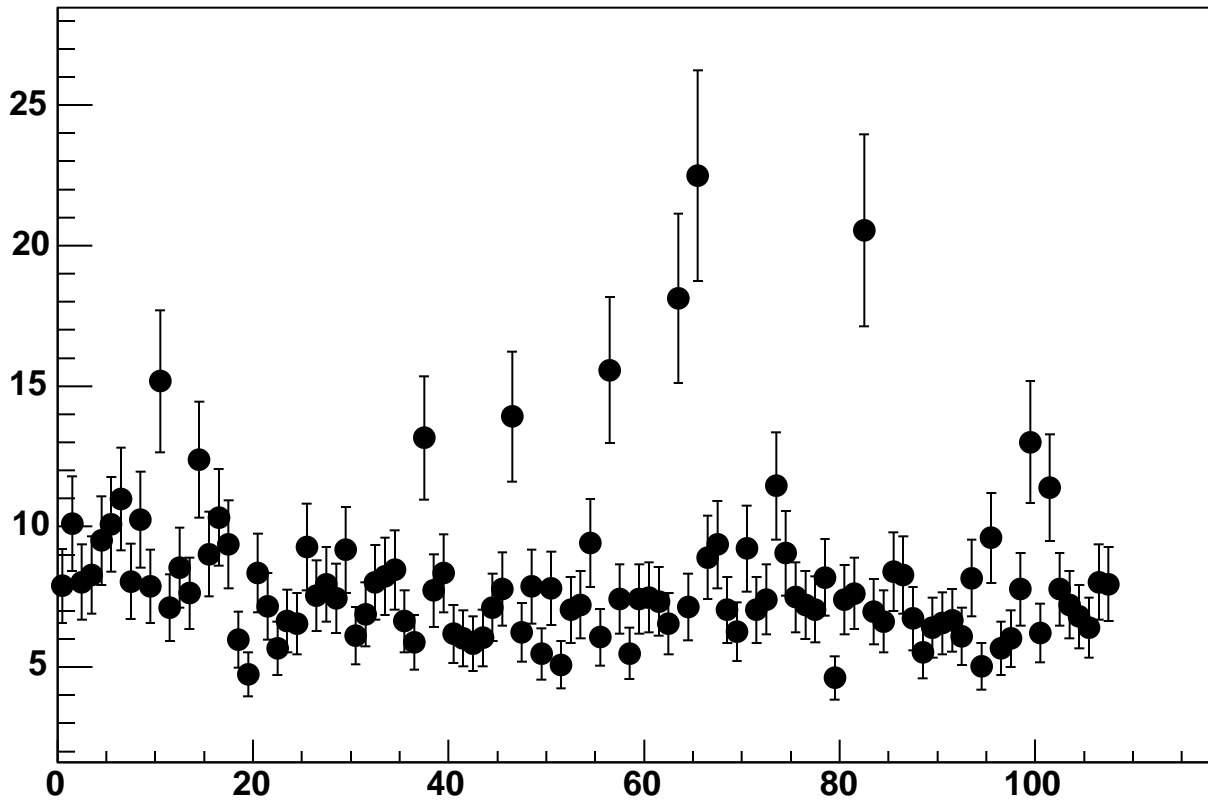
Enable 3, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



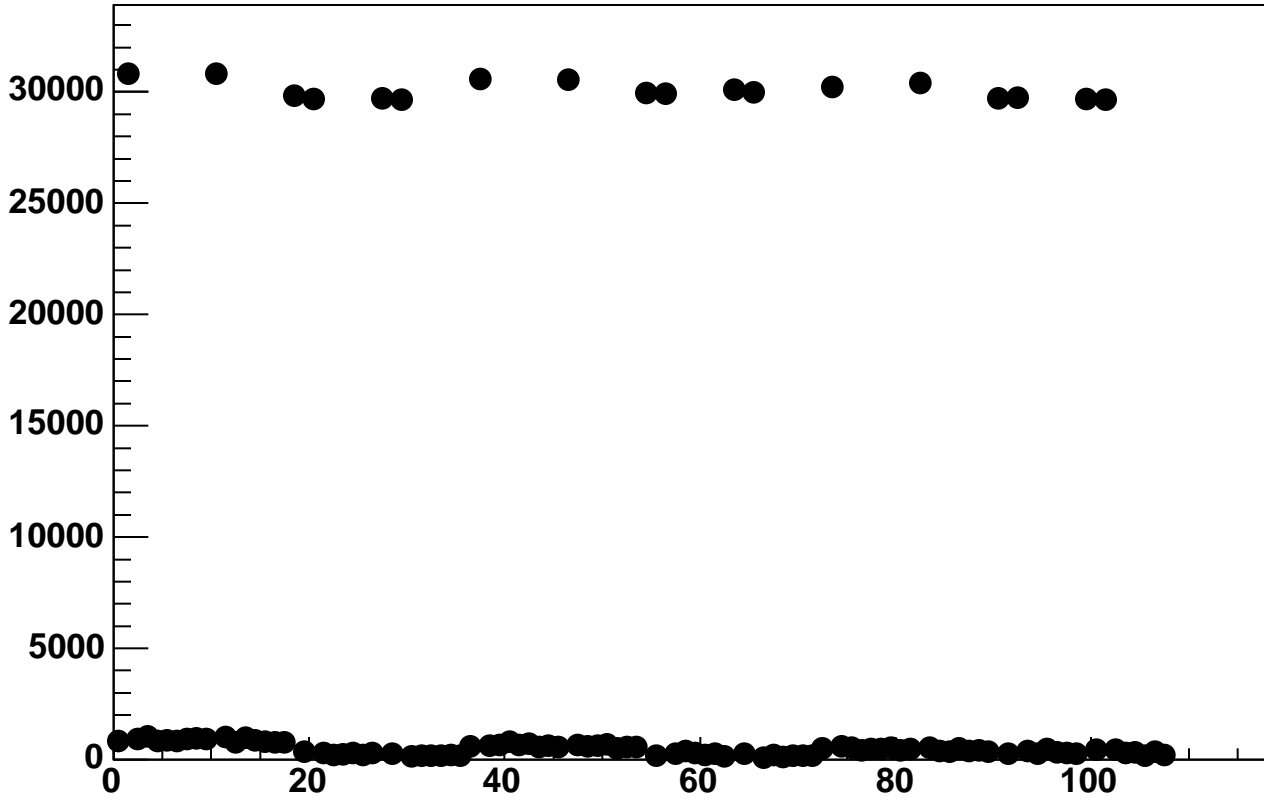
Enable 3, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



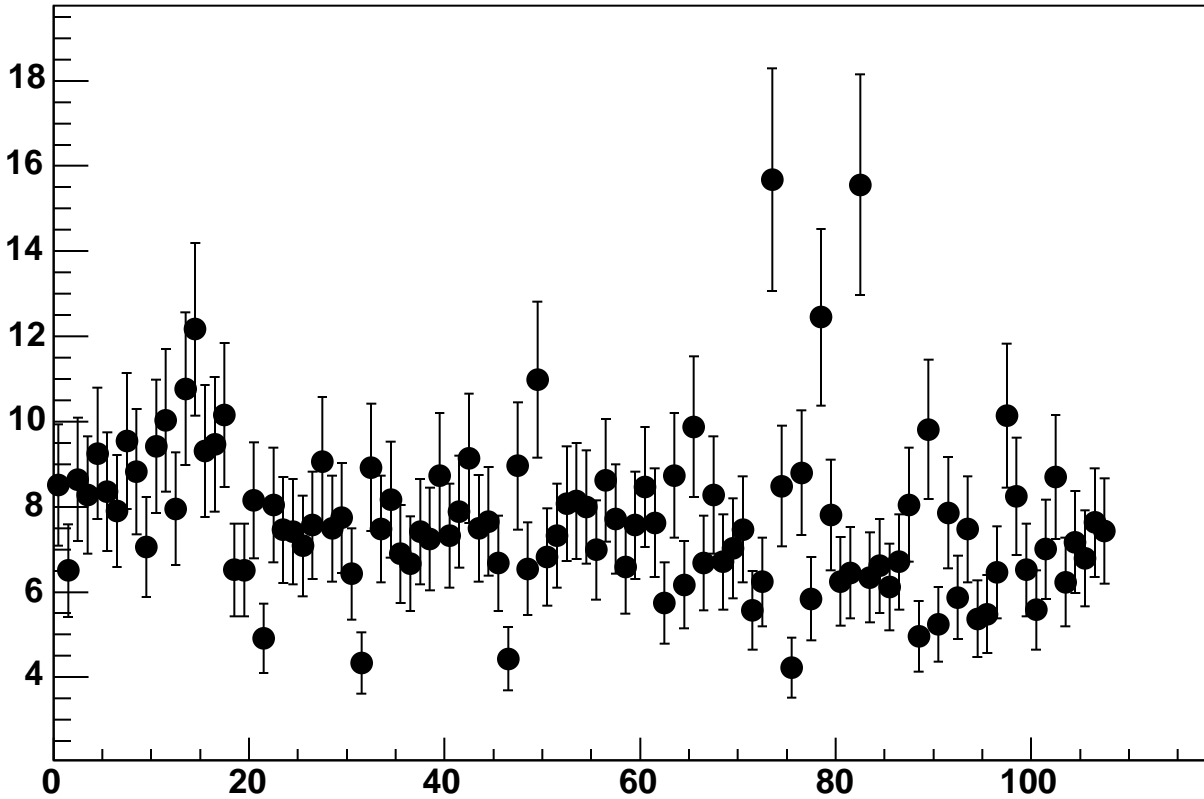
Enable 3, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



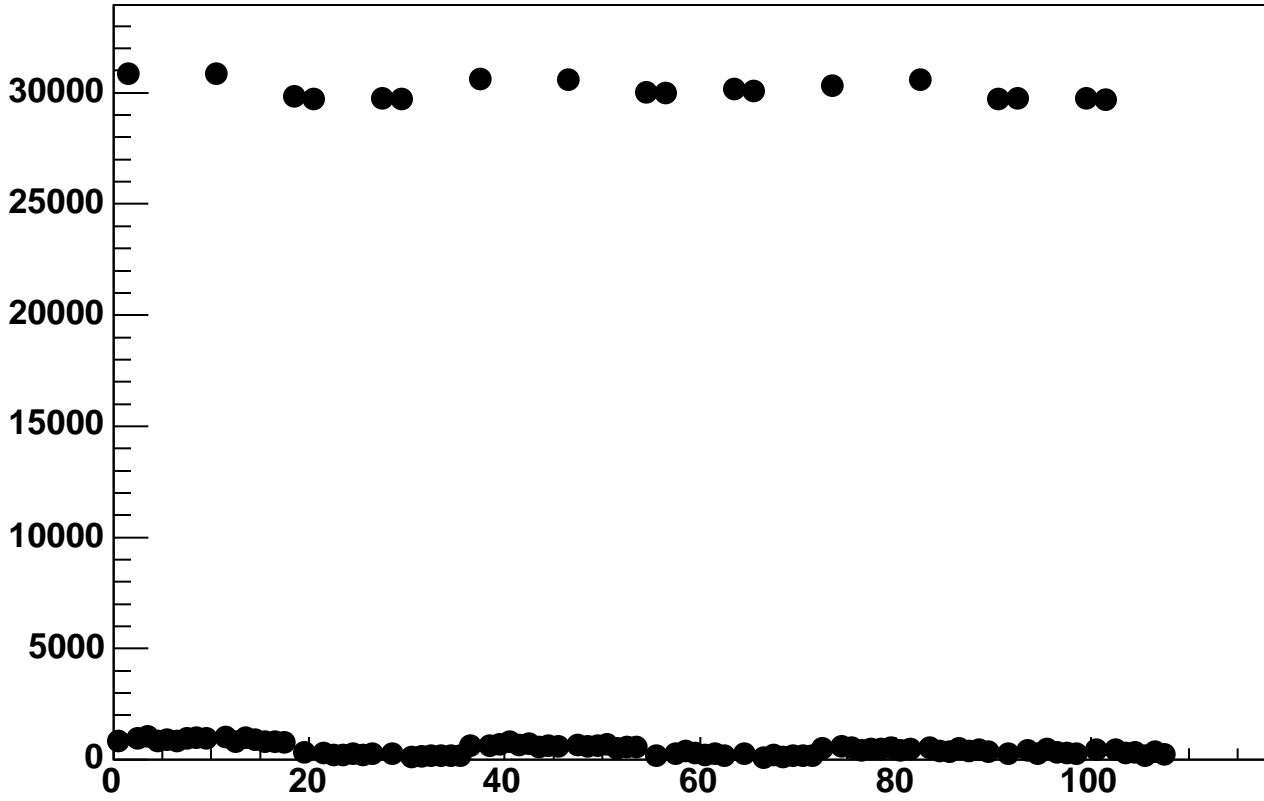
Enable 3, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



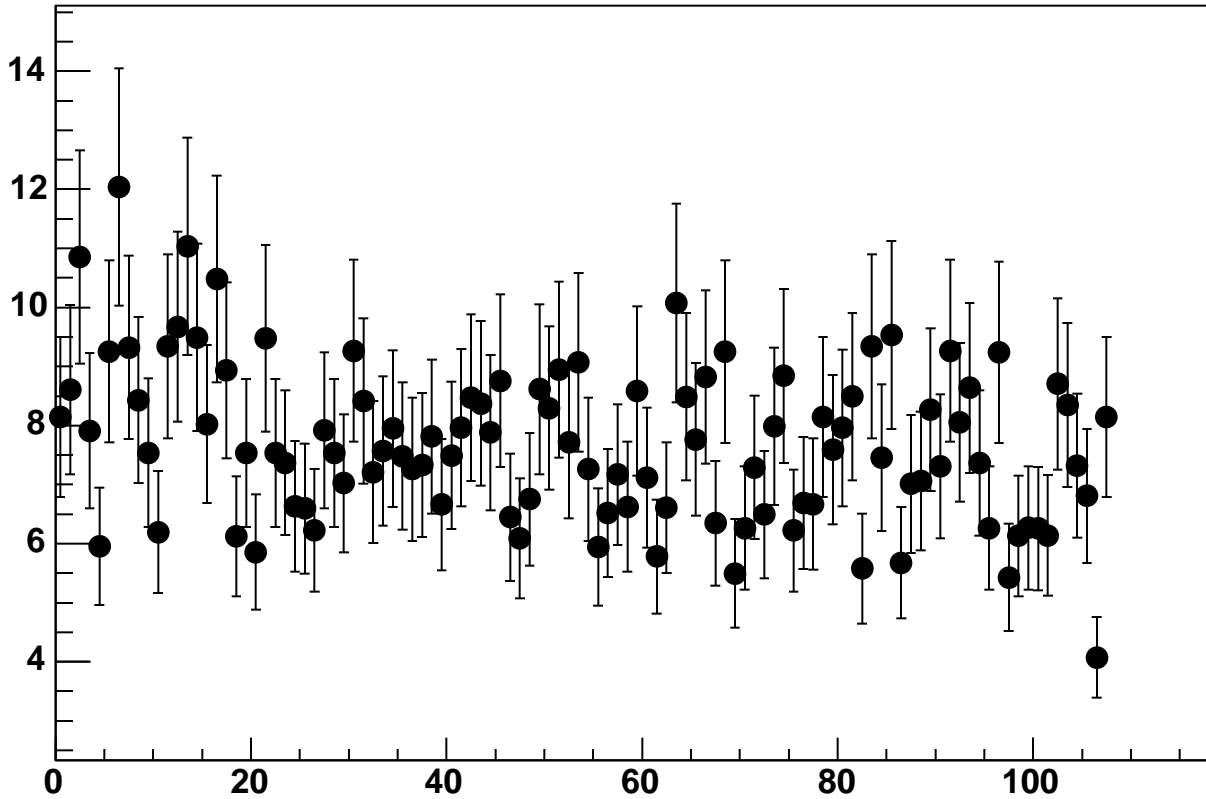
Enable 3, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



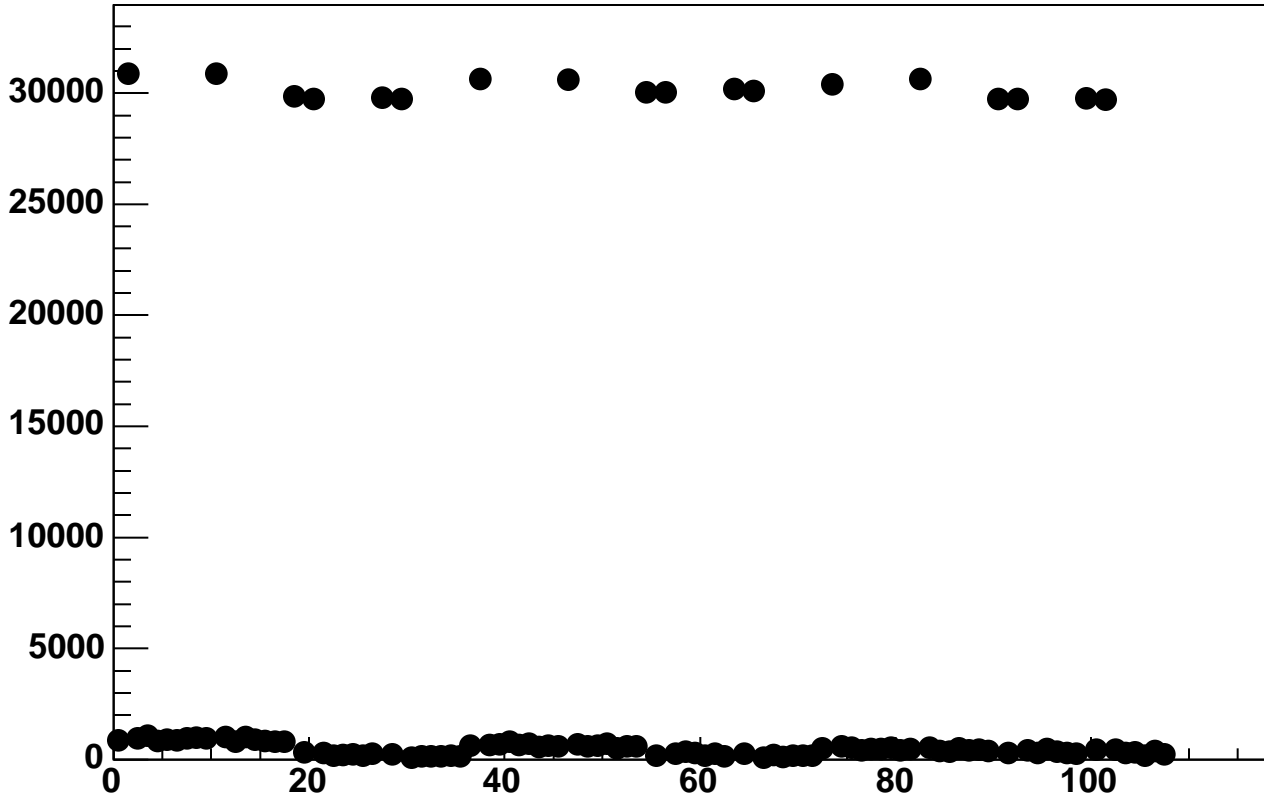
Enable 3, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



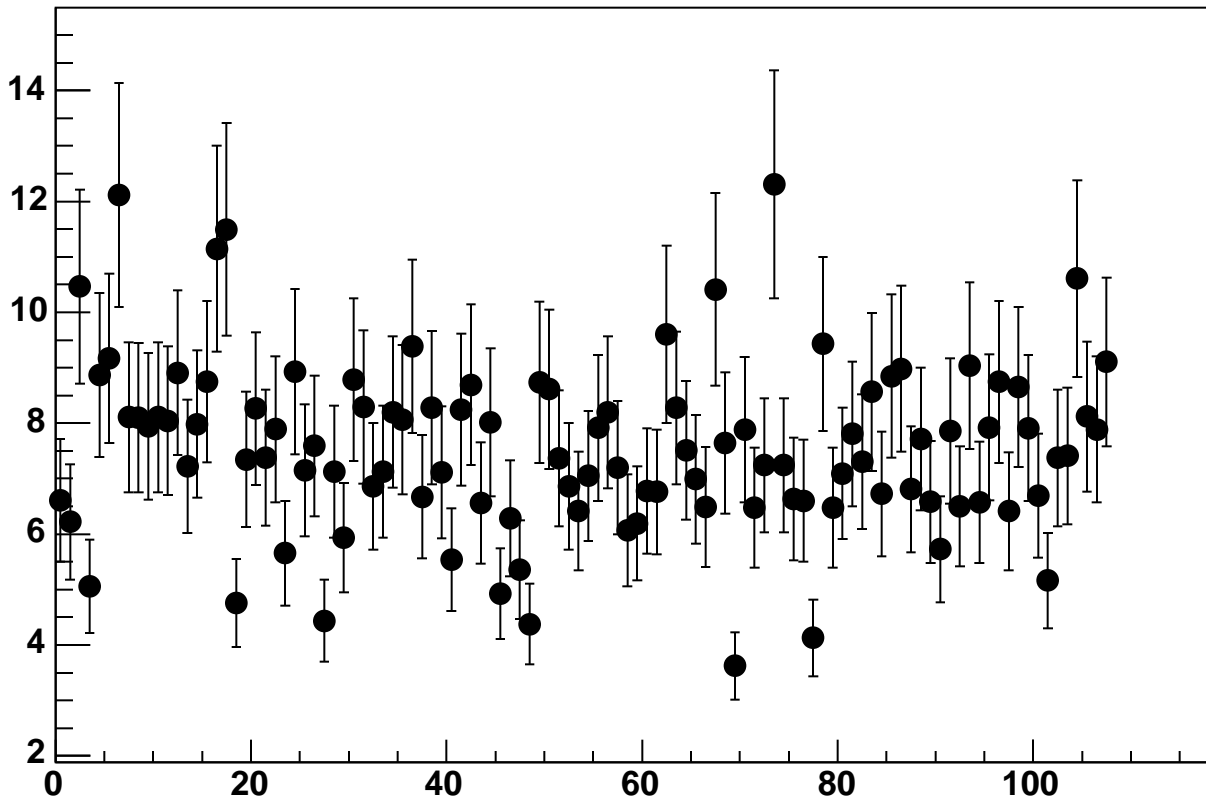
Enable 3, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



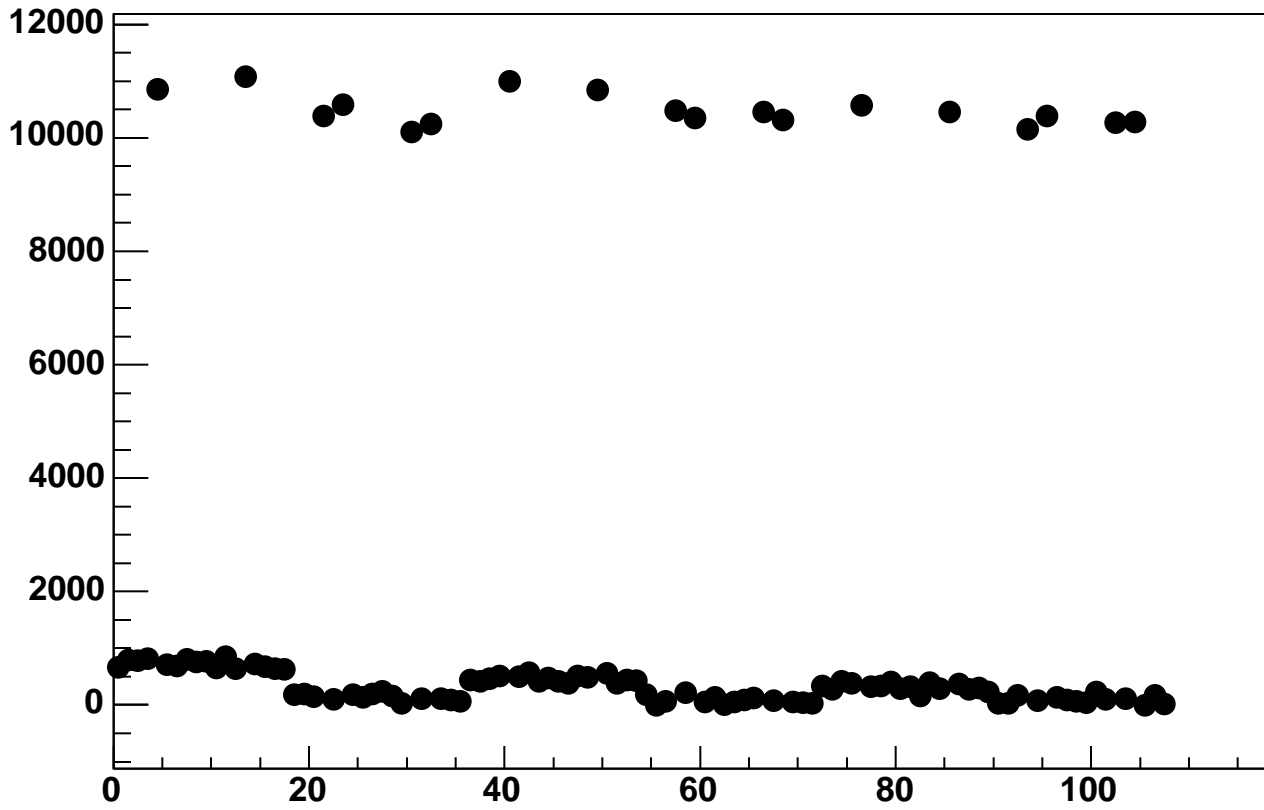
Enable 3, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



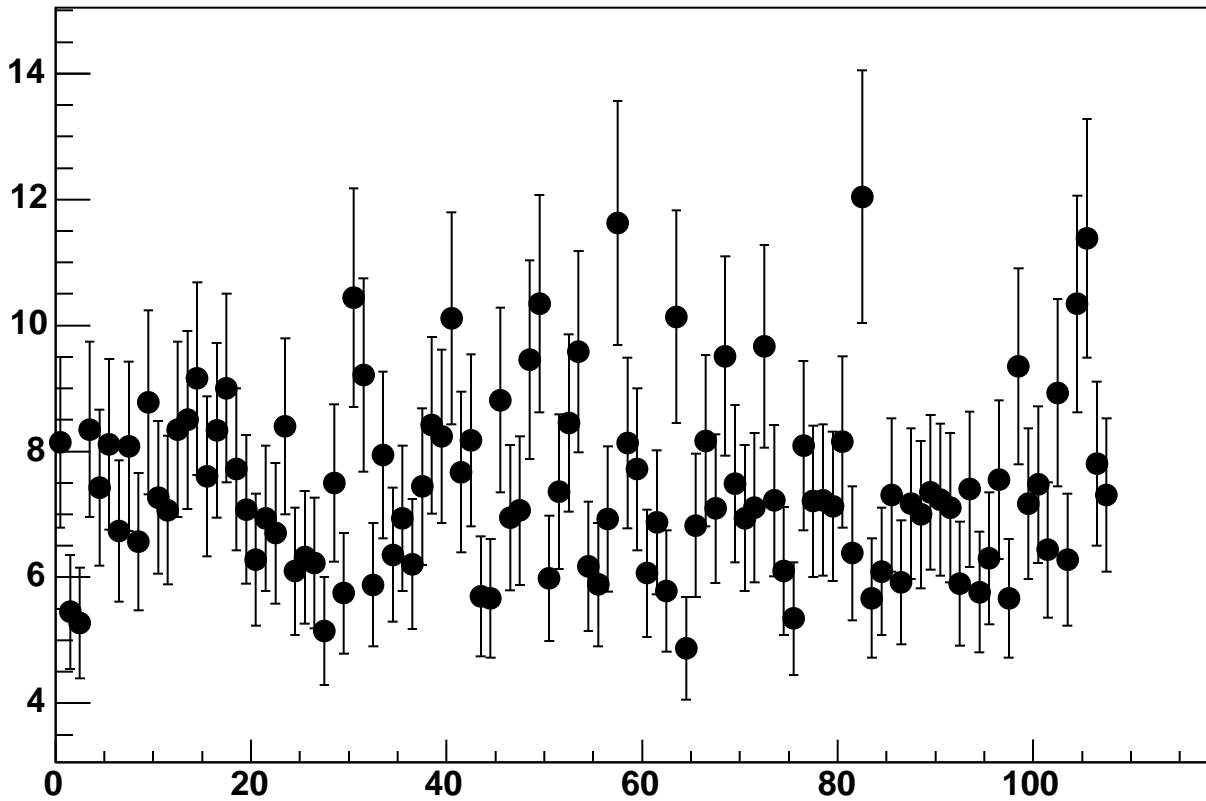
Enable 3, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



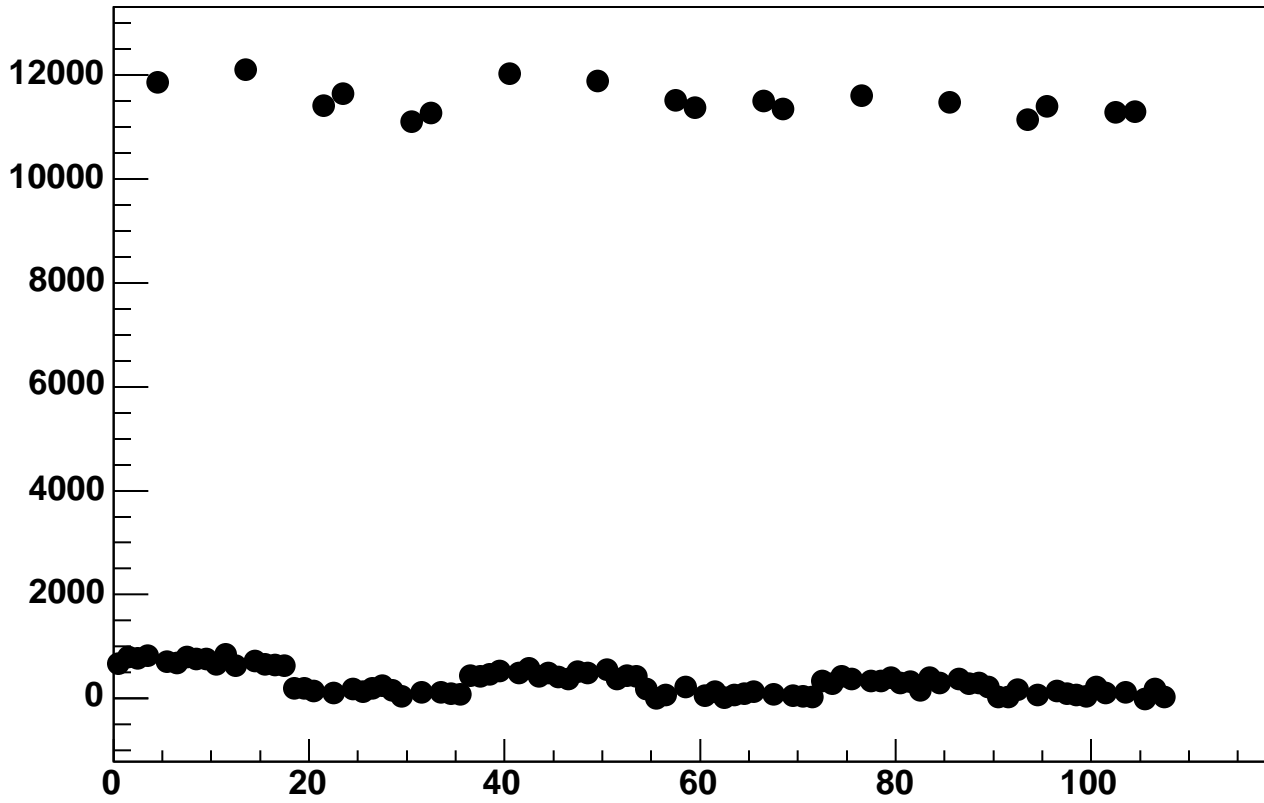
Enable 4, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



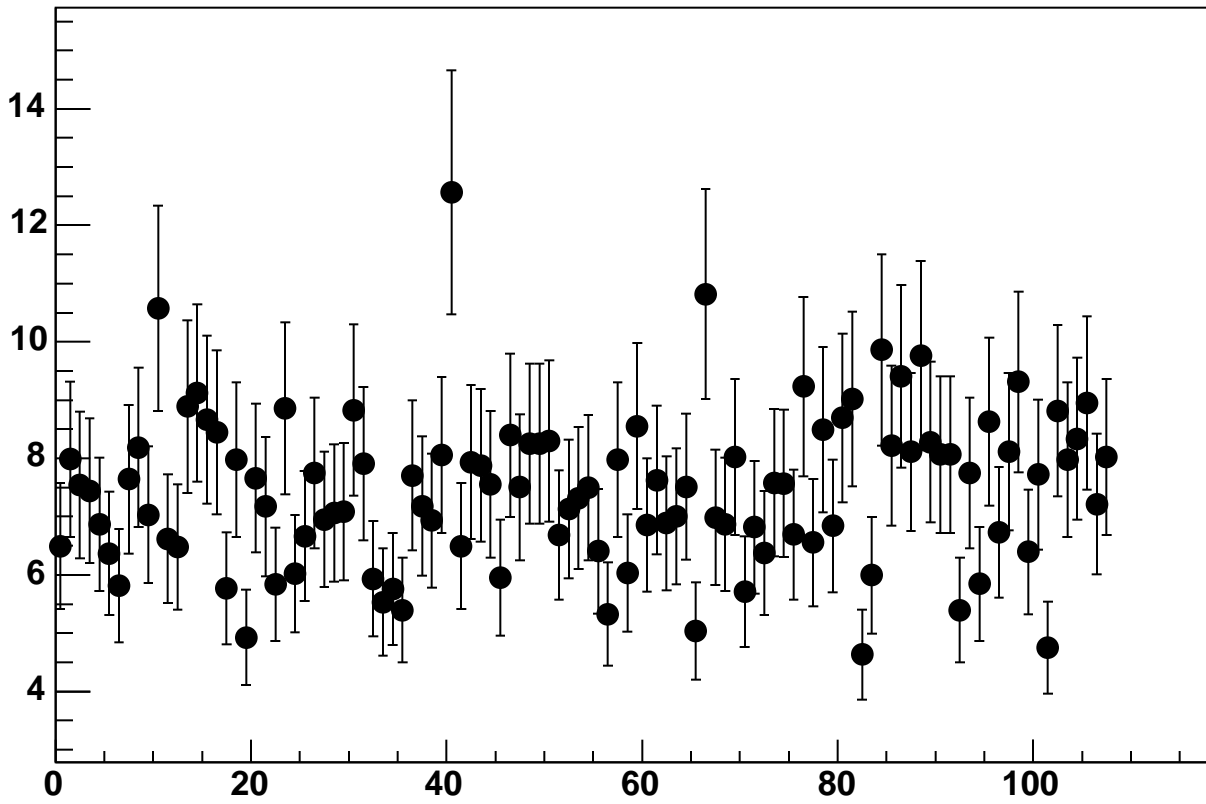
Enable 4, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



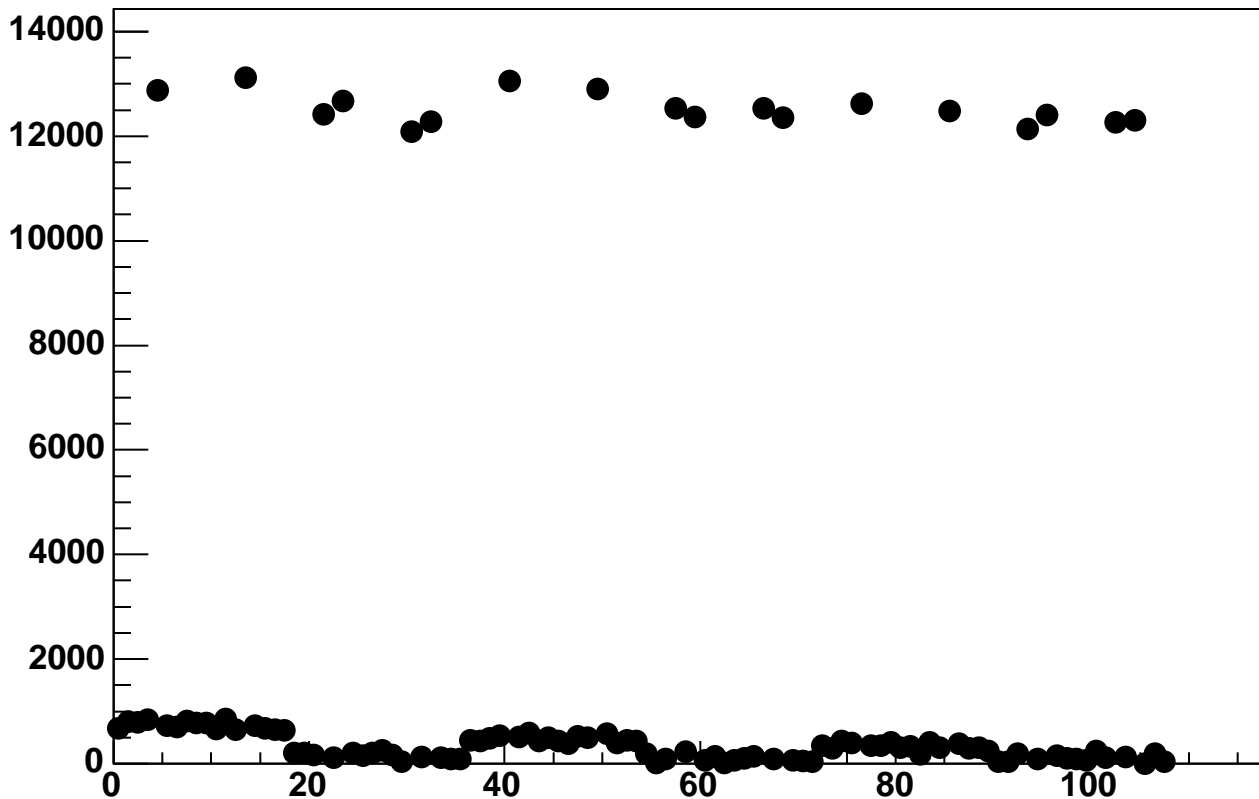
Enable 4, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



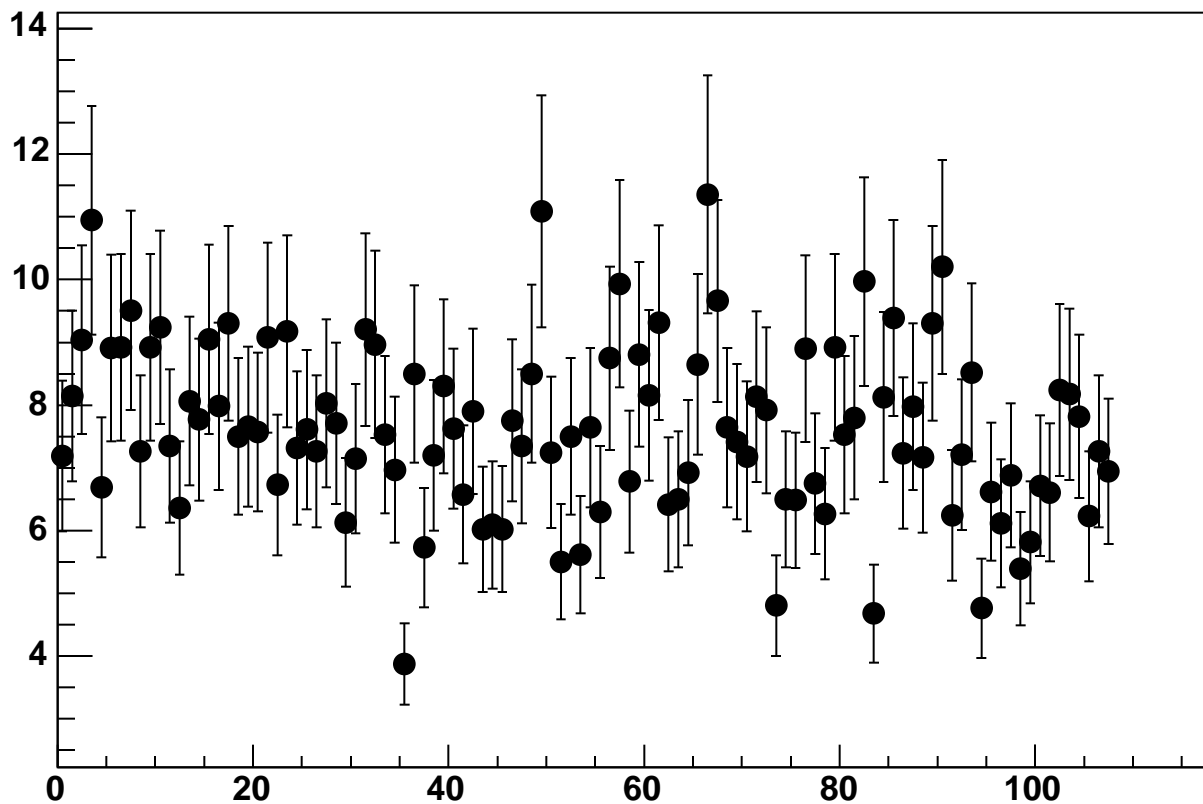
Enable 4, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



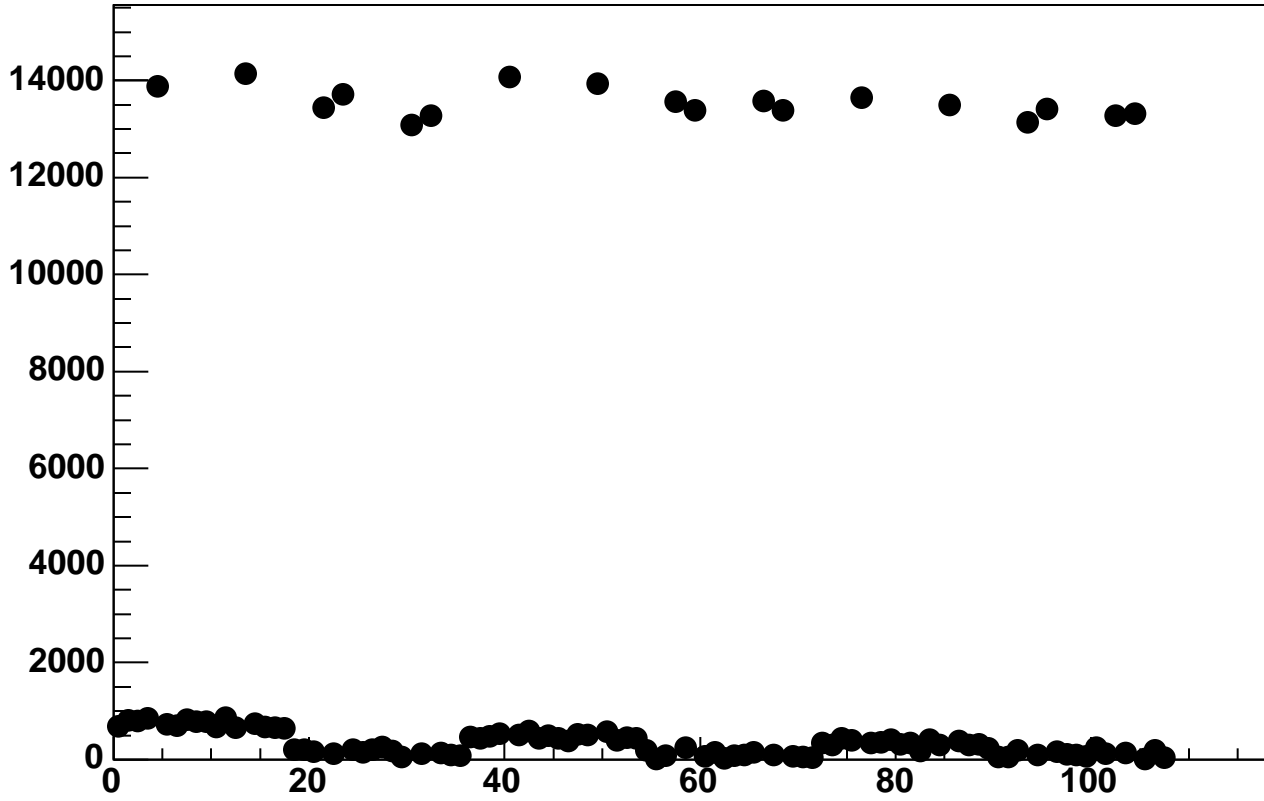
Enable 4, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



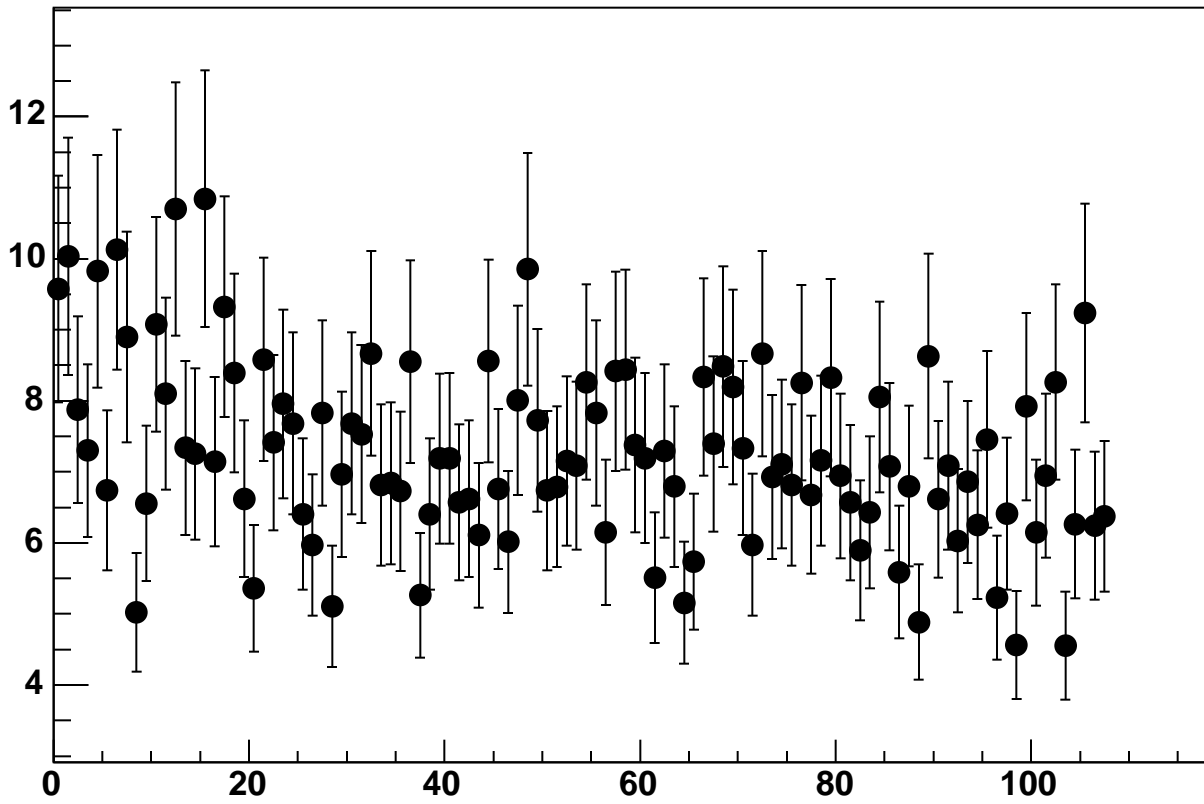
Enable 4, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



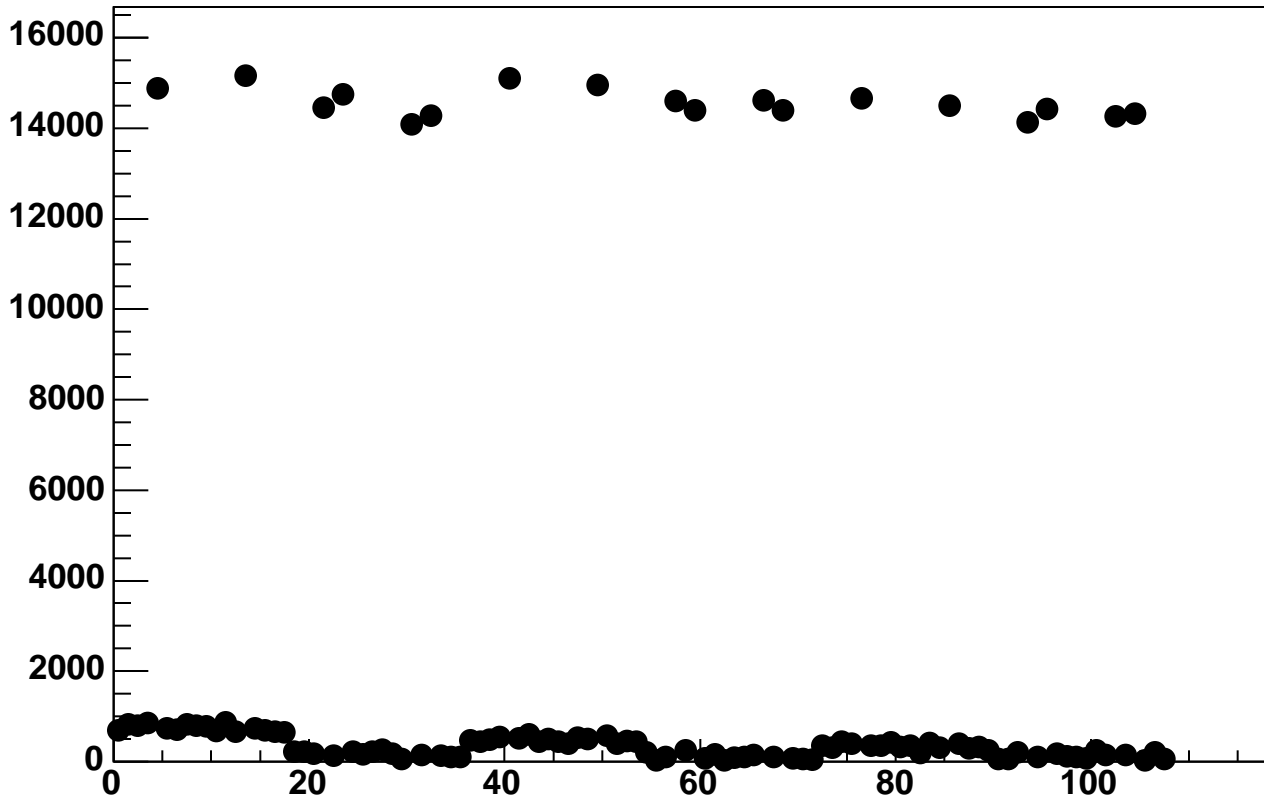
Enable 4, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



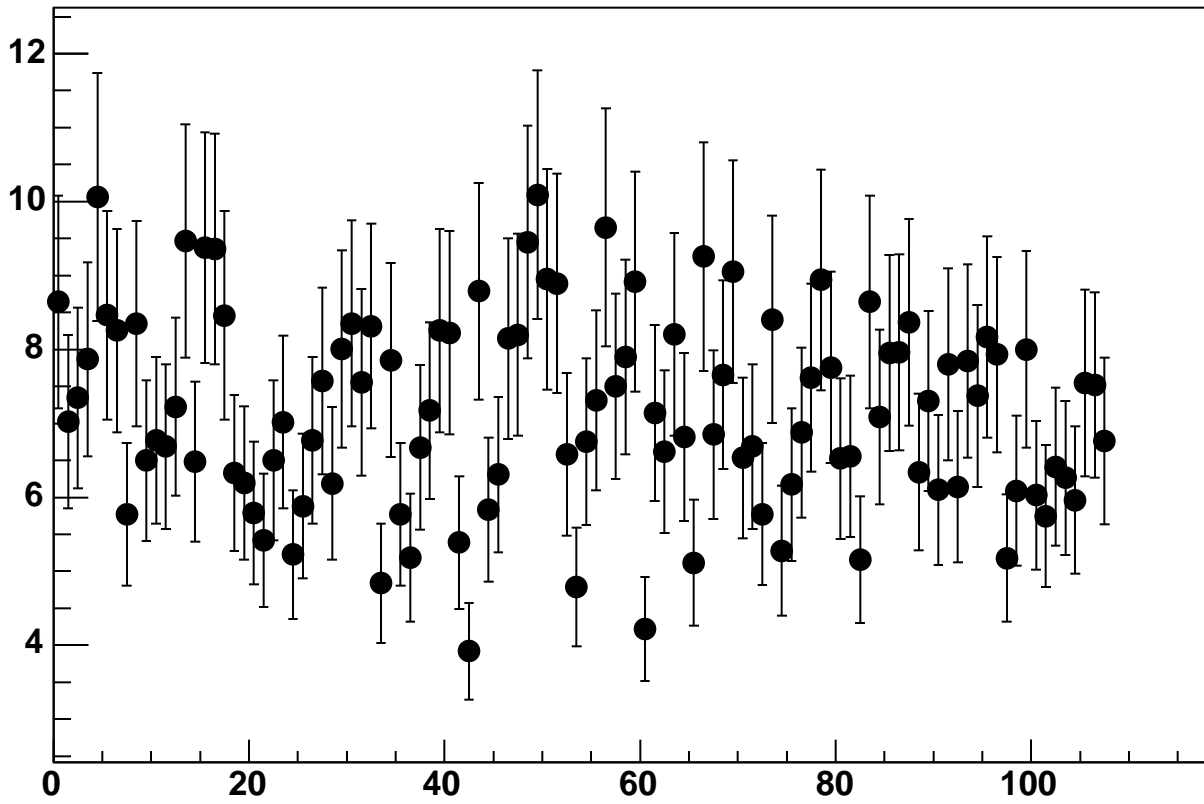
Enable 4, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



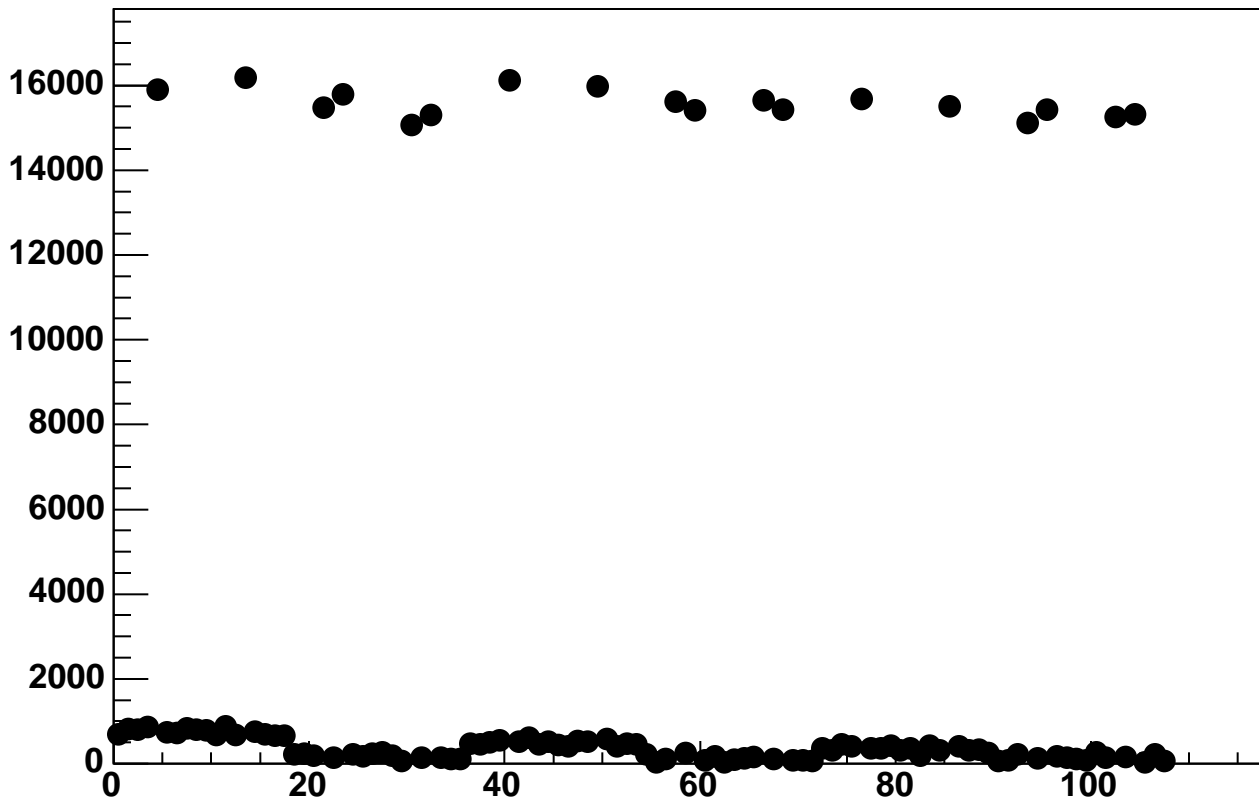
Enable 4, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



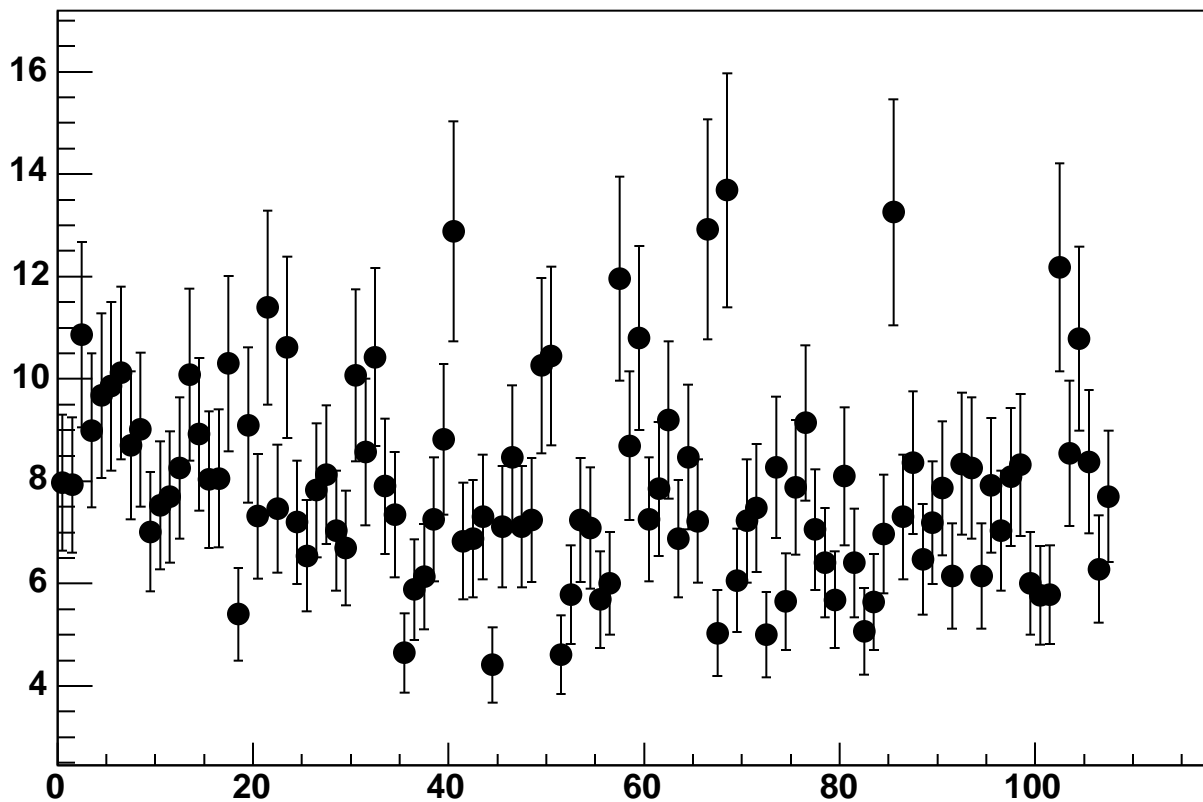
Enable 4, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



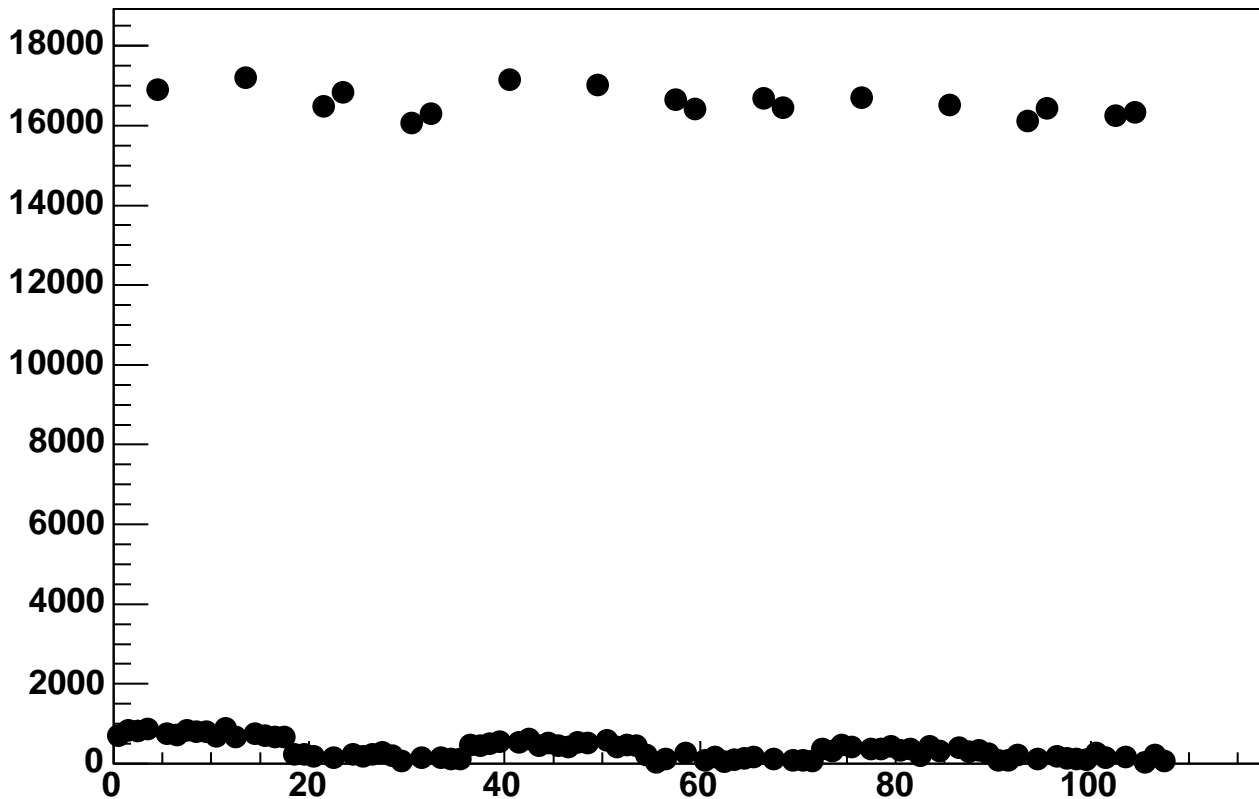
Enable 4, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



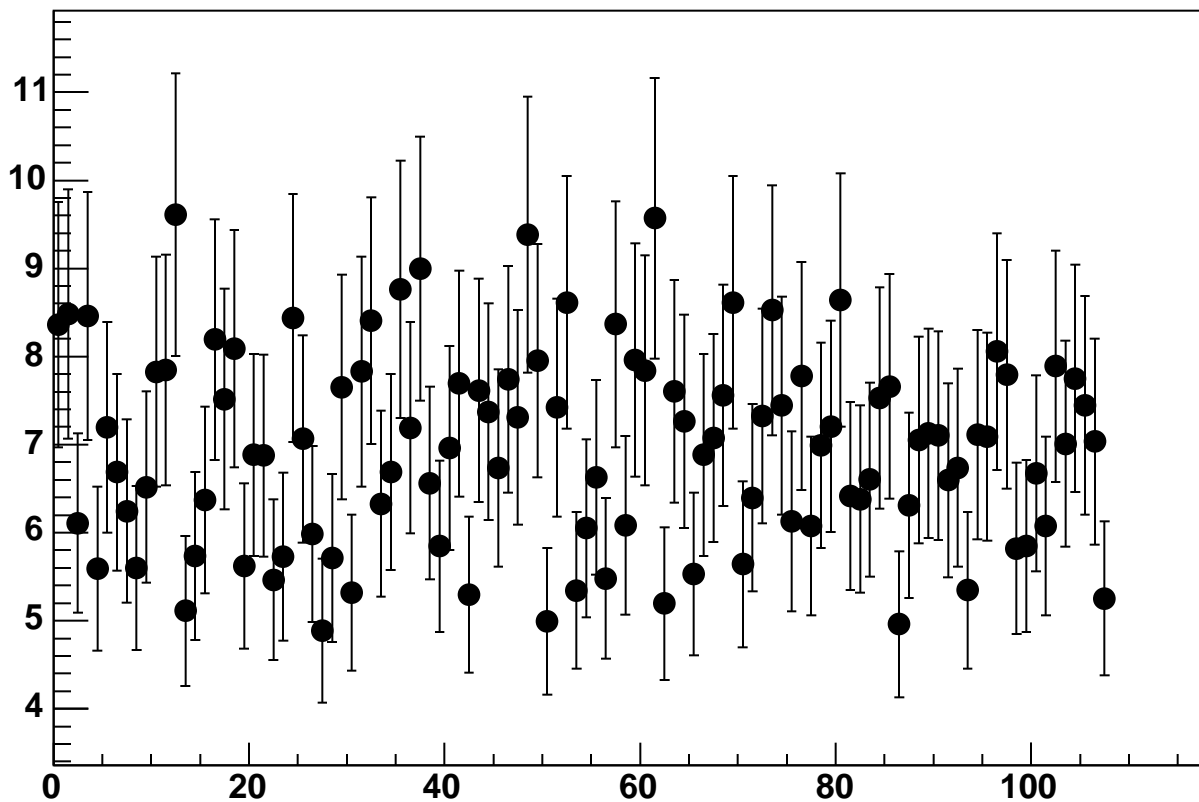
Enable 4, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



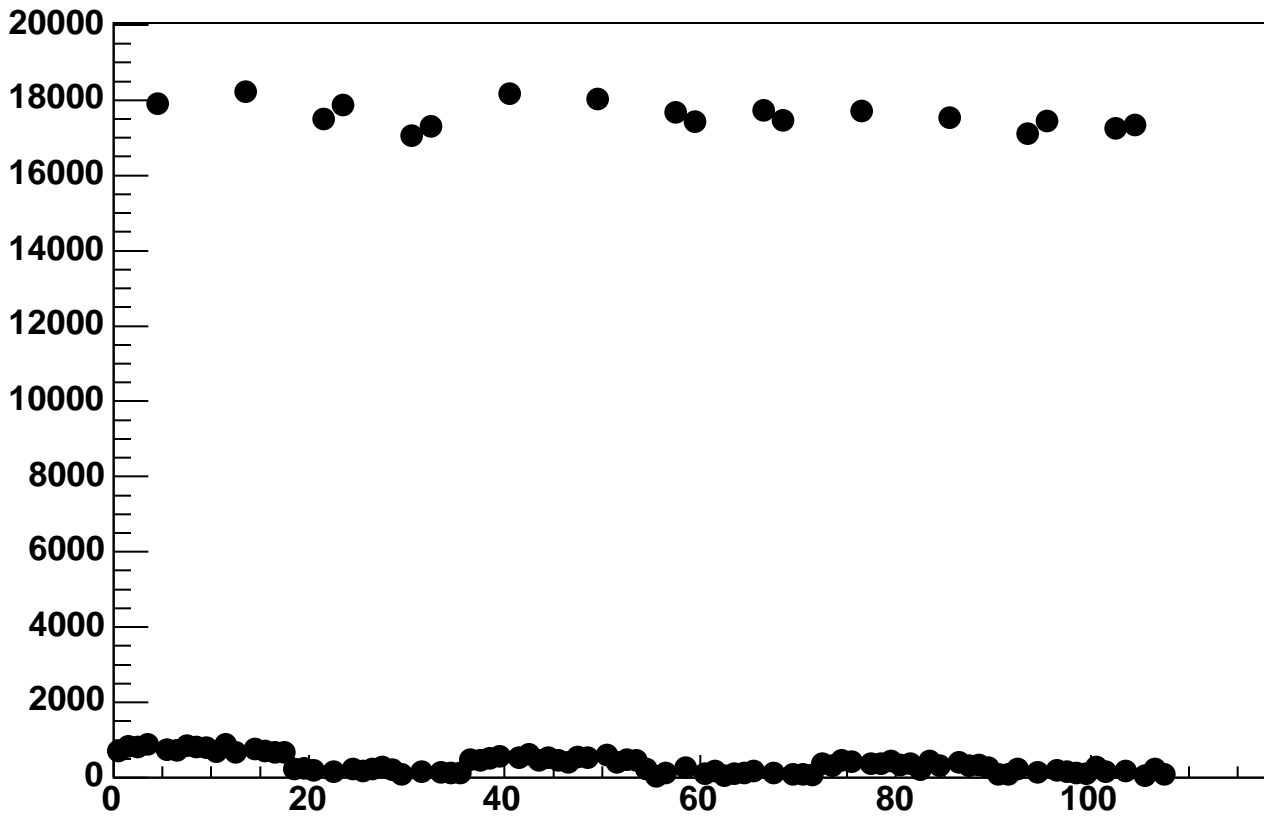
Enable 4, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



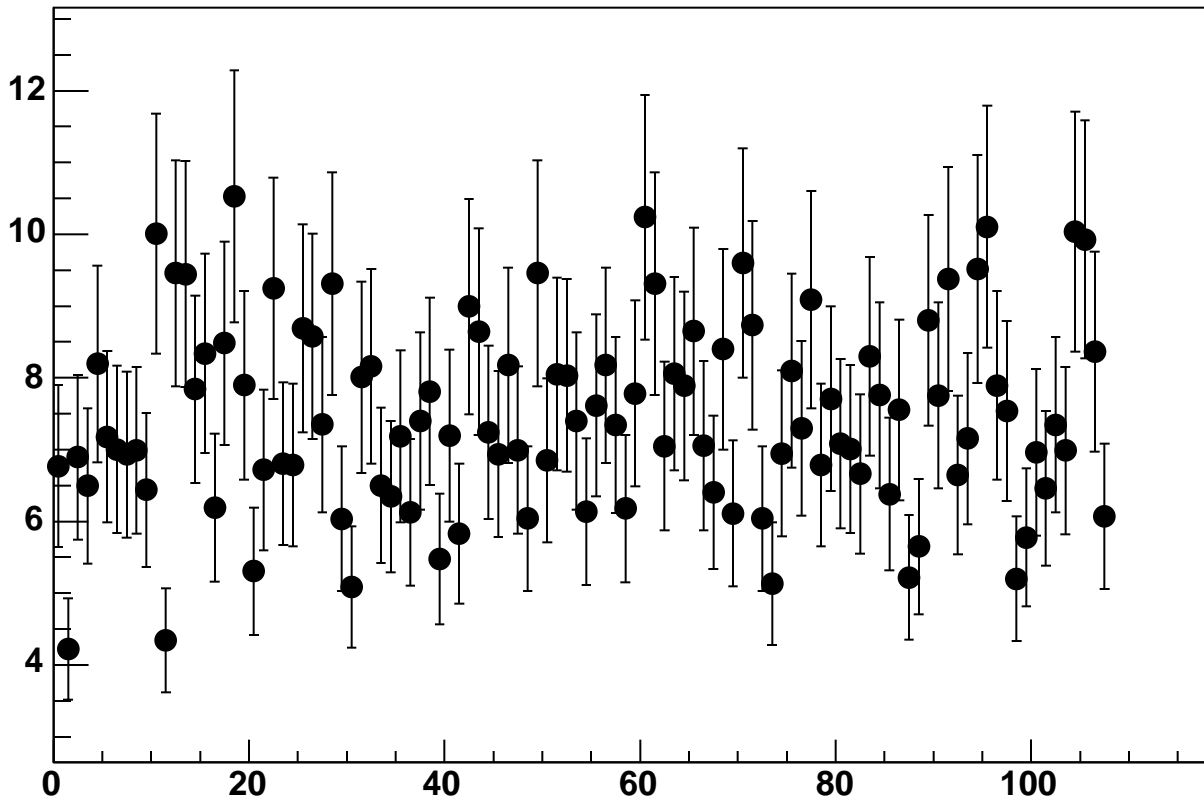
Enable 4, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



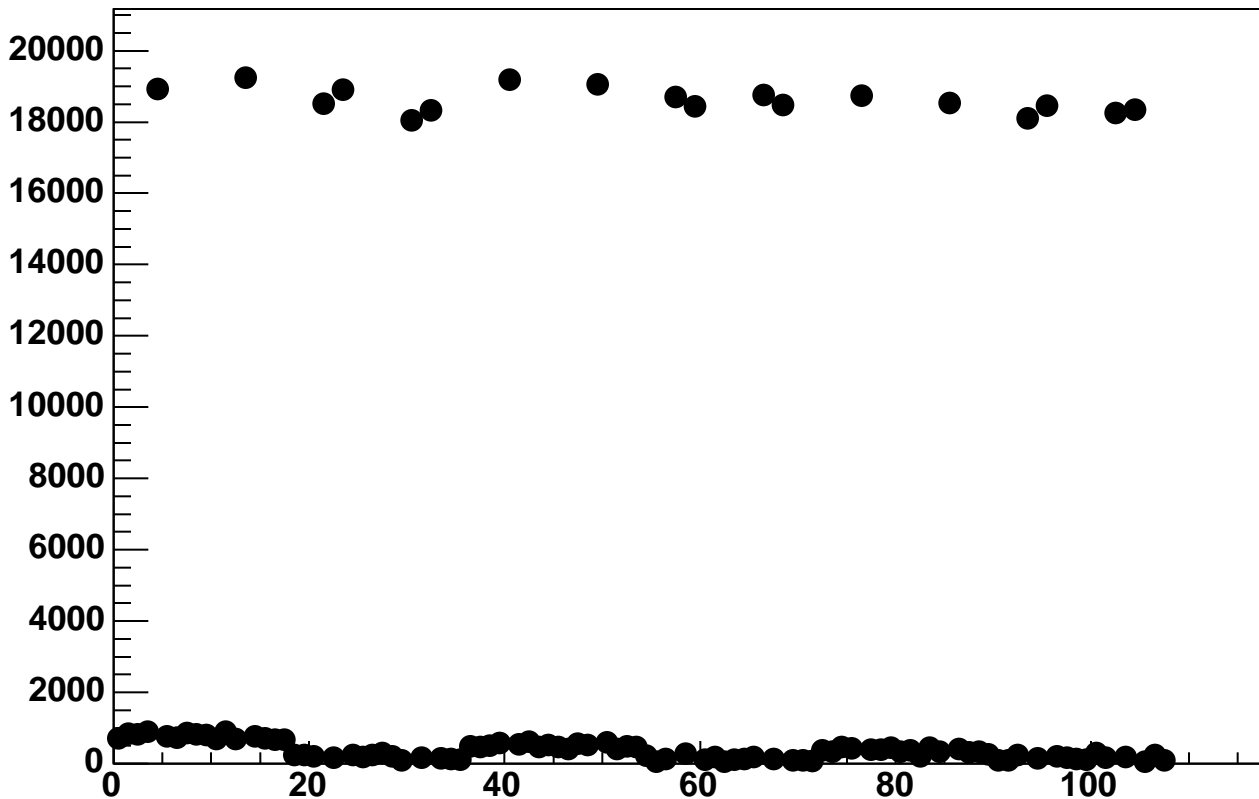
Enable 4, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



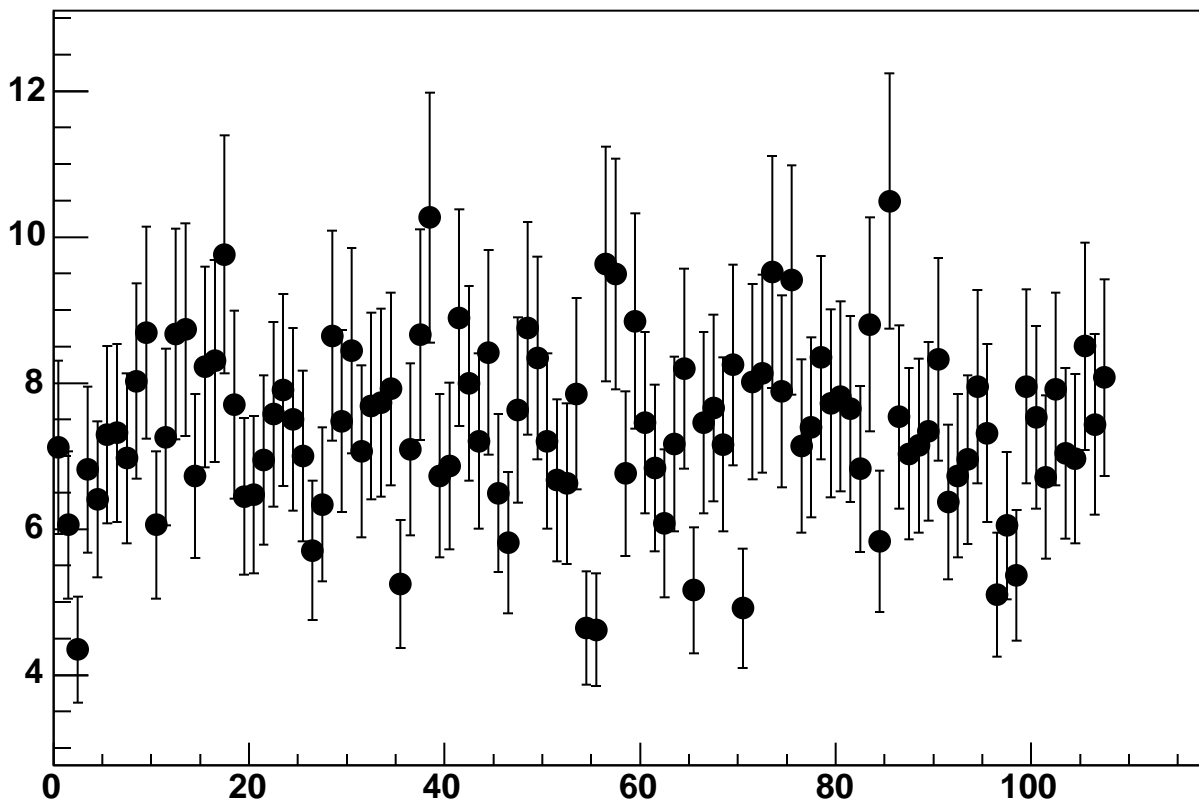
Enable 4, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



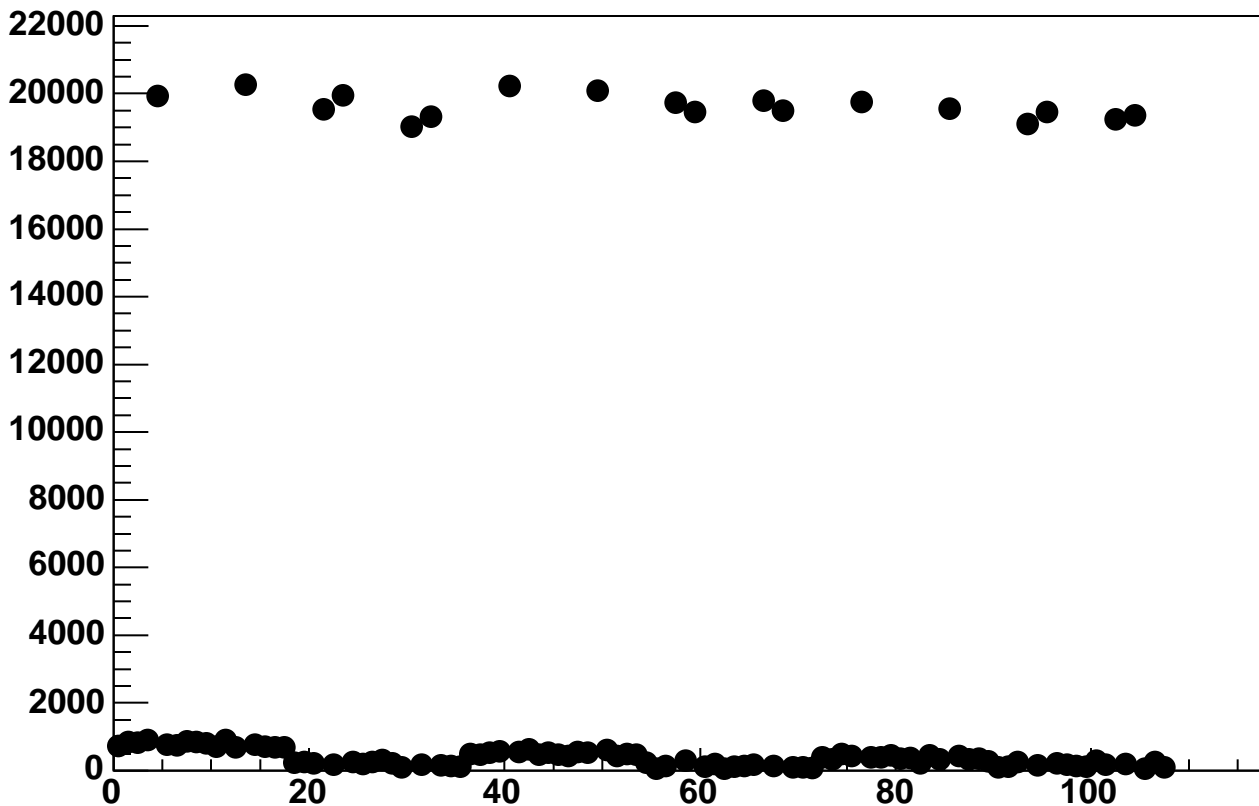
Enable 4, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



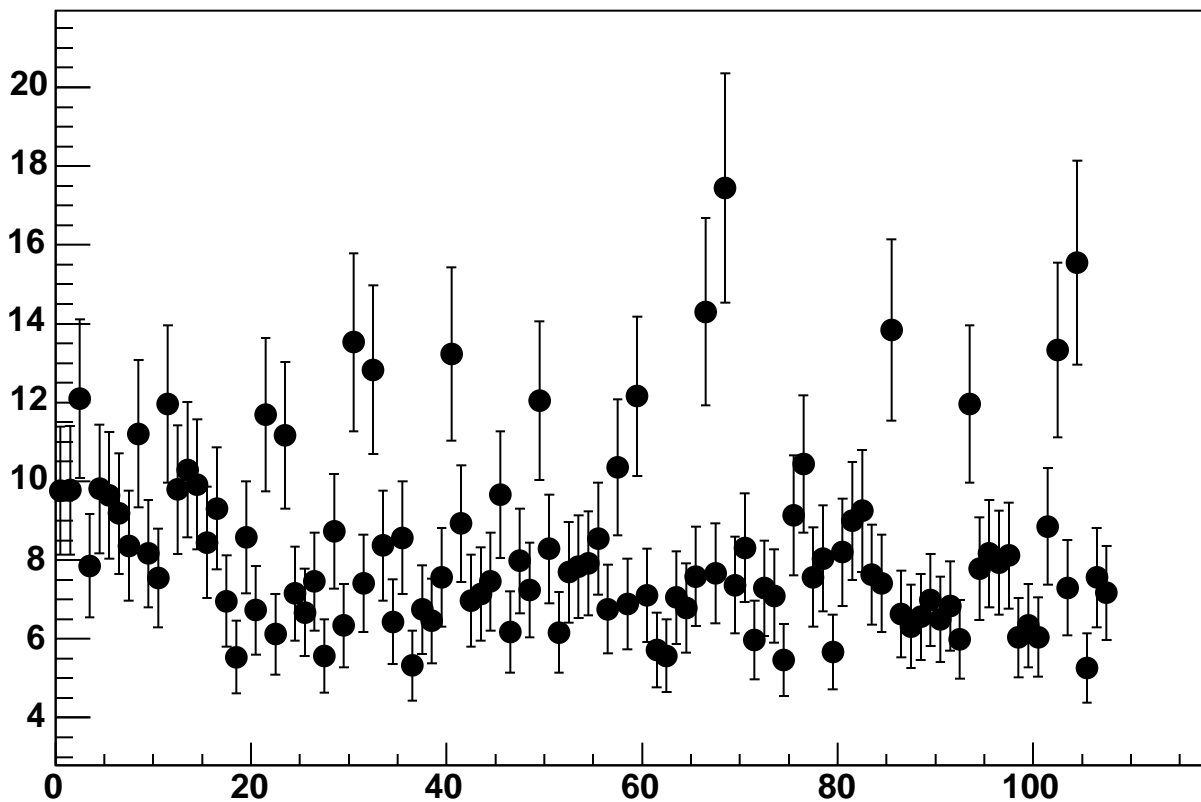
Enable 4, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



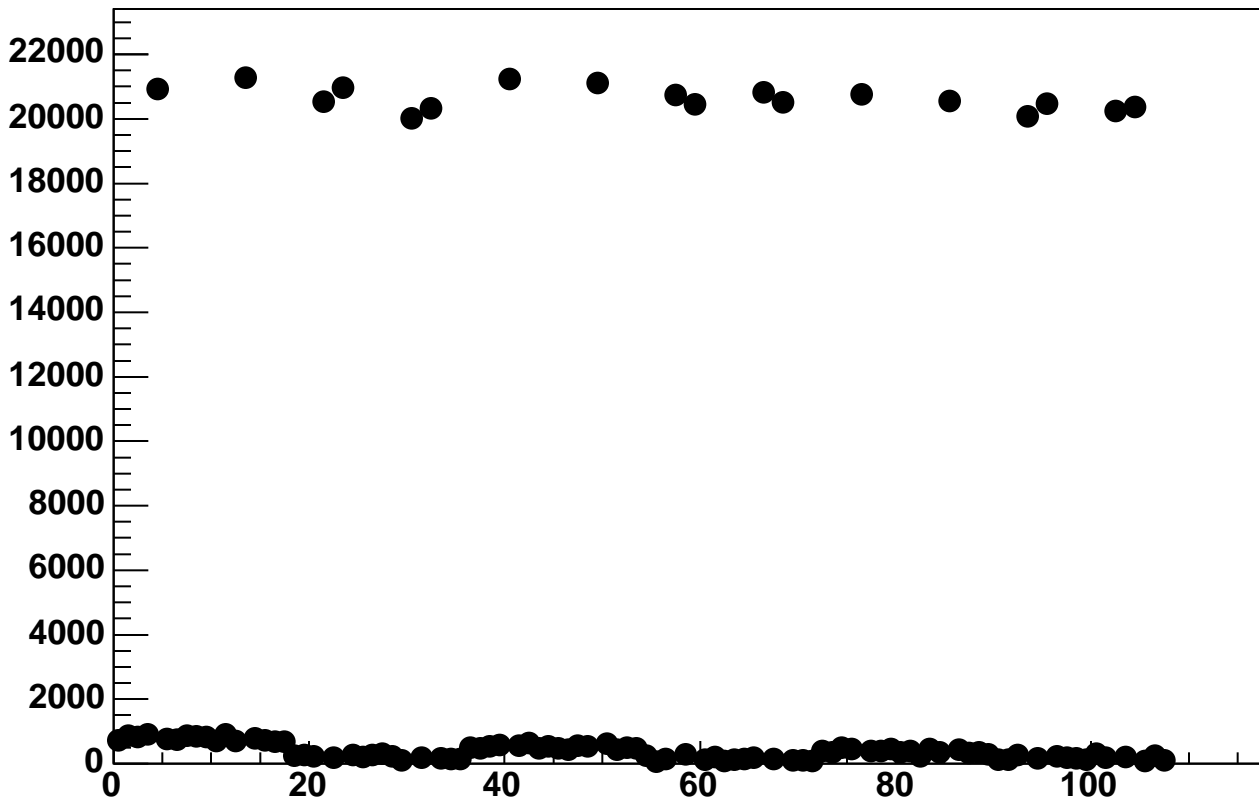
Enable 4, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



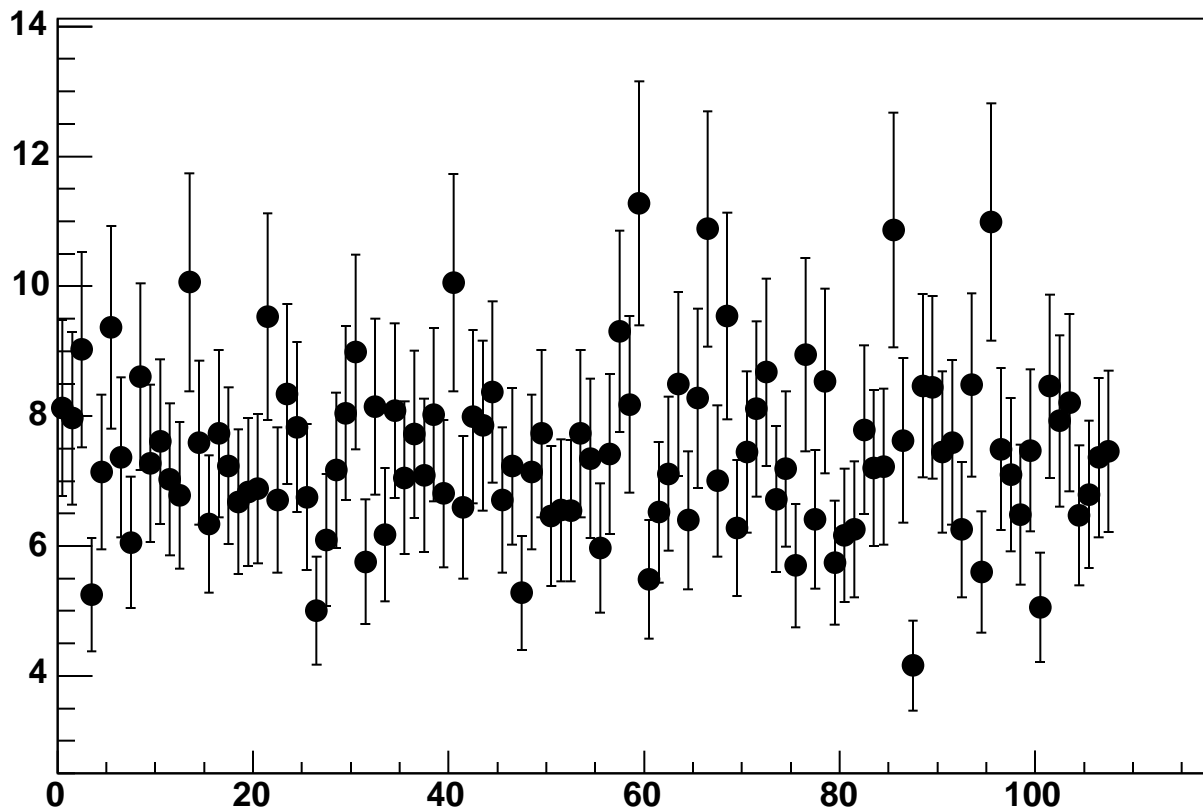
Enable 4, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



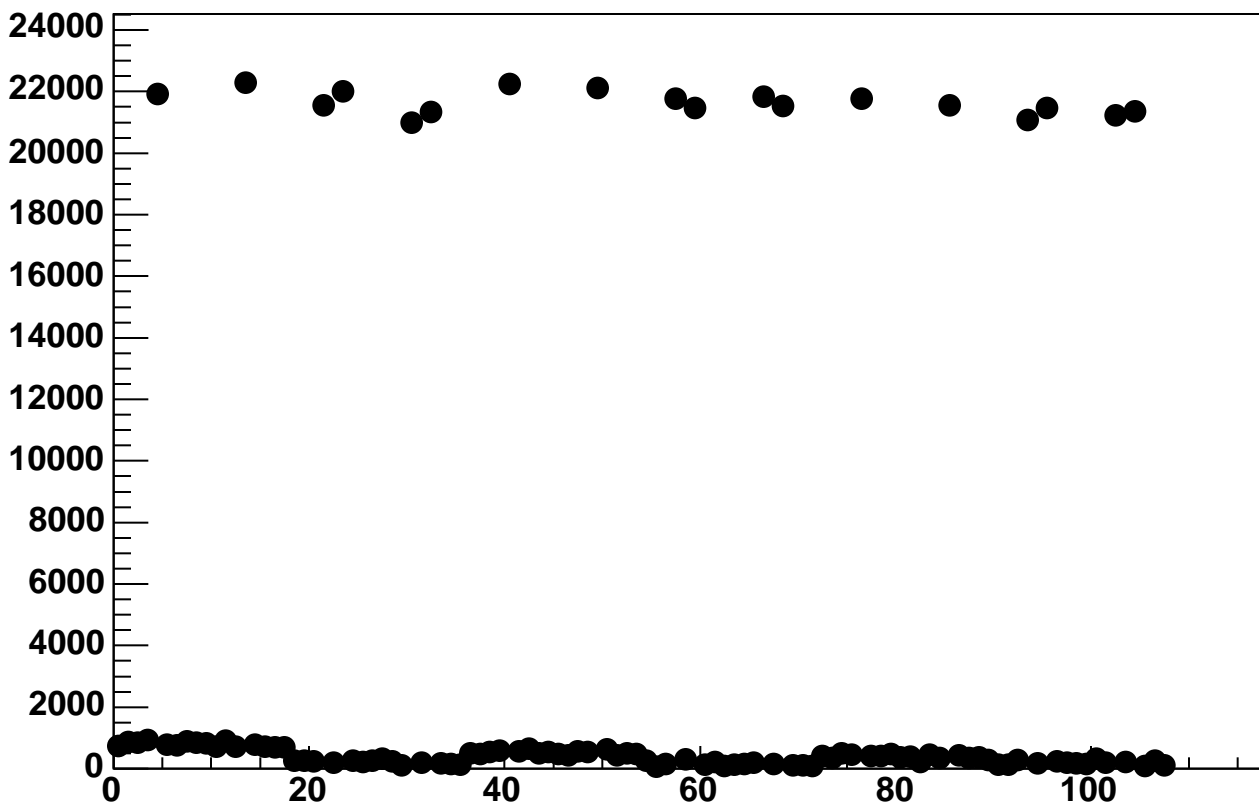
Enable 4, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



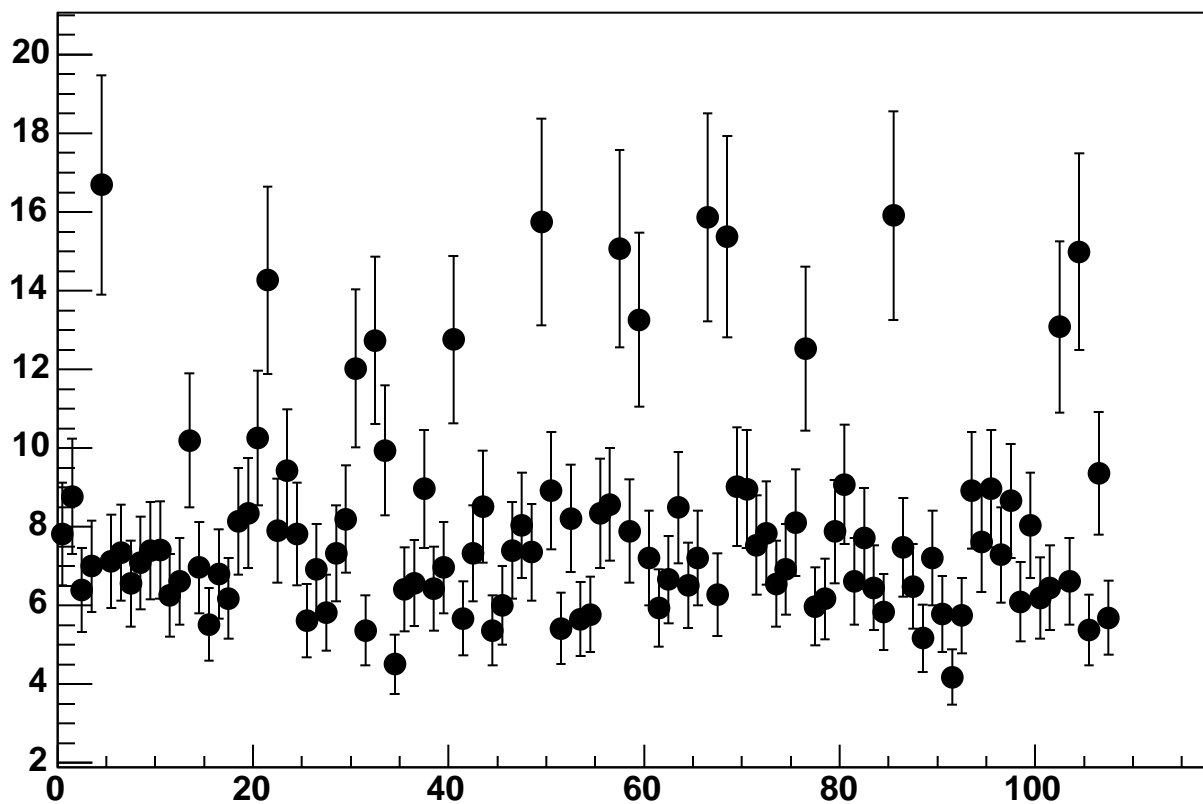
Enable 4, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



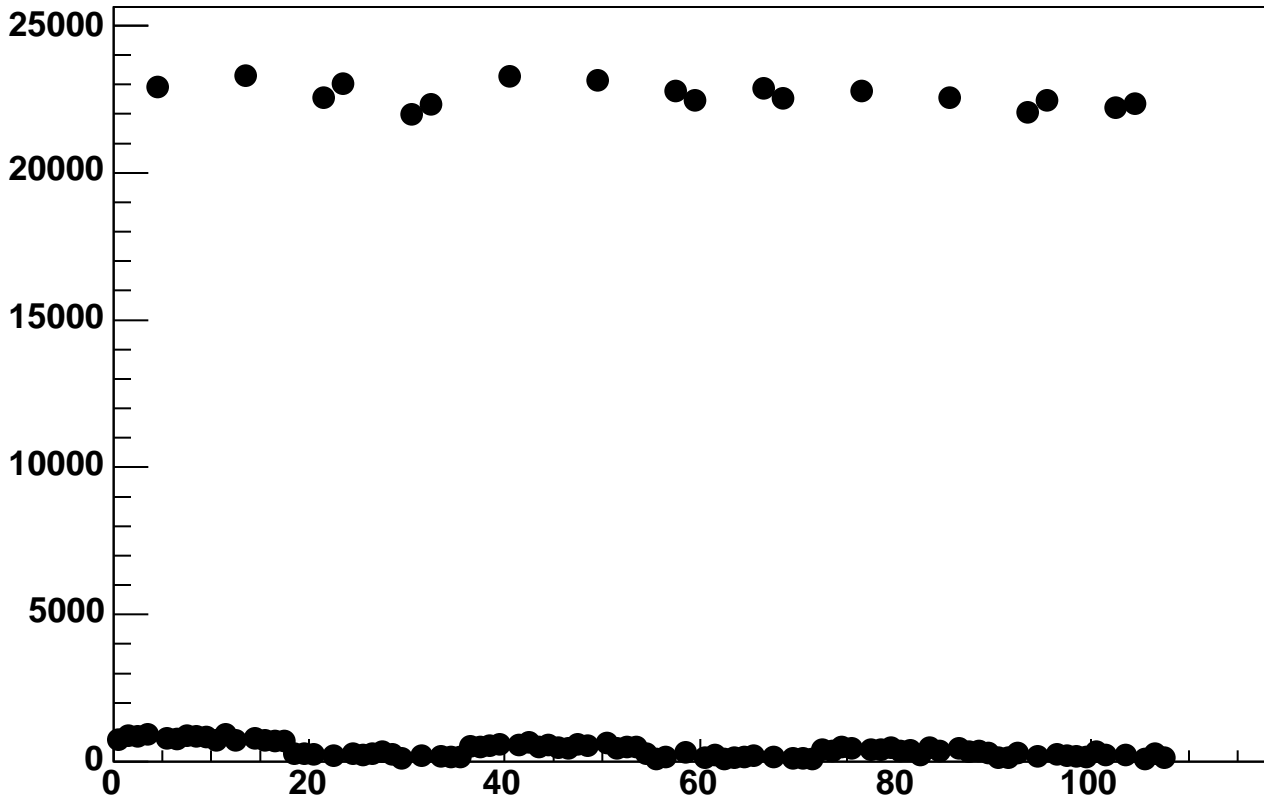
Enable 4, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



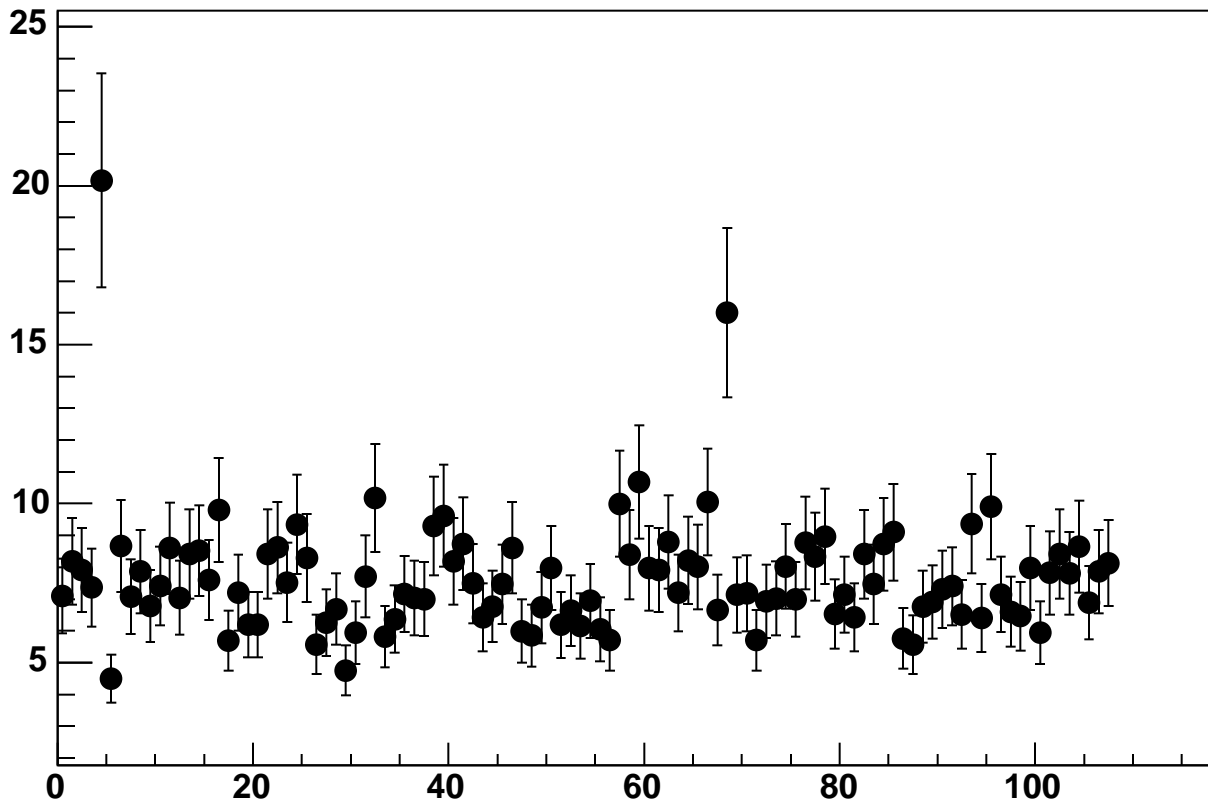
Enable 4, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



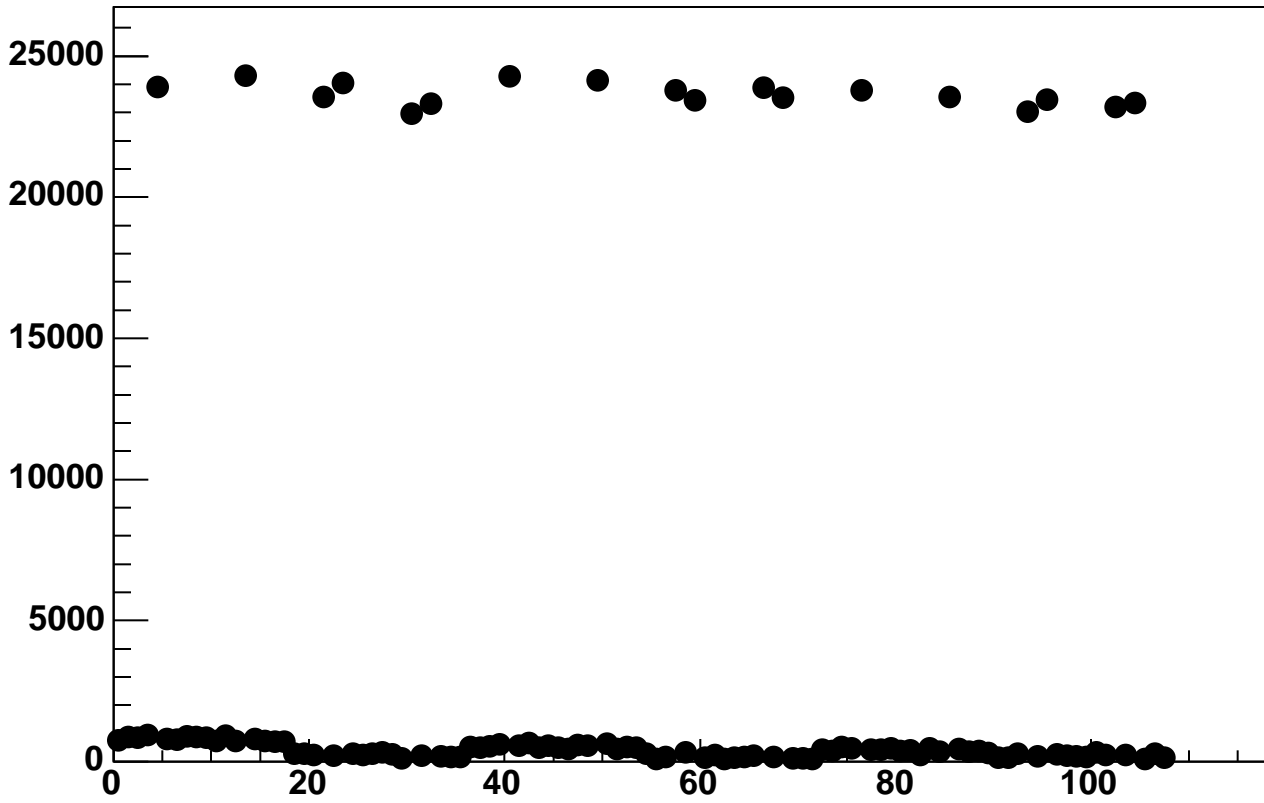
Enable 4, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



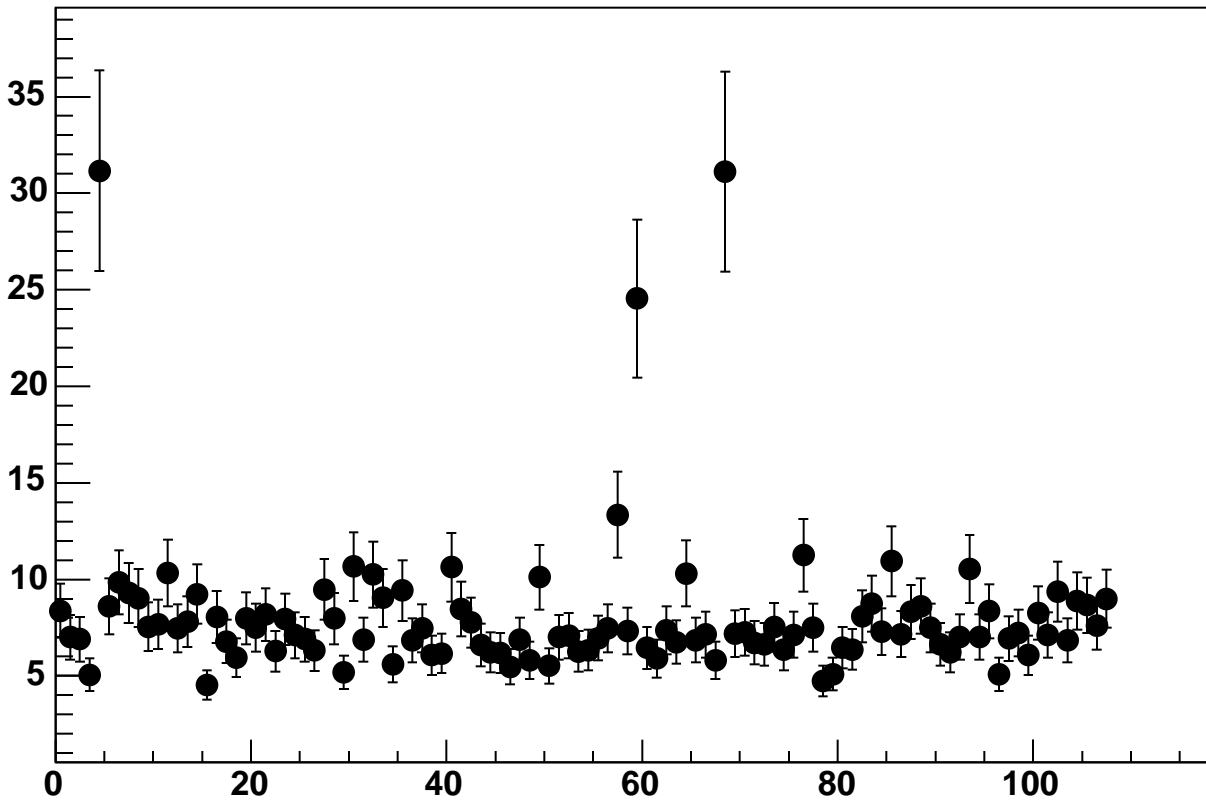
Enable 4, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



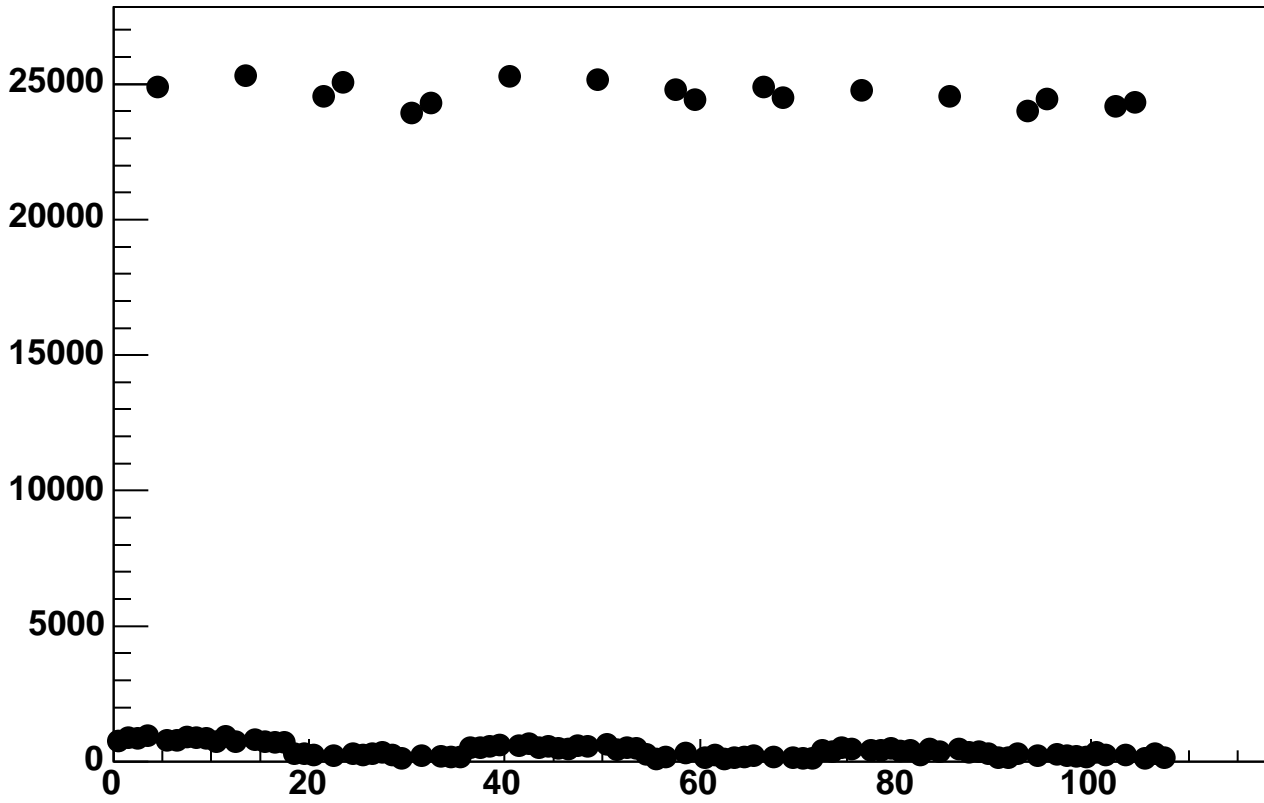
Enable 4, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



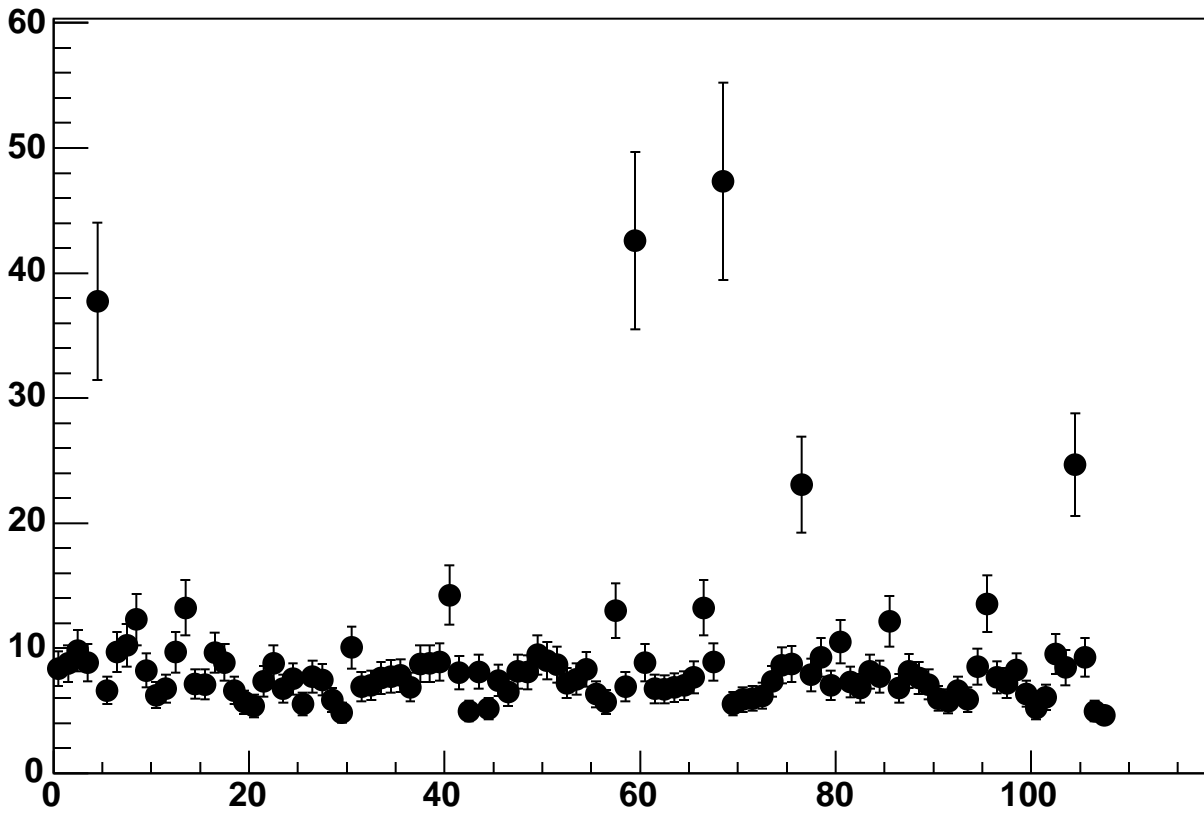
Enable 4, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



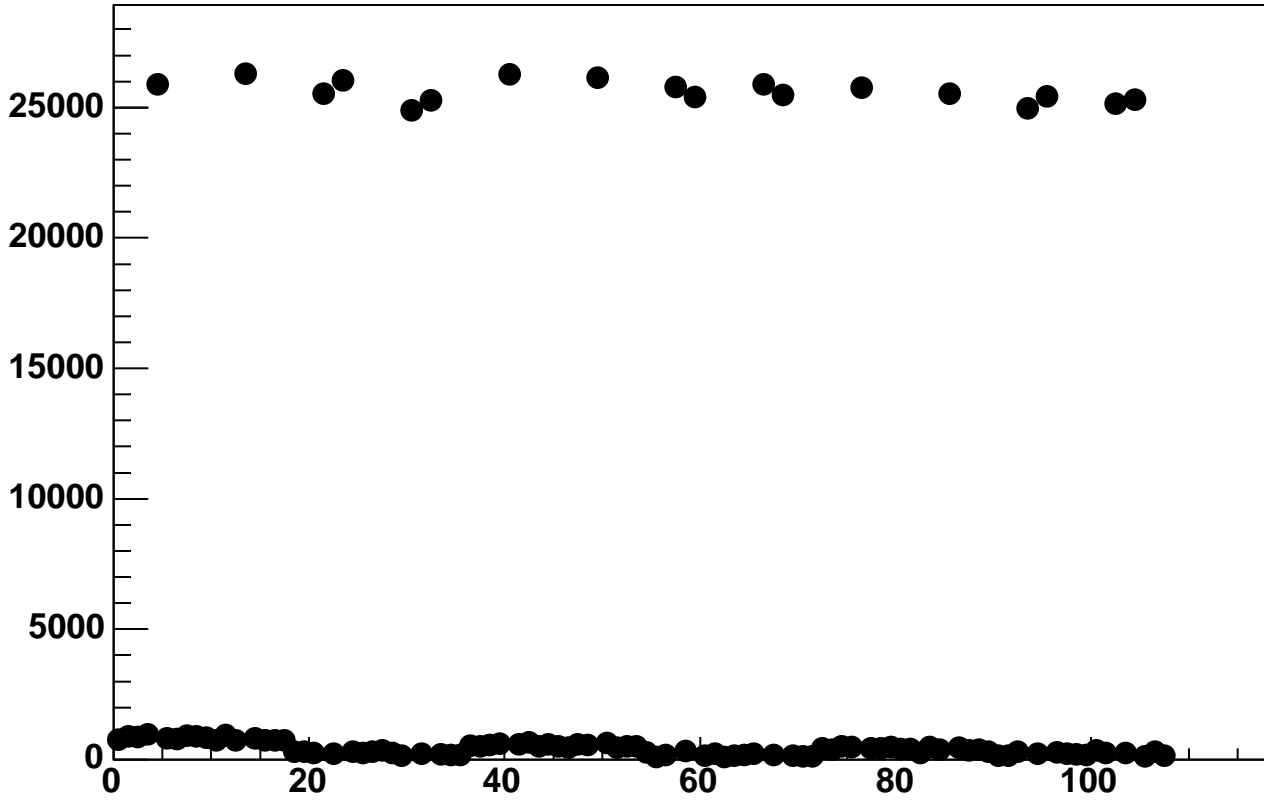
Enable 4, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



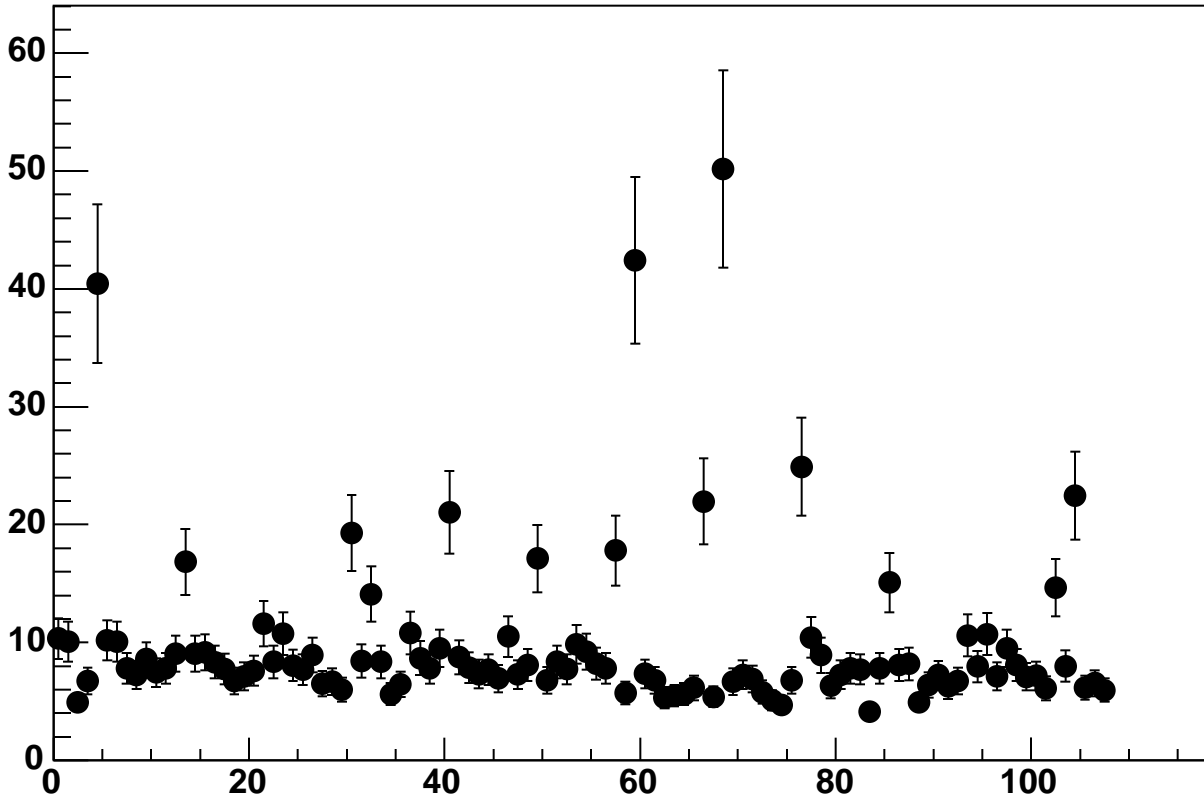
Enable 4, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



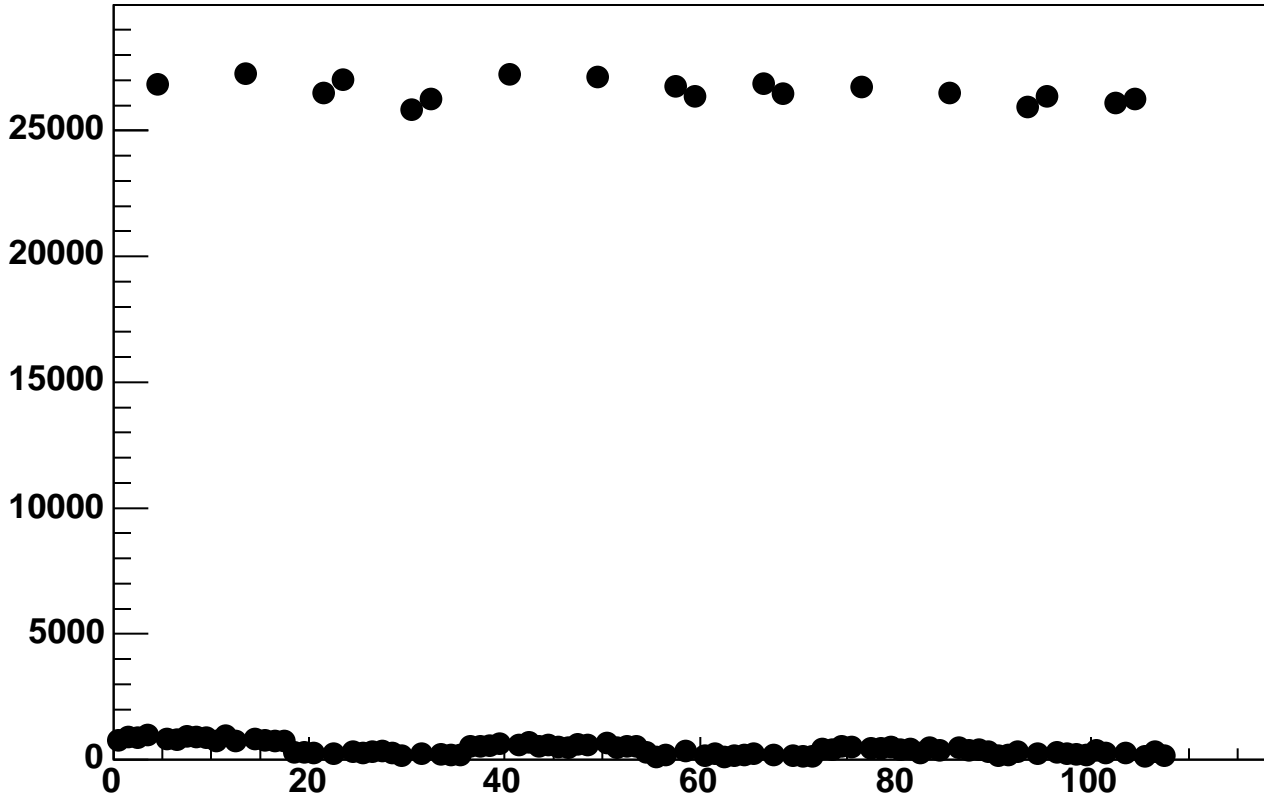
Enable 4, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



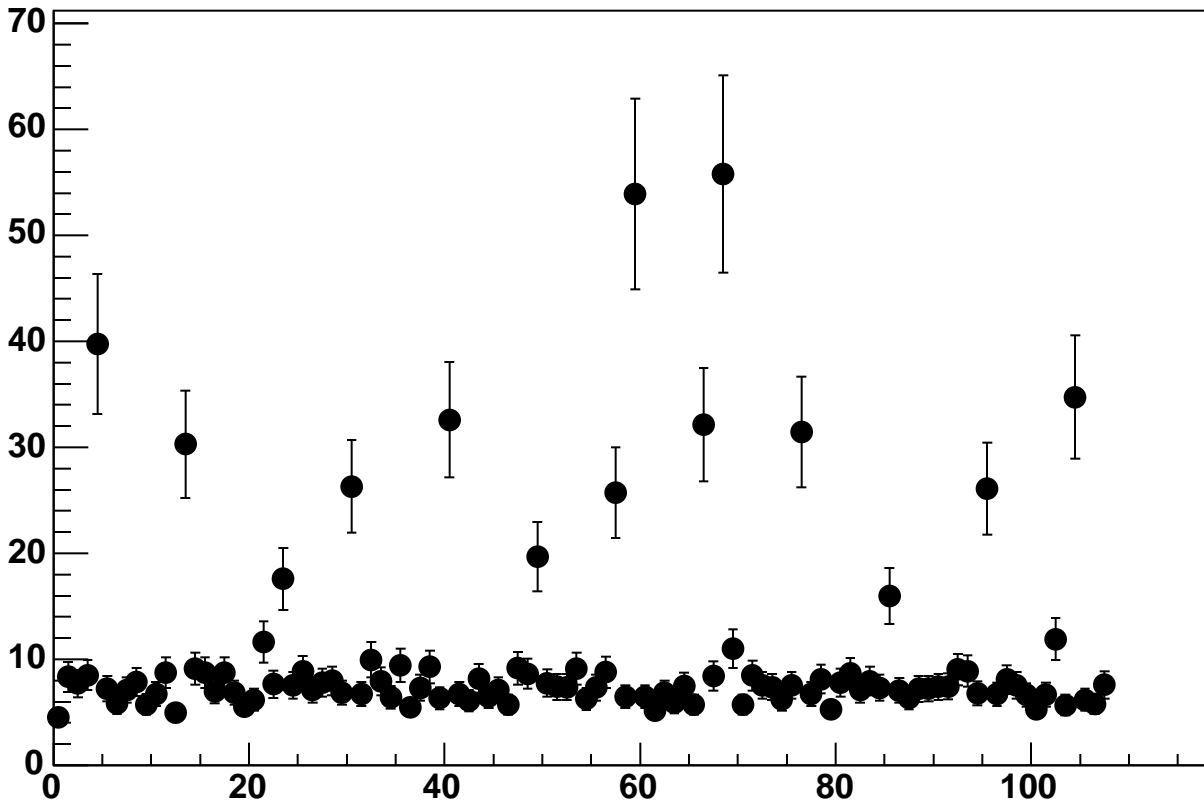
Enable 4, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



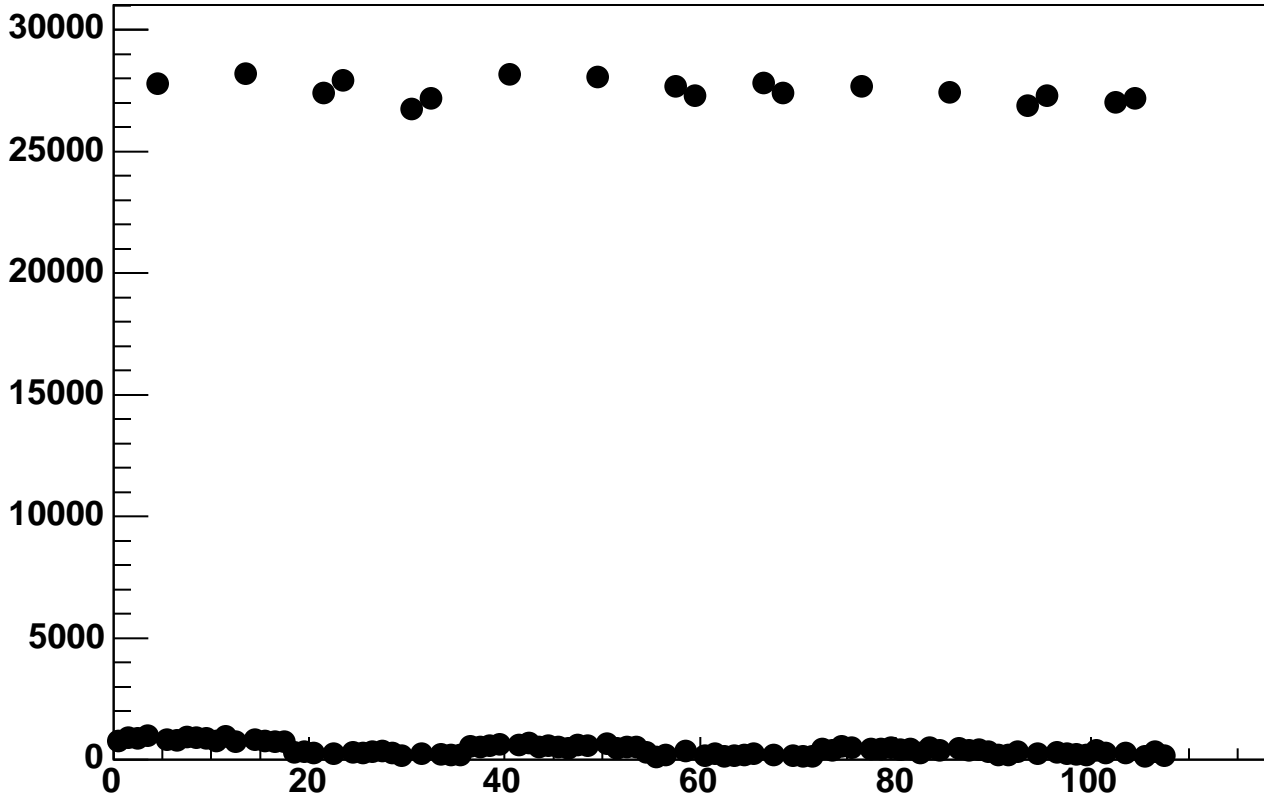
Enable 4, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



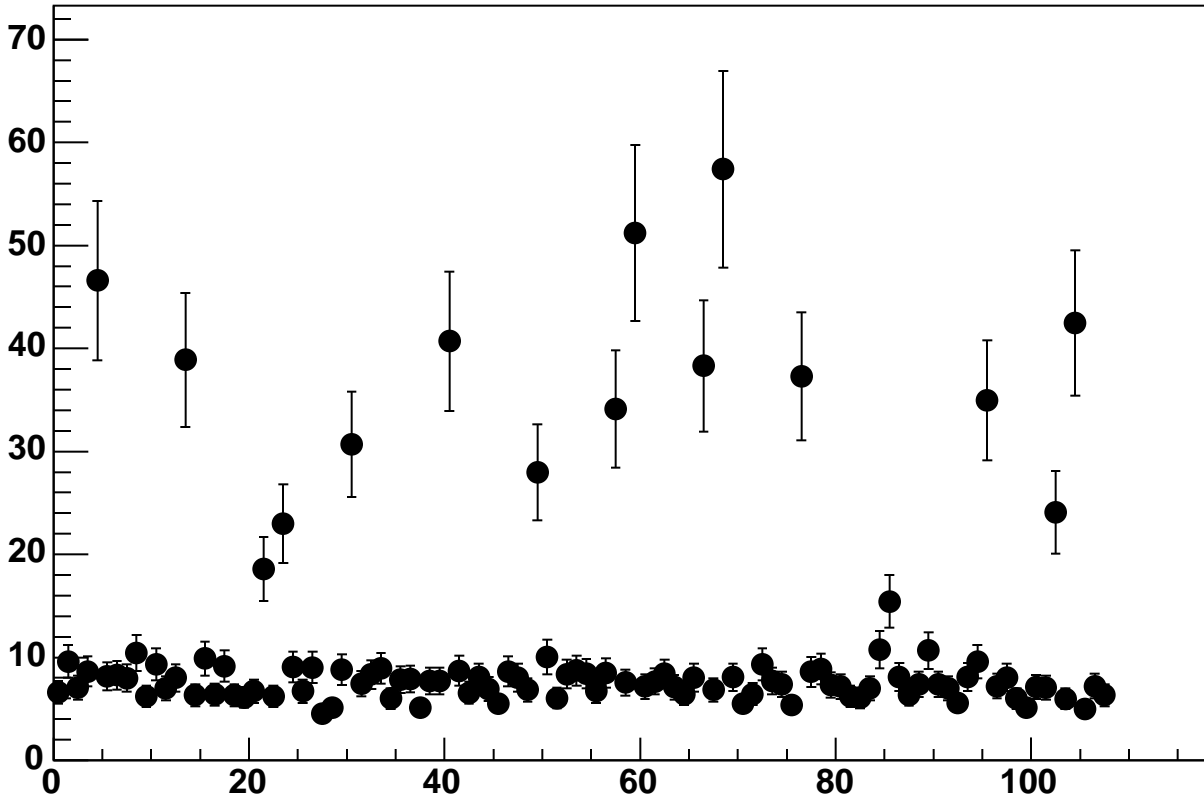
Enable 4, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



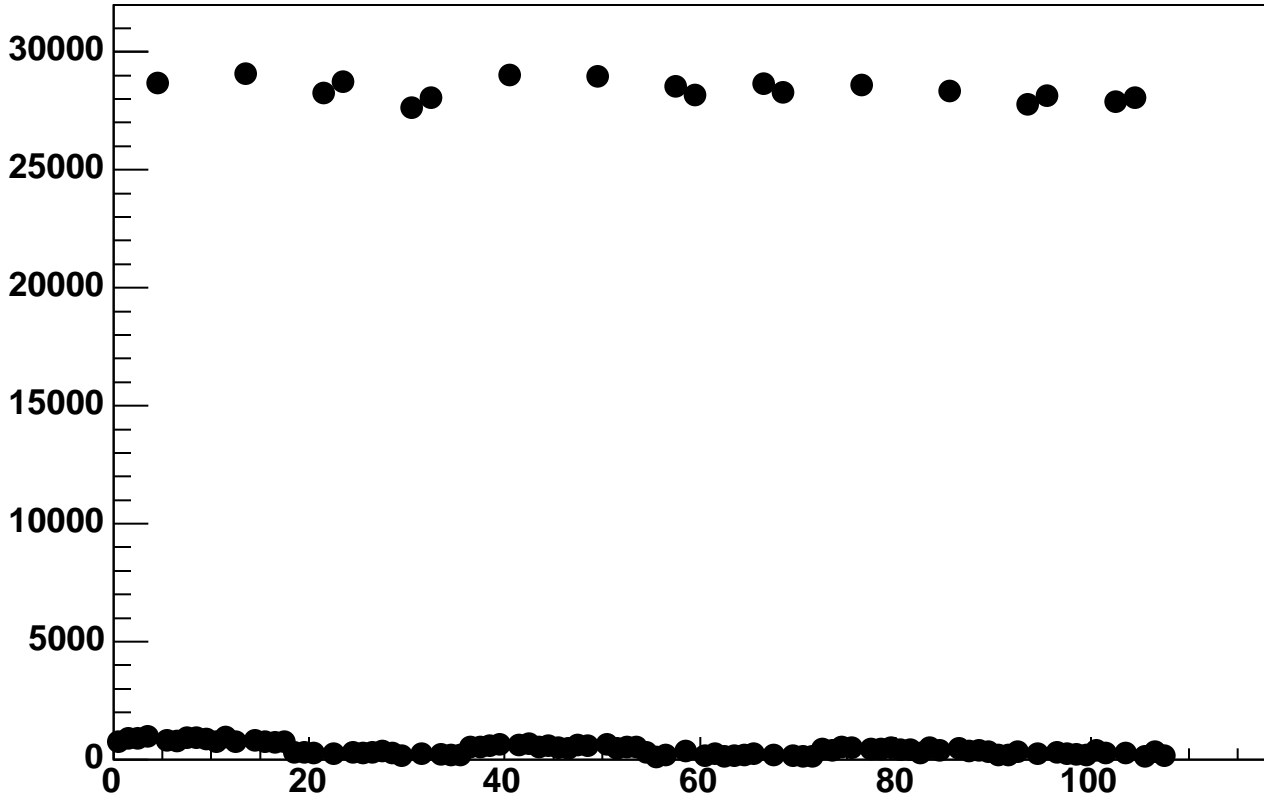
Enable 4, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



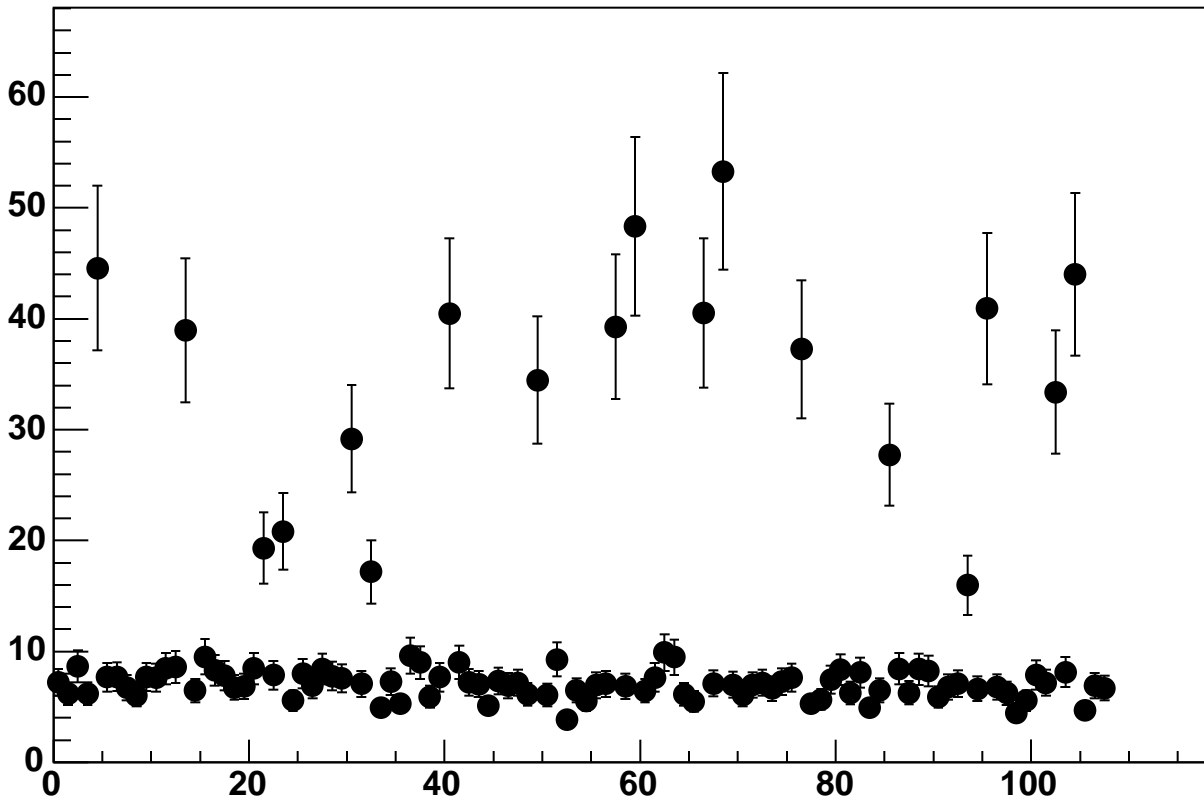
Enable 4, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



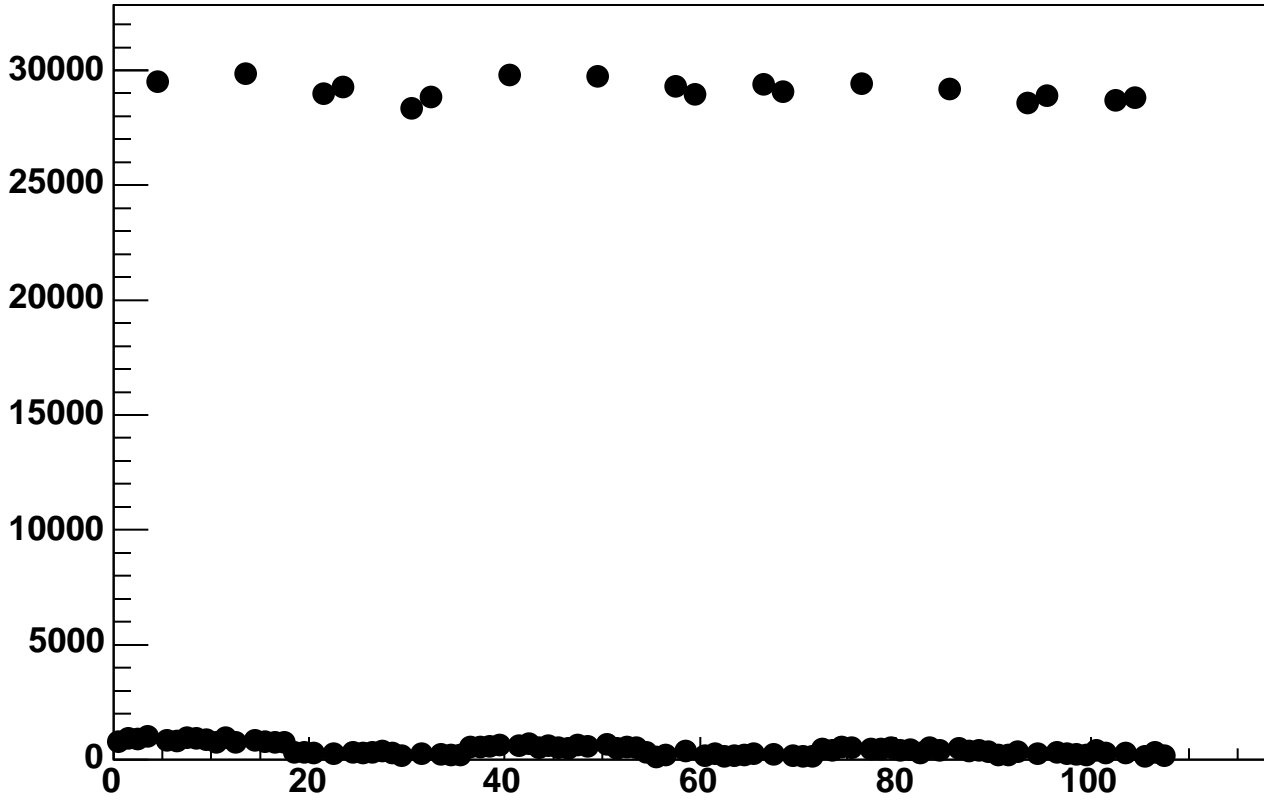
Enable 4, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



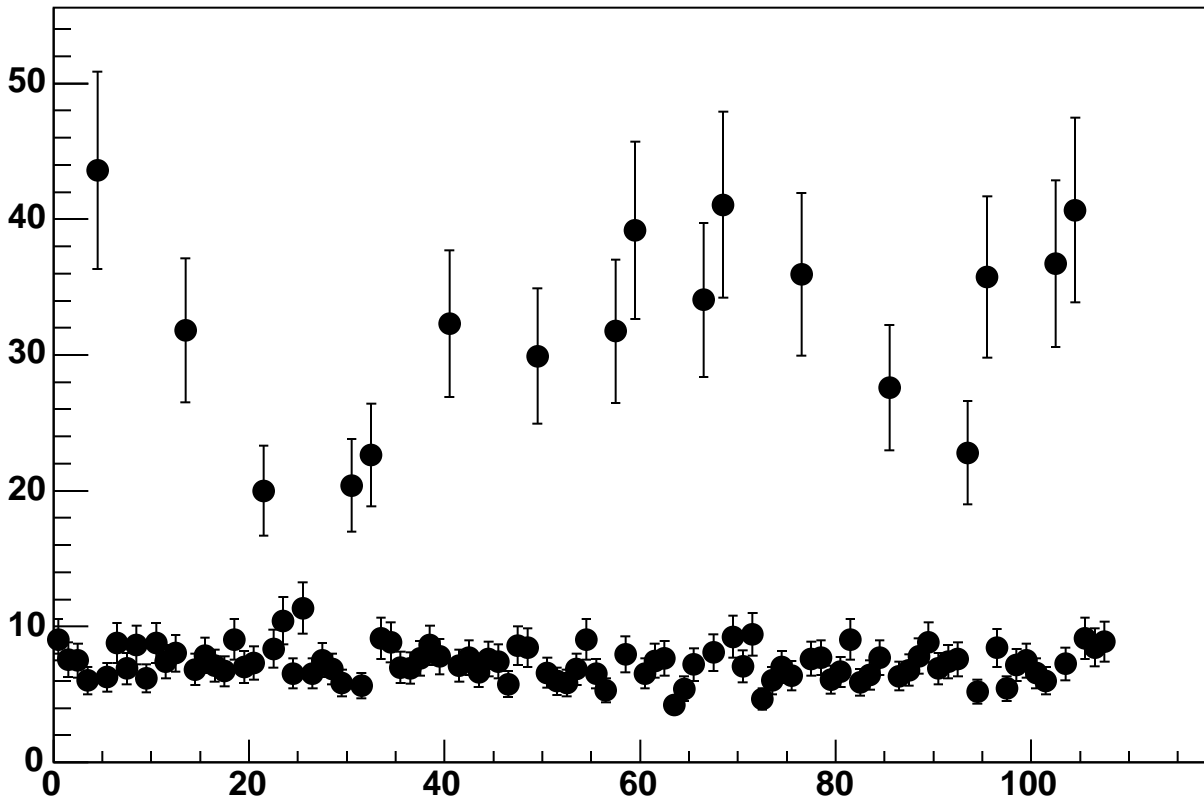
Enable 4, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



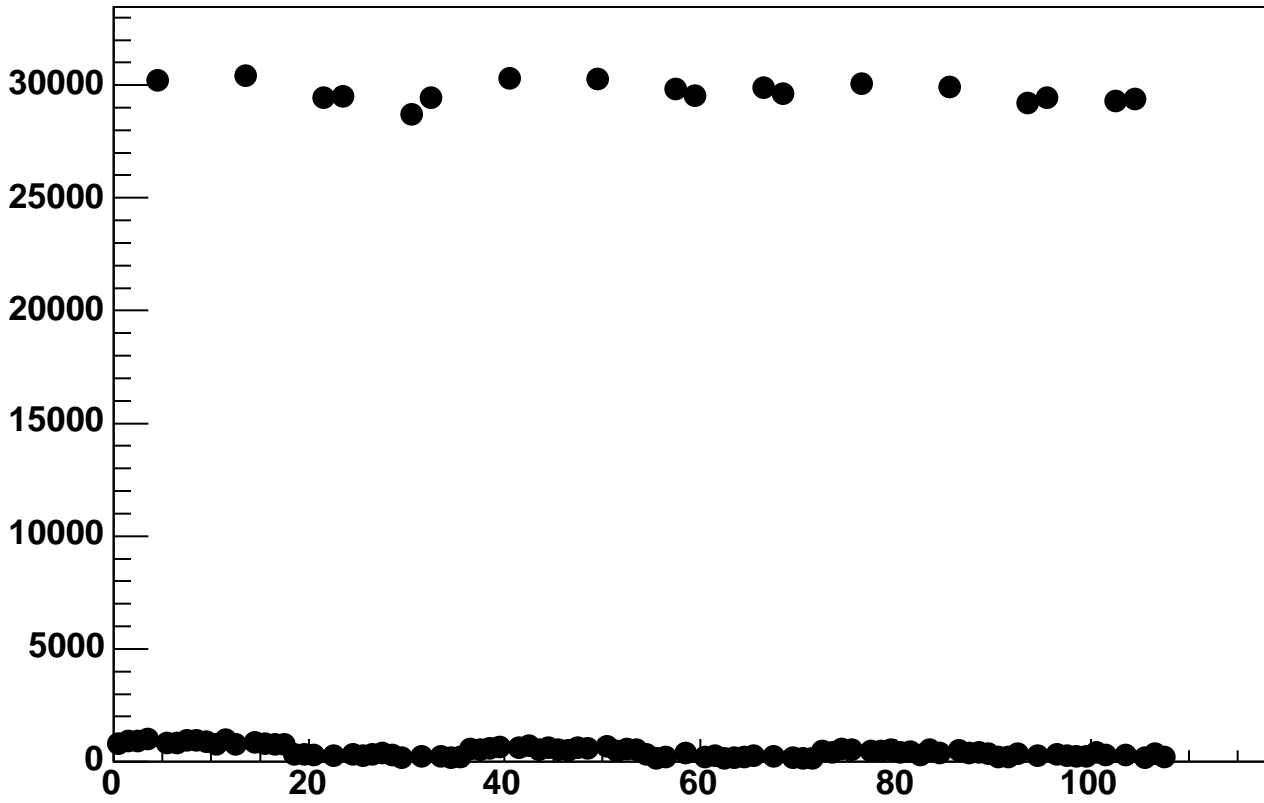
Enable 4, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



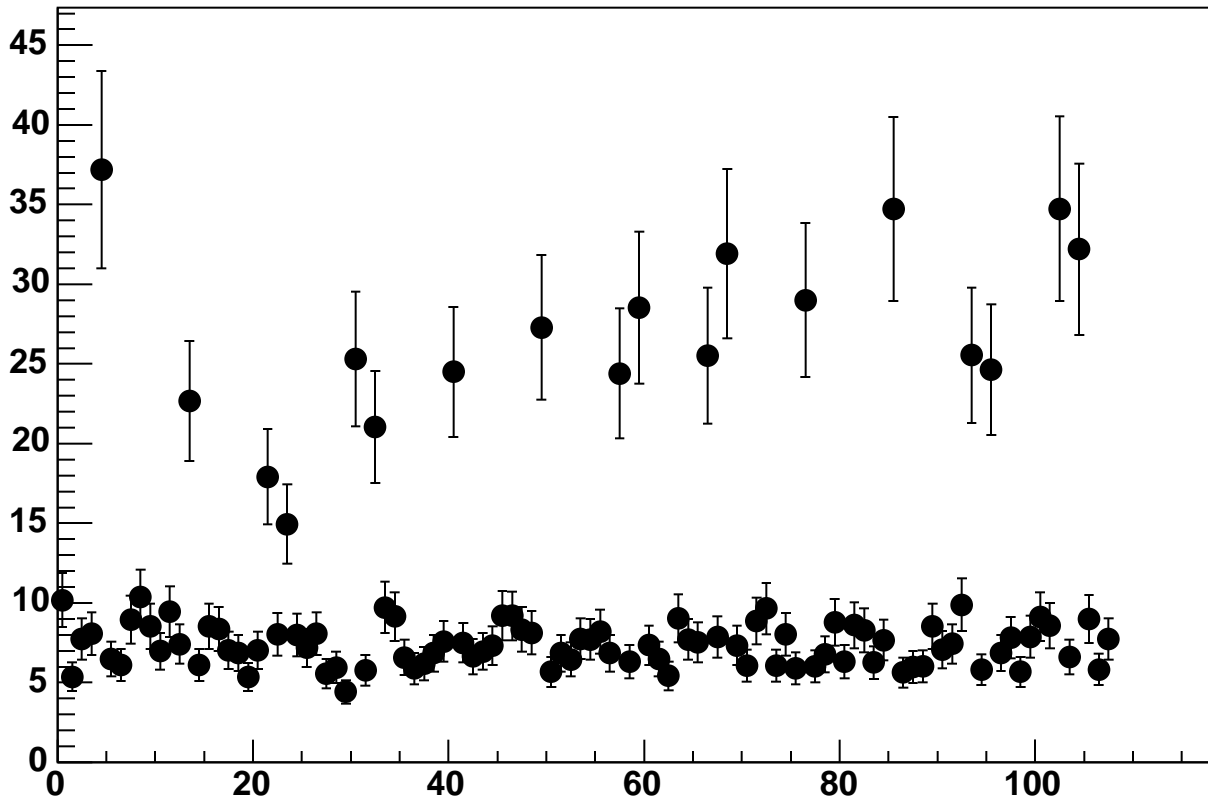
Enable 4, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



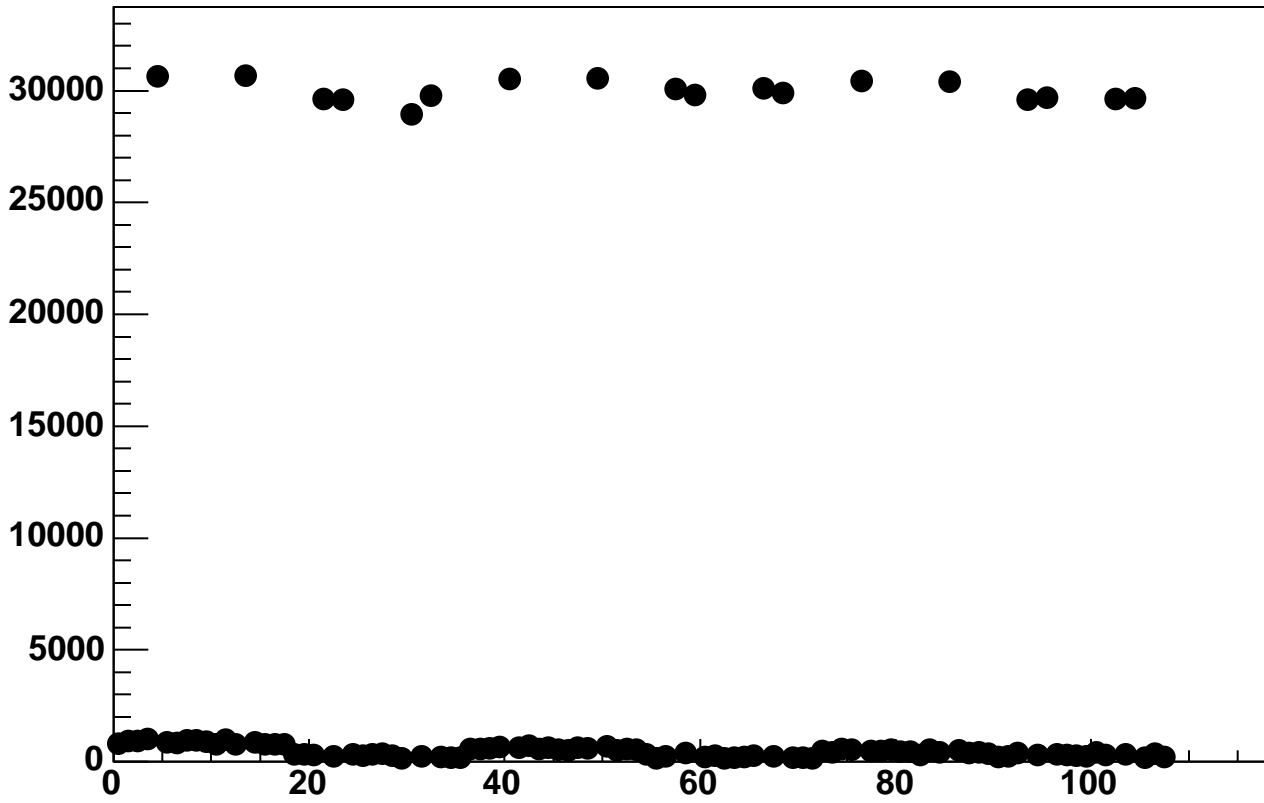
Enable 4, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



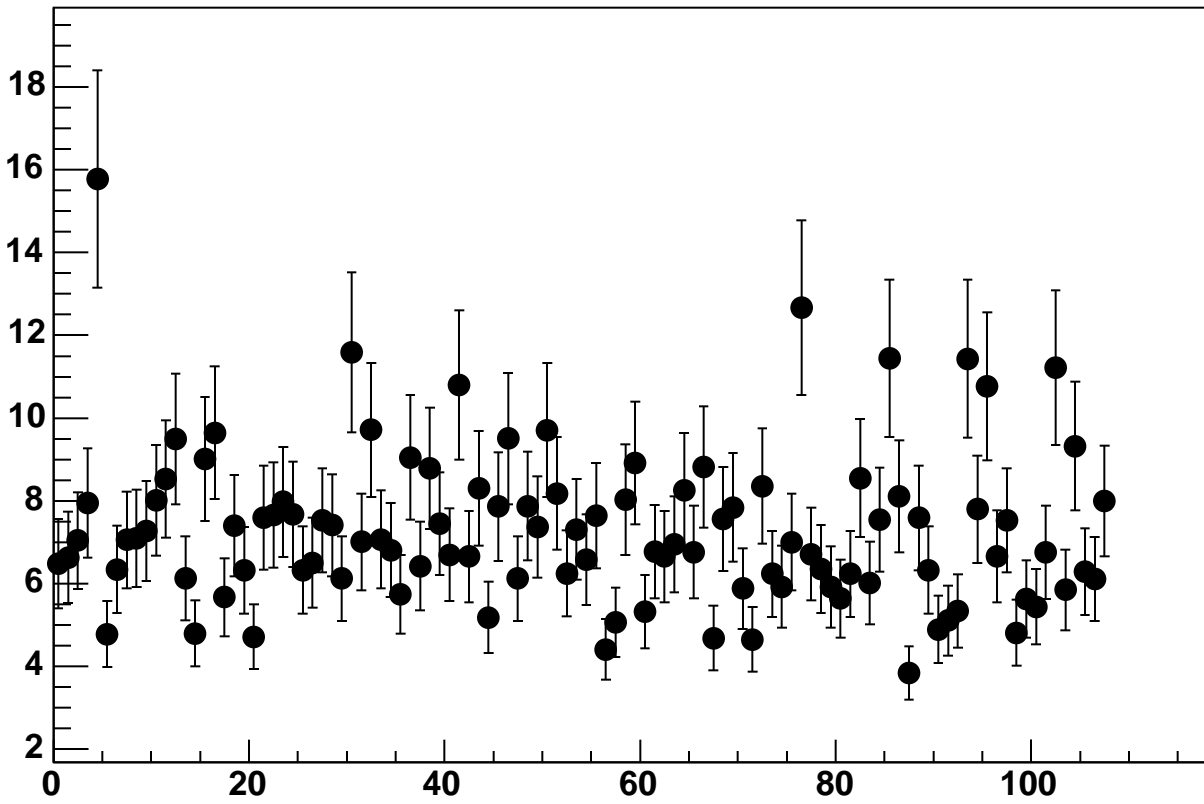
Enable 4, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



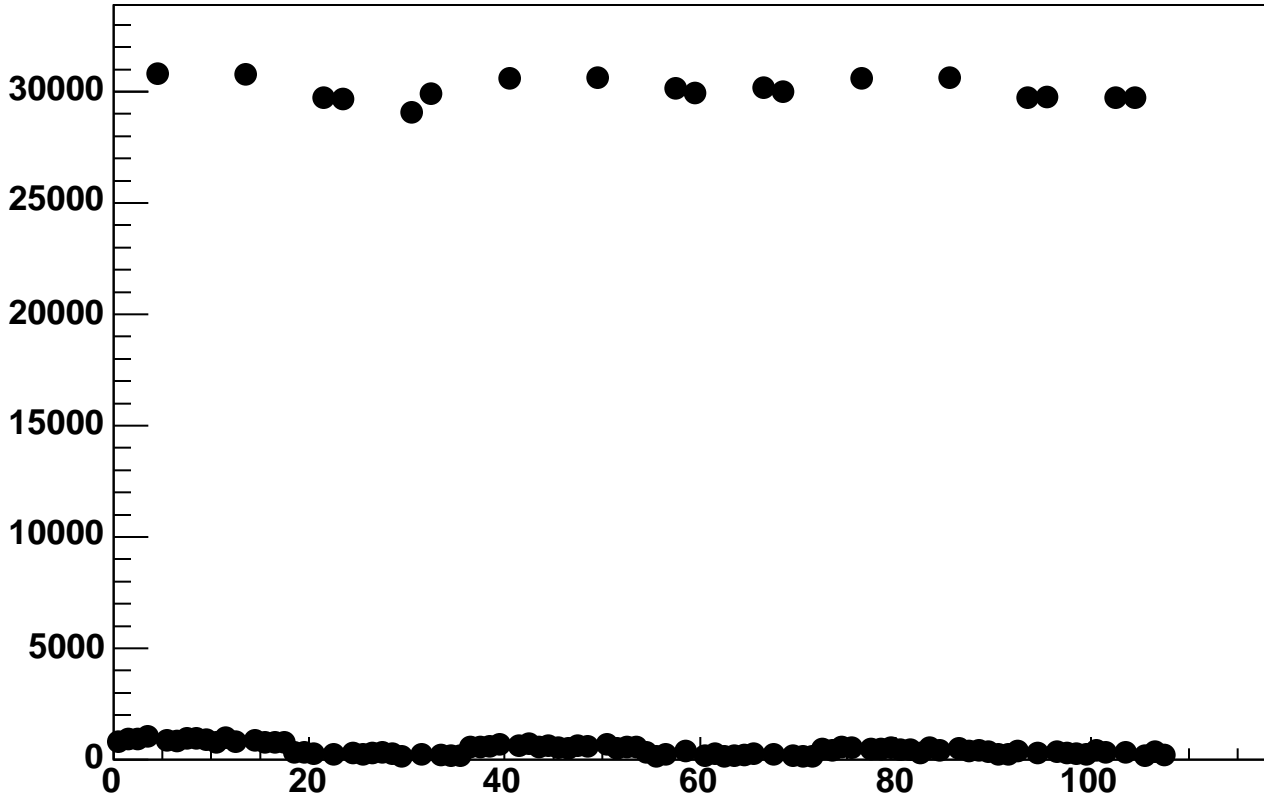
Enable 4, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



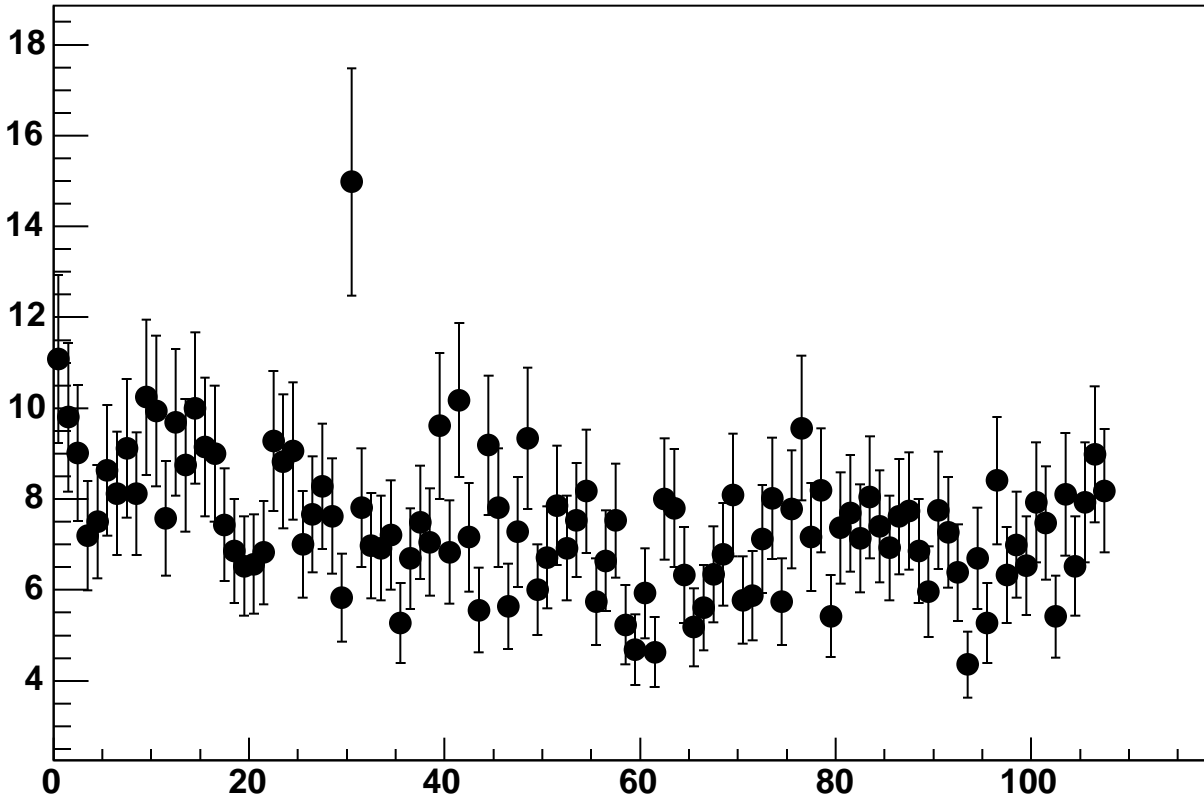
Enable 4, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



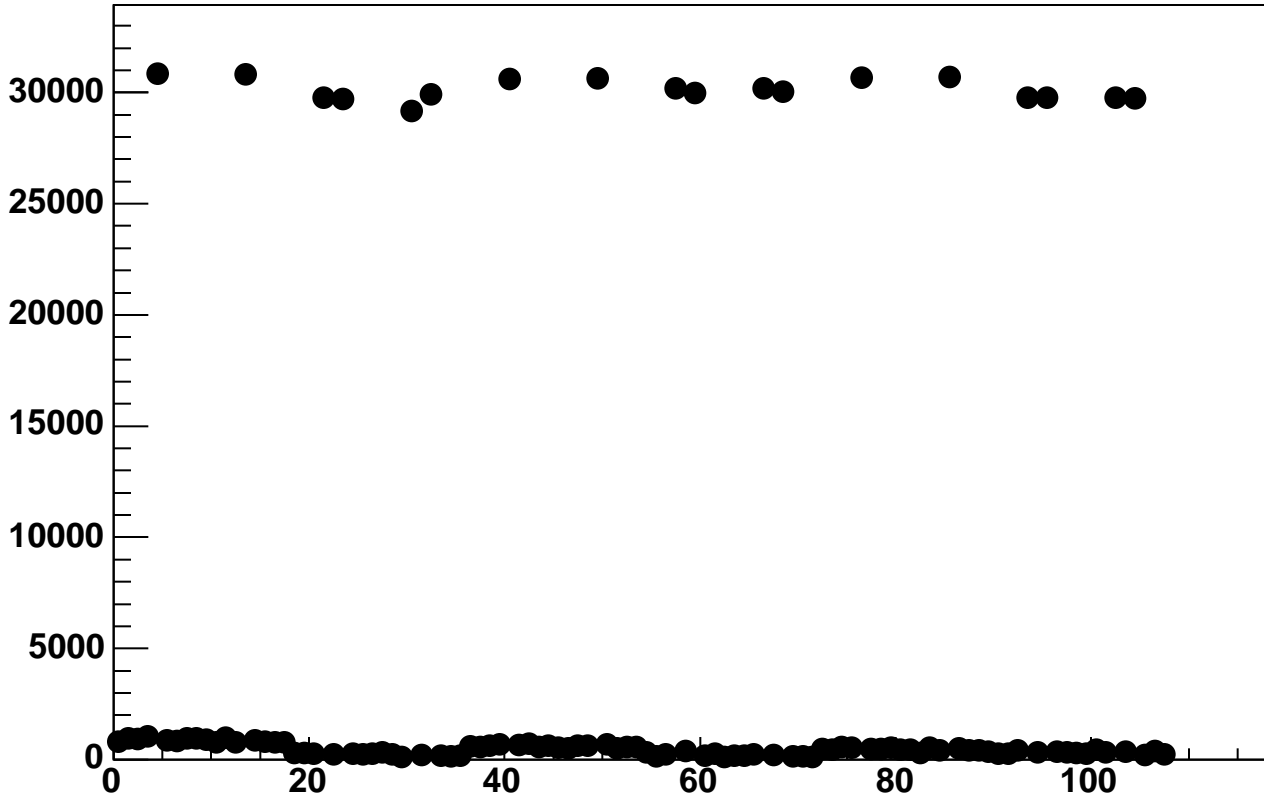
Enable 4, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



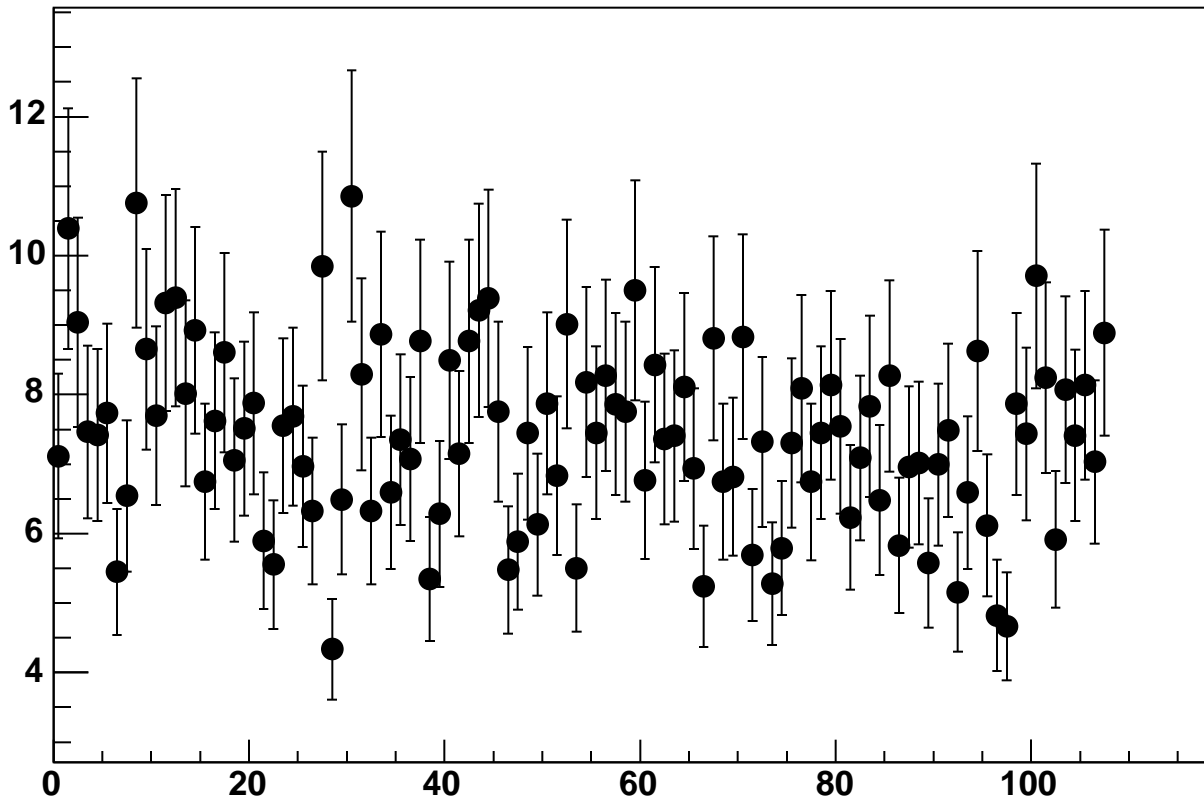
Enable 4, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



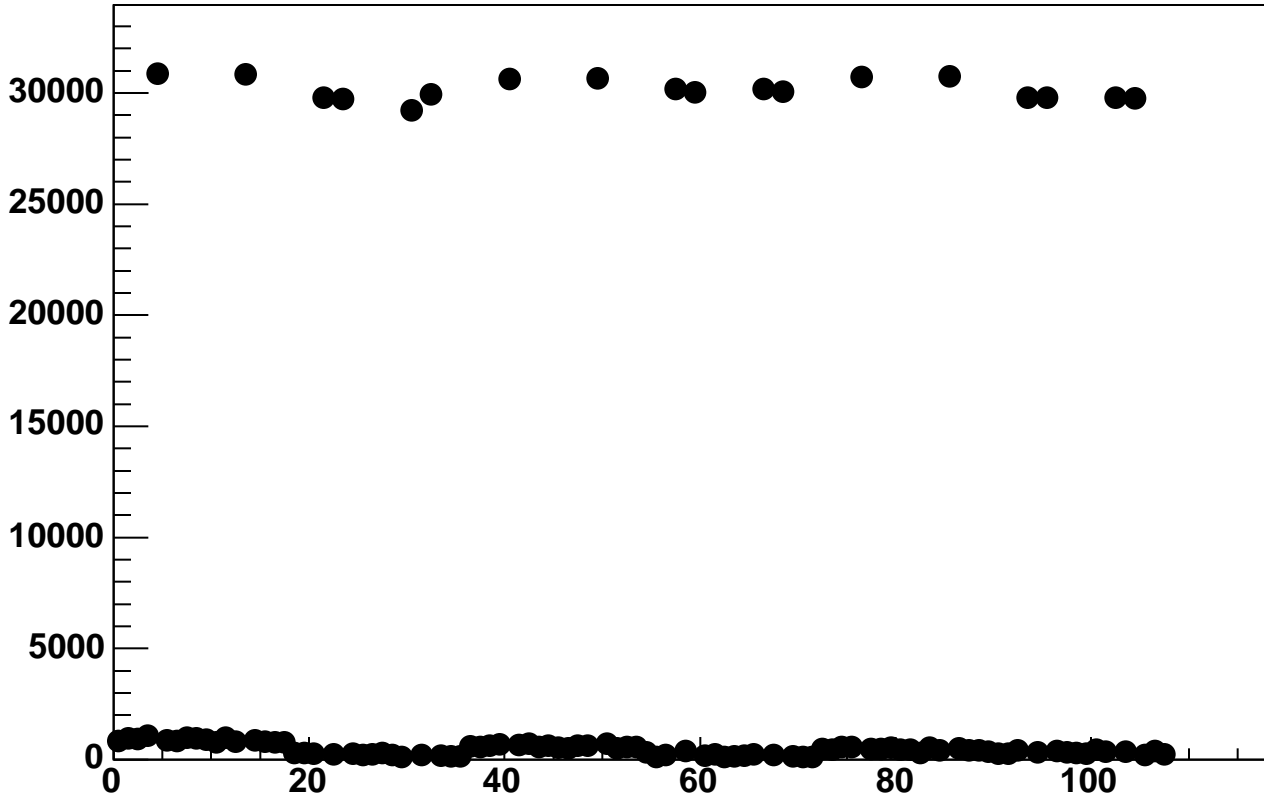
Enable 4, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



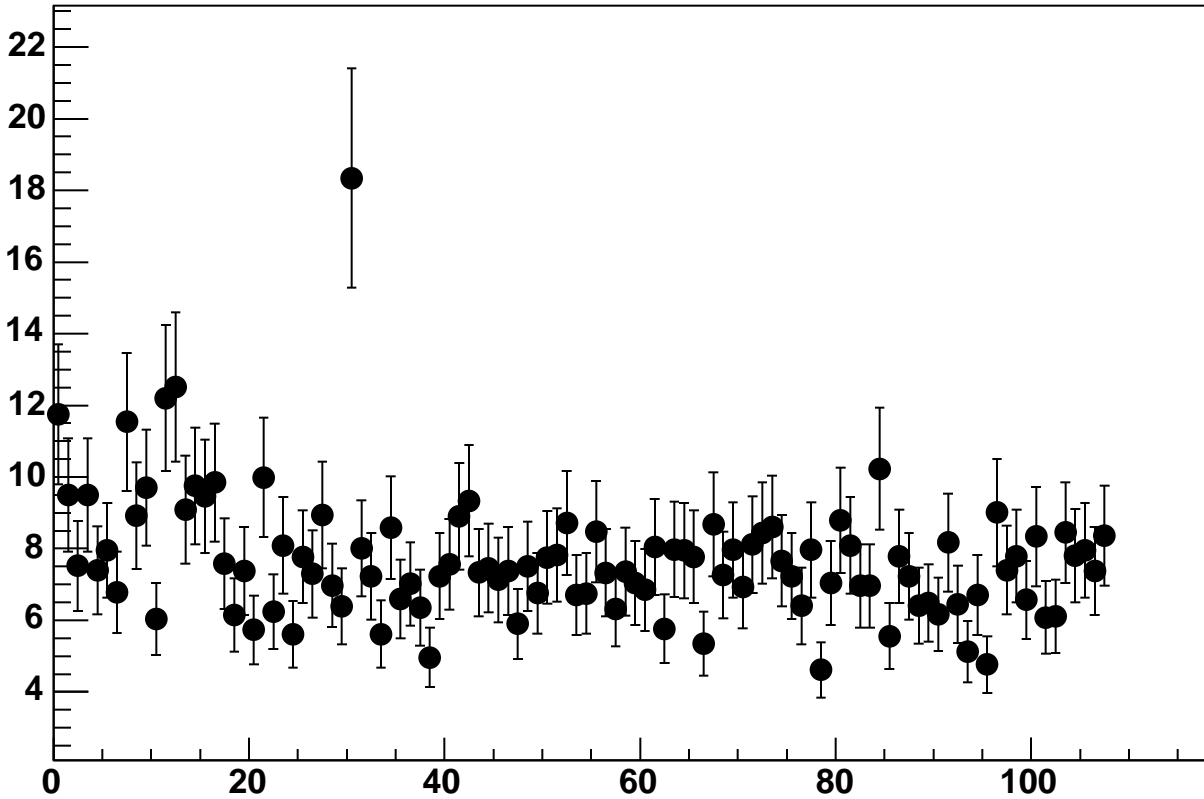
Enable 4, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



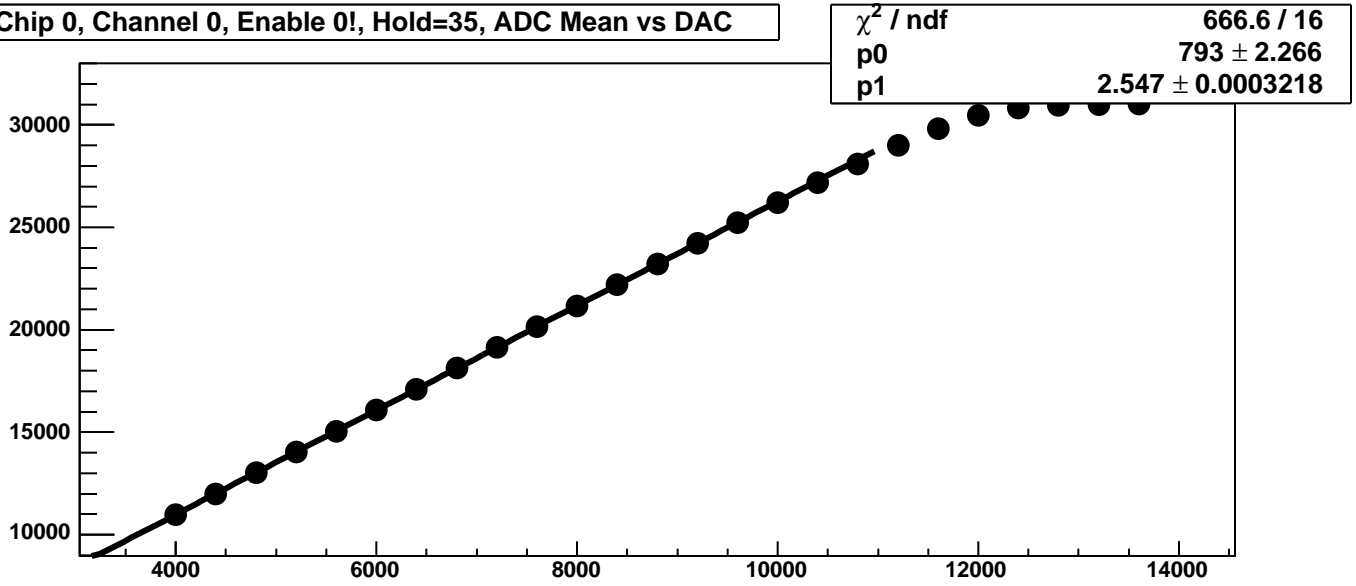
Enable 4, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



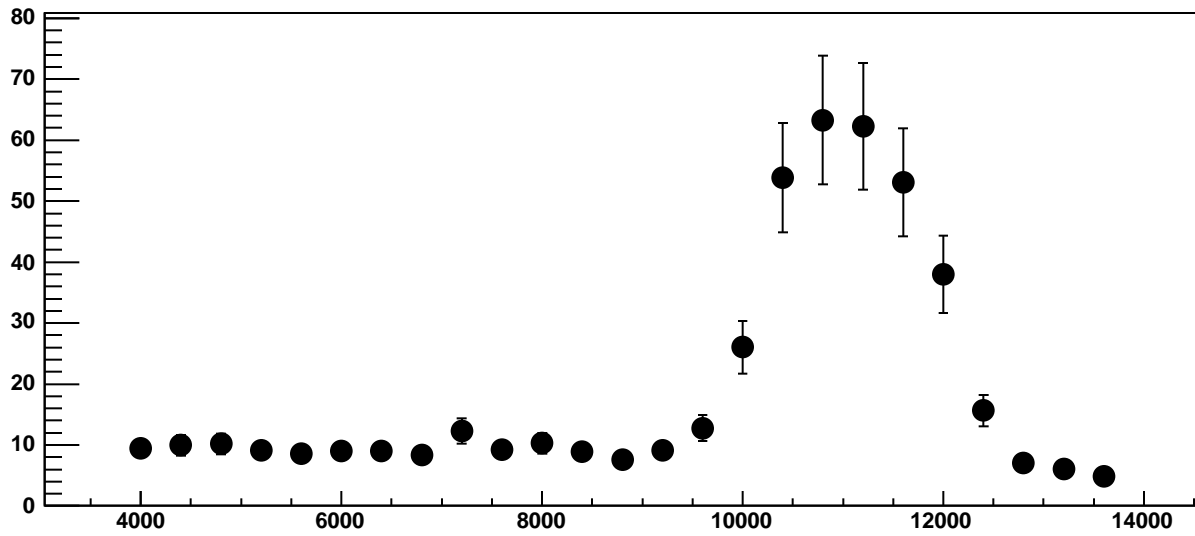
Enable 4, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



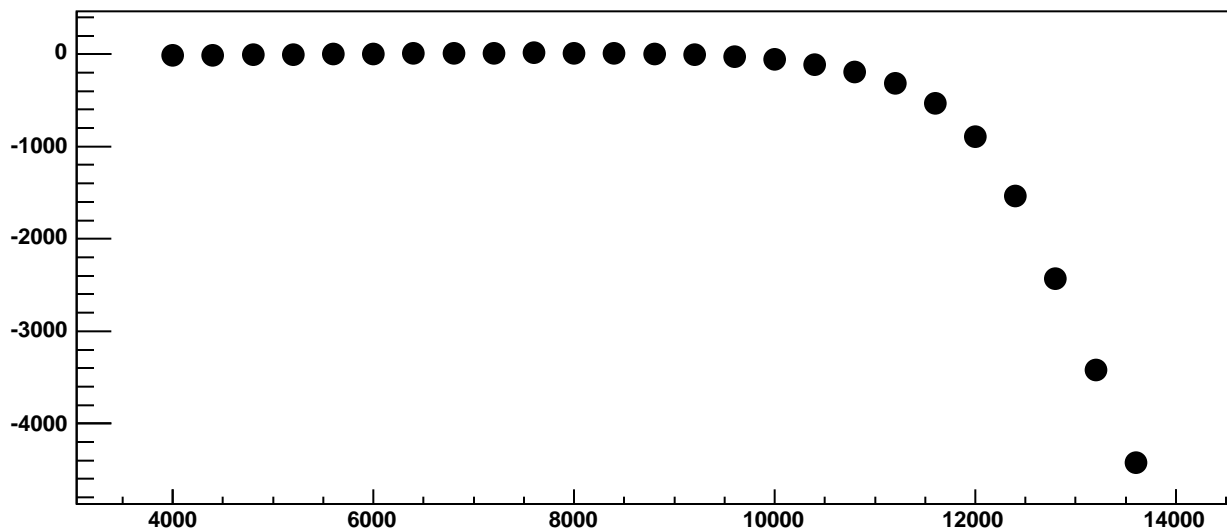
Chip 0, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



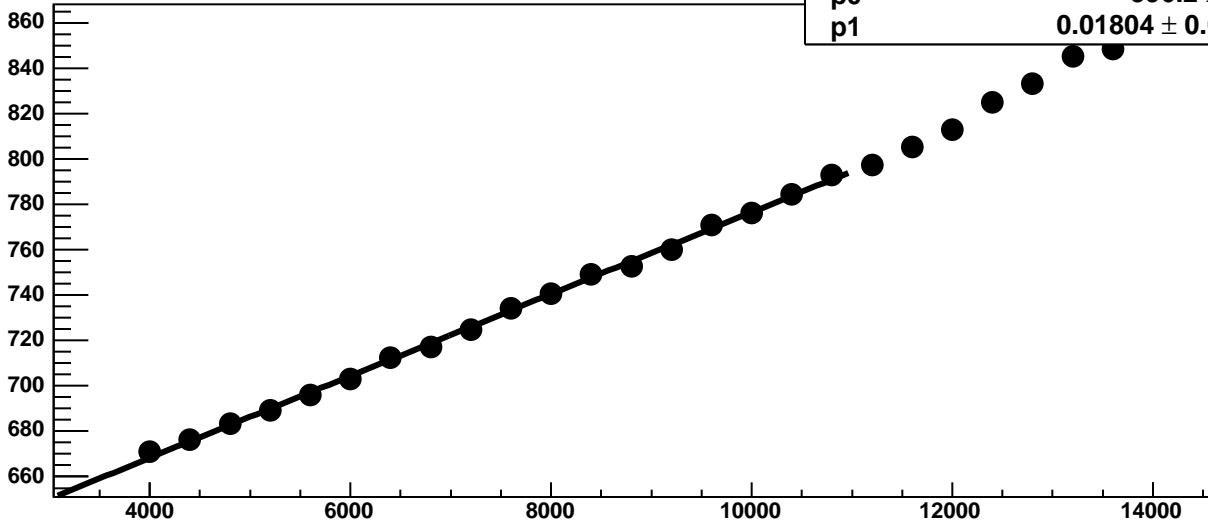
Chip 0, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

10.85 / 16

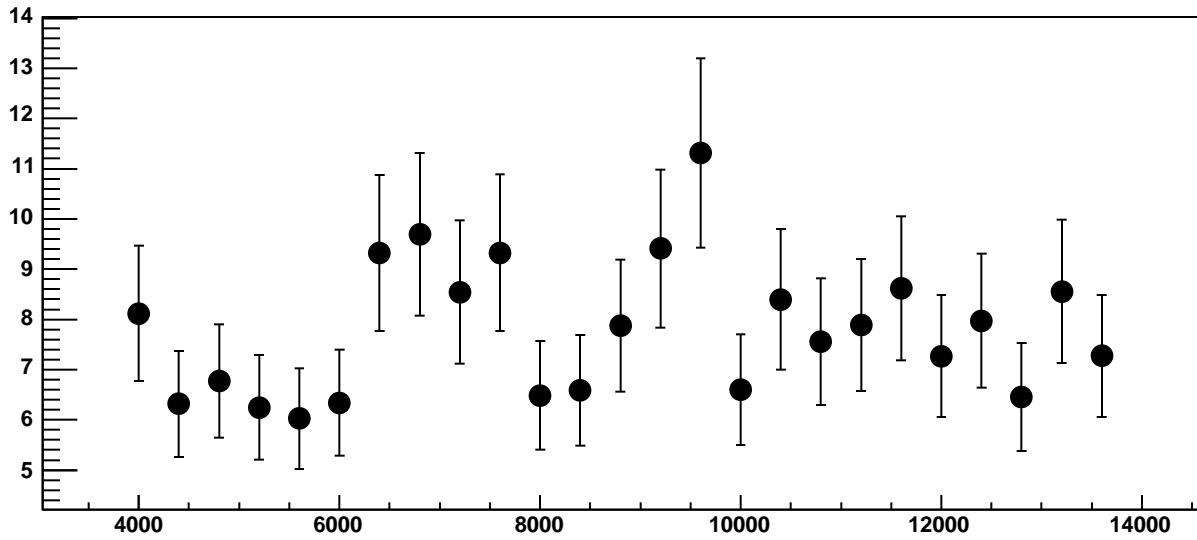
p0

596.2 ± 1.429

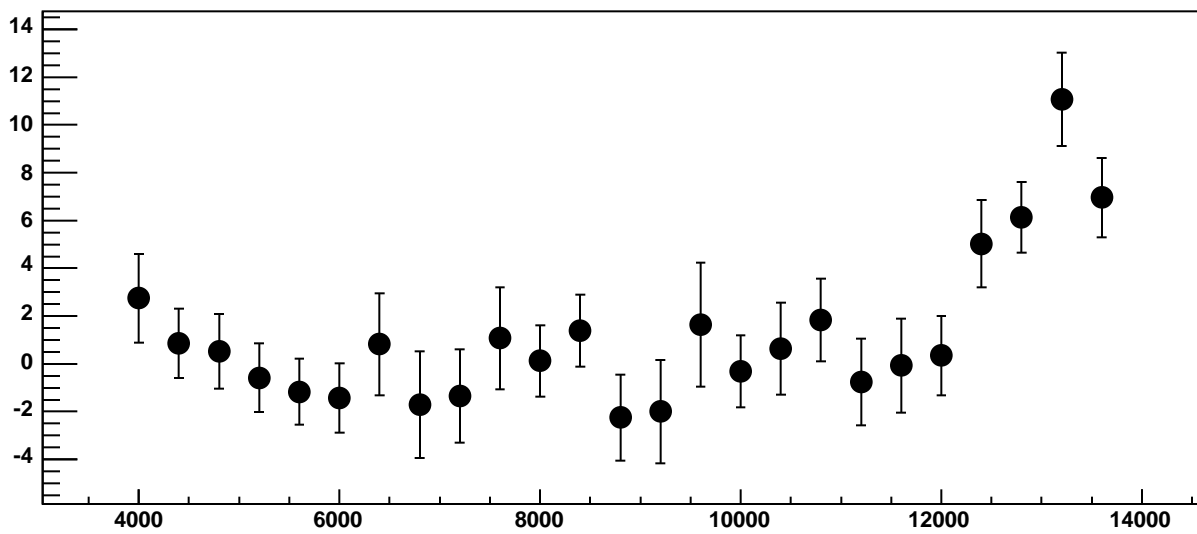
p1

0.01804 ± 0.000192

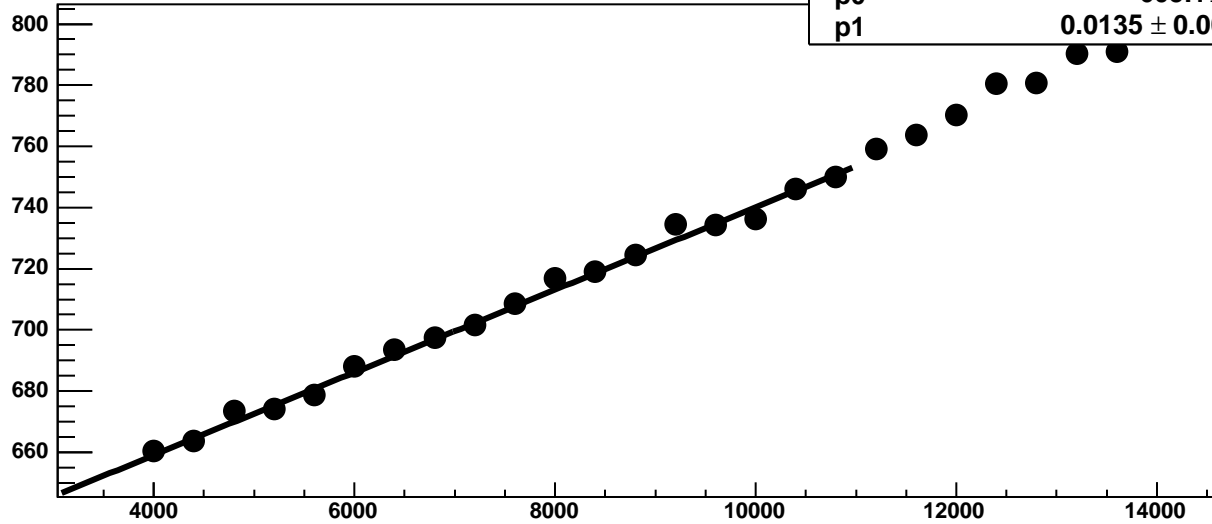
Chip 0, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

26.48 / 16

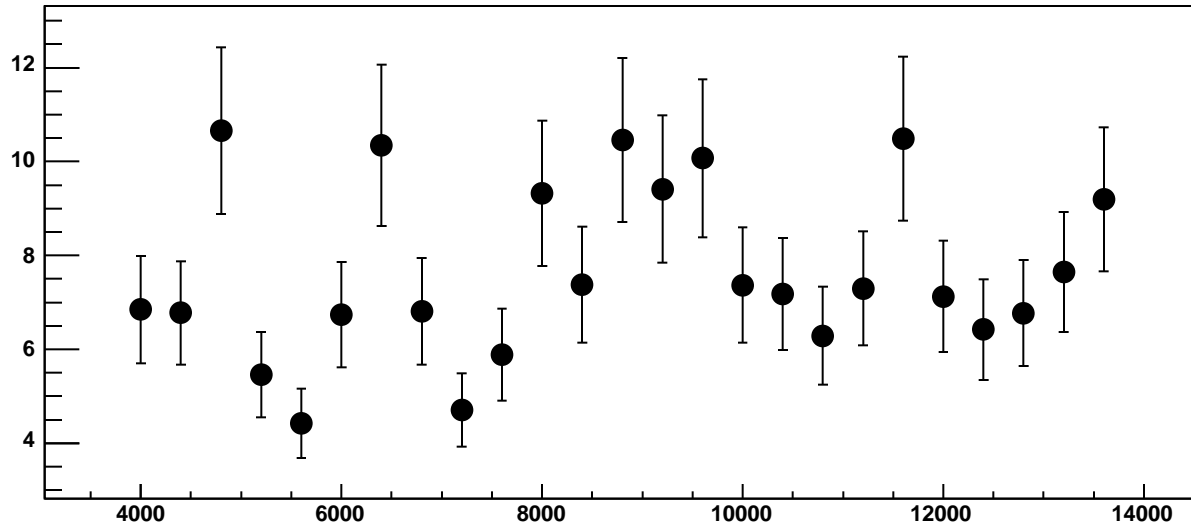
p0

605.1 ± 1.346

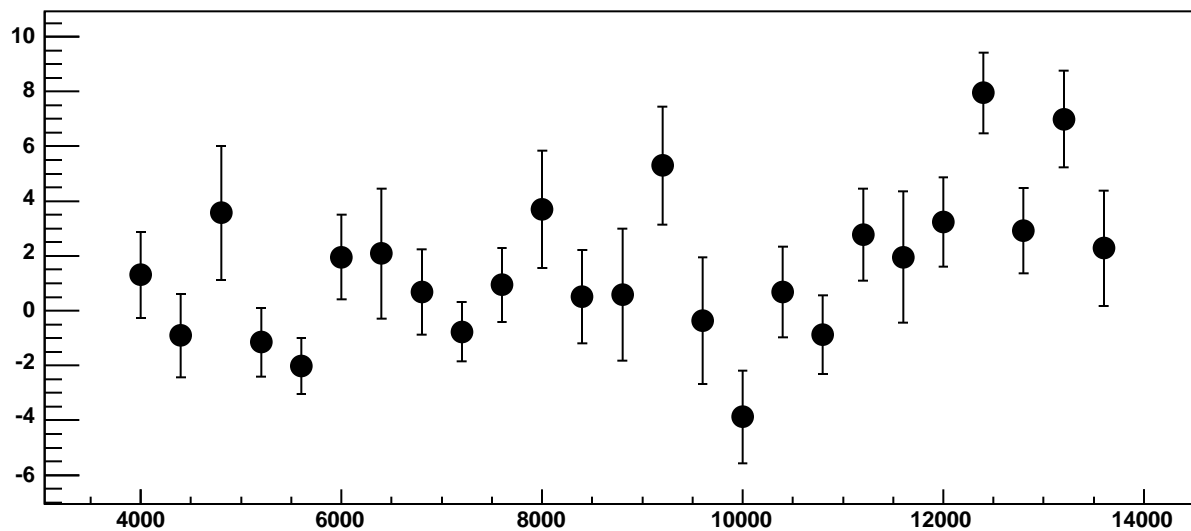
p1

0.0135 ± 0.0001822

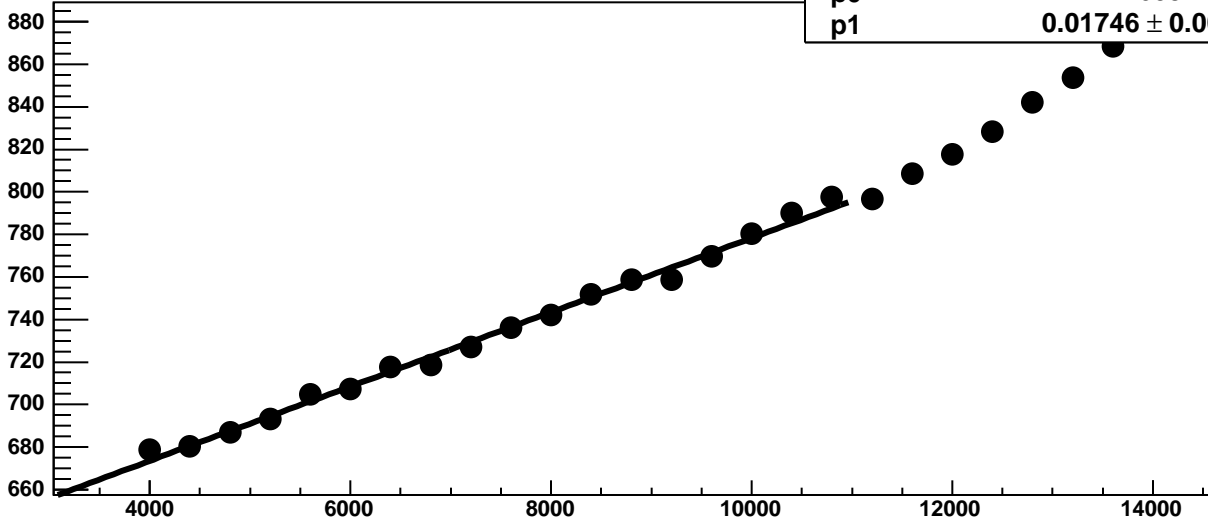
Chip 0, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

40.04 / 16

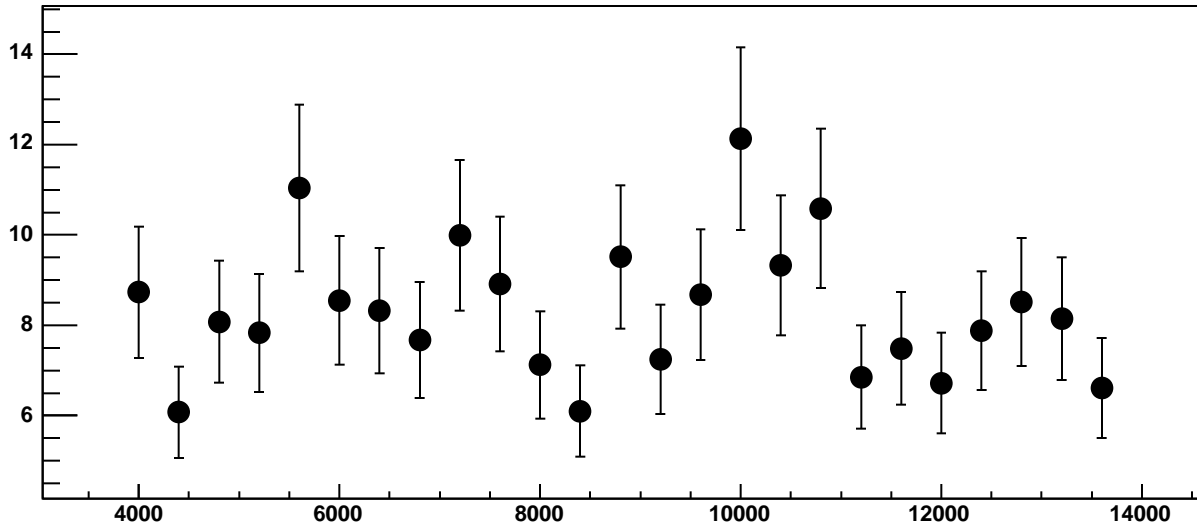
p0

603.7 ± 1.65

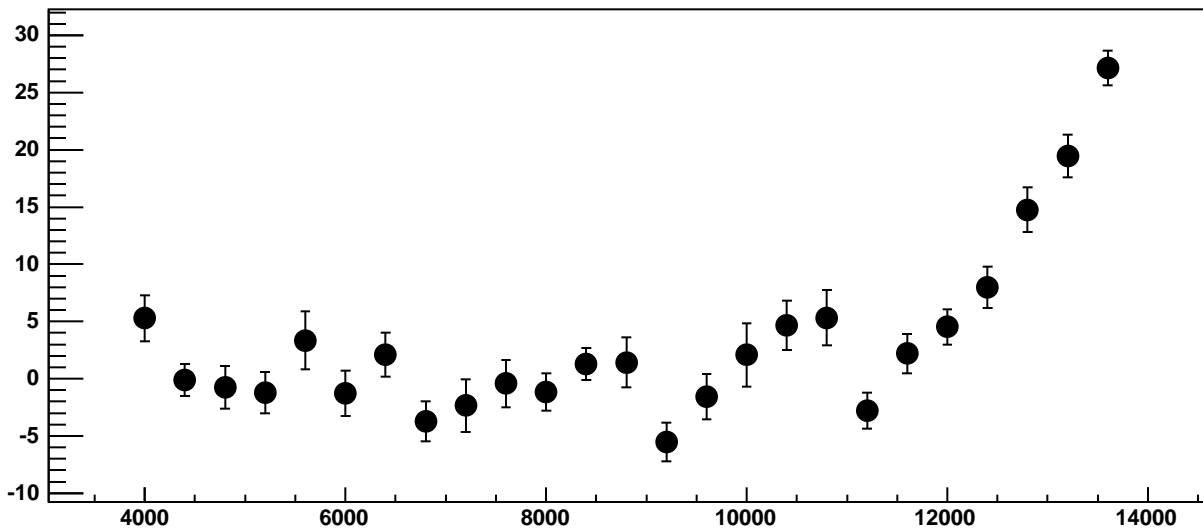
p1

0.01746 ± 0.0002214

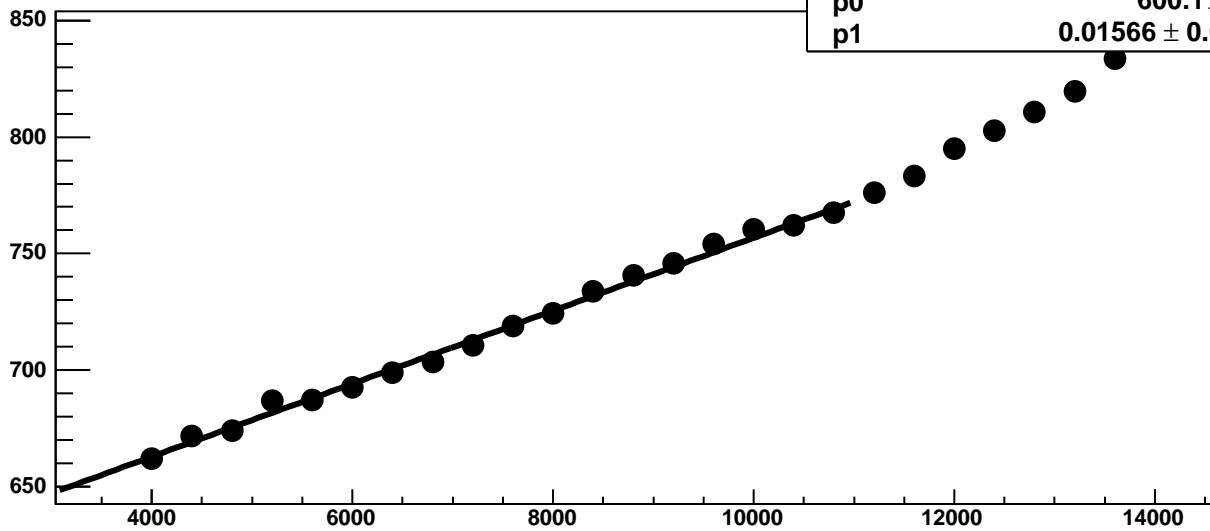
Chip 0, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

29.85 / 16

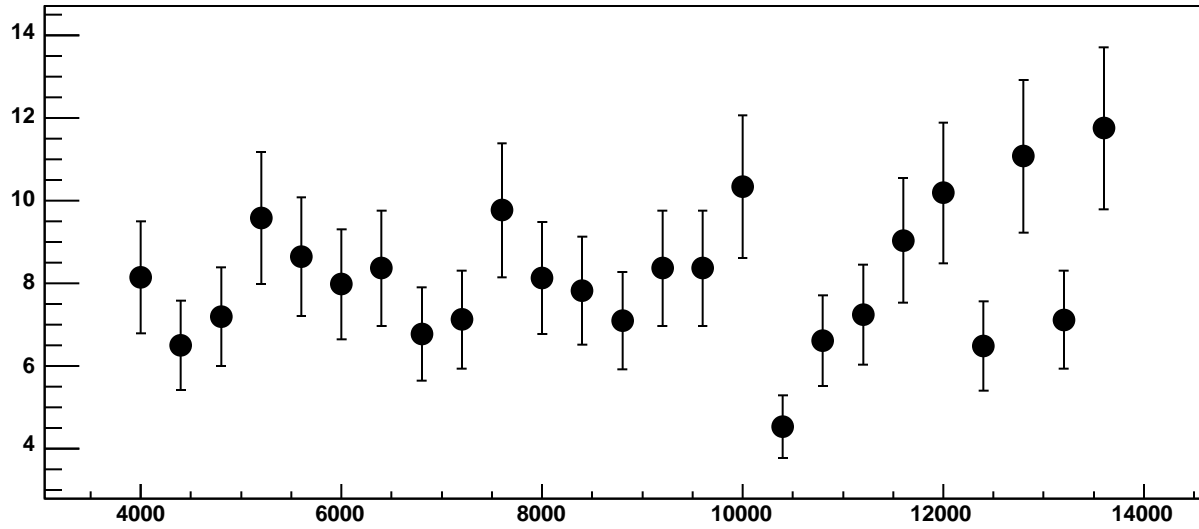
p0

600.1 ± 1.452

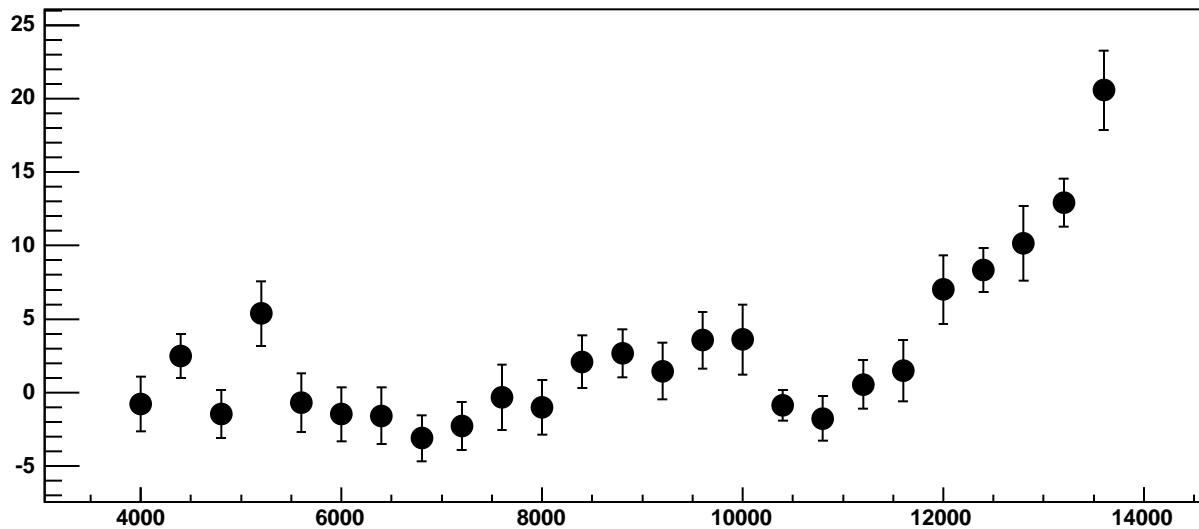
p1

0.01566 ± 0.000182

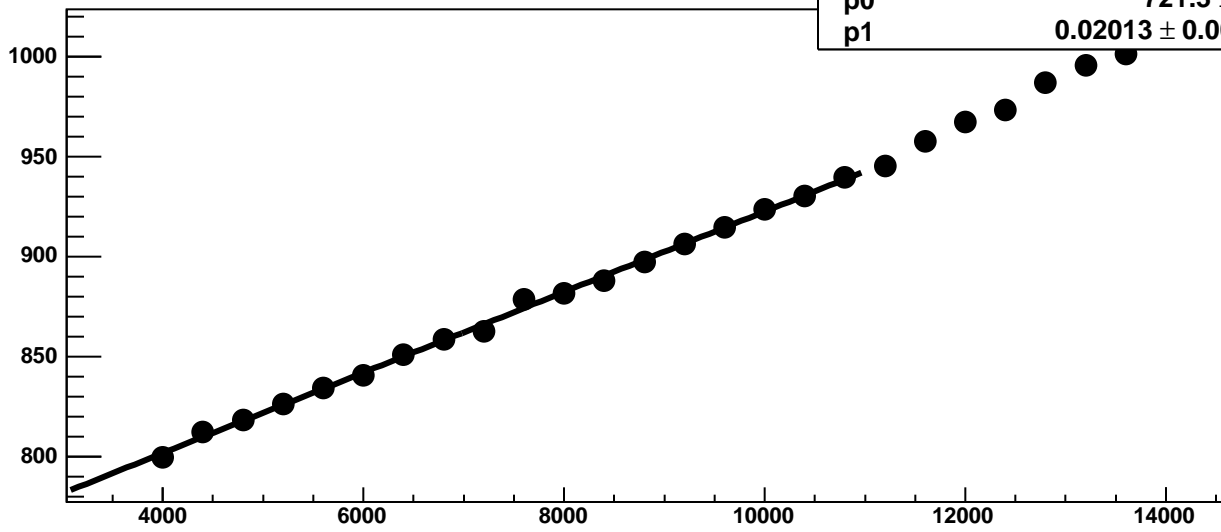
Chip 0, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

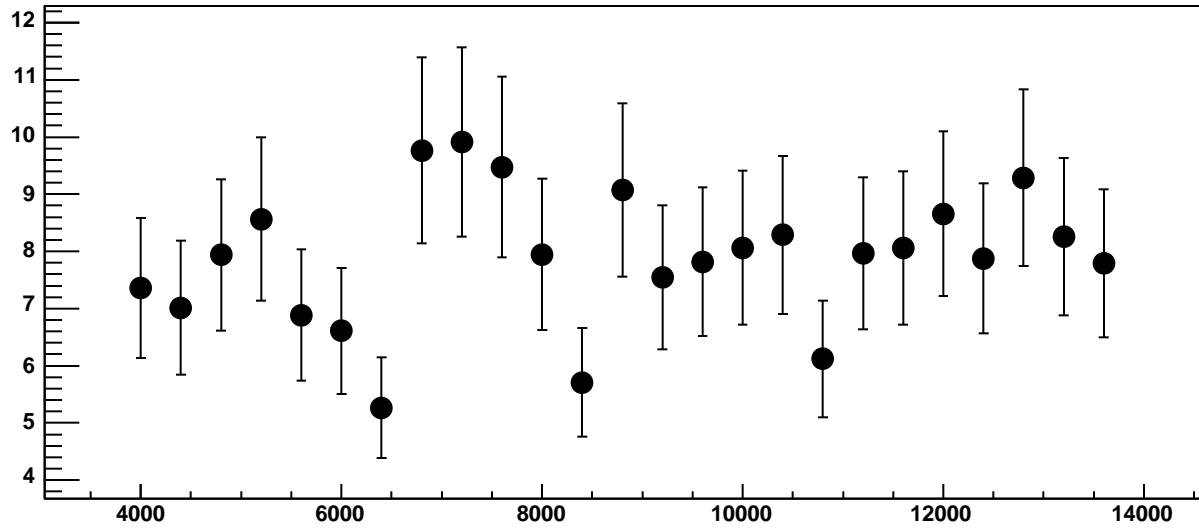


Chip 0, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC

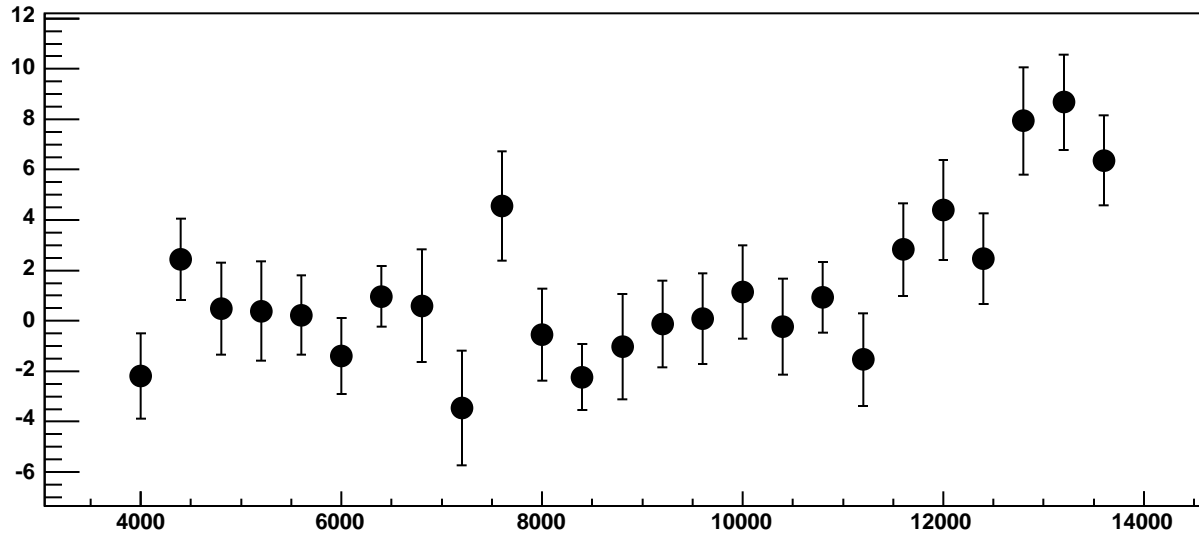


χ^2 / ndf 16.46 / 16
p0 721.3 ± 1.455
p1 0.02013 ± 0.0001897

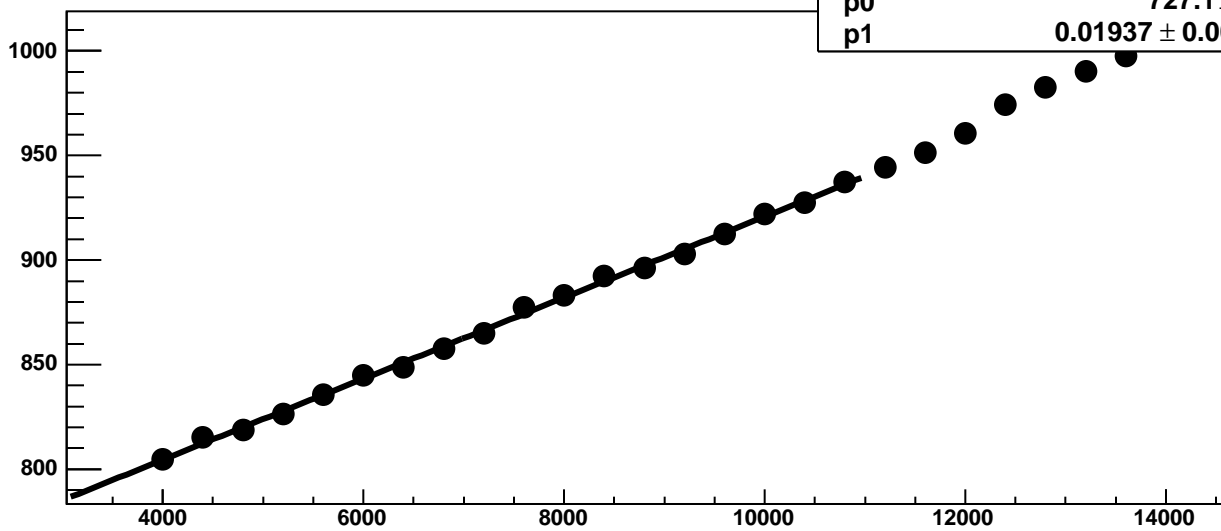
Chip 0, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

14.07 / 16

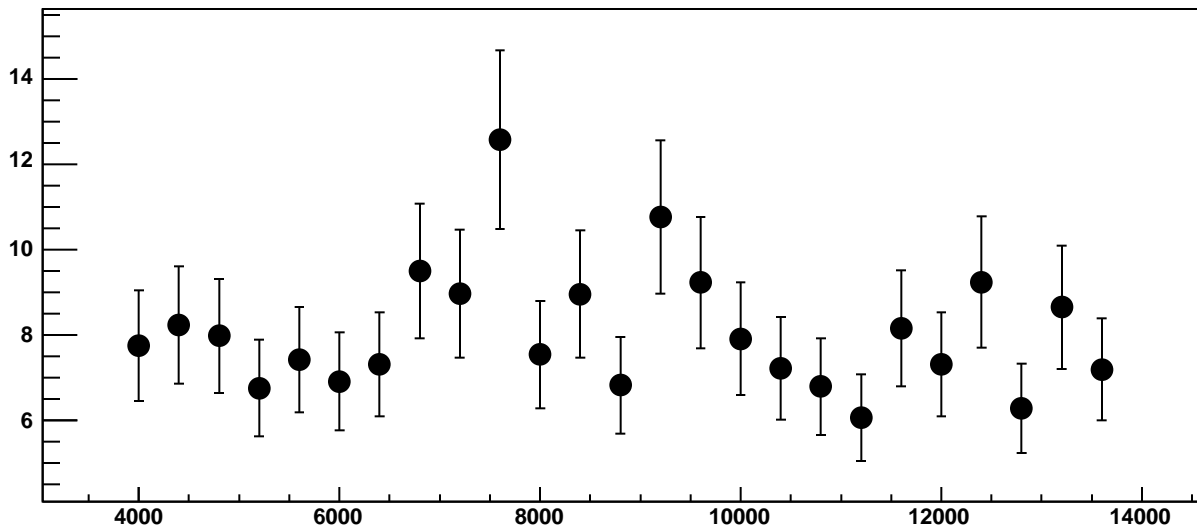
p0

727.1 ± 1.523

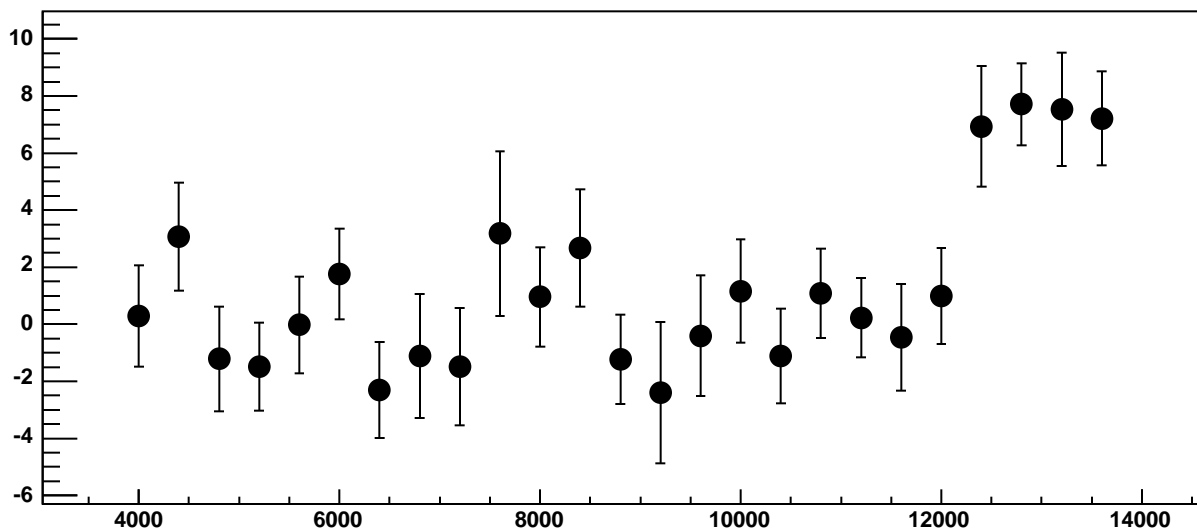
p1

0.01937 ± 0.0001985

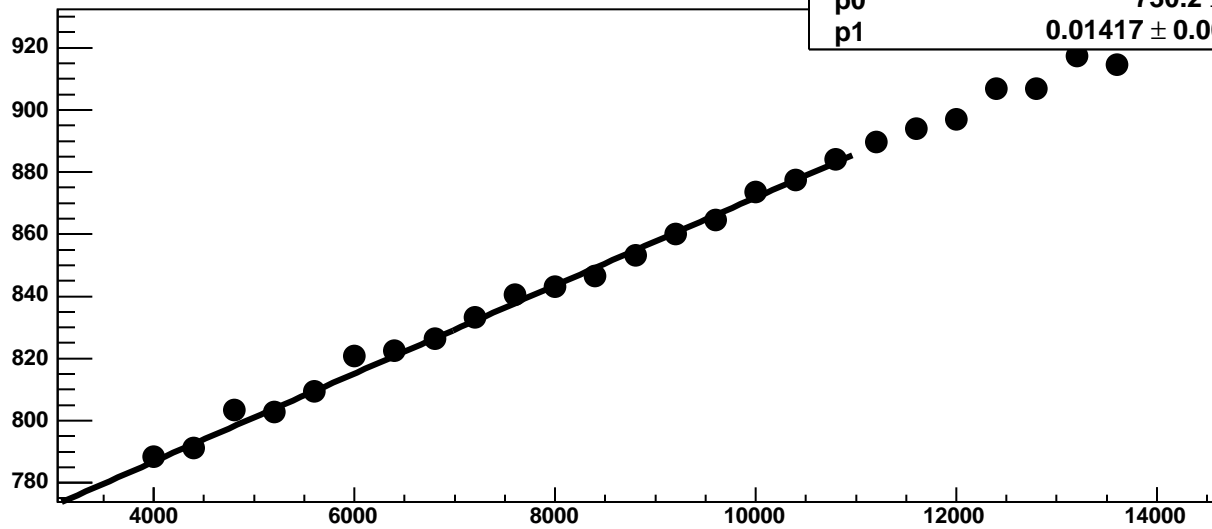
Chip 0, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

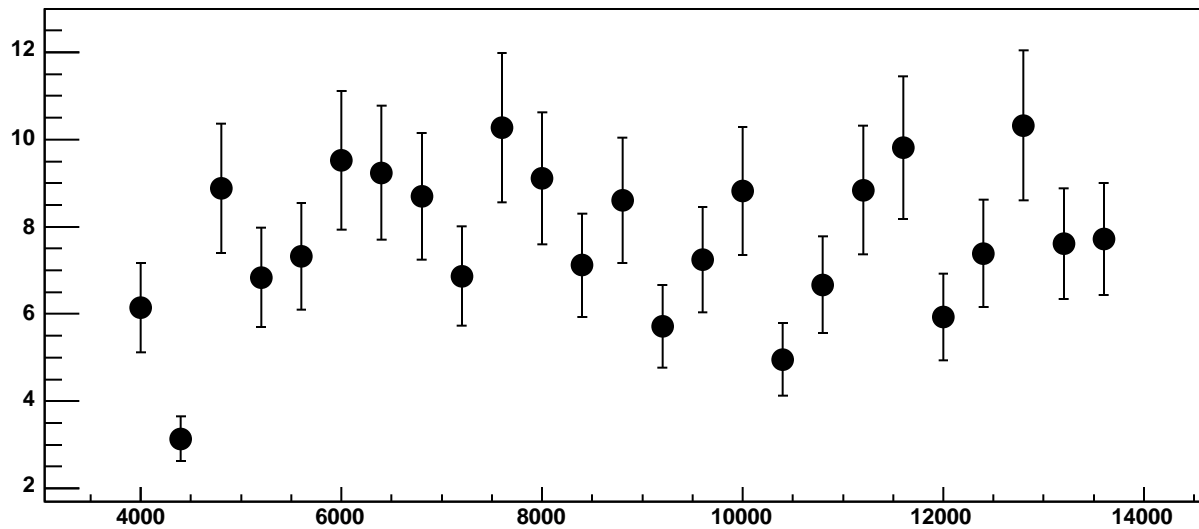


Chip 0, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

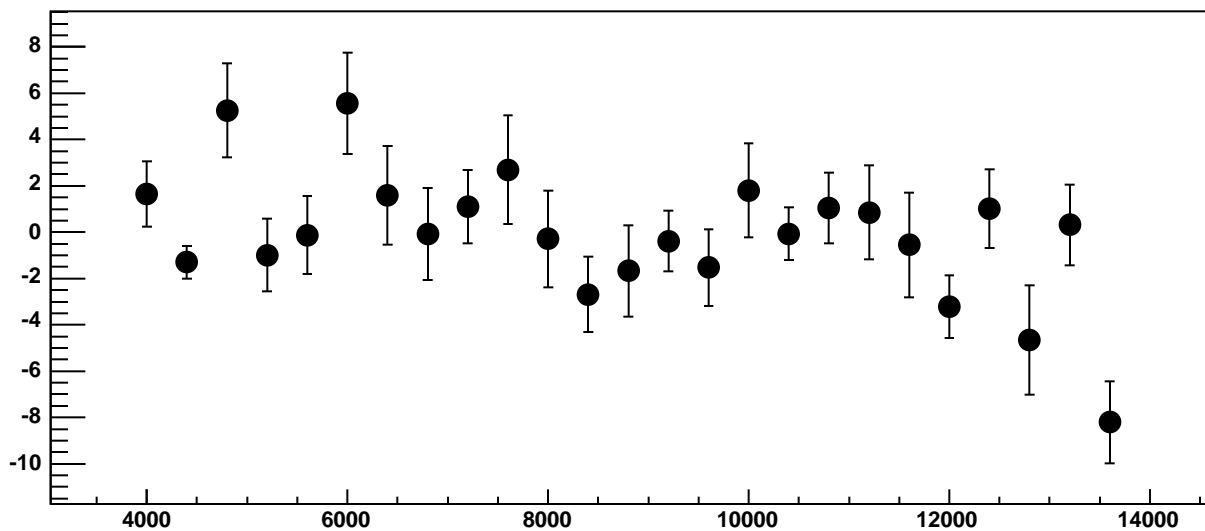


χ^2 / ndf 26.35 / 16
p0 730.2 ± 1.083
p1 0.01417 ± 0.0001472

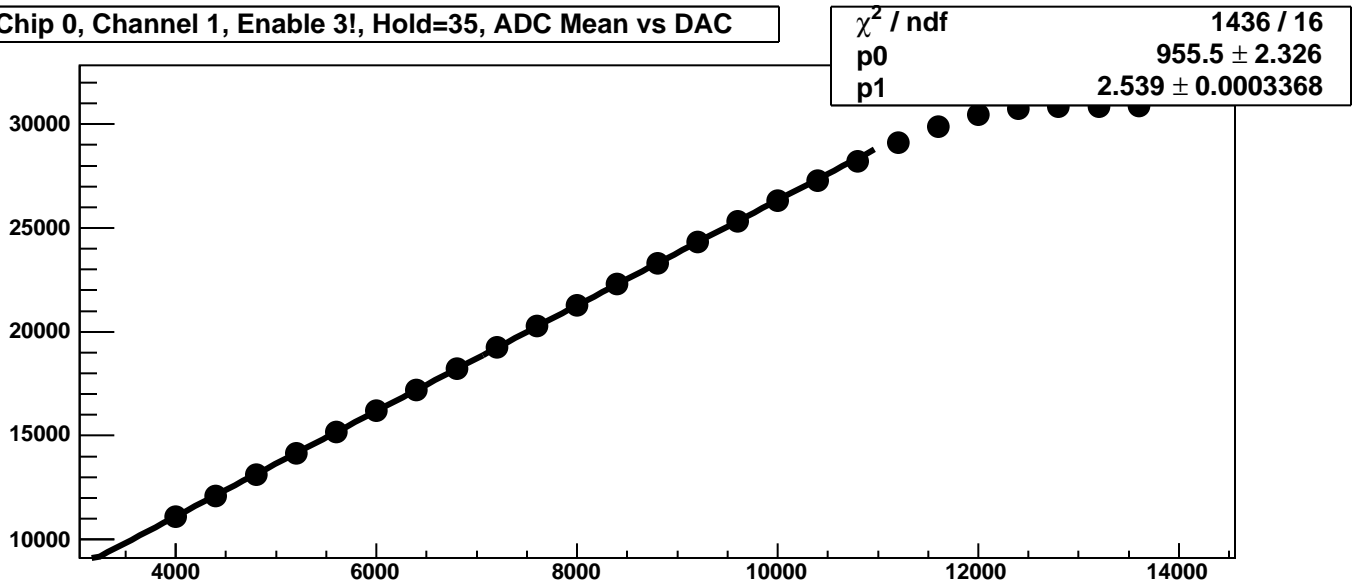
Chip 0, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



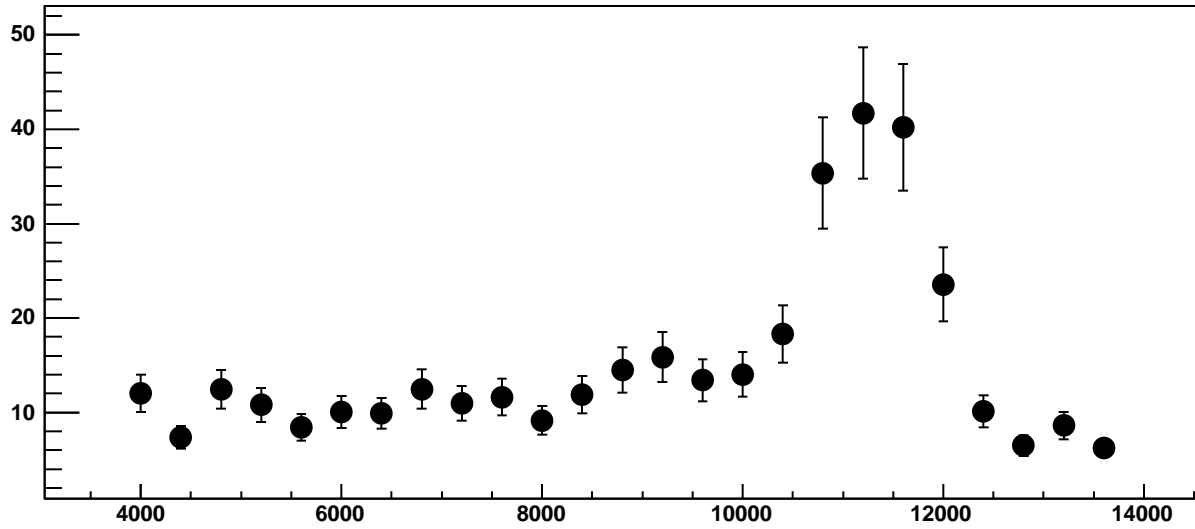
Chip 0, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



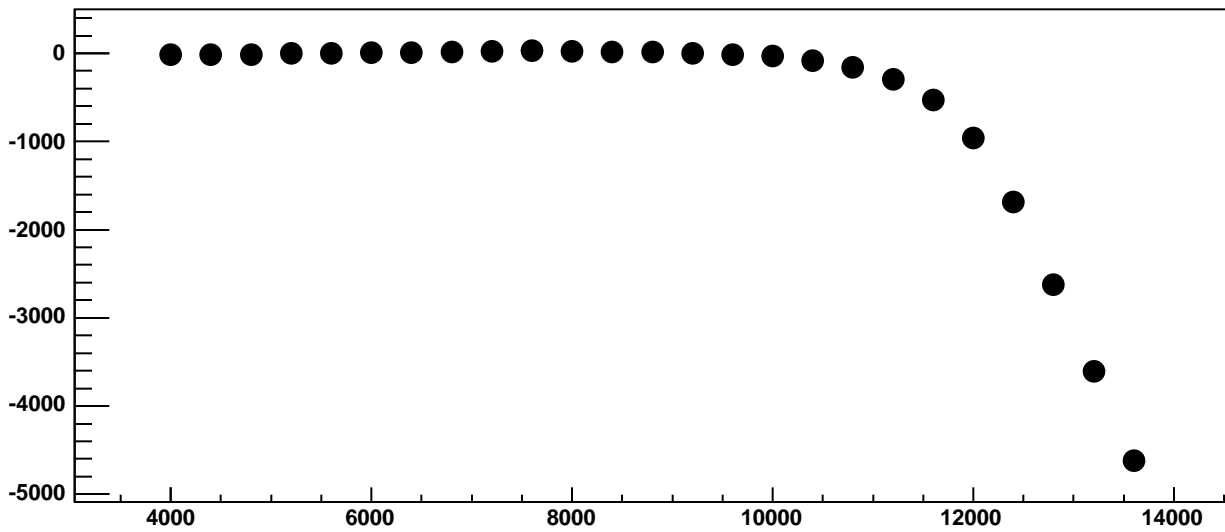
Chip 0, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC



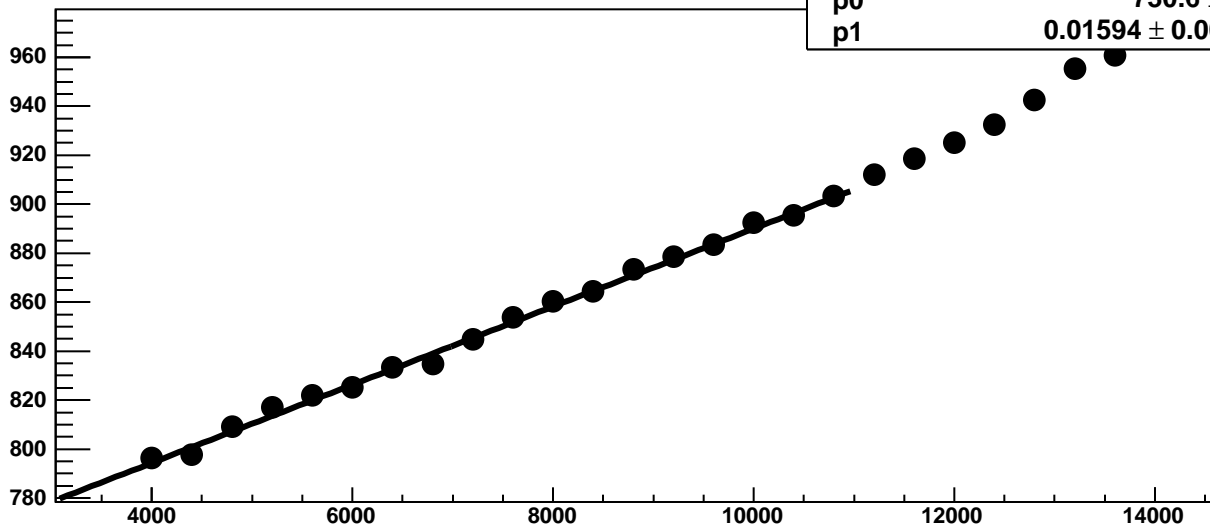
Chip 0, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



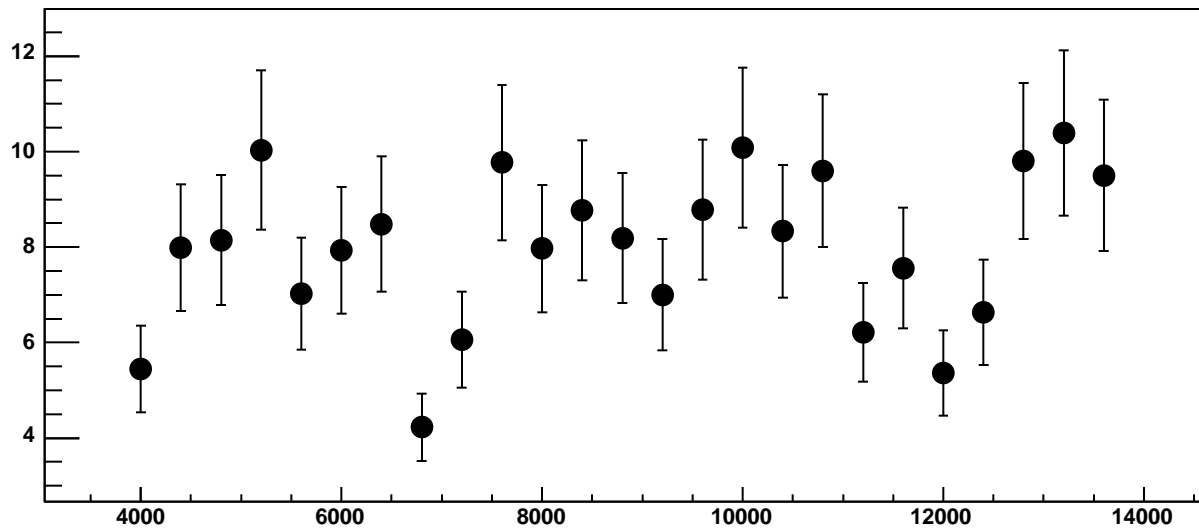
Chip 0, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC



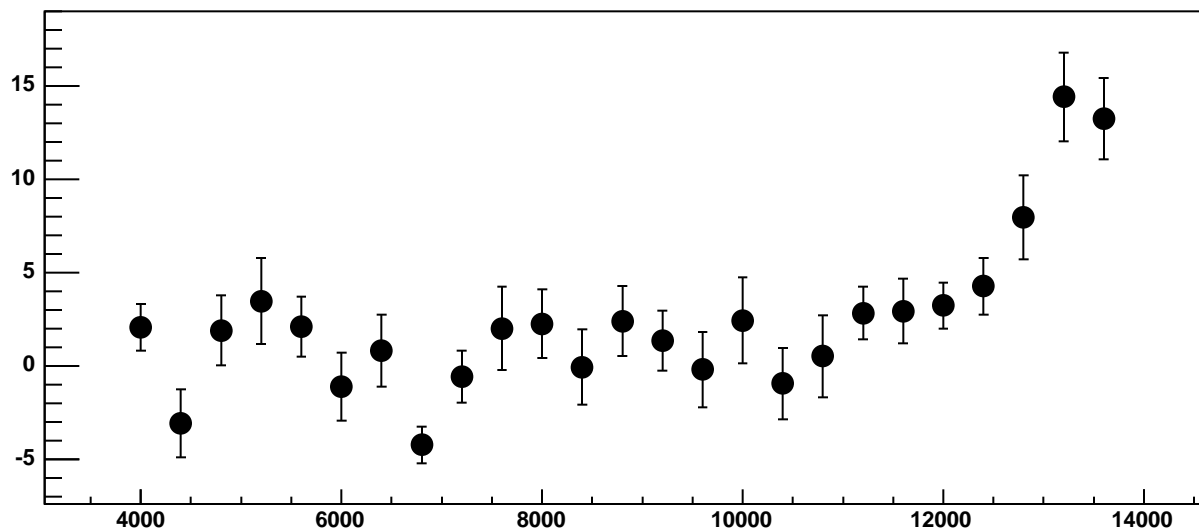
Chip 0, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



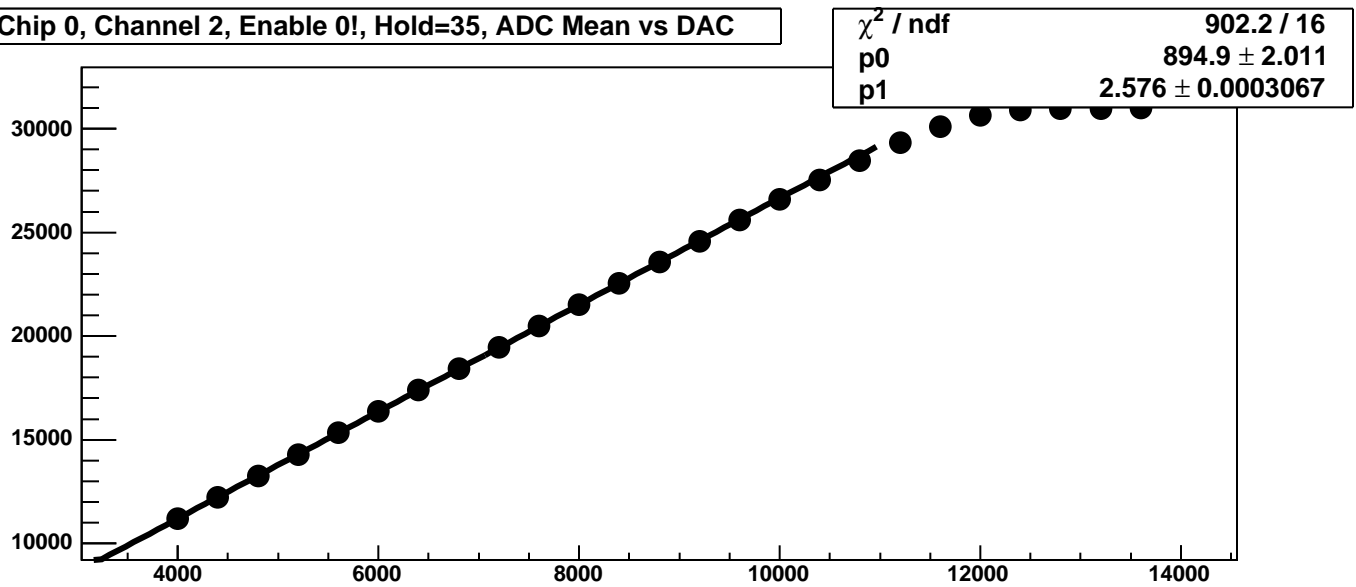
Chip 0, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



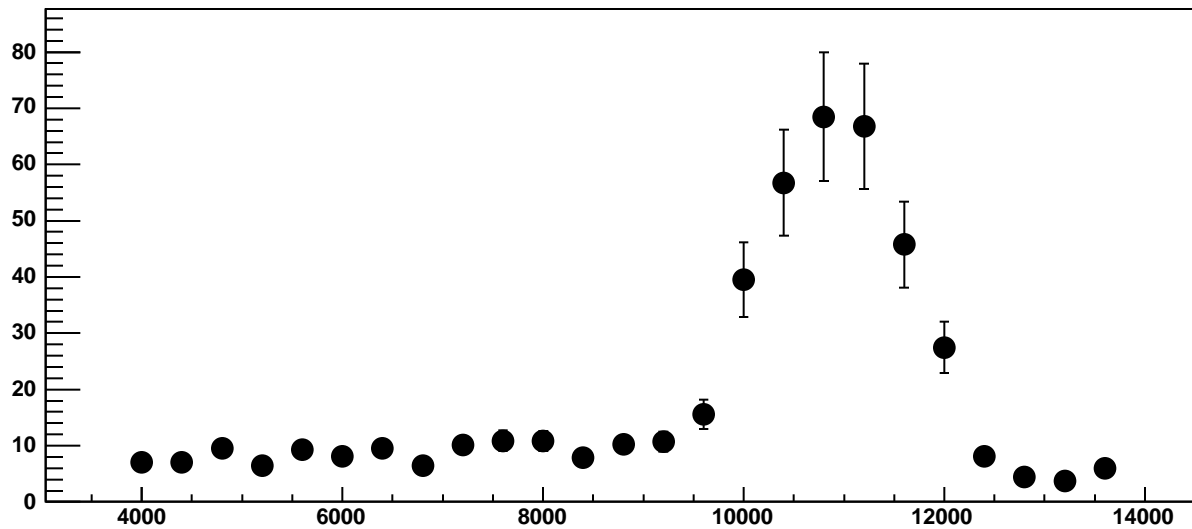
Chip 0, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



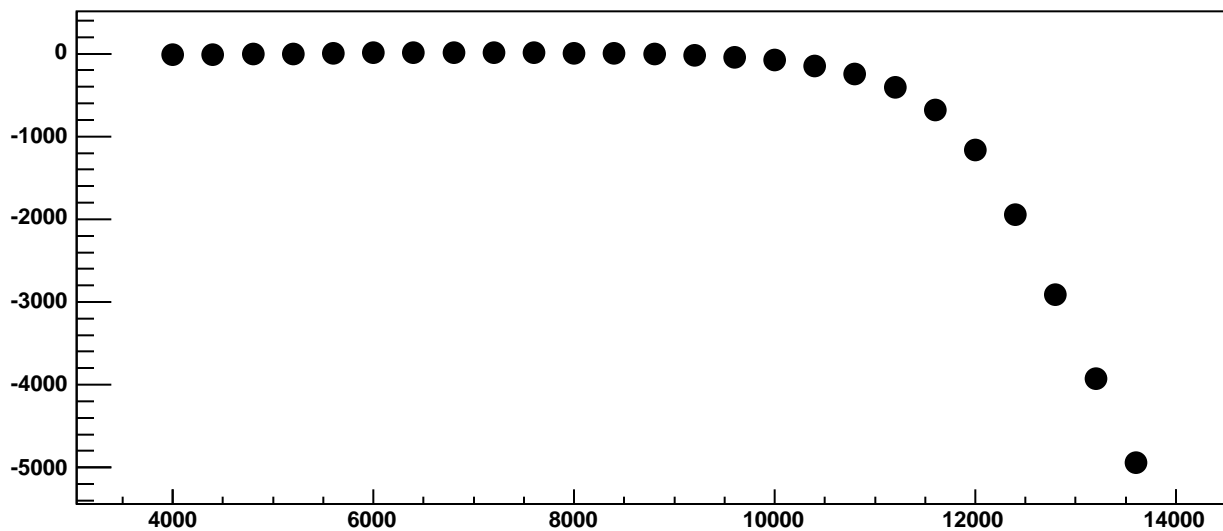
Chip 0, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC



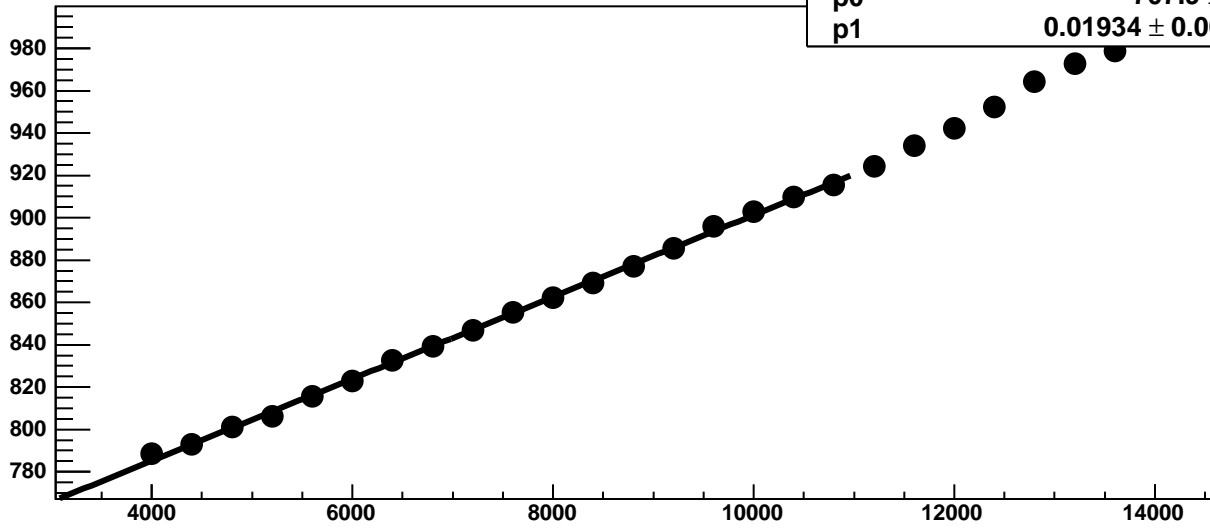
Chip 0, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

10.55 / 16

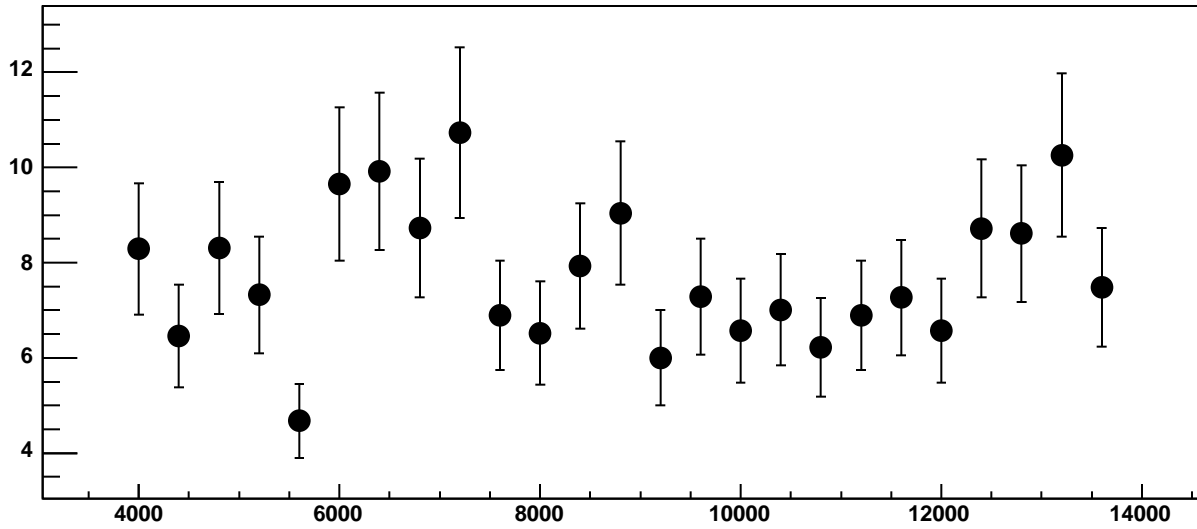
p0

707.9 ± 1.406

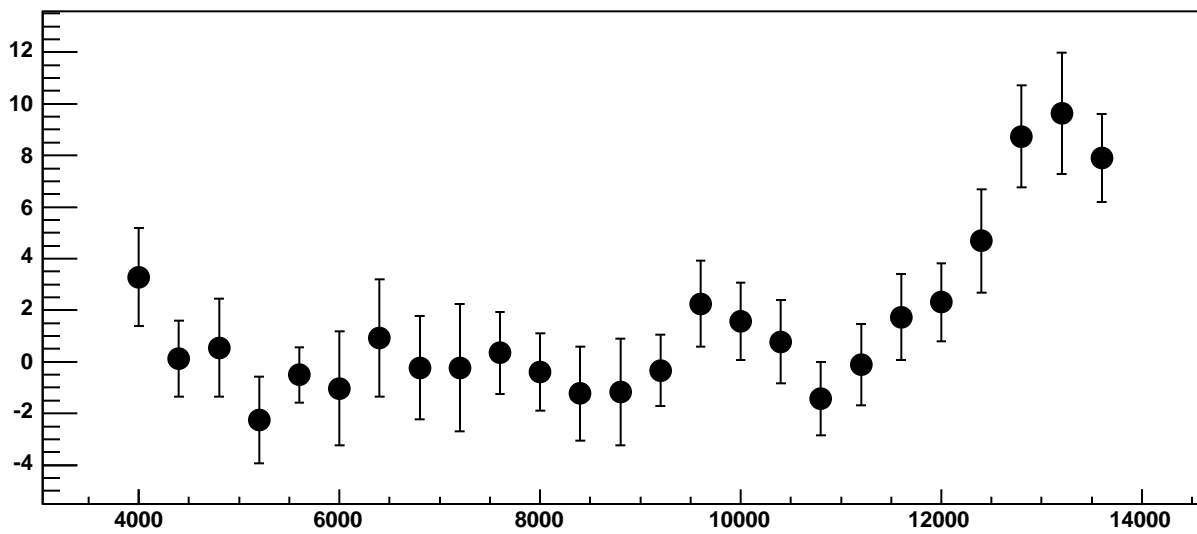
p1

0.01934 ± 0.0001802

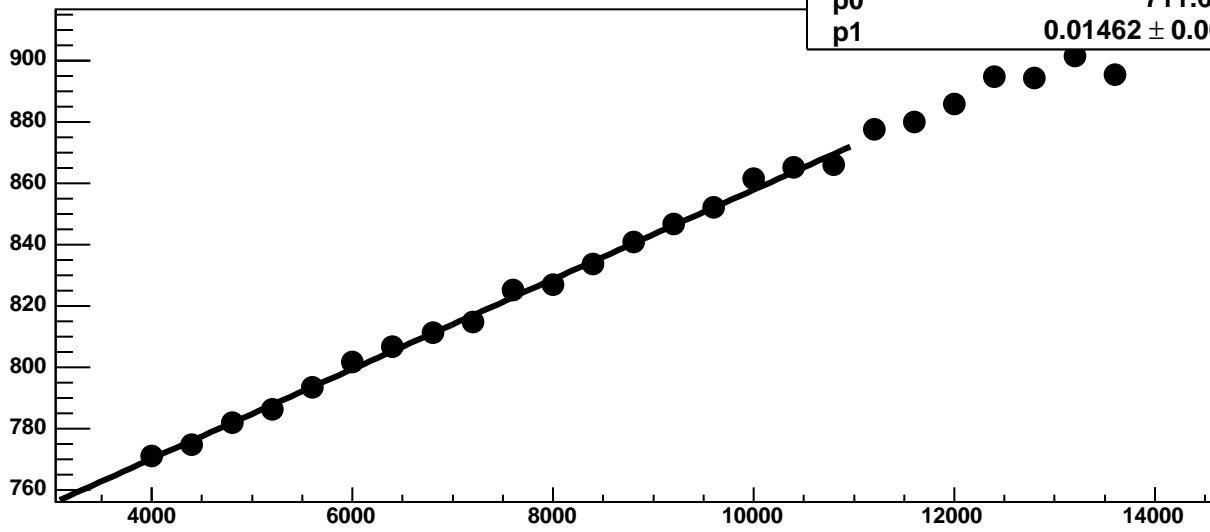
Chip 0, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

16.05 / 16

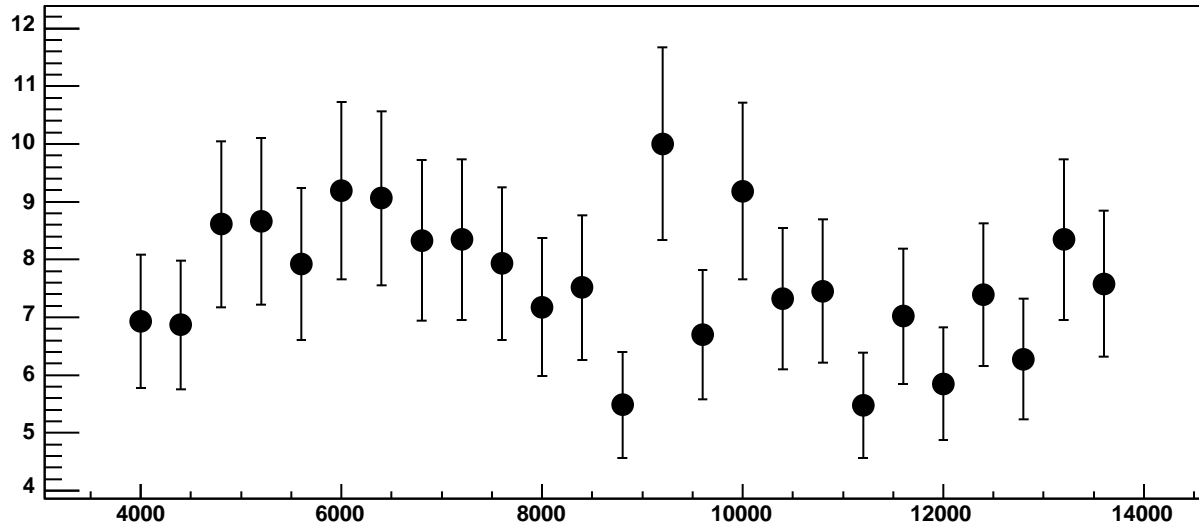
p0

711.6 ± 1.52

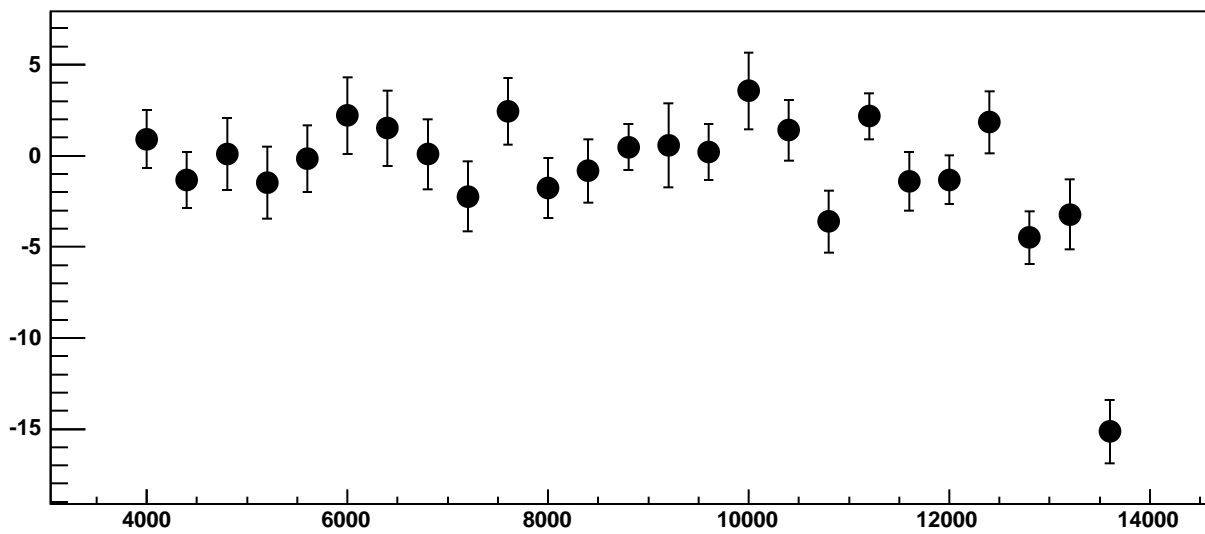
p1

0.01462 ± 0.0001957

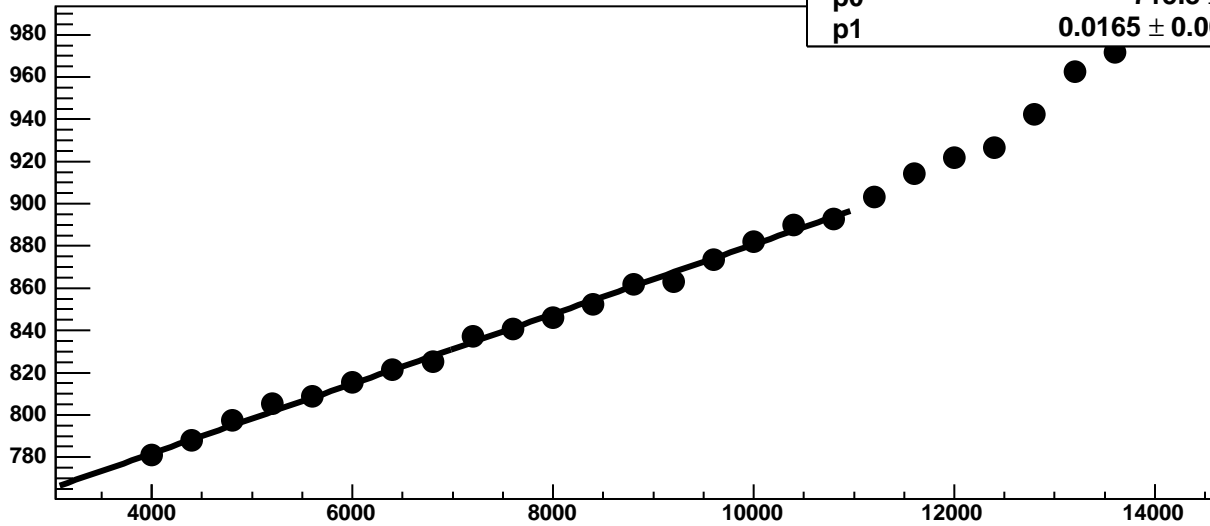
Chip 0, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

22.09 / 16

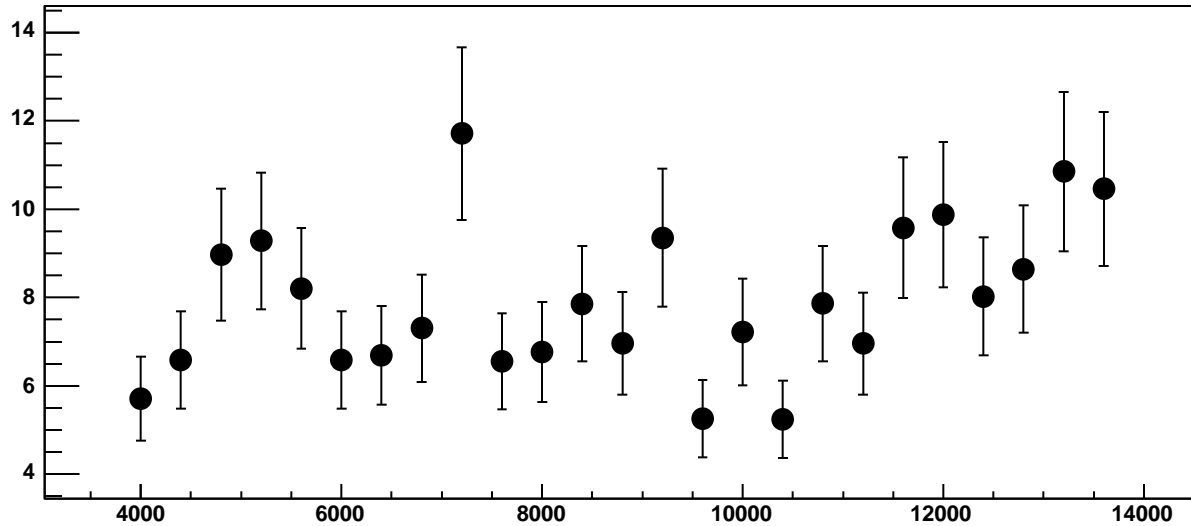
p0

715.8 ± 1.365

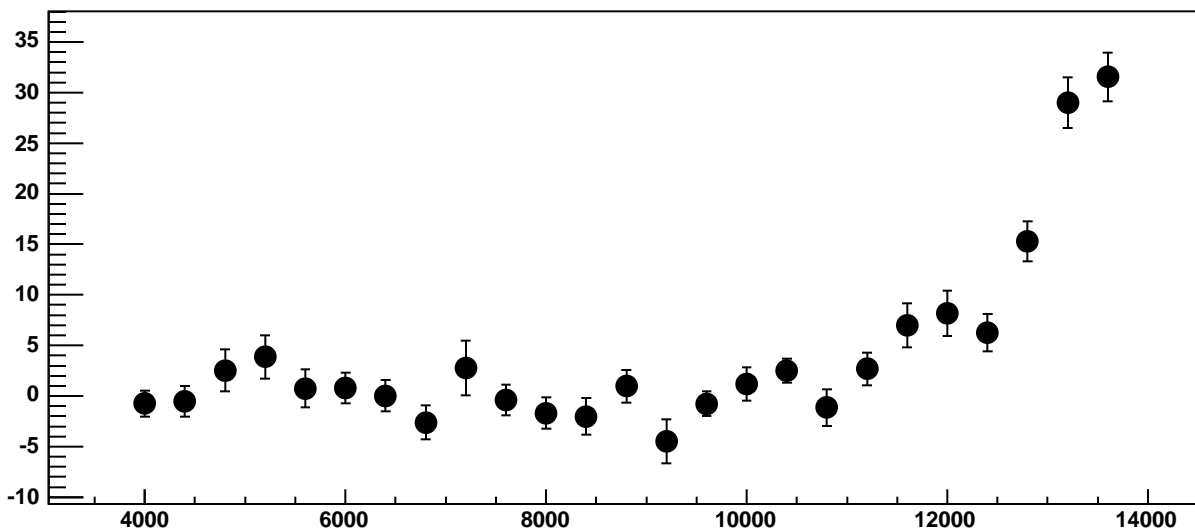
p1

0.0165 ± 0.0001739

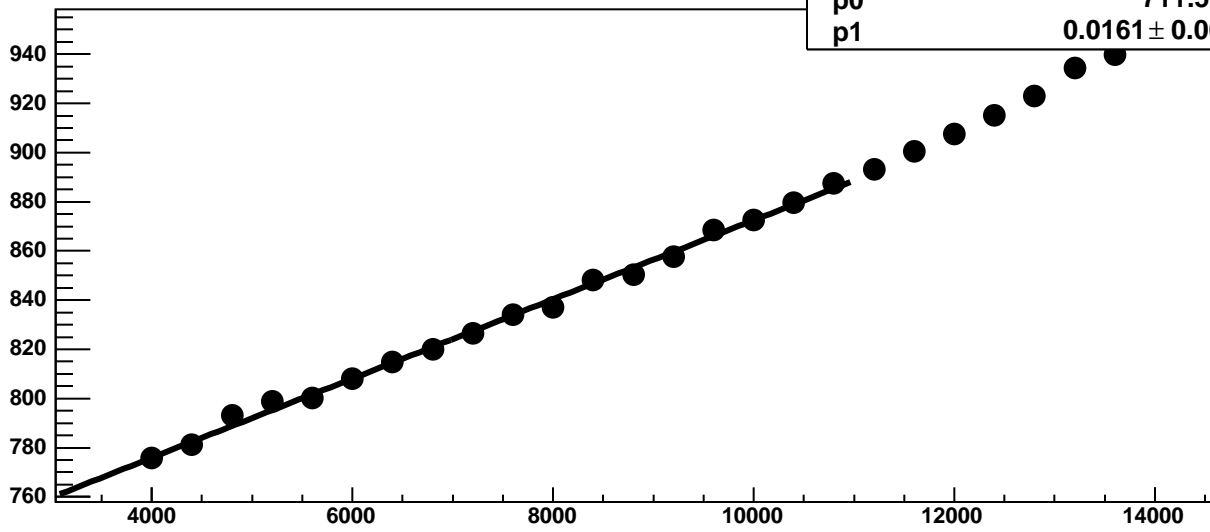
Chip 0, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

21.38 / 16

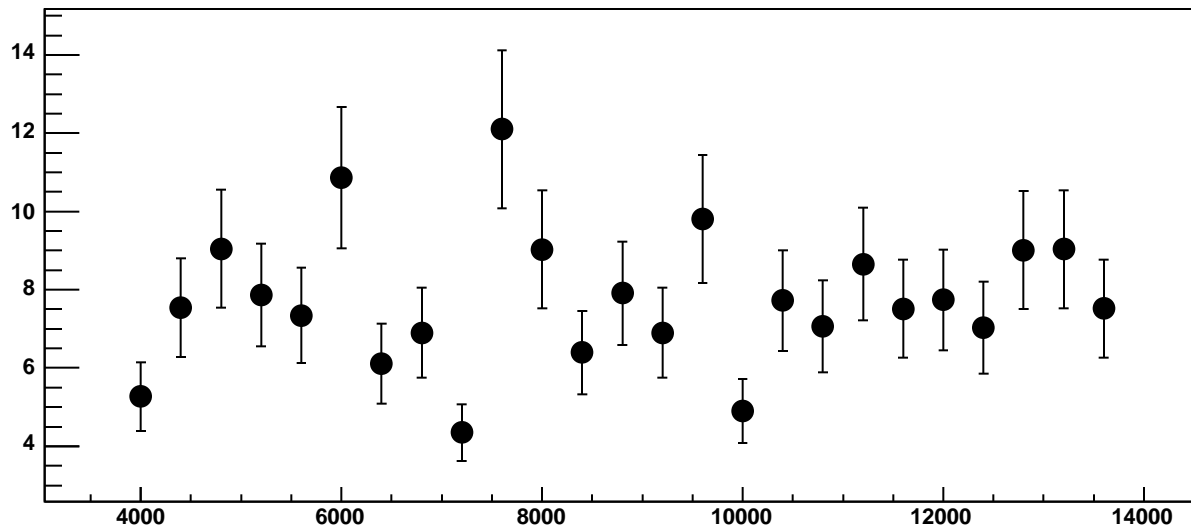
p0

711.5 ± 1.36

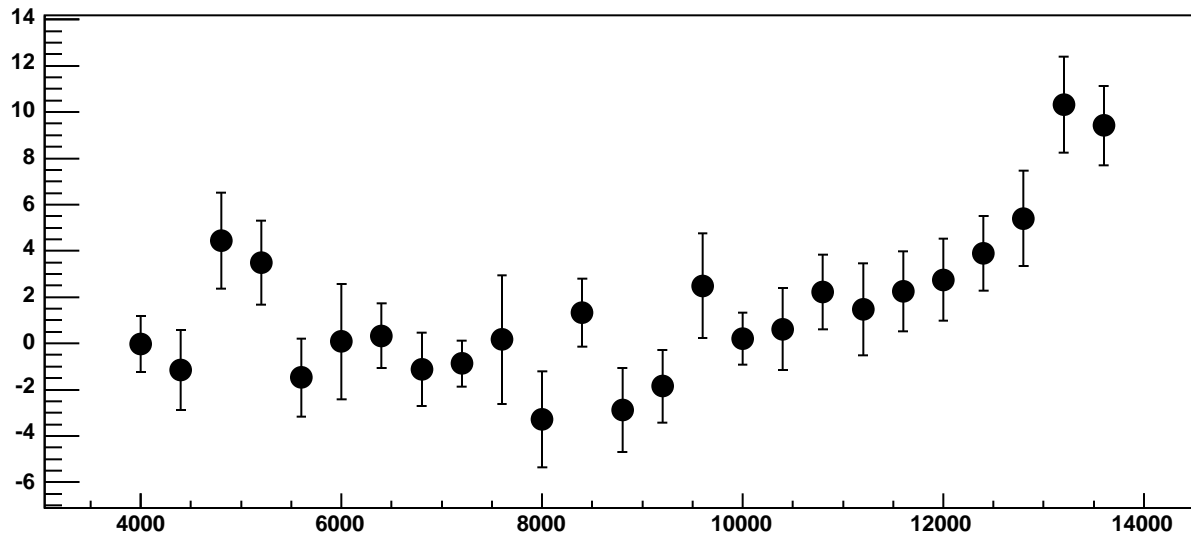
p1

0.0161 ± 0.0001765

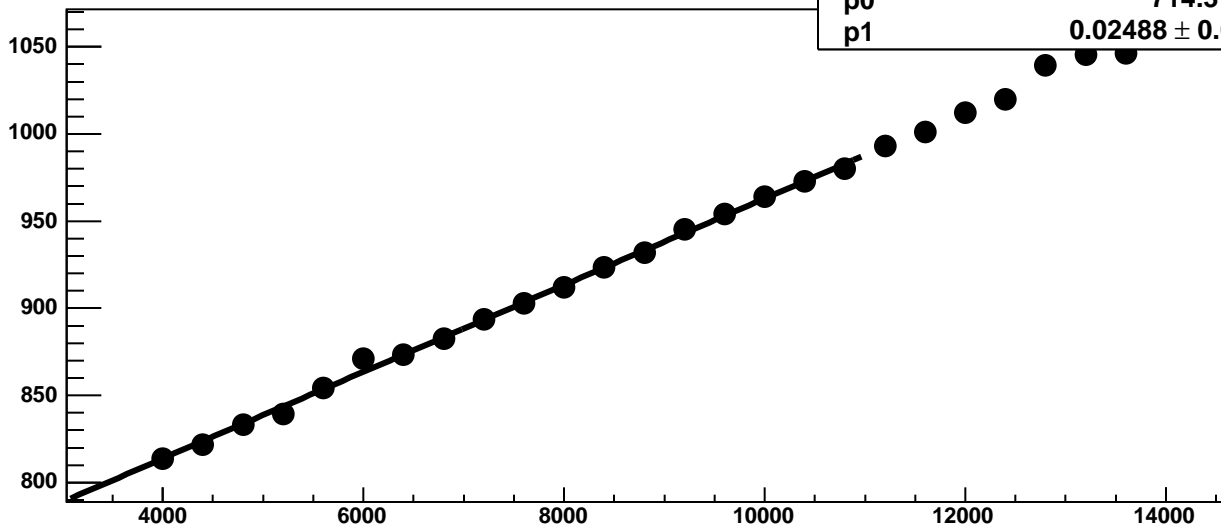
Chip 0, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

34.15 / 16

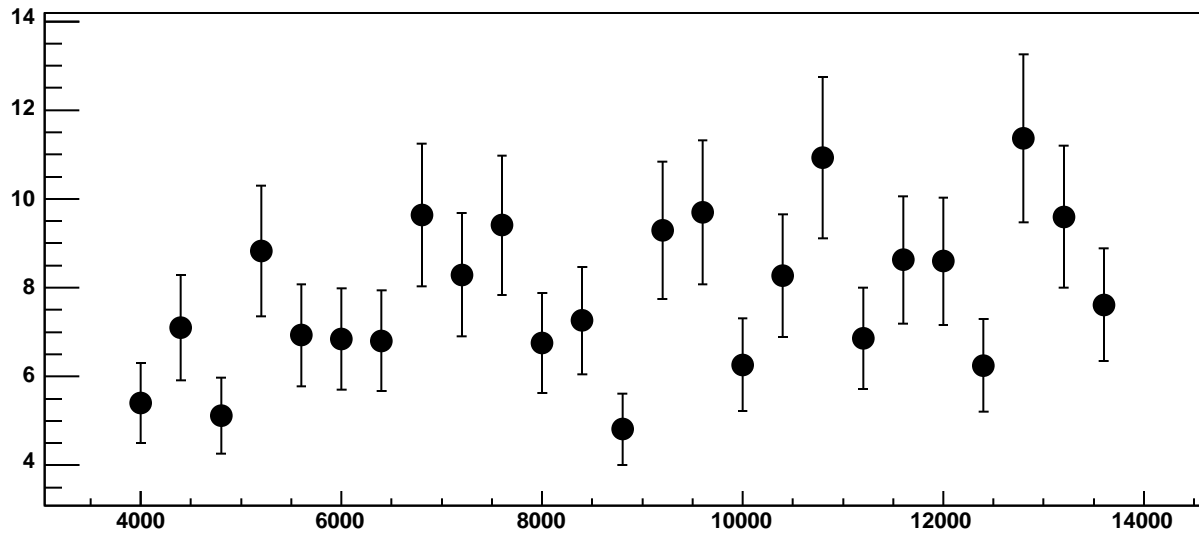
p0

714.3 ± 1.34

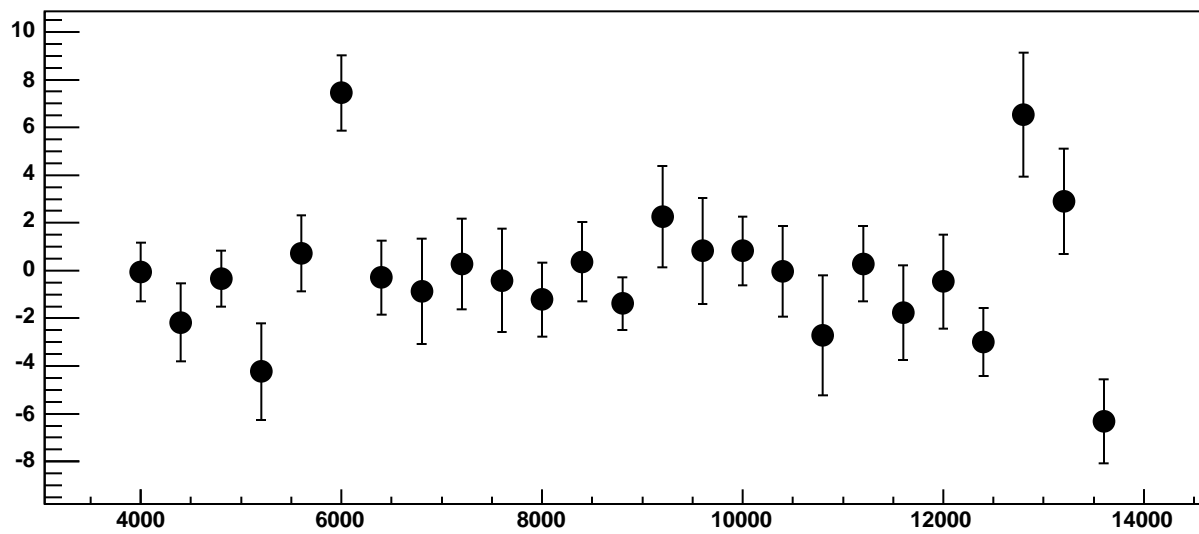
p1

0.02488 ± 0.000182

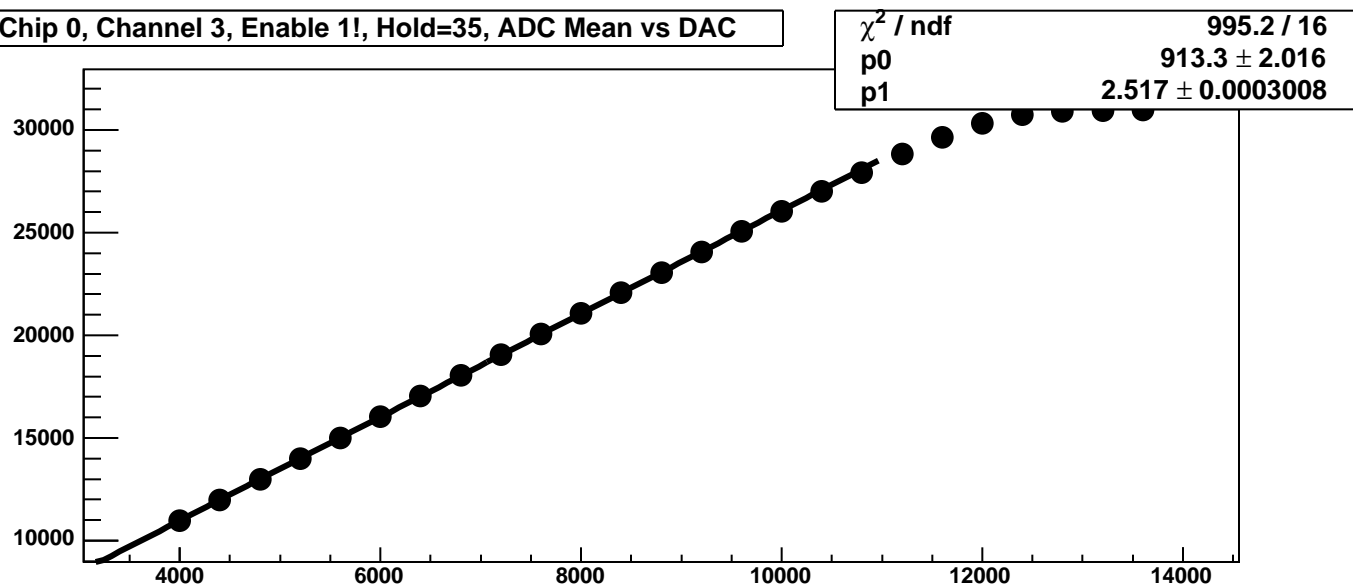
Chip 0, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



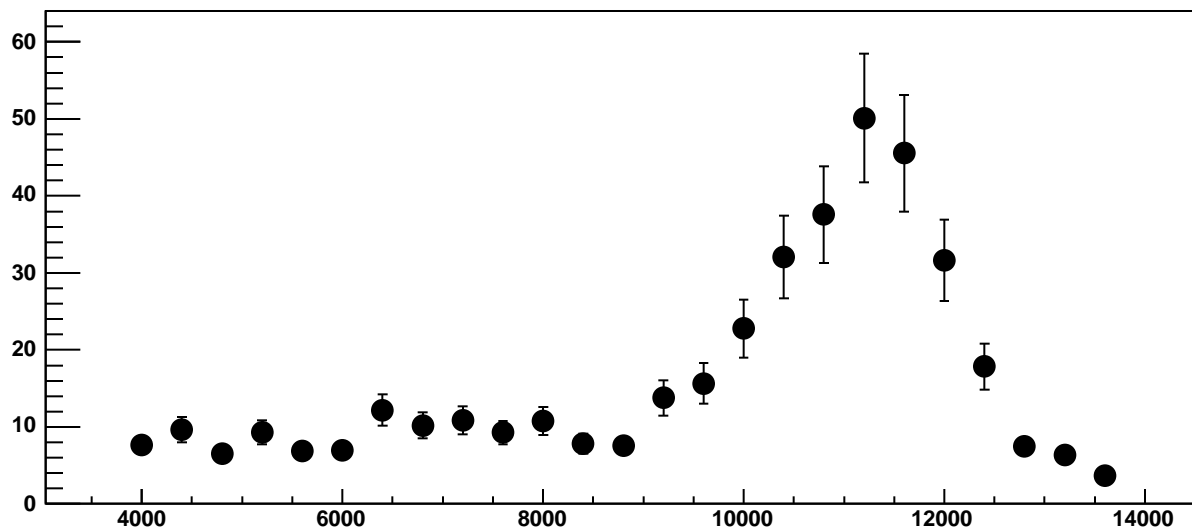
Chip 0, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



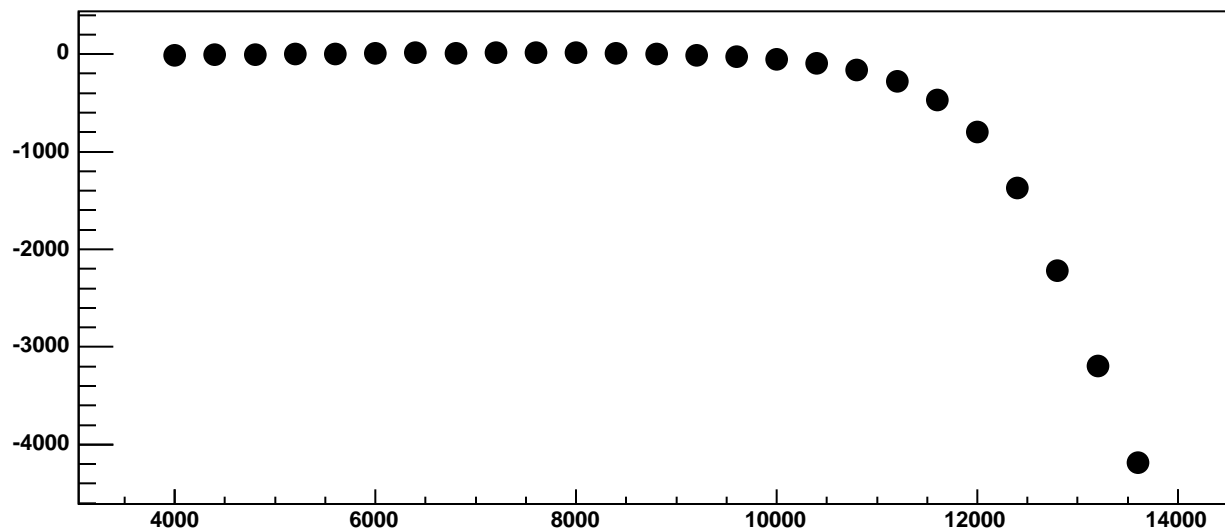
Chip 0, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC



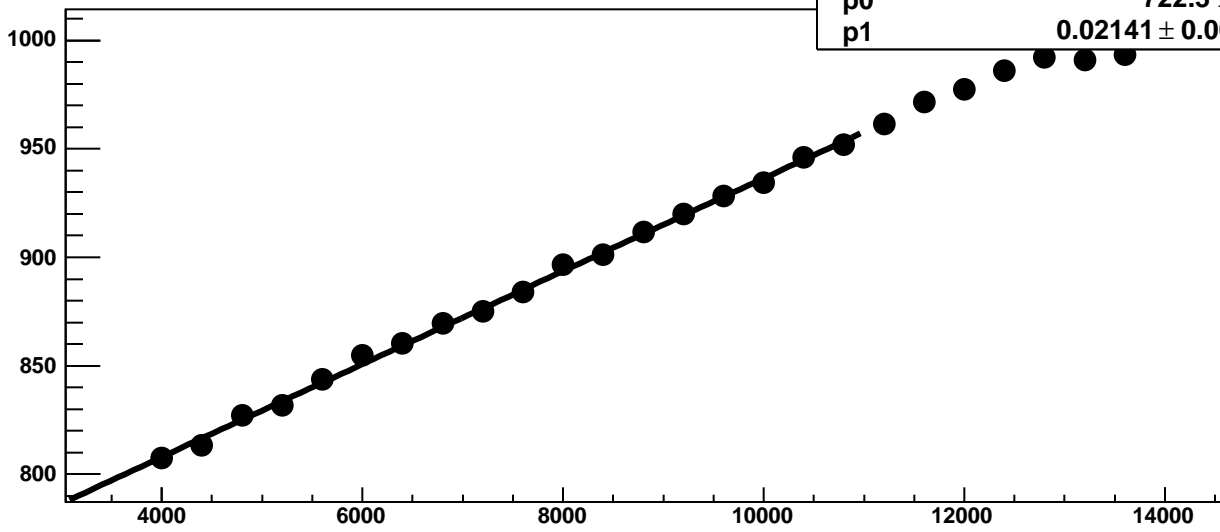
Chip 0, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC

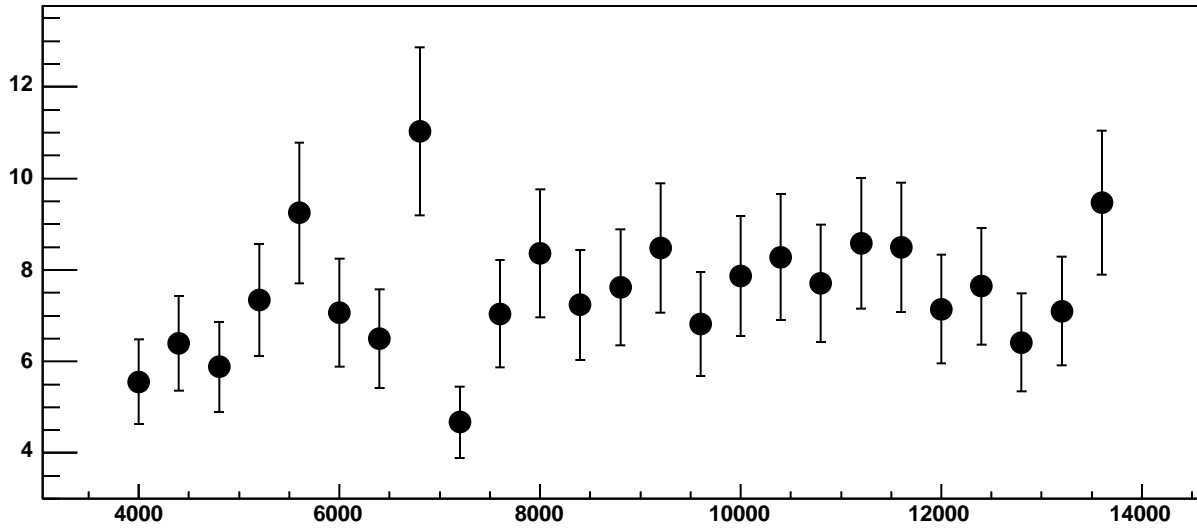


Chip 0, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

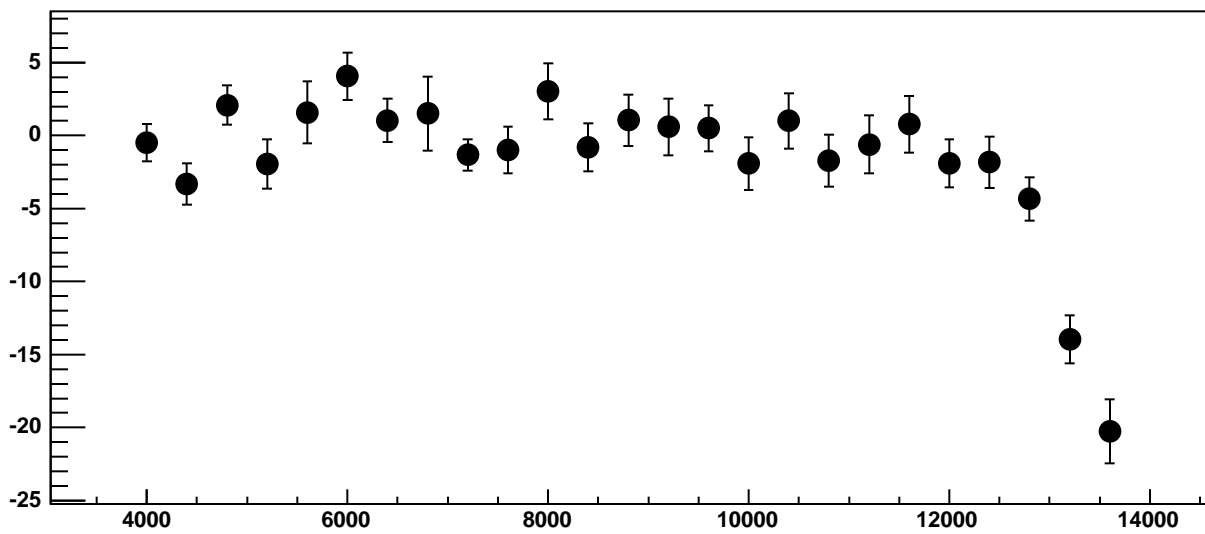


χ^2 / ndf 24.42 / 16
p0 722.3 ± 1.337
p1 0.02141 ± 0.0001813

Chip 0, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC

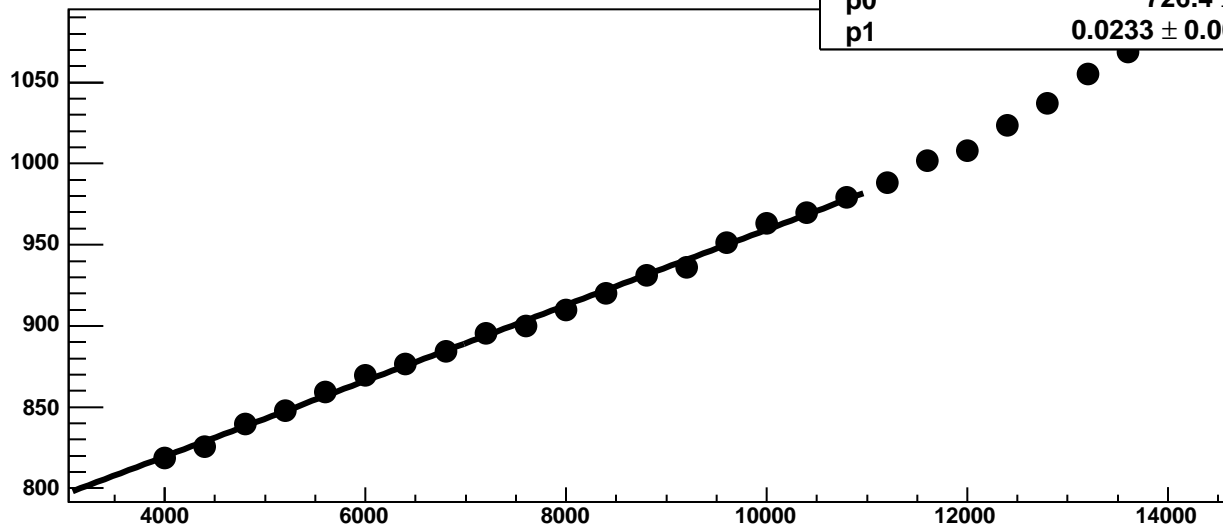


Chip 0, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

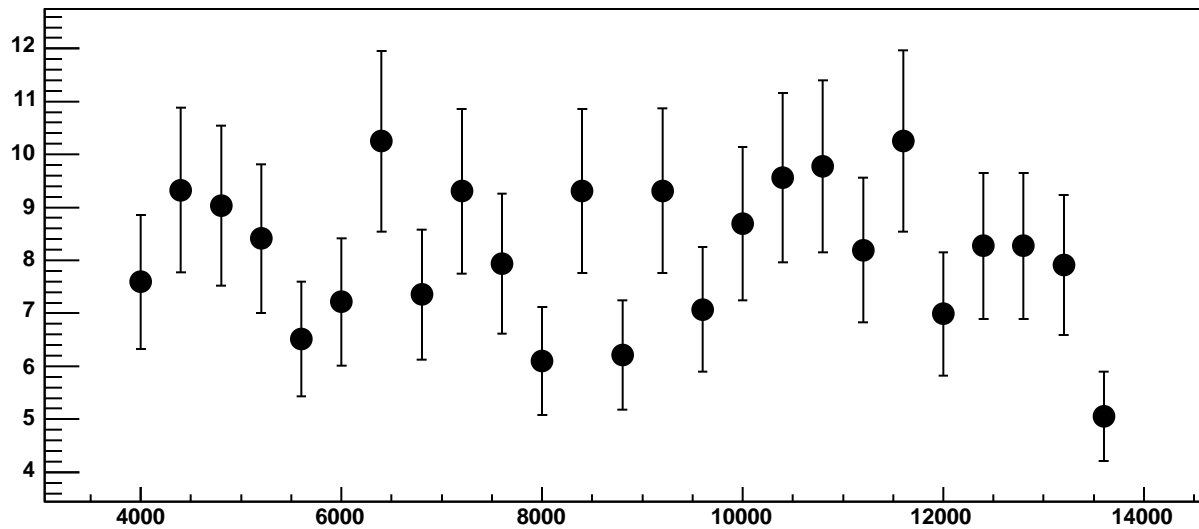


Chip 0, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

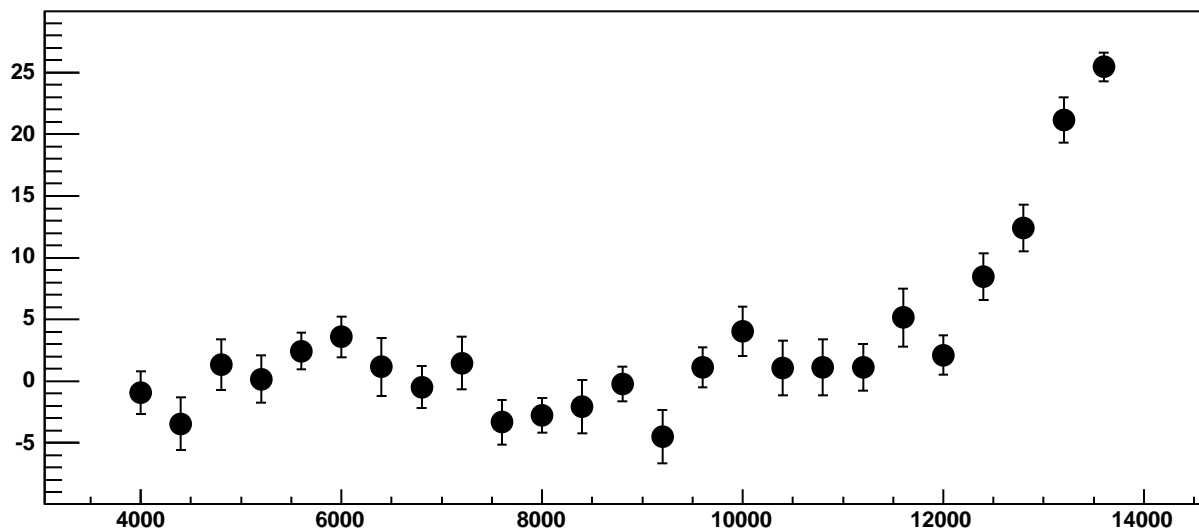
χ^2 / ndf 29.07 / 16
p0 726.4 ± 1.664
p1 0.0233 ± 0.0002187



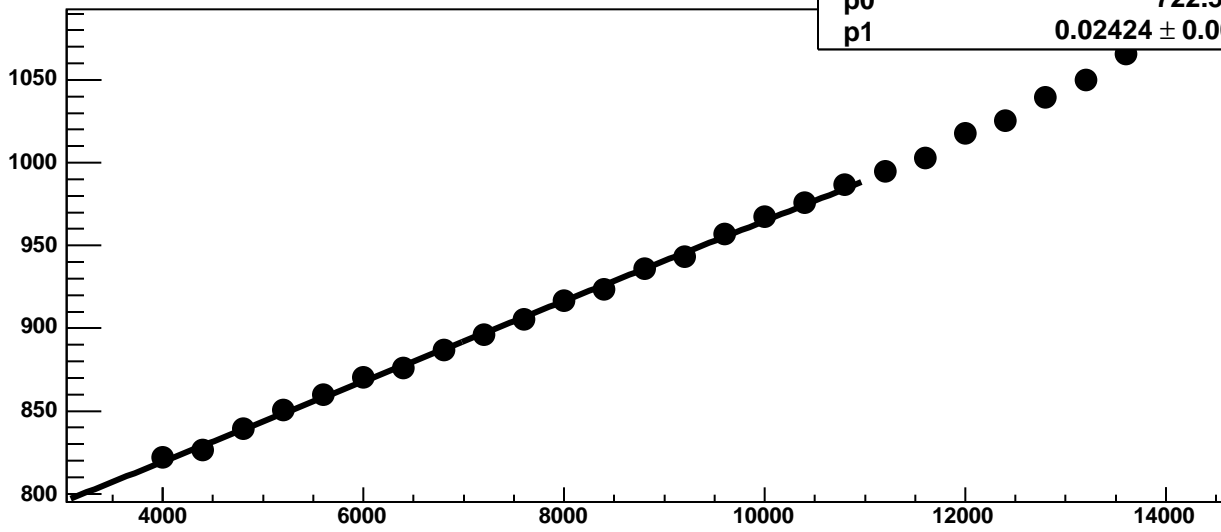
Chip 0, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

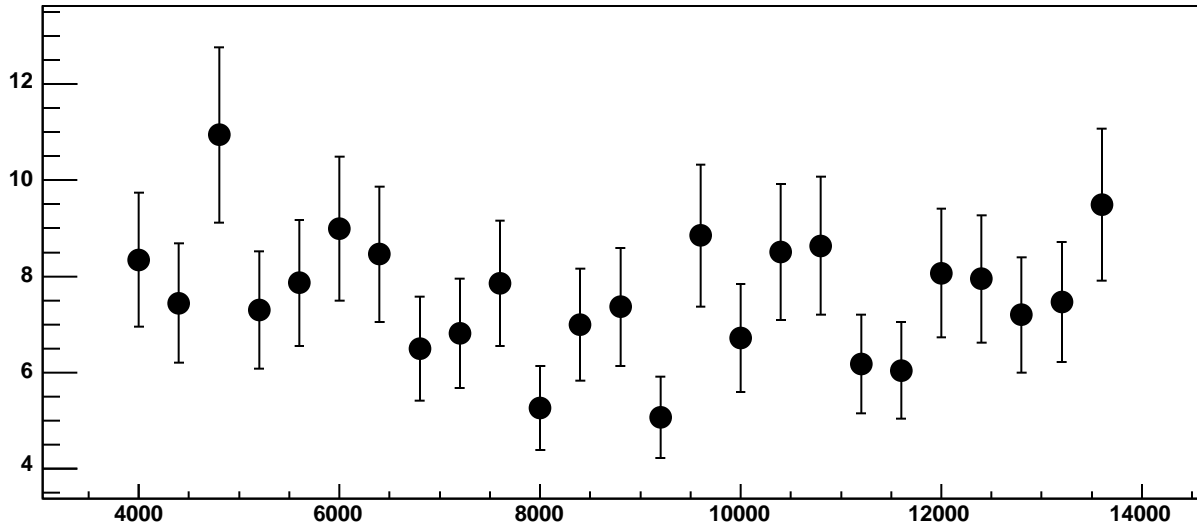


Chip 0, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

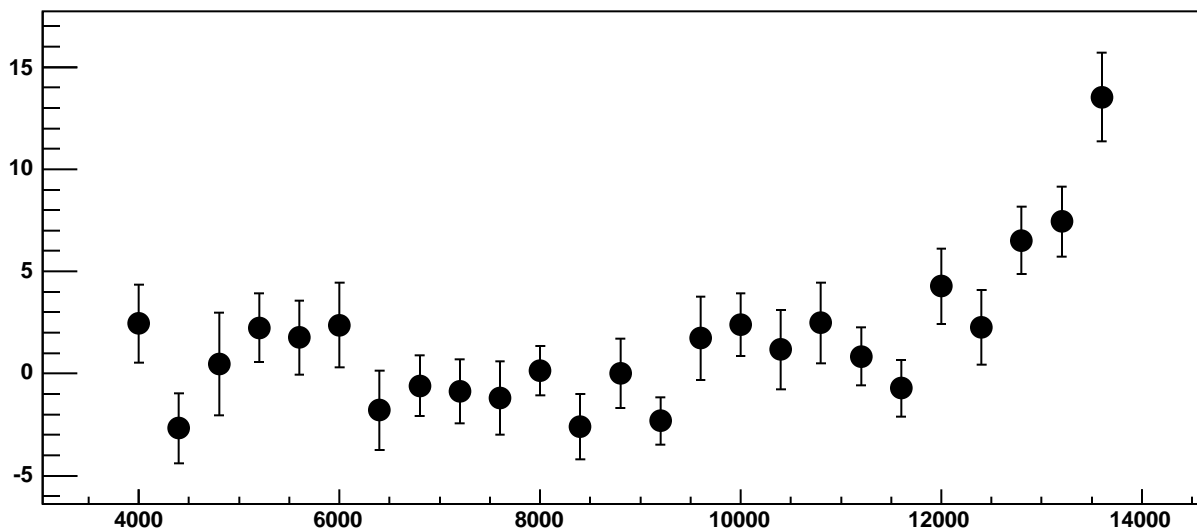


χ^2 / ndf 21.64 / 16
p0 722.5 \pm 1.61
p1 0.02424 \pm 0.0002053

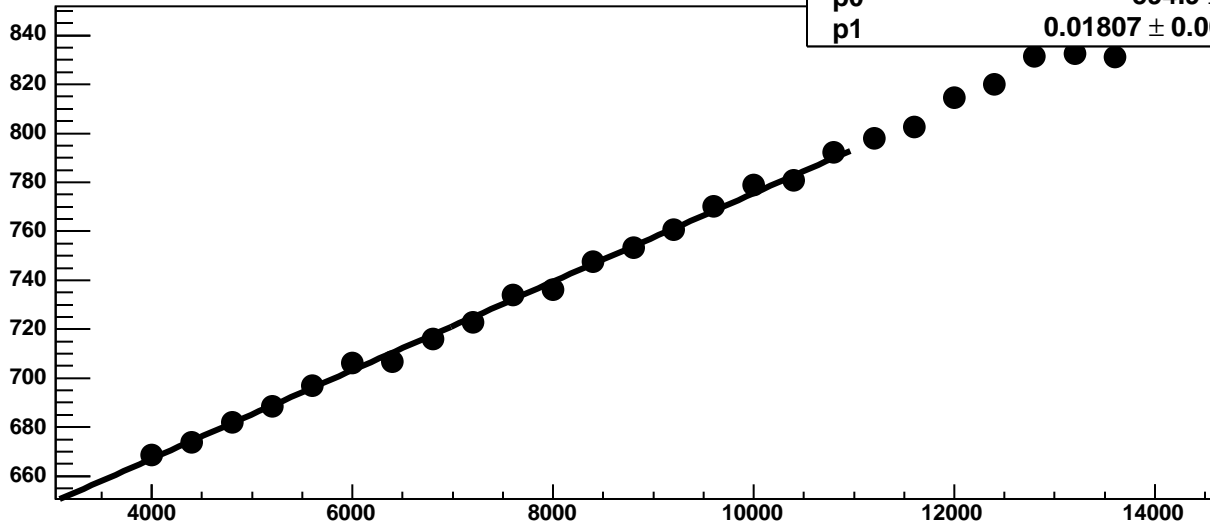
Chip 0, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC

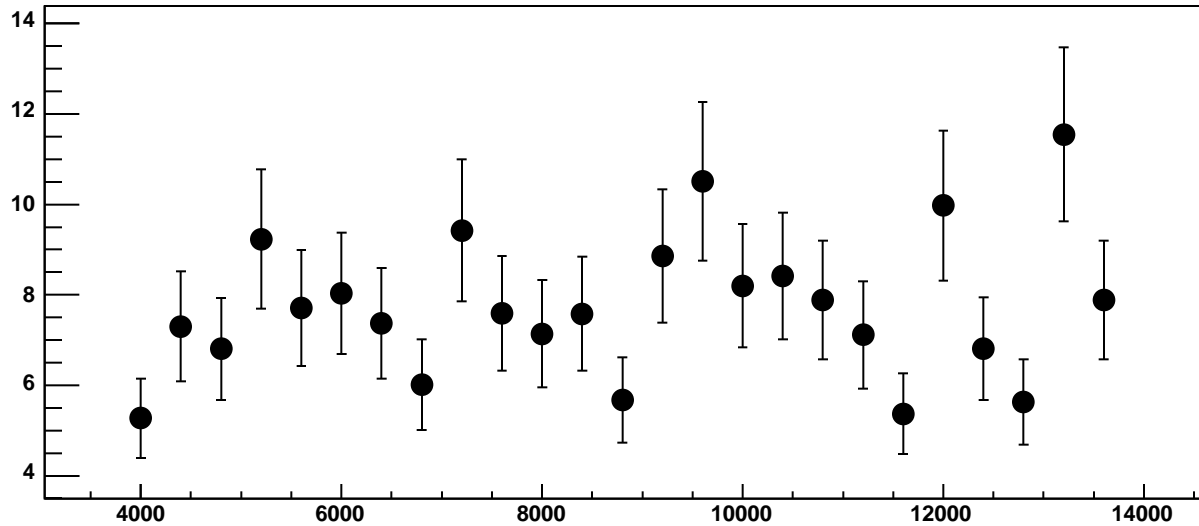


Chip 0, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

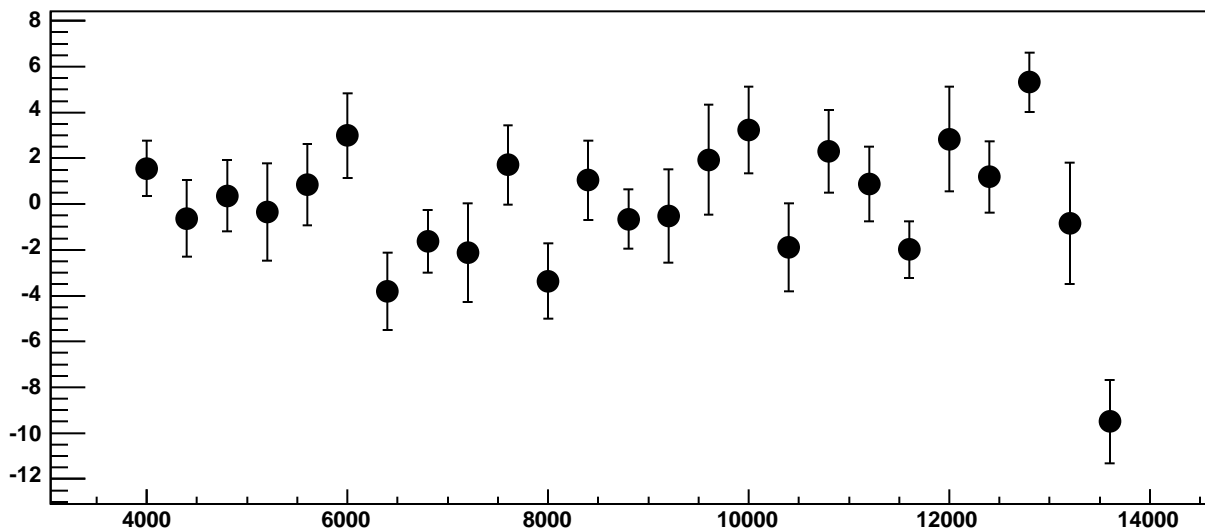


χ^2 / ndf 24.2 / 16
p0 594.9 ± 1.409
p1 0.01807 ± 0.0001899

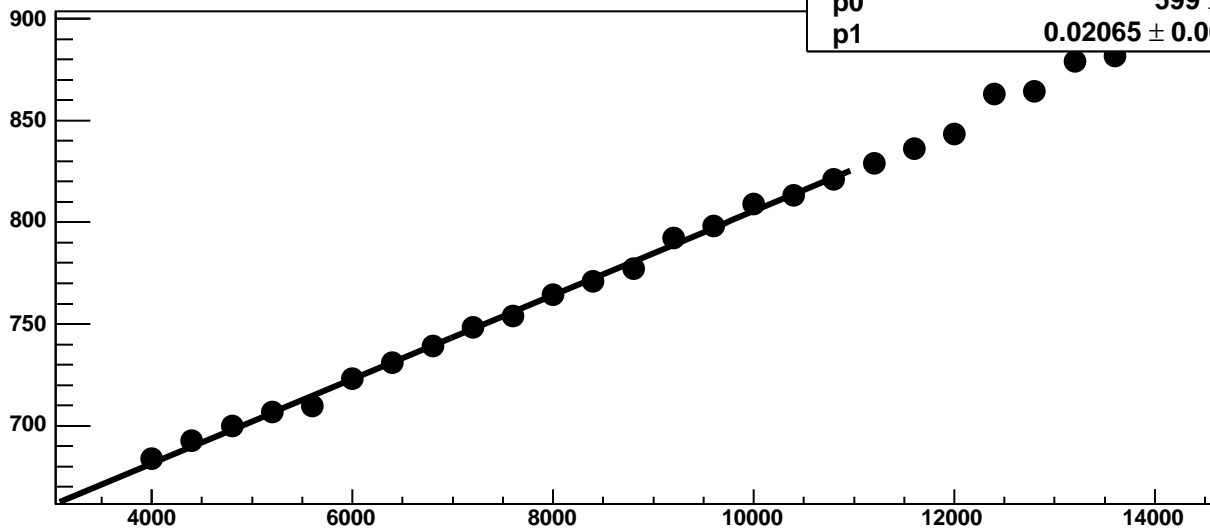
Chip 0, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

24.86 / 16

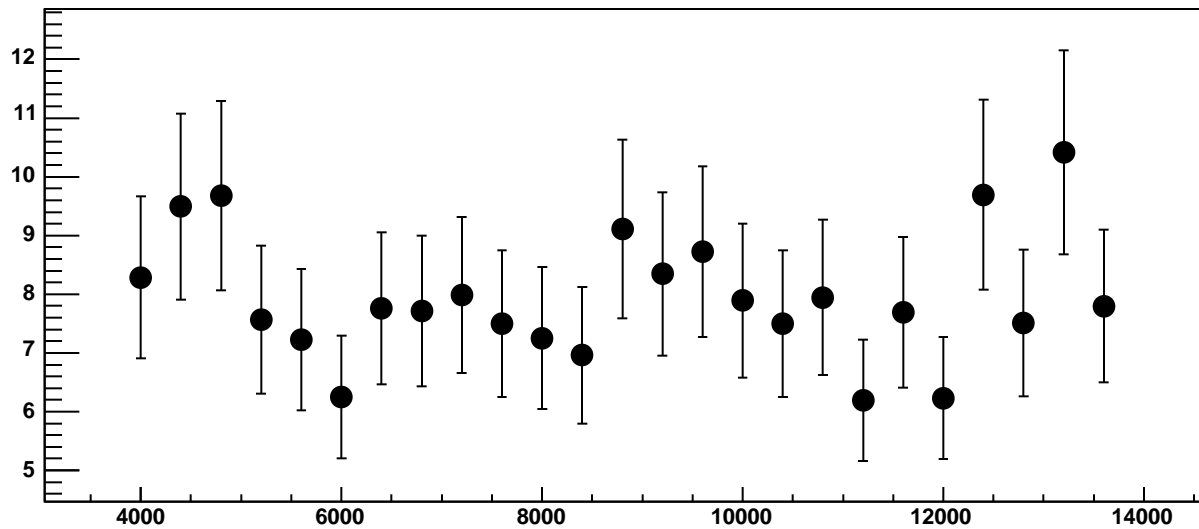
p0

599 ± 1.628

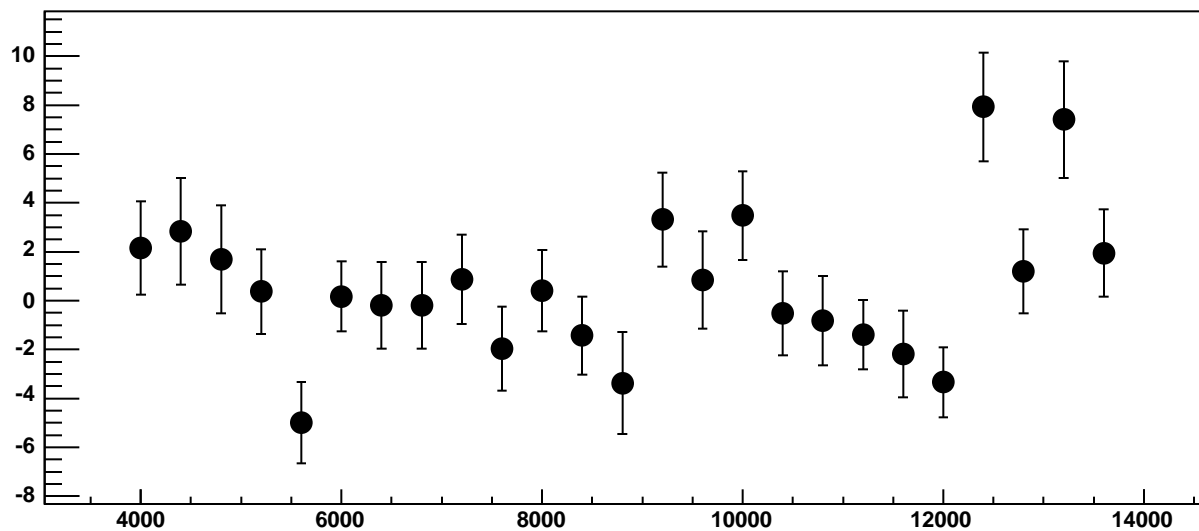
p1

0.02065 ± 0.0002117

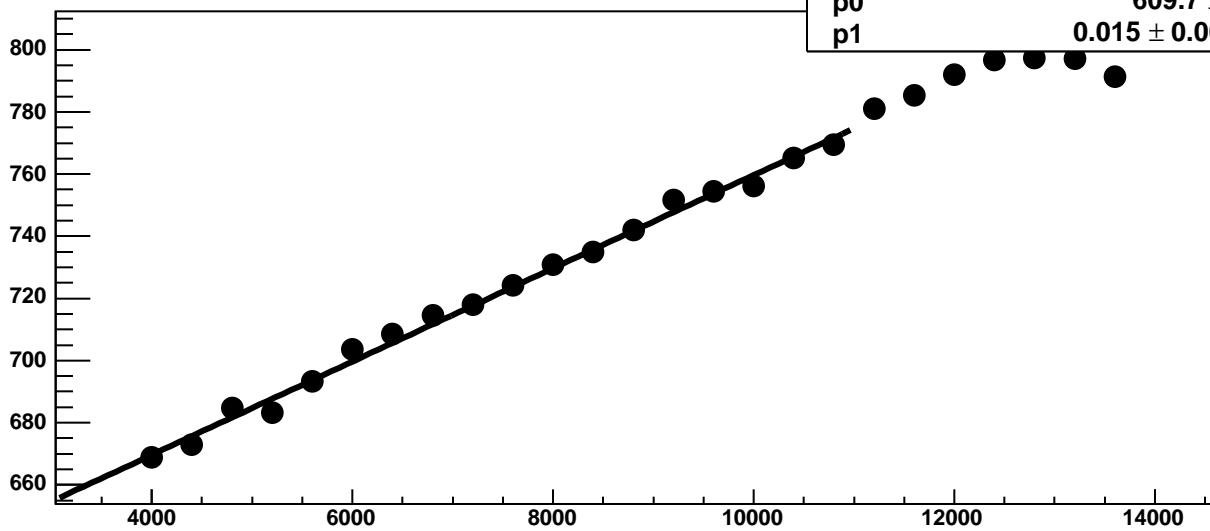
Chip 0, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



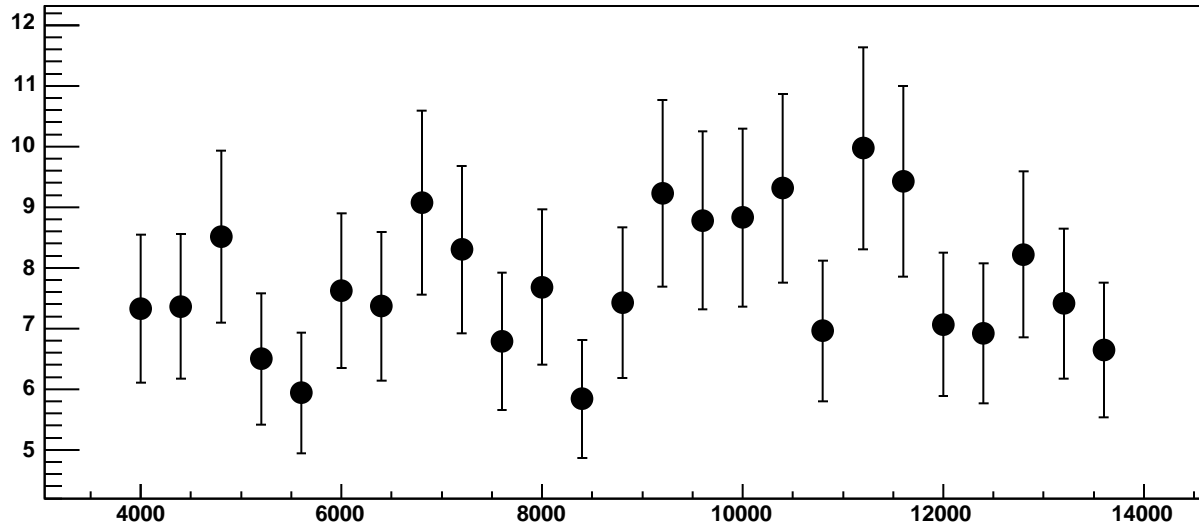
Chip 0, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC



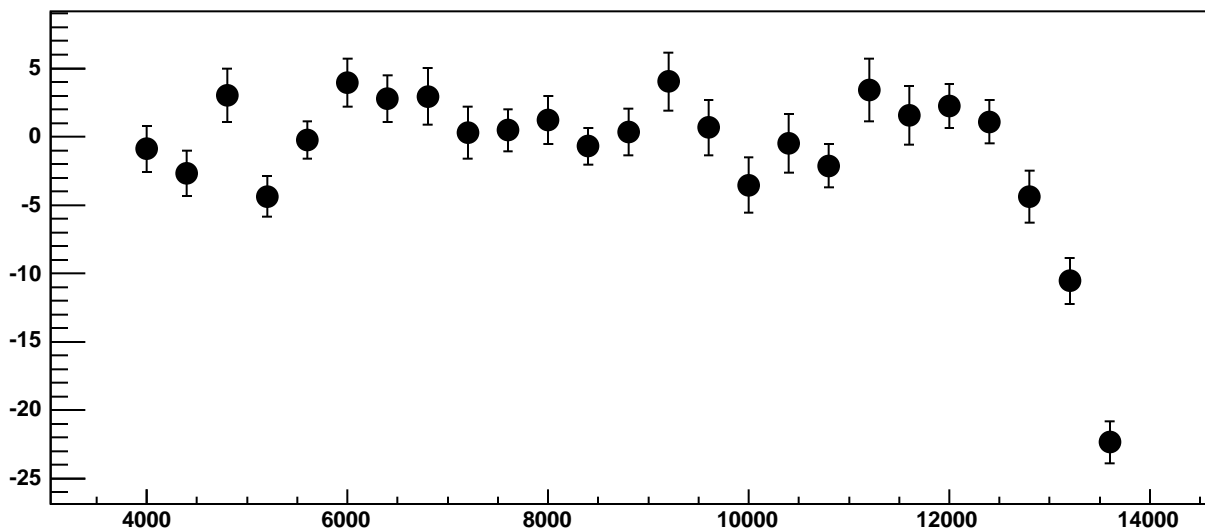
Chip 0, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



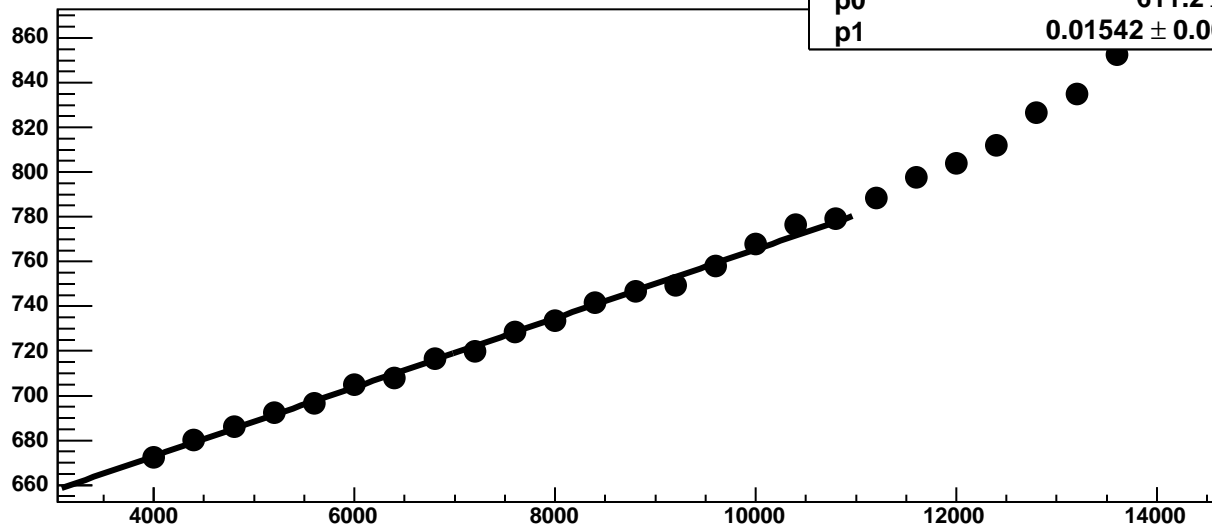
Chip 0, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

18.61 / 16

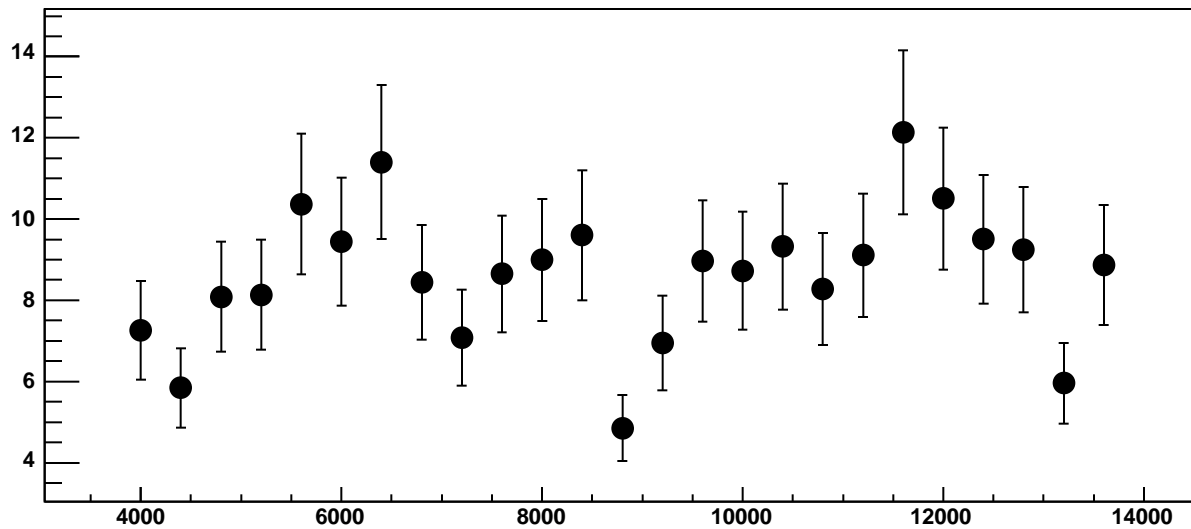
p0

611.2 ± 1.532

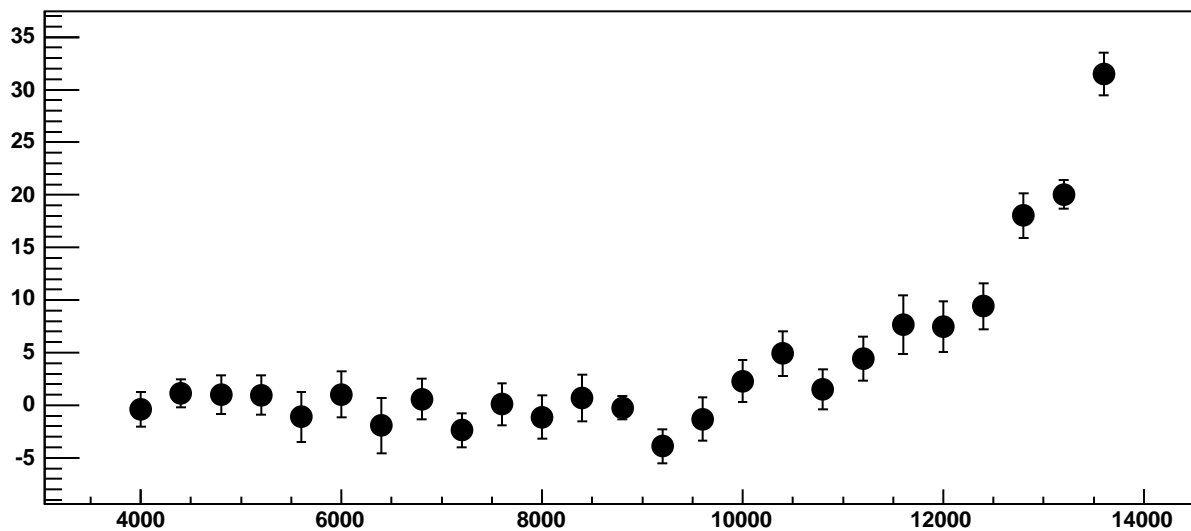
p1

0.01542 ± 0.0002003

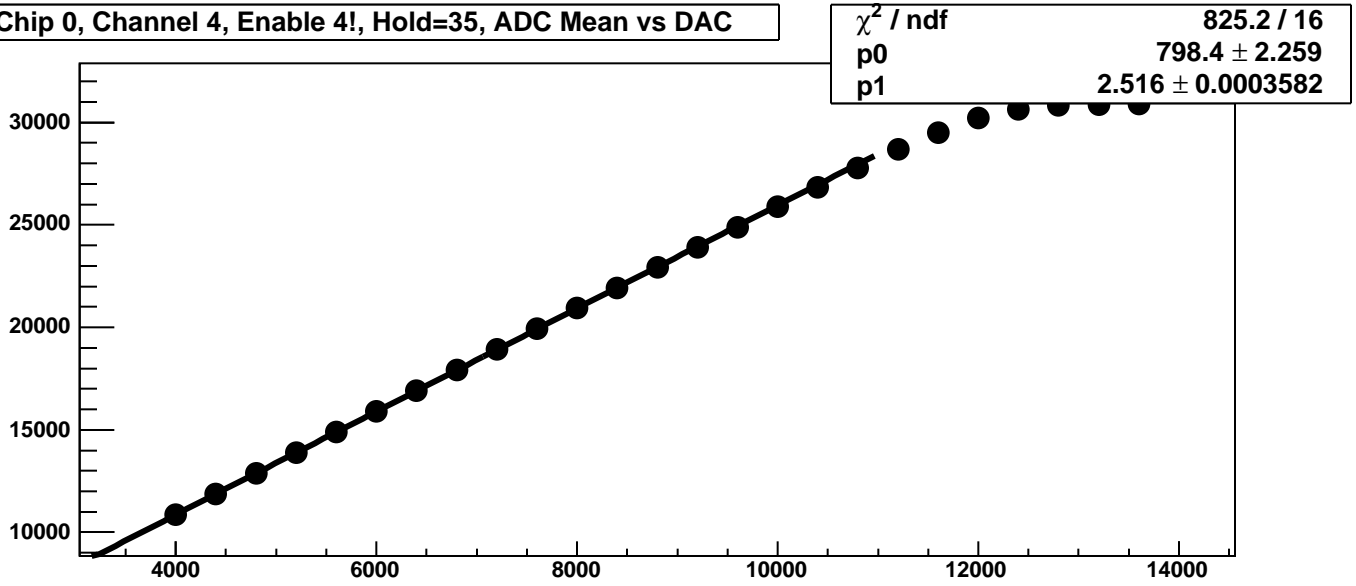
Chip 0, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



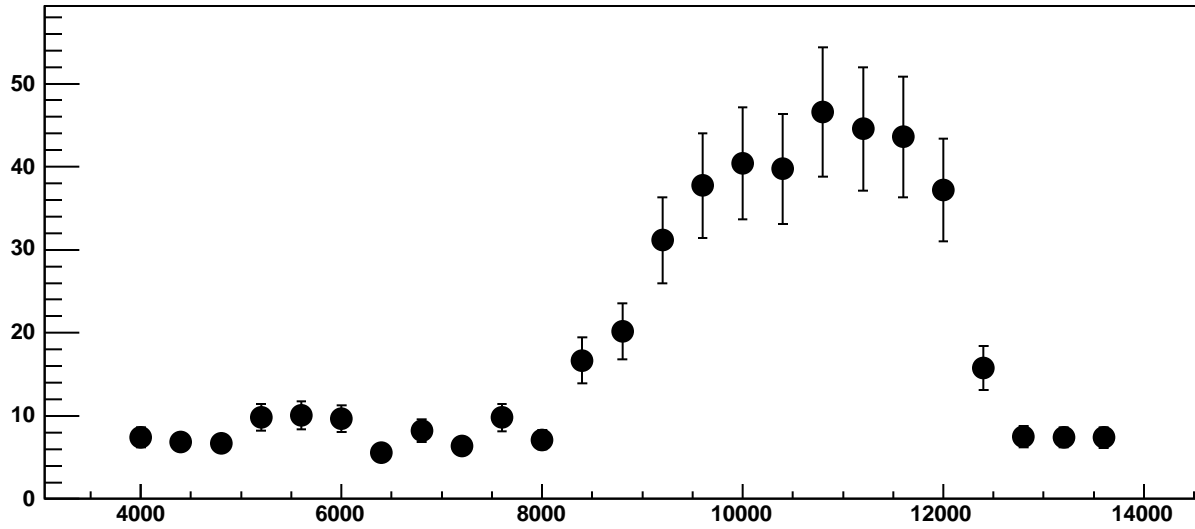
Chip 0, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



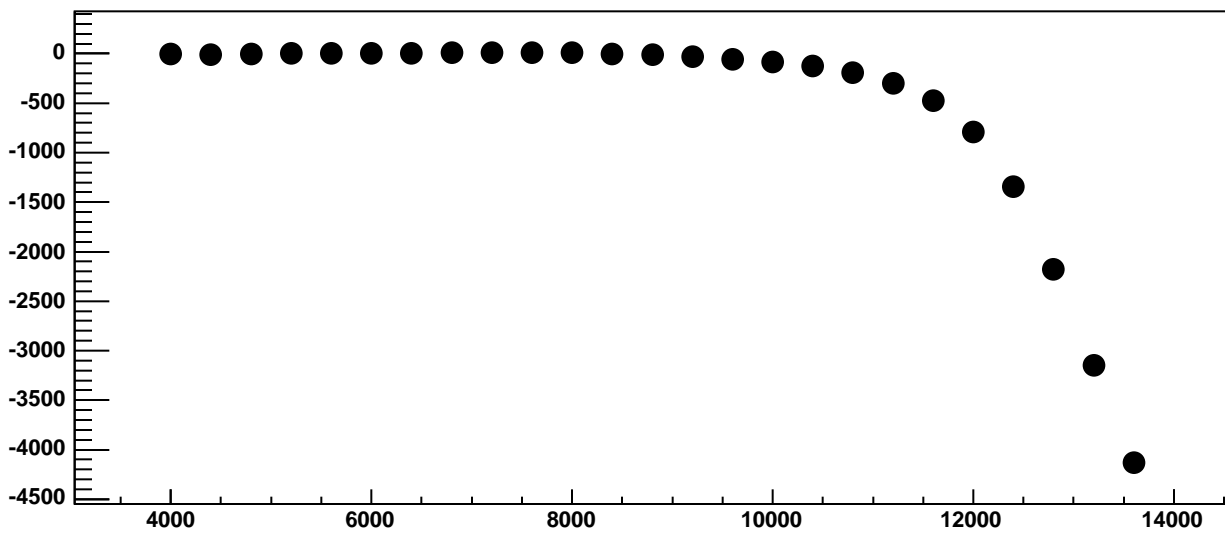
Chip 0, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC



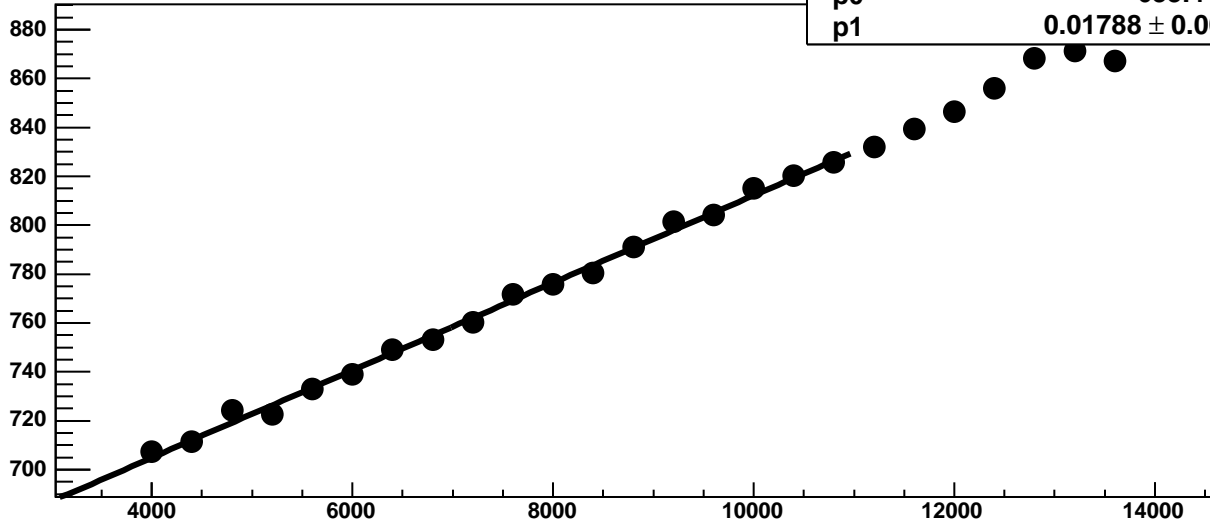
Chip 0, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

28.05 / 16

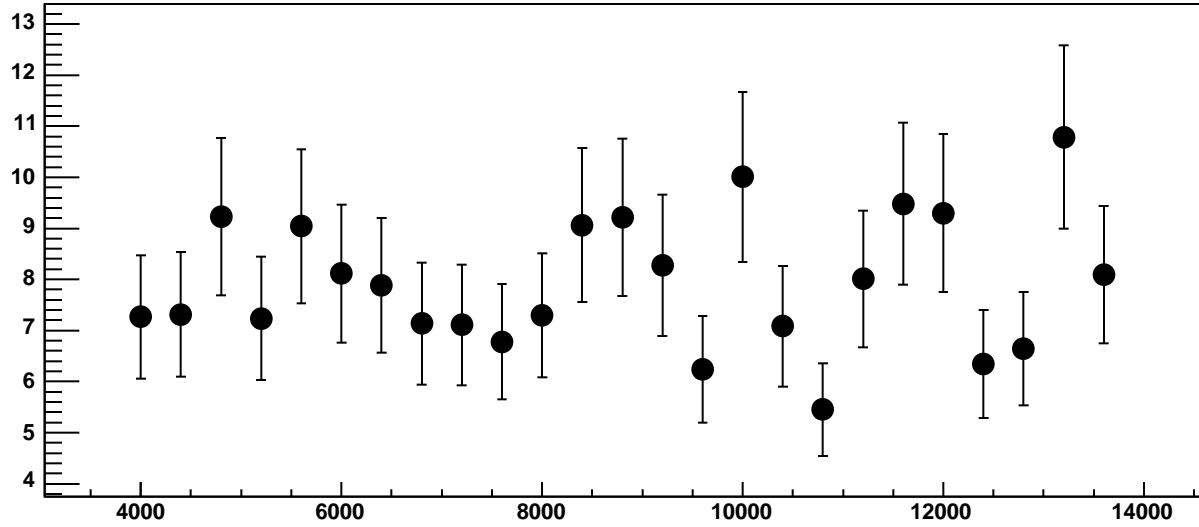
p0

633.4 ± 1.481

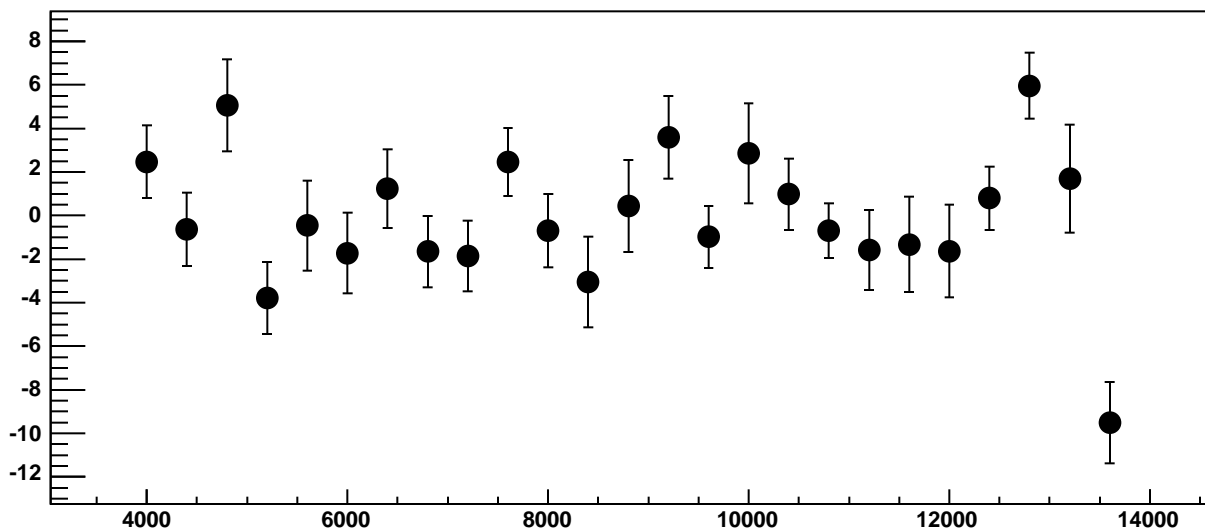
p1

0.01788 ± 0.0001879

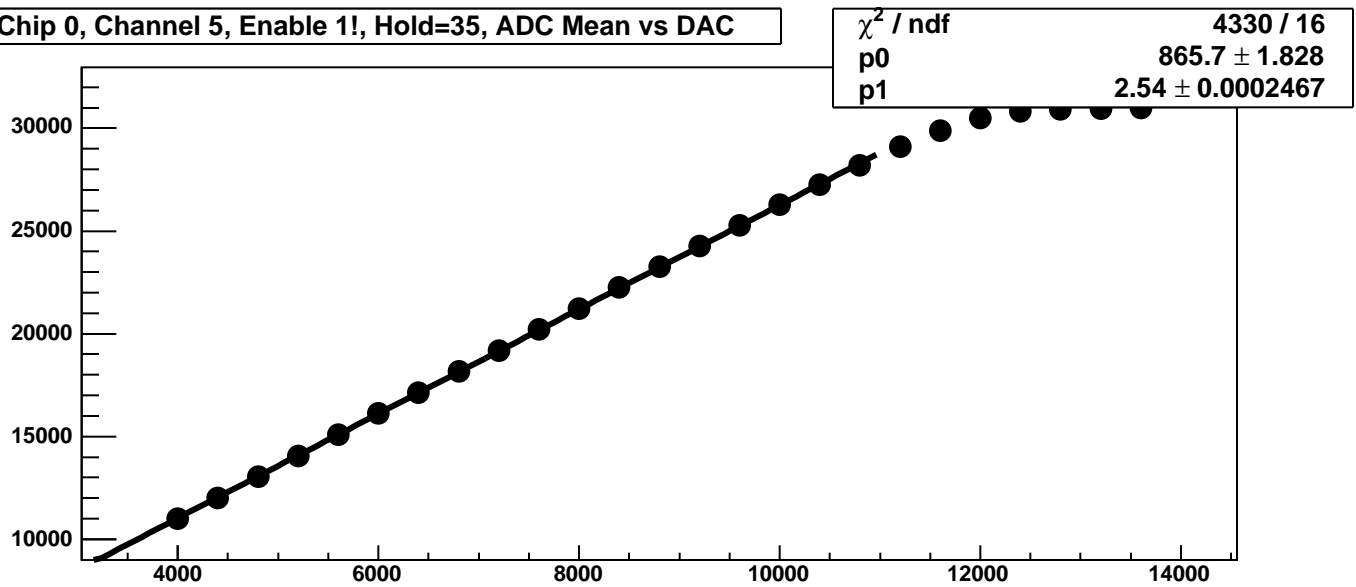
Chip 0, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



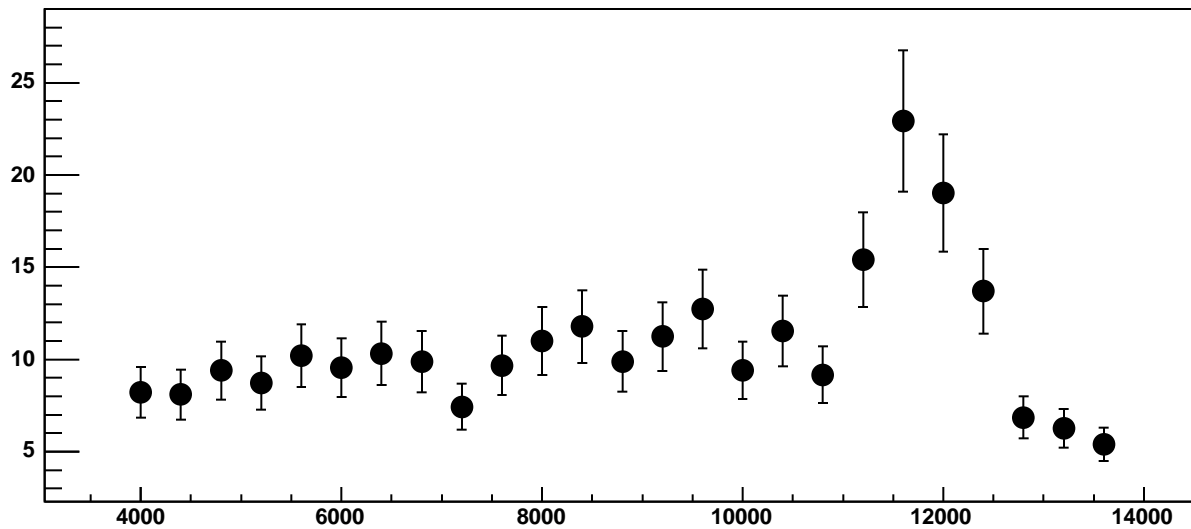
Chip 0, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



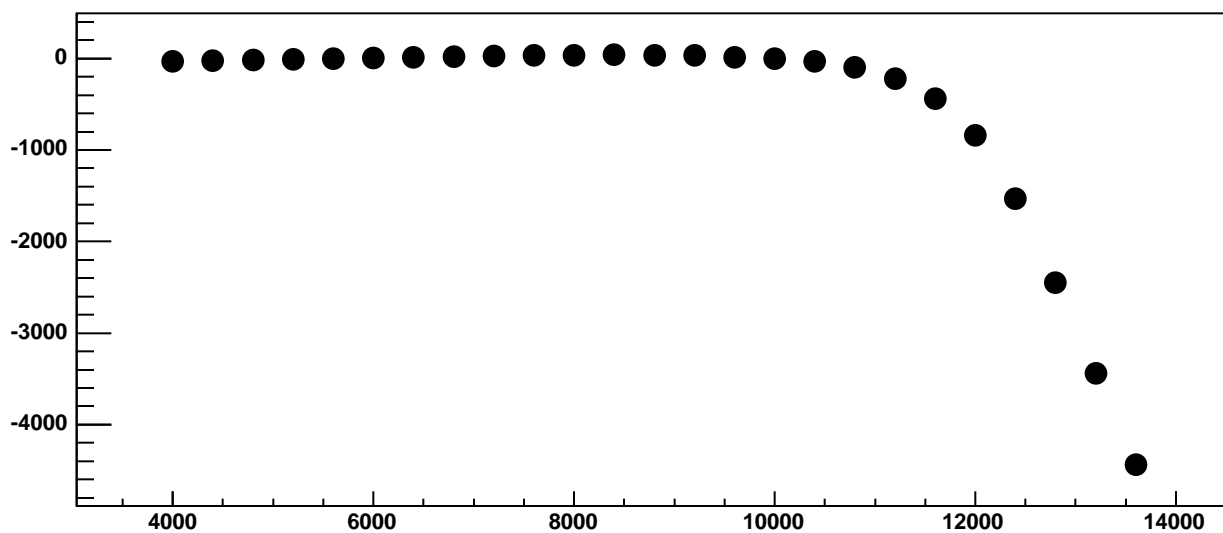
Chip 0, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC



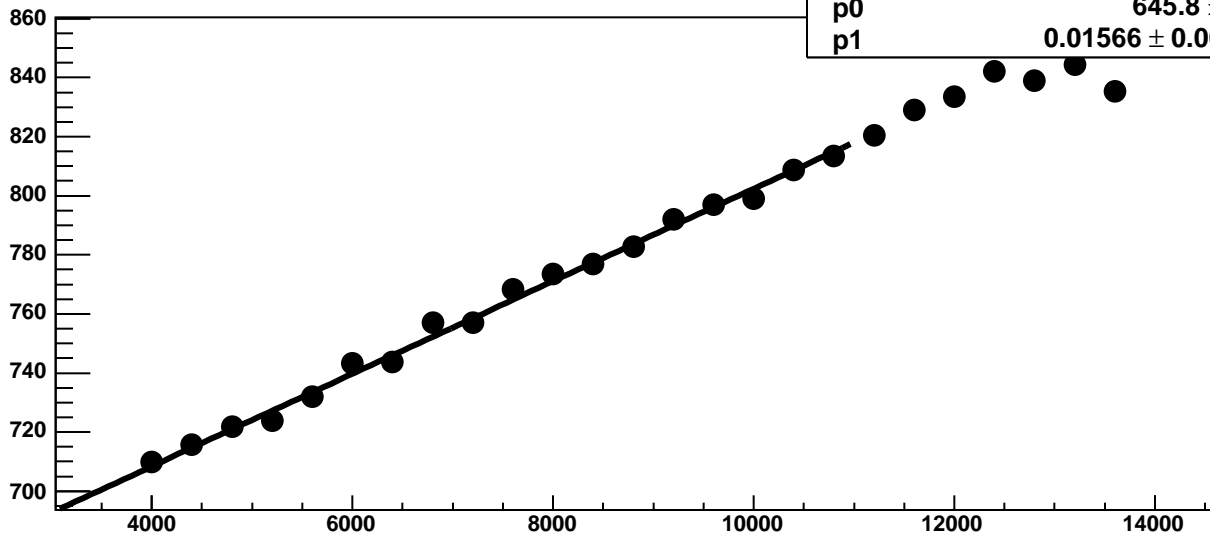
Chip 0, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



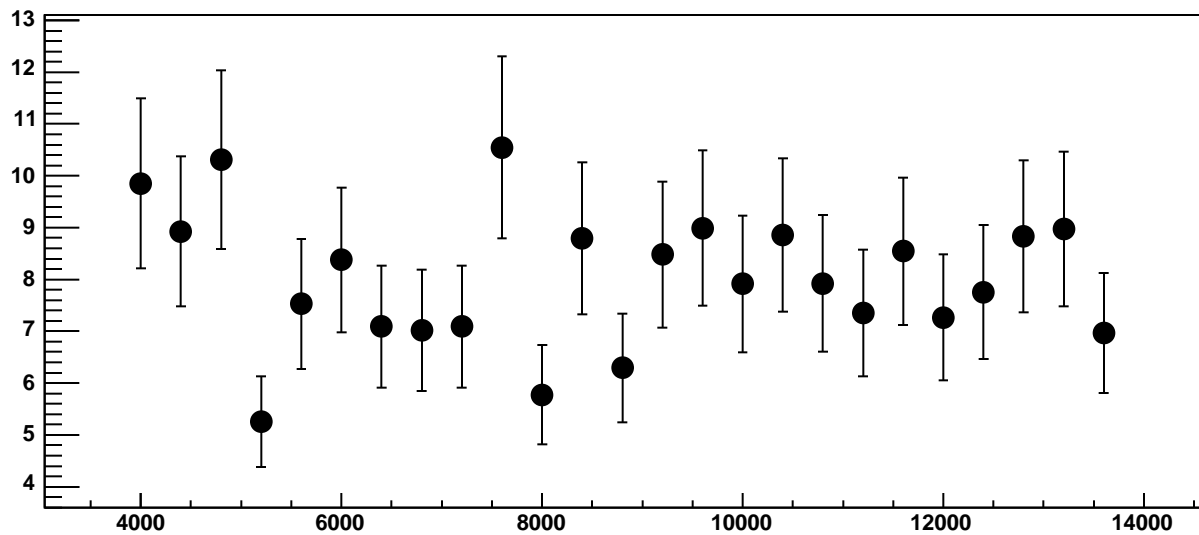
Chip 0, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC



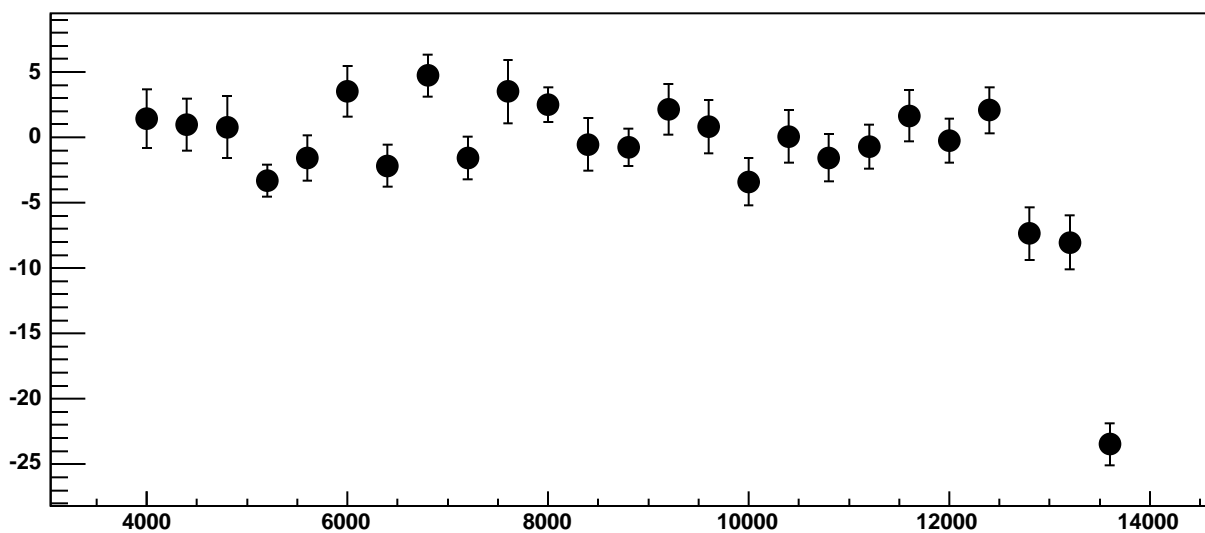
Chip 0, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



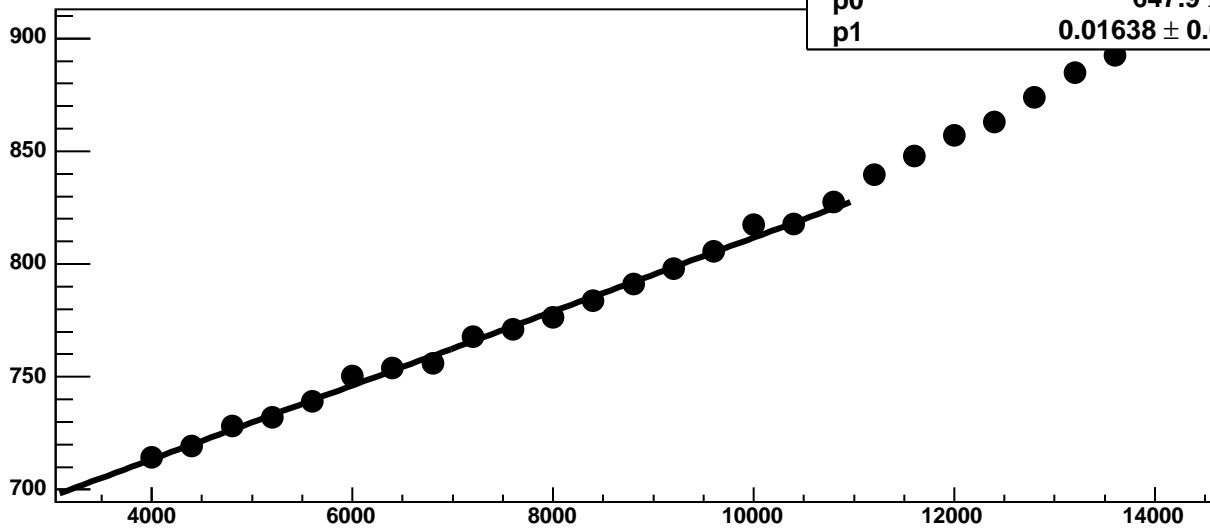
Chip 0, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



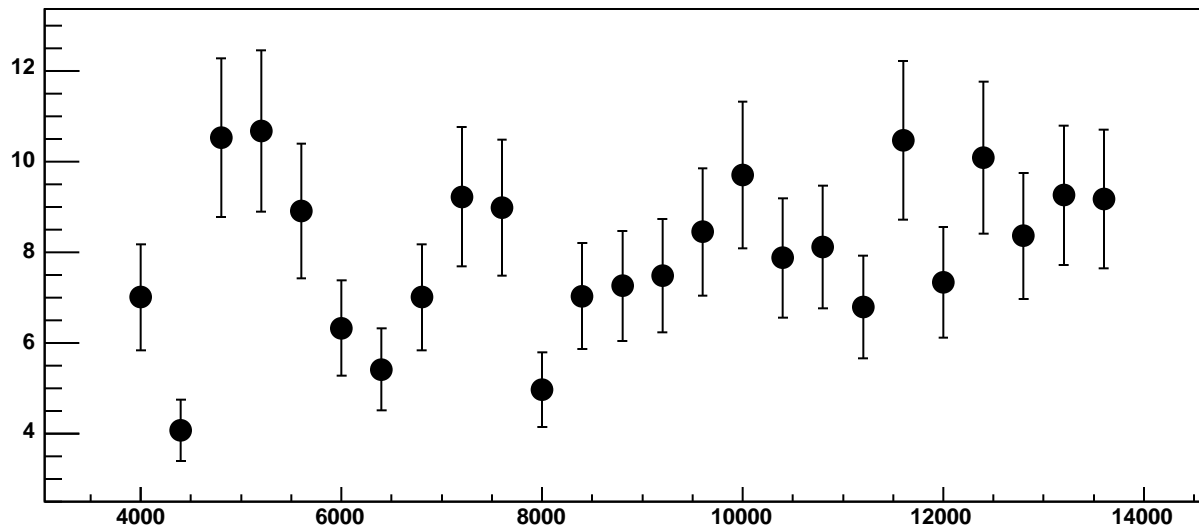
Chip 0, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



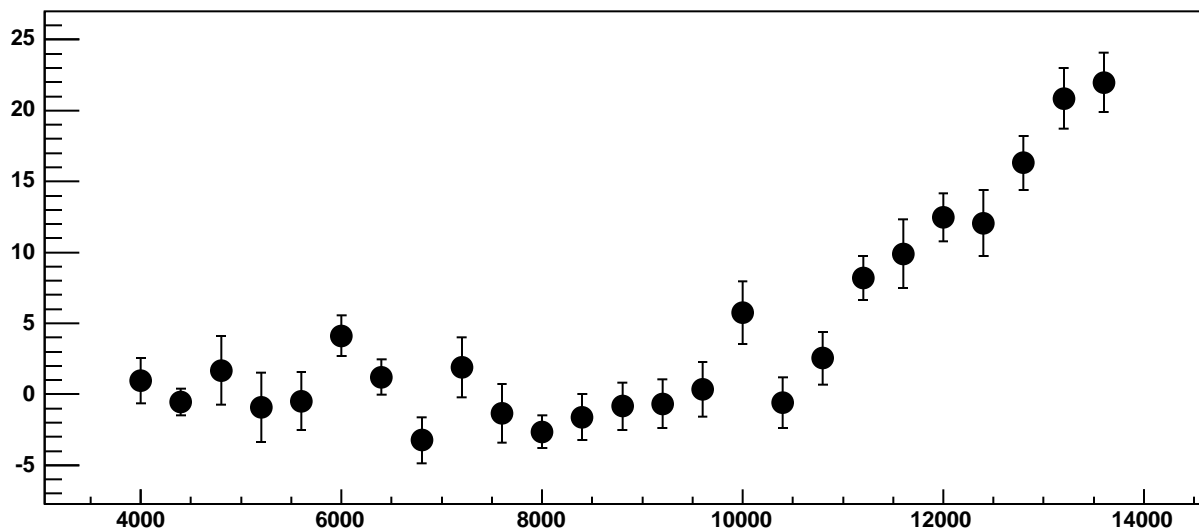
Chip 0, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



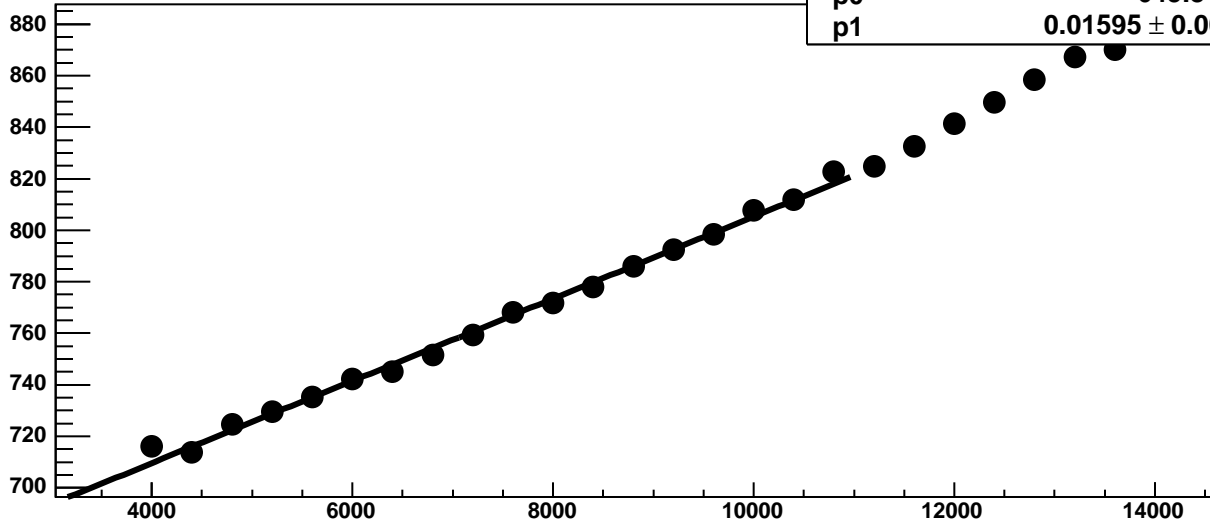
Chip 0, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



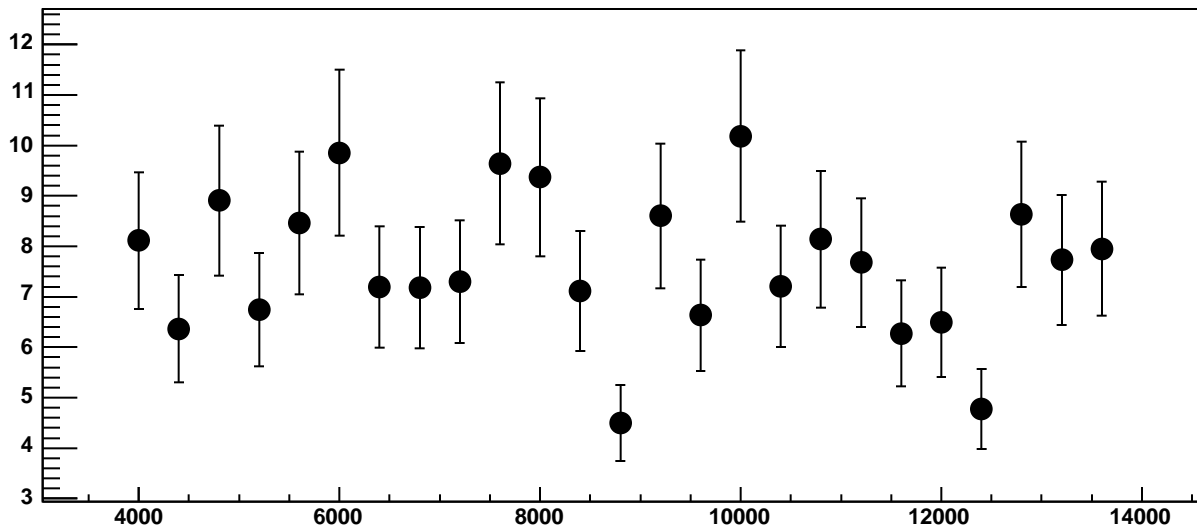
Chip 0, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



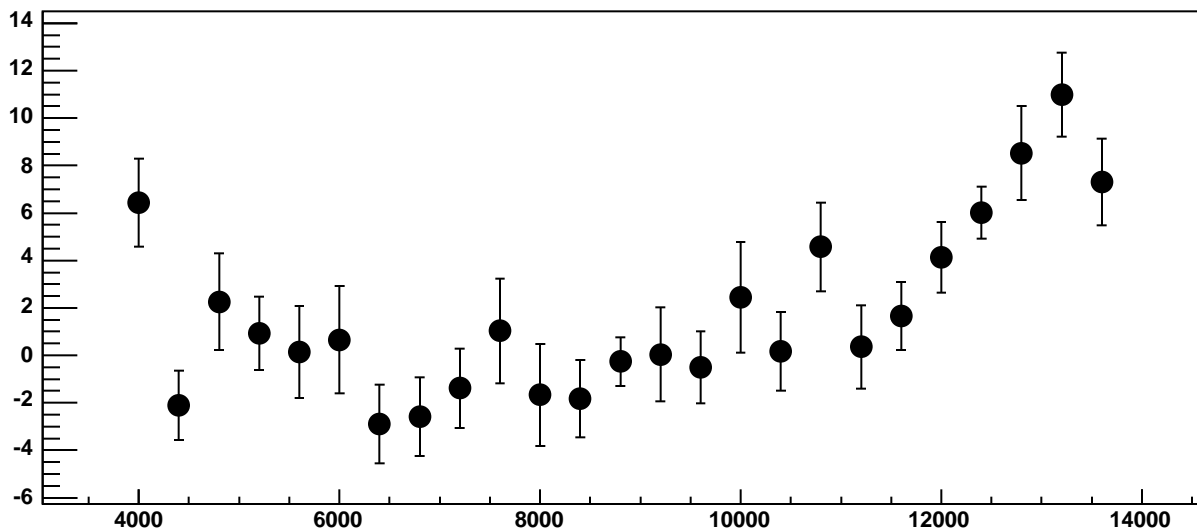
Chip 0, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC



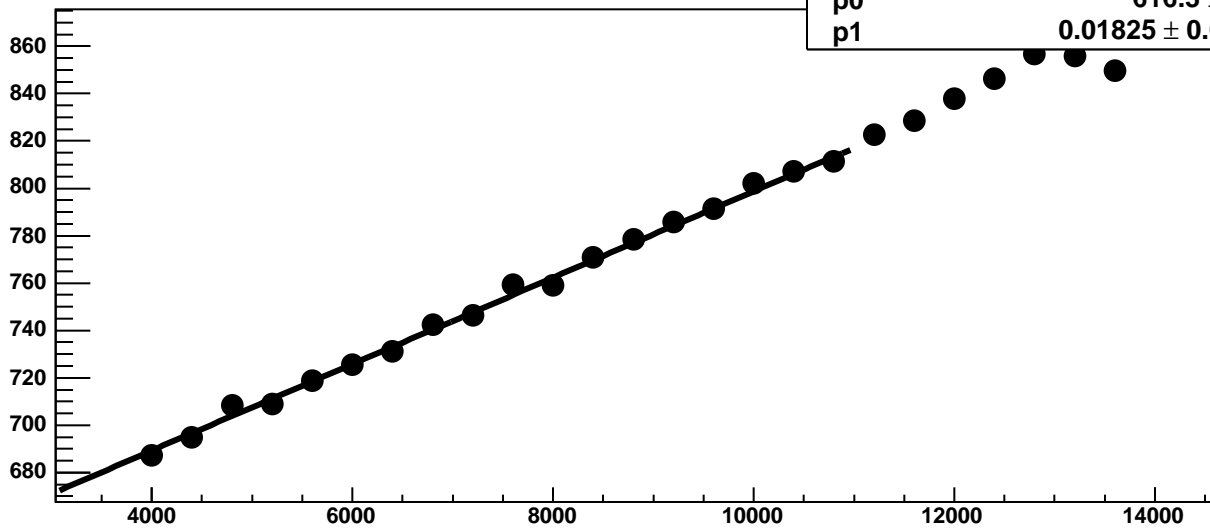
Chip 0, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC

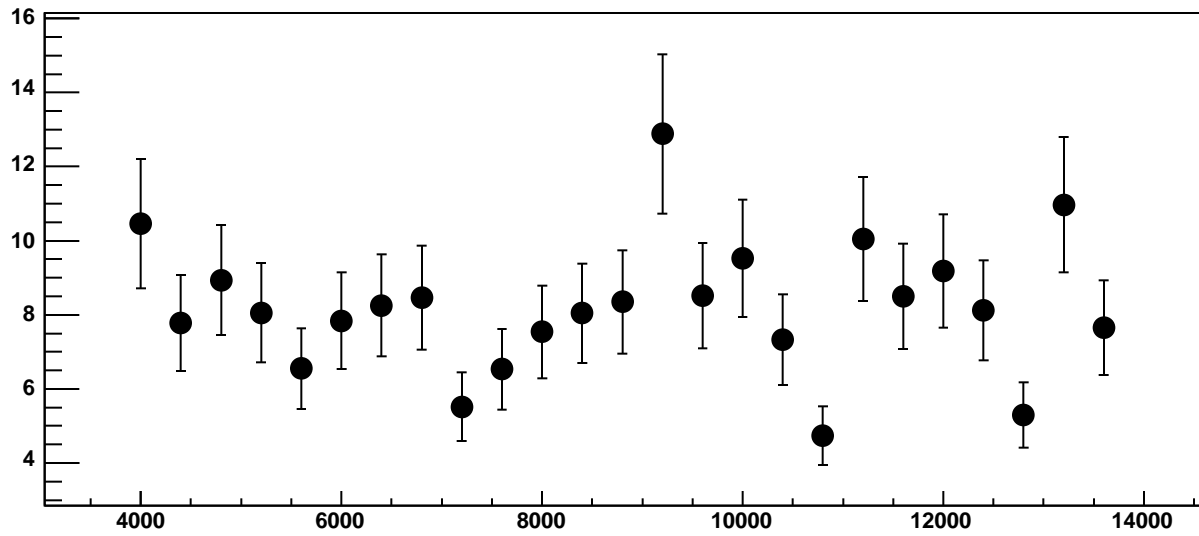


Chip 0, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

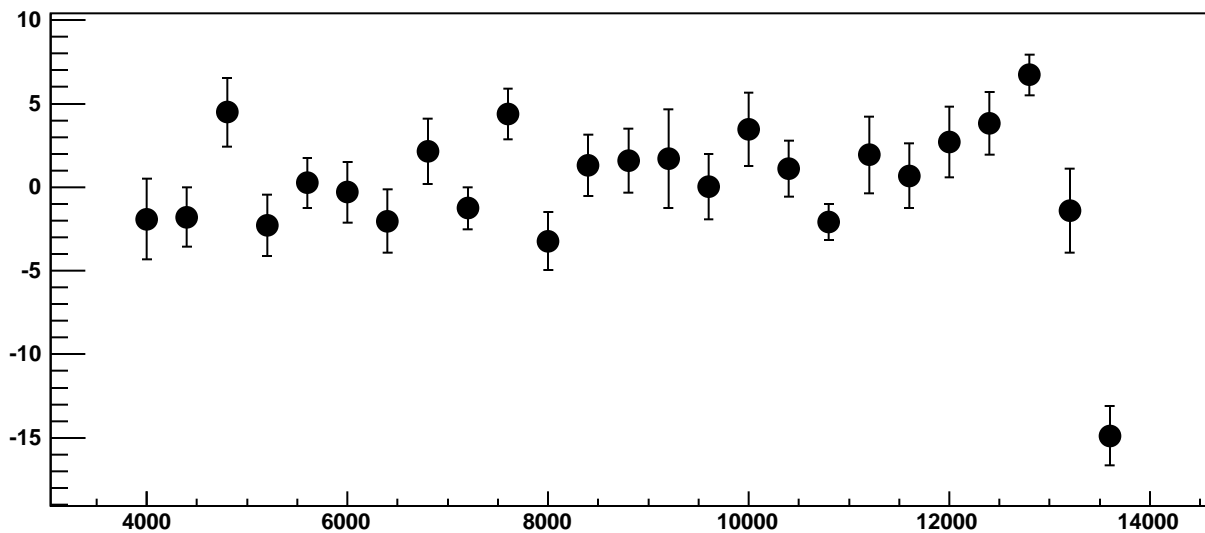


χ^2 / ndf 31.57 / 16
p0 616.3 ± 1.535
p1 0.01825 ± 0.000193

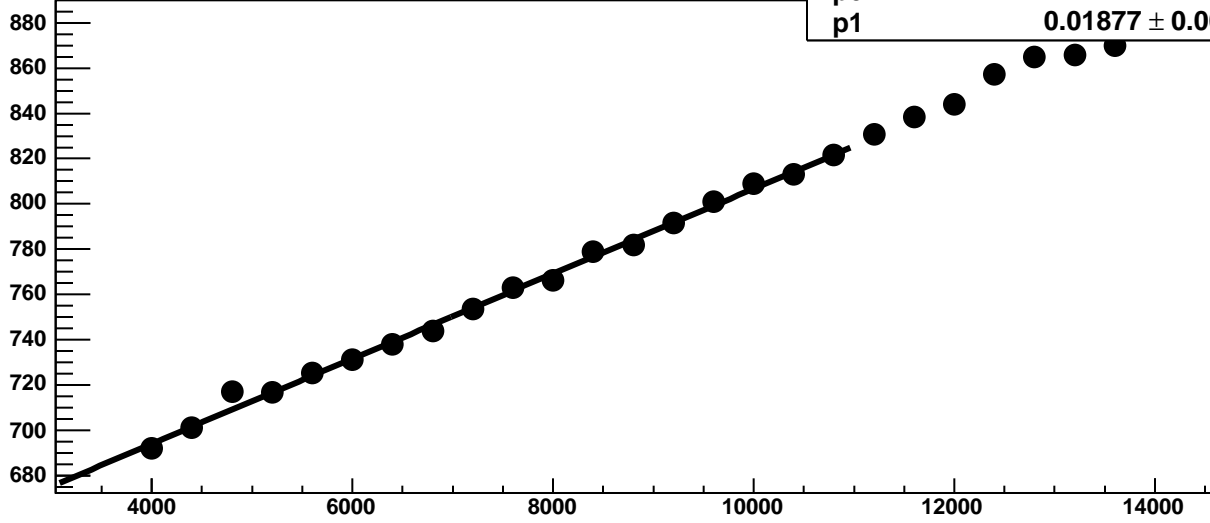
Chip 0, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

26.65 / 16

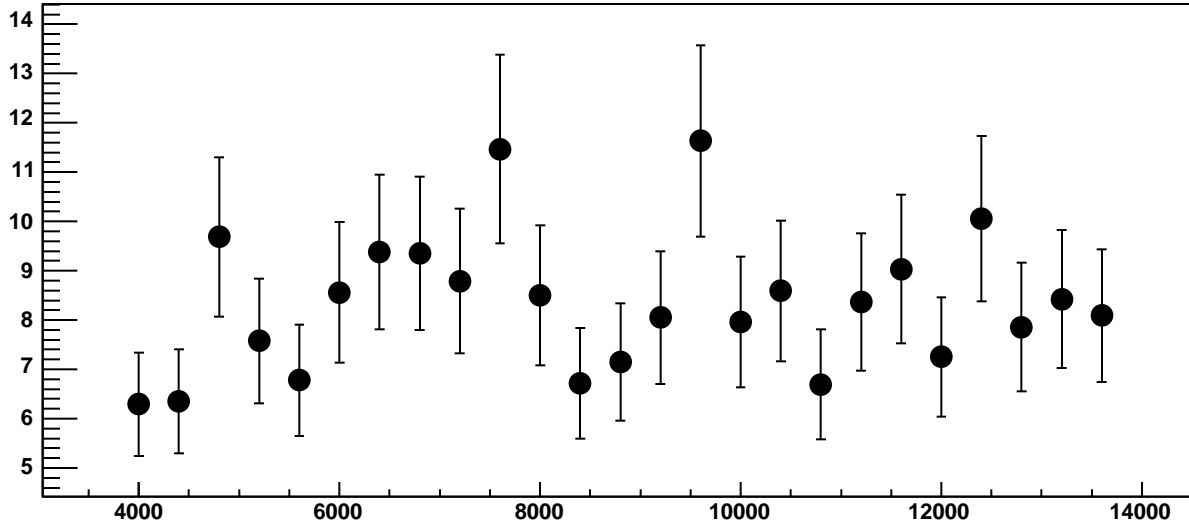
p0

619 ± 1.461

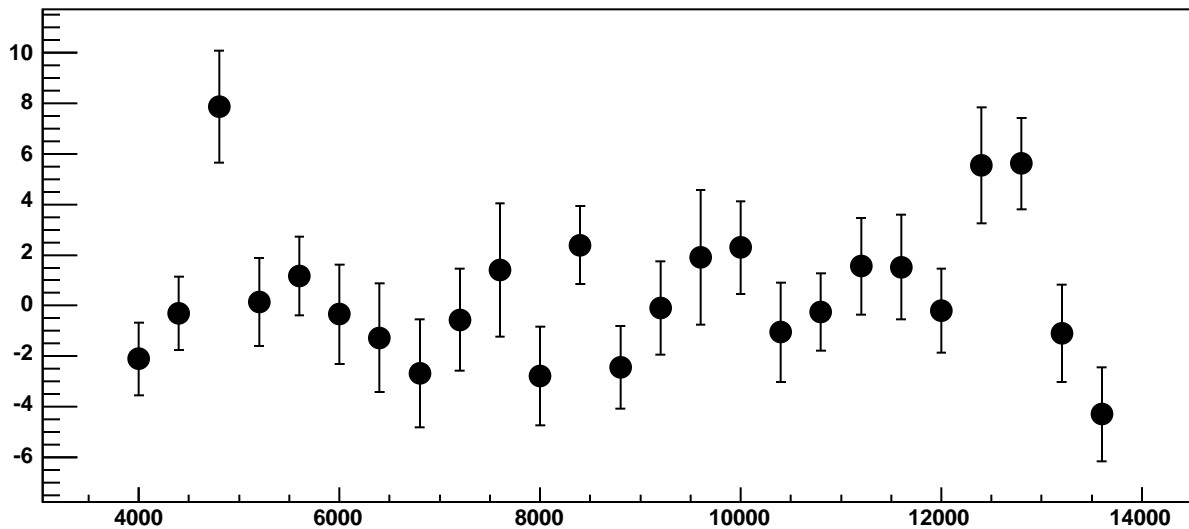
p1

0.01877 ± 0.0001928

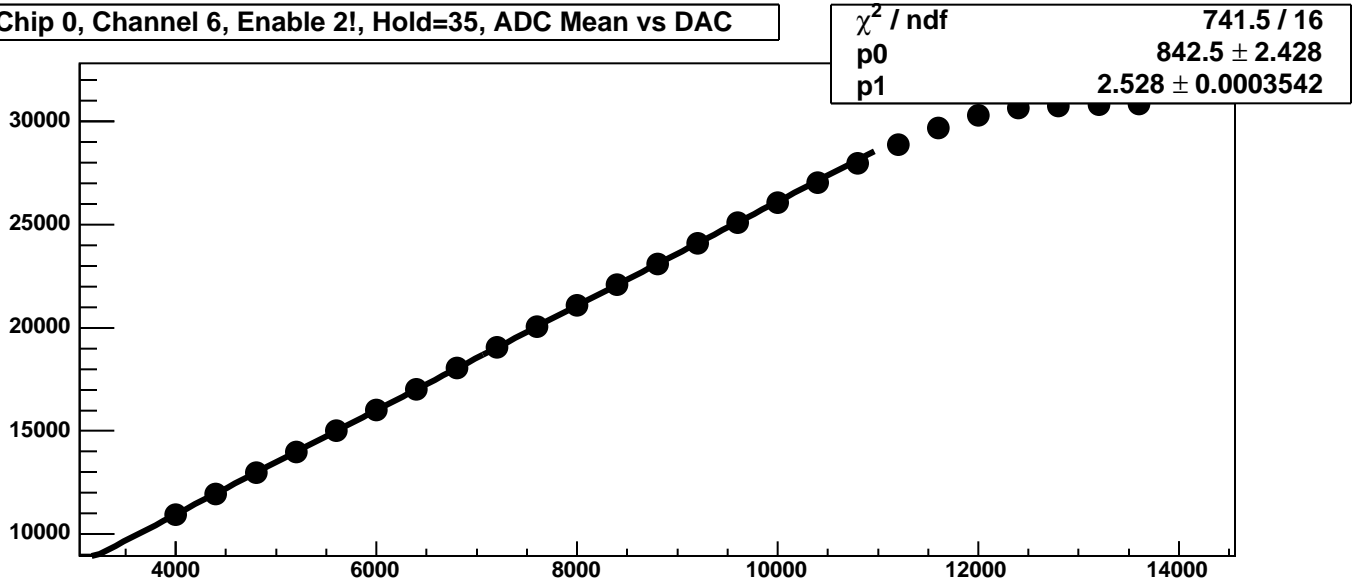
Chip 0, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



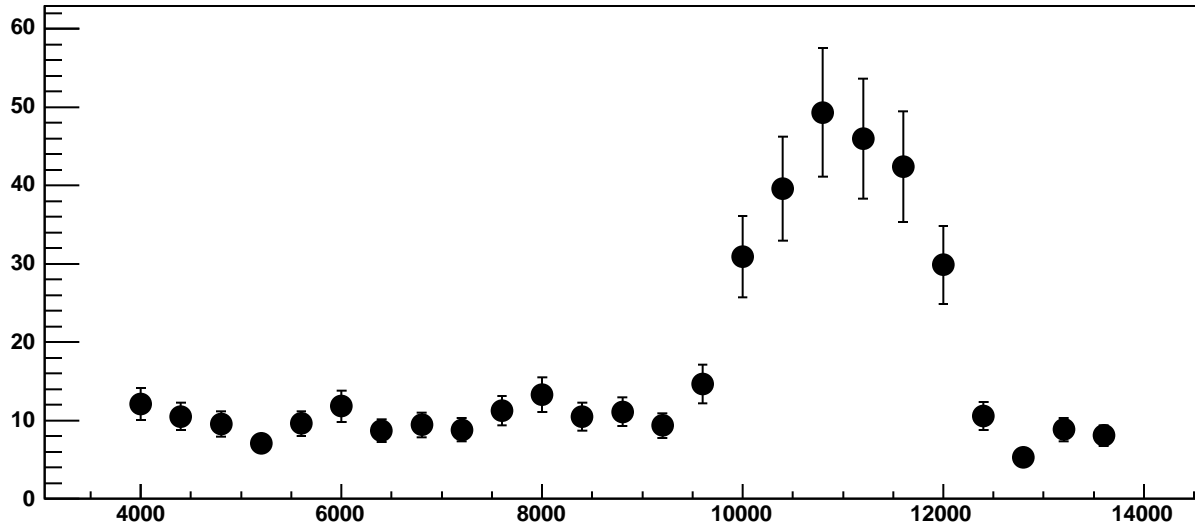
Chip 0, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



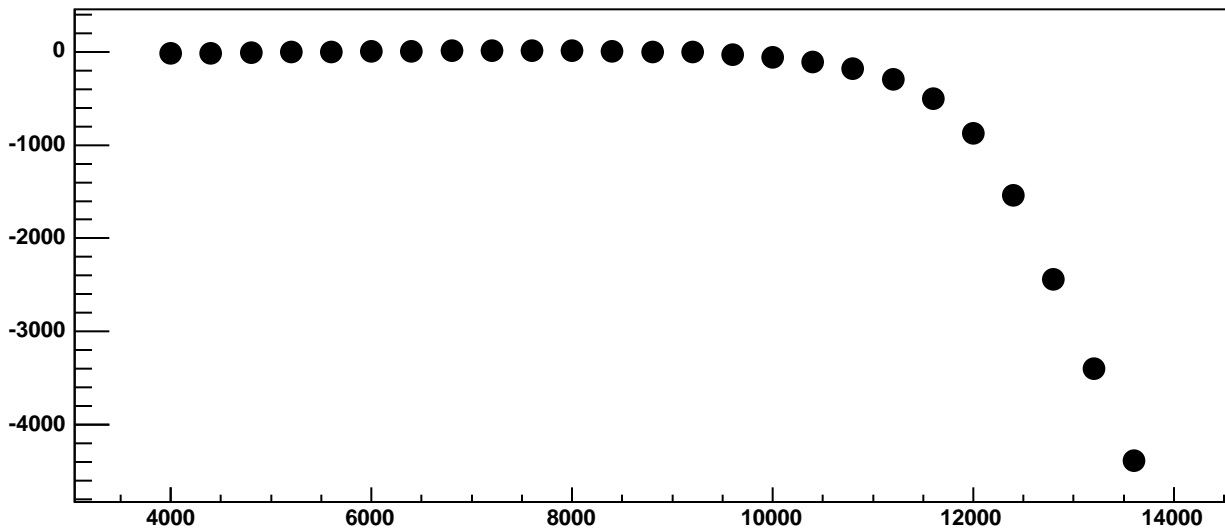
Chip 0, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



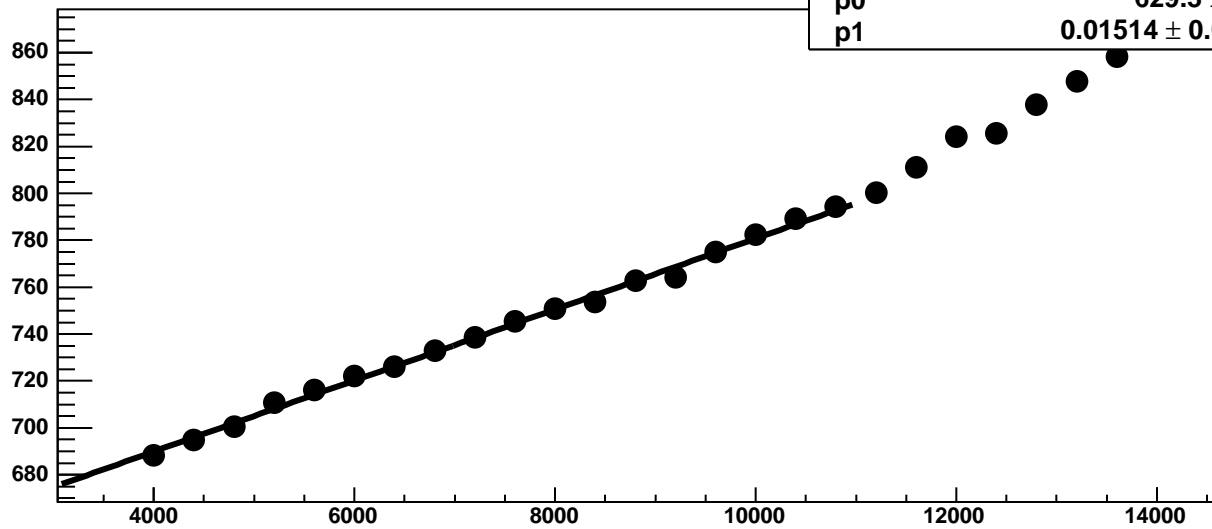
Chip 0, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC

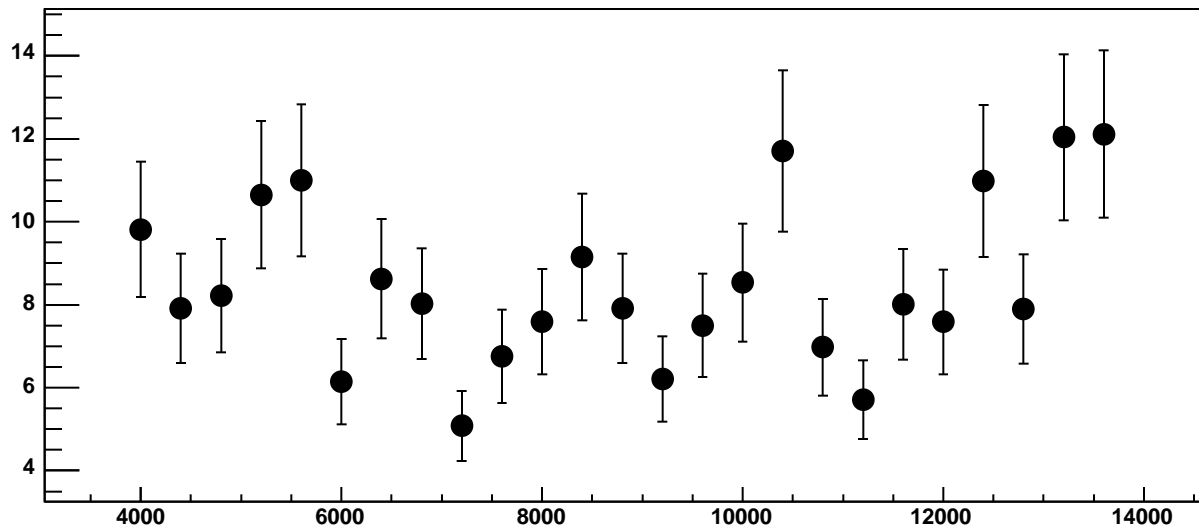


Chip 0, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

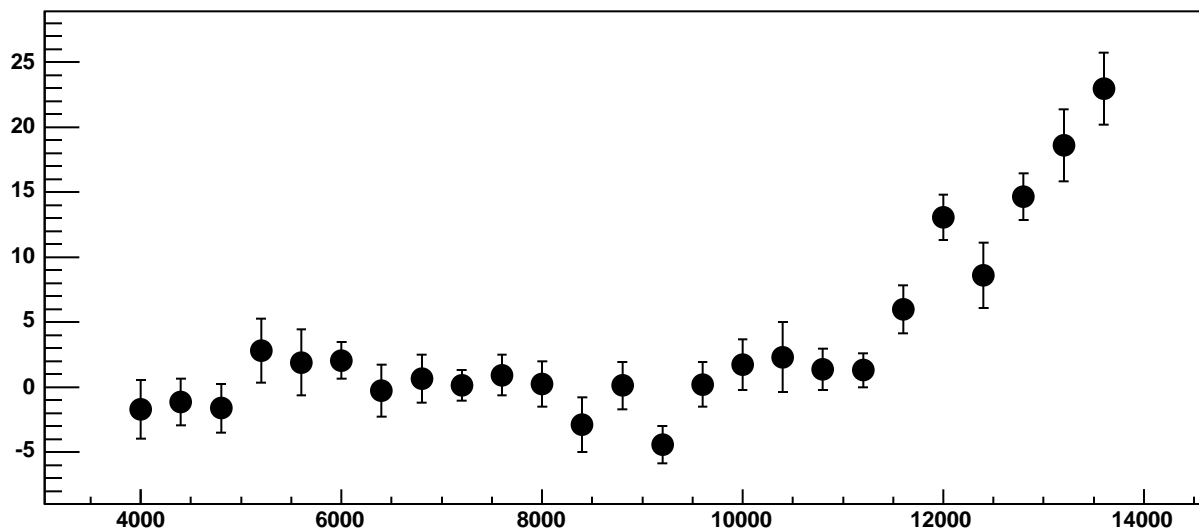


χ^2 / ndf 19.98 / 16
p0 629.3 ± 1.686
p1 0.01514 ± 0.000217

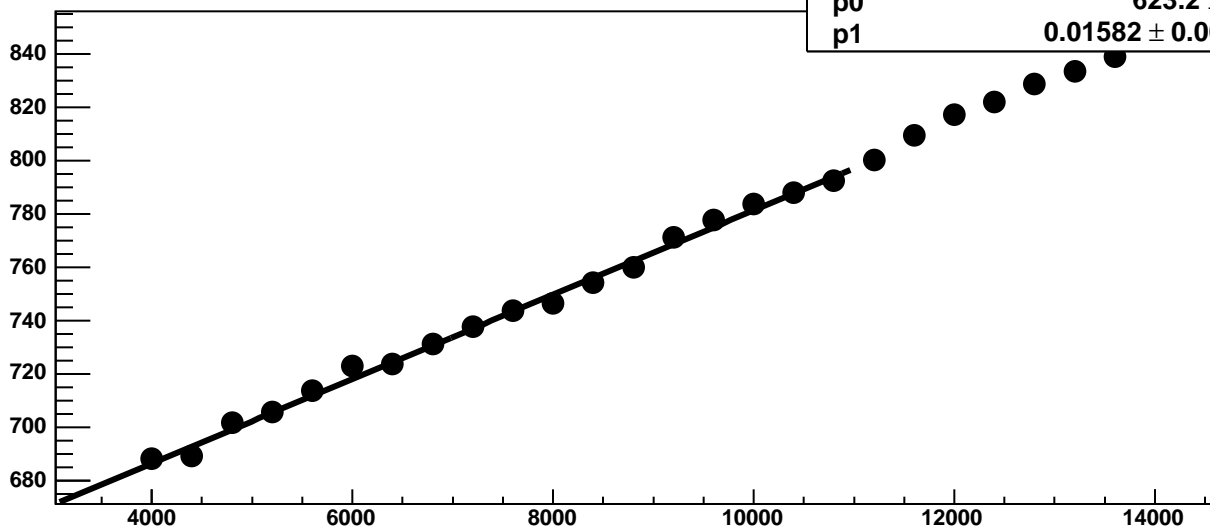
Chip 0, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

27.55 / 16

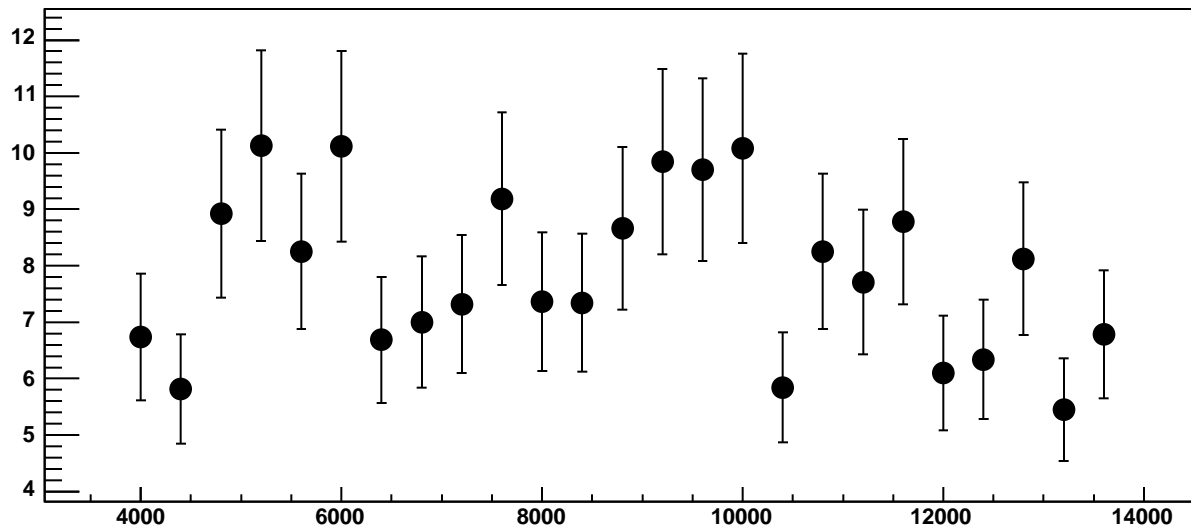
p0

623.2 ± 1.479

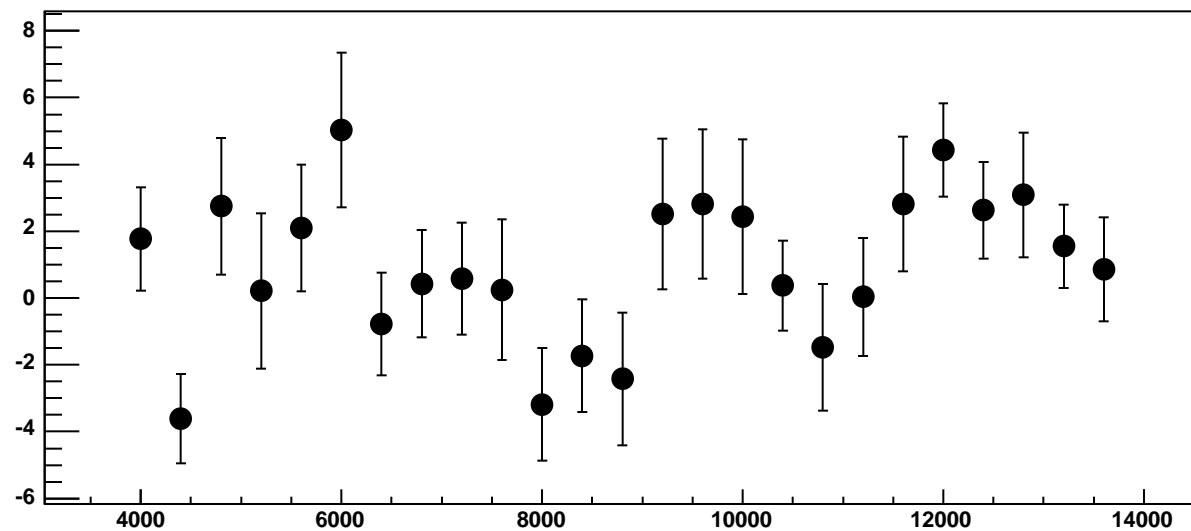
p1

0.01582 ± 0.0001952

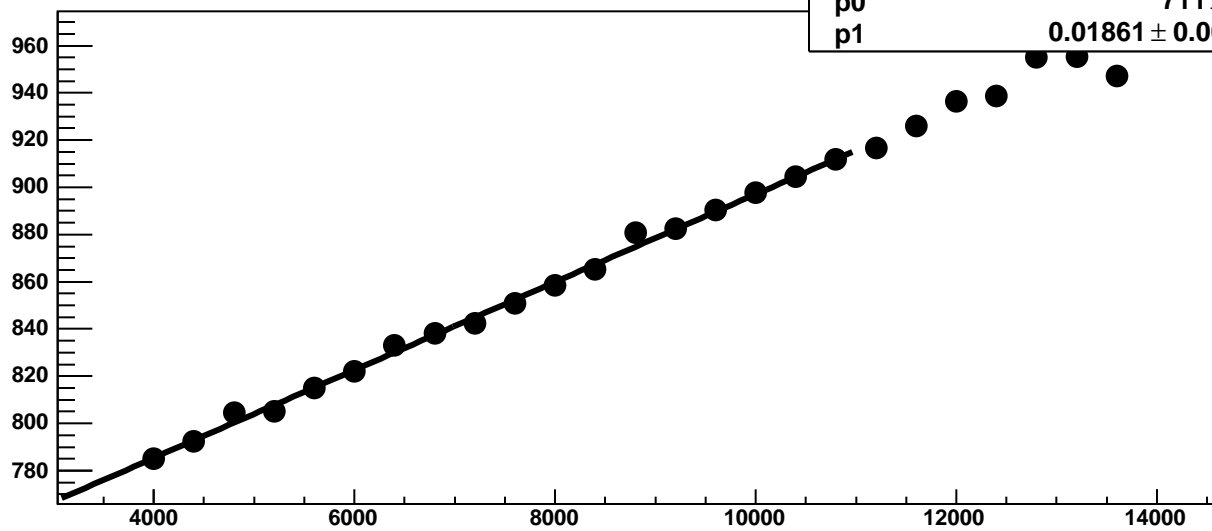
Chip 0, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

24.85 / 16

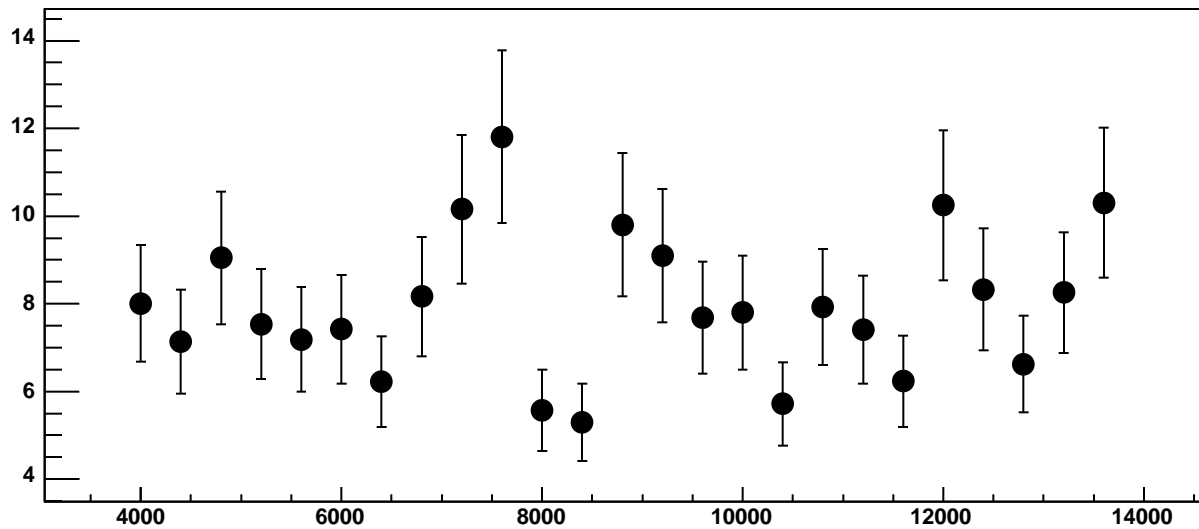
p0

711 ± 1.506

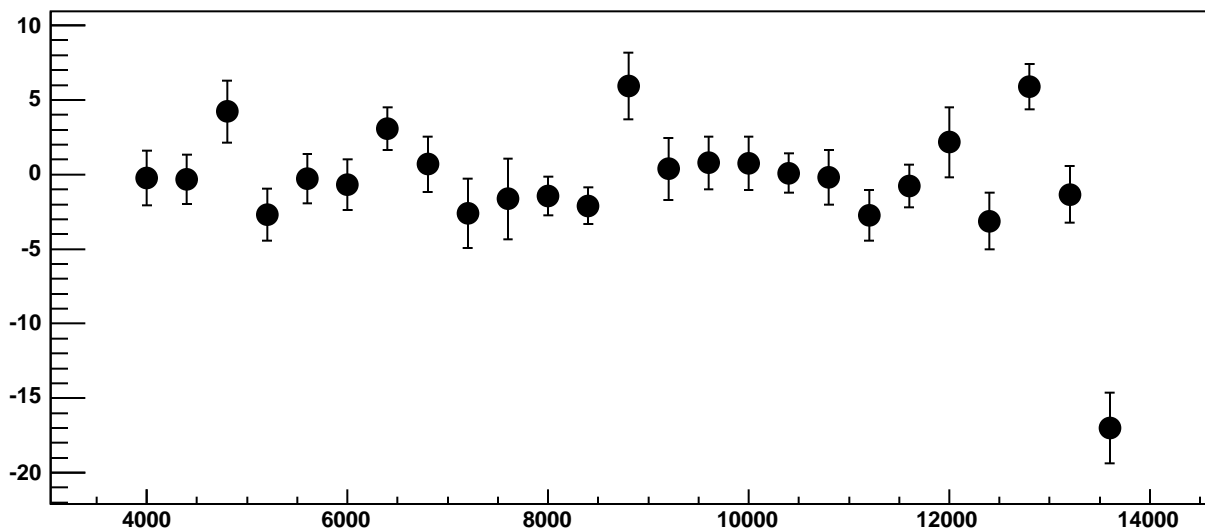
p1

0.01861 ± 0.0001932

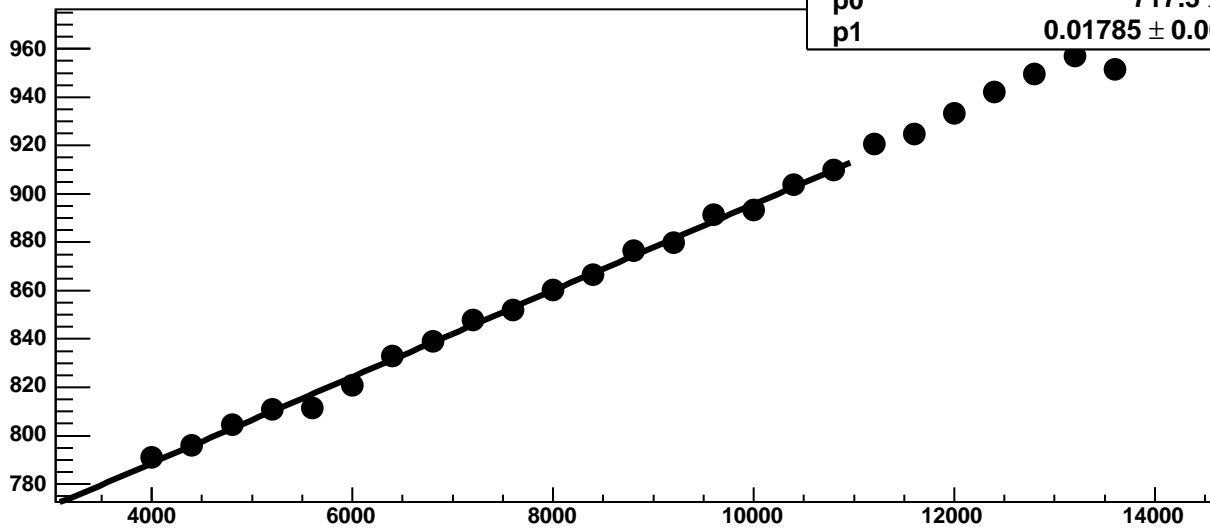
Chip 0, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



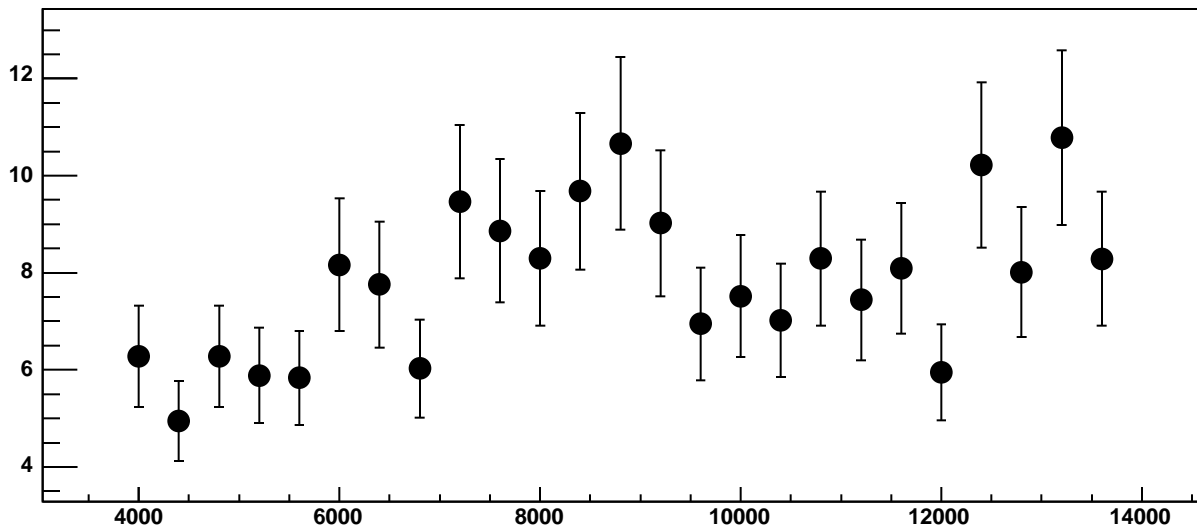
Chip 0, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



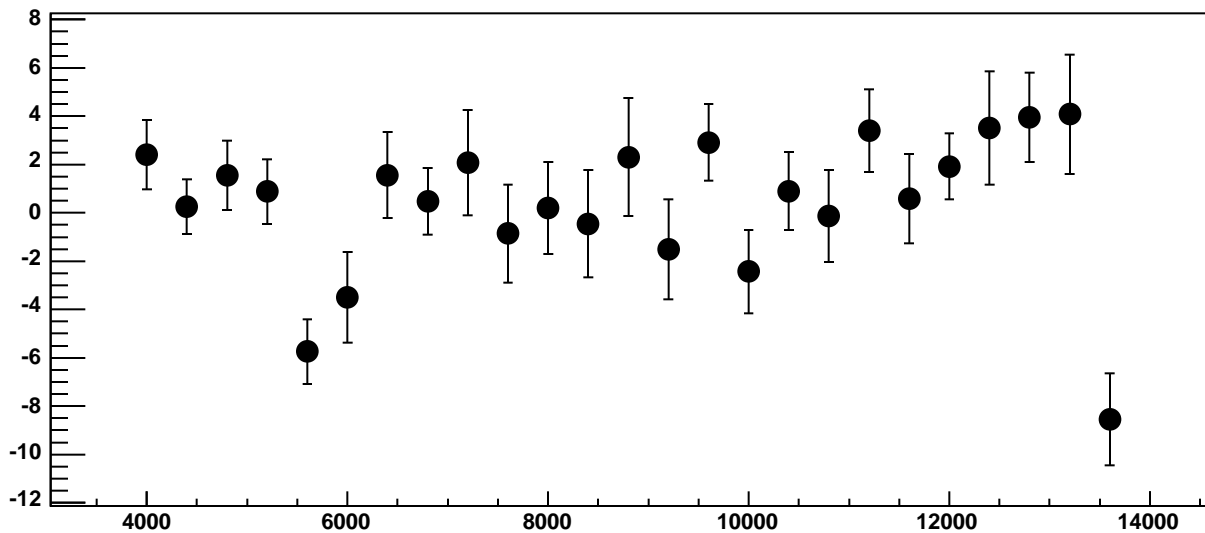
Chip 0, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



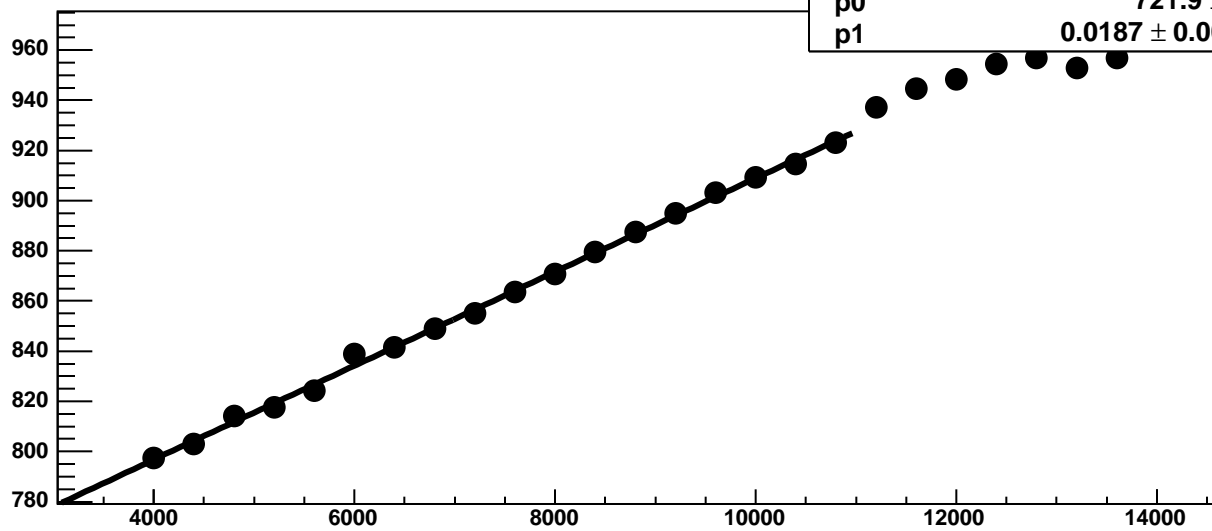
Chip 0, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



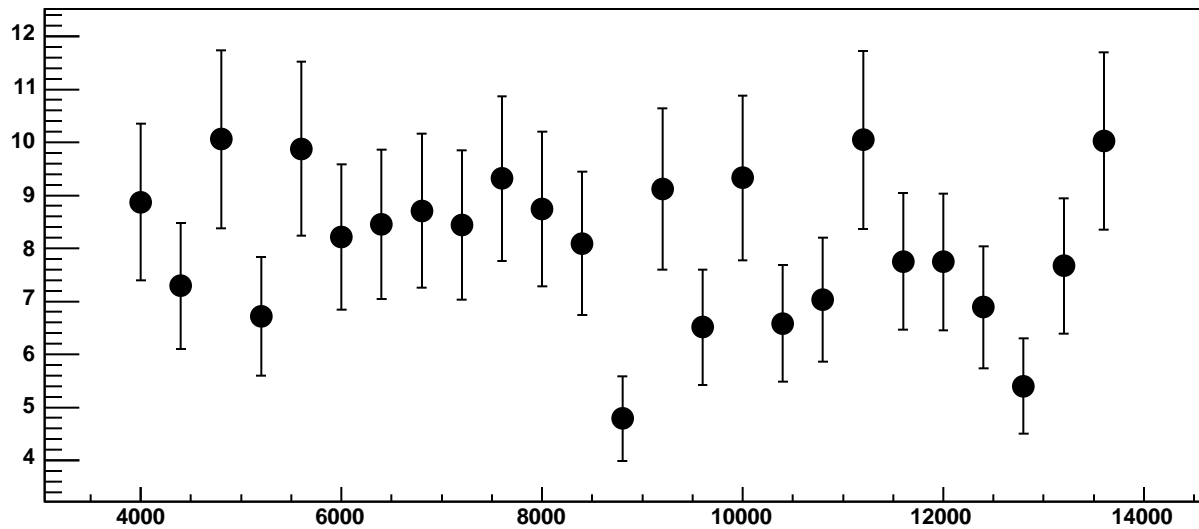
Chip 0, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



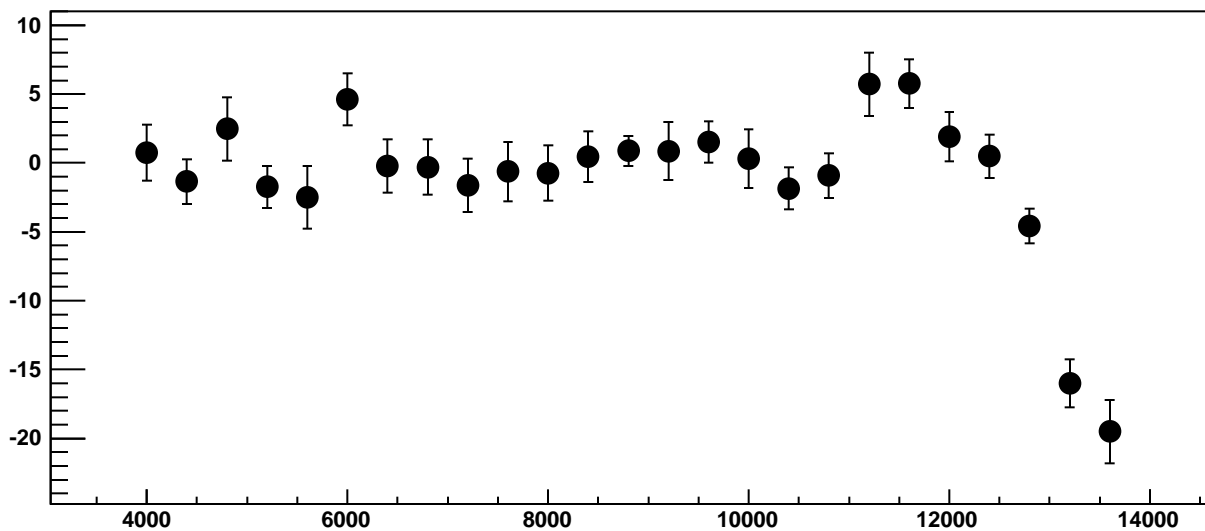
Chip 0, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC



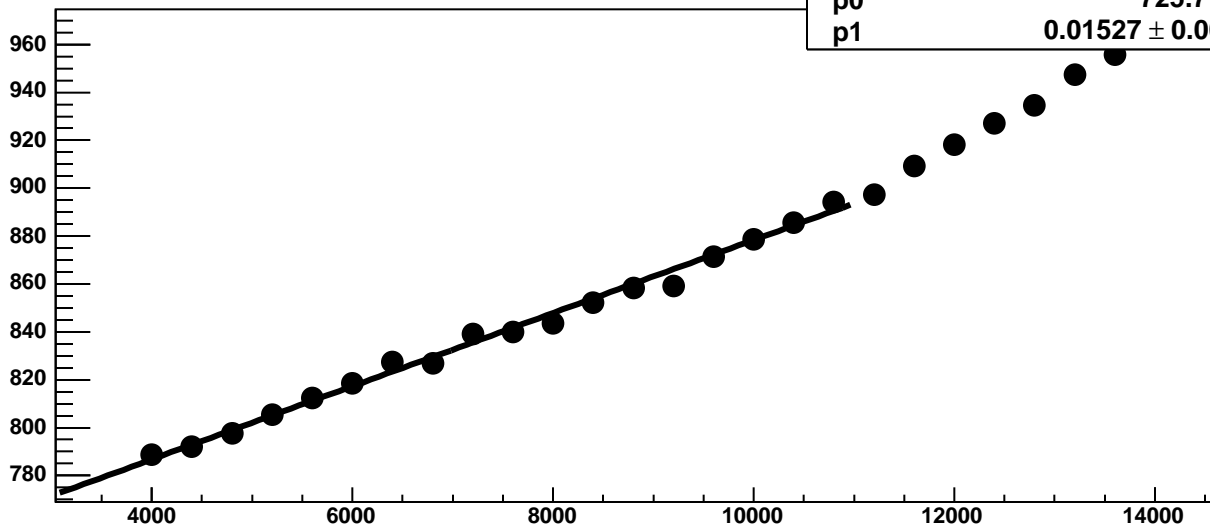
Chip 0, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

36.57 / 16

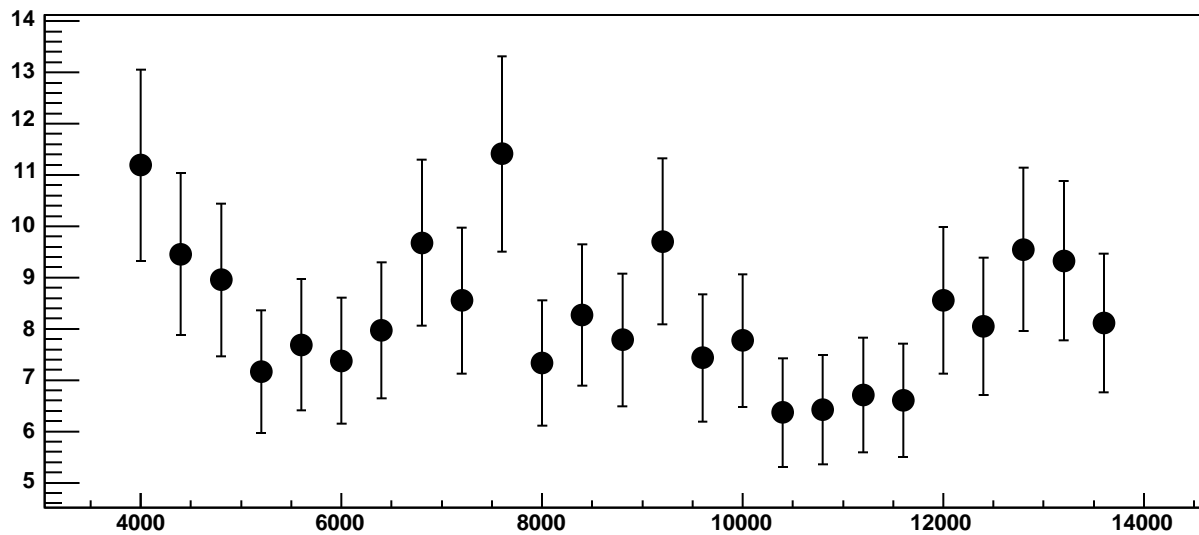
p0

725.7 ± 1.65

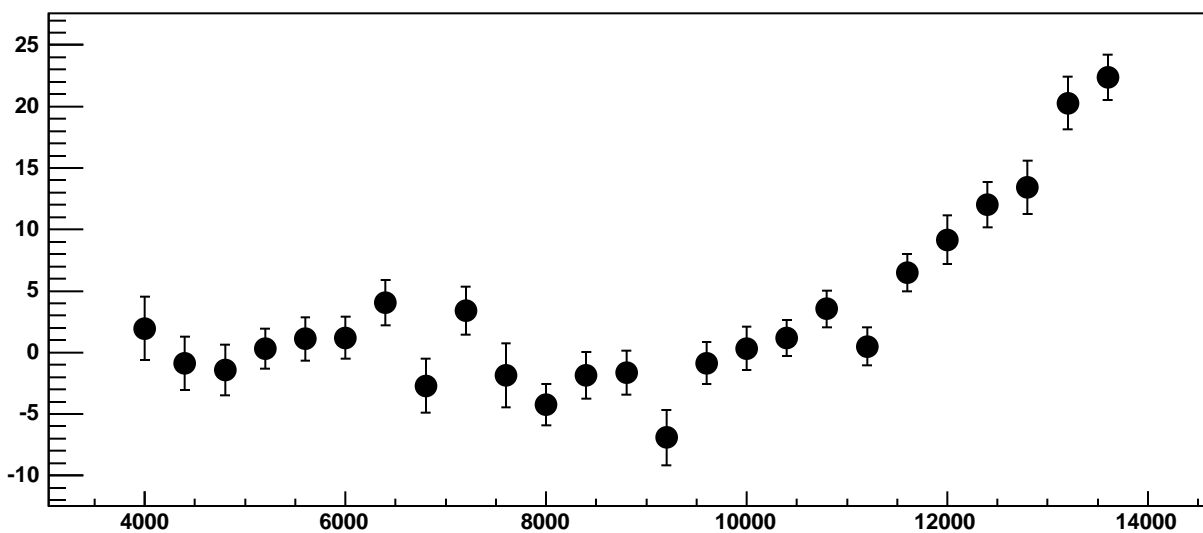
p1

0.01527 ± 0.0002058

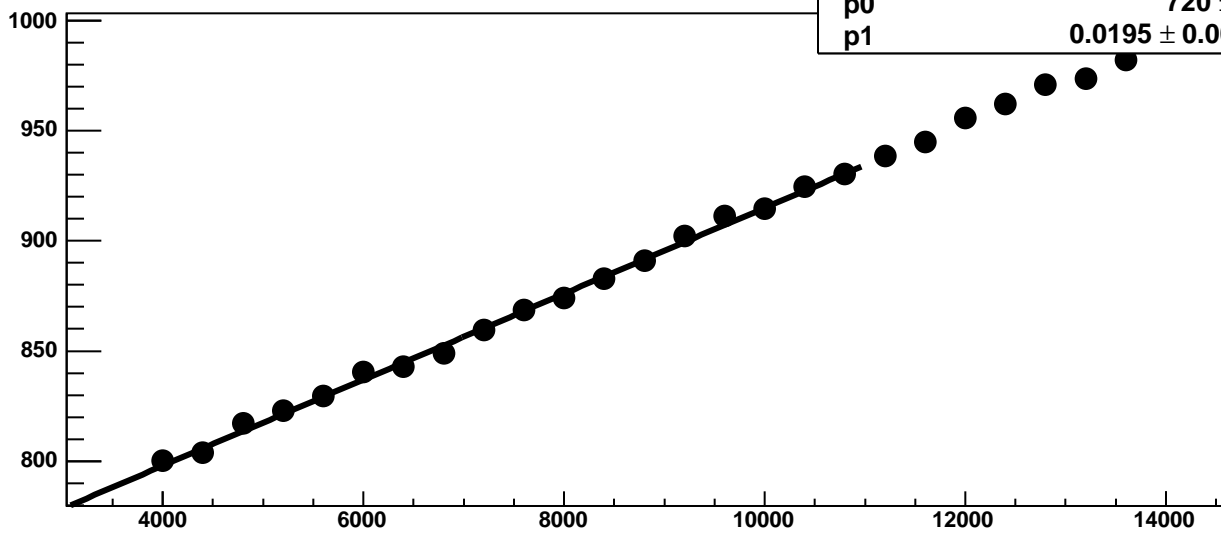
Chip 0, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

26.15 / 16

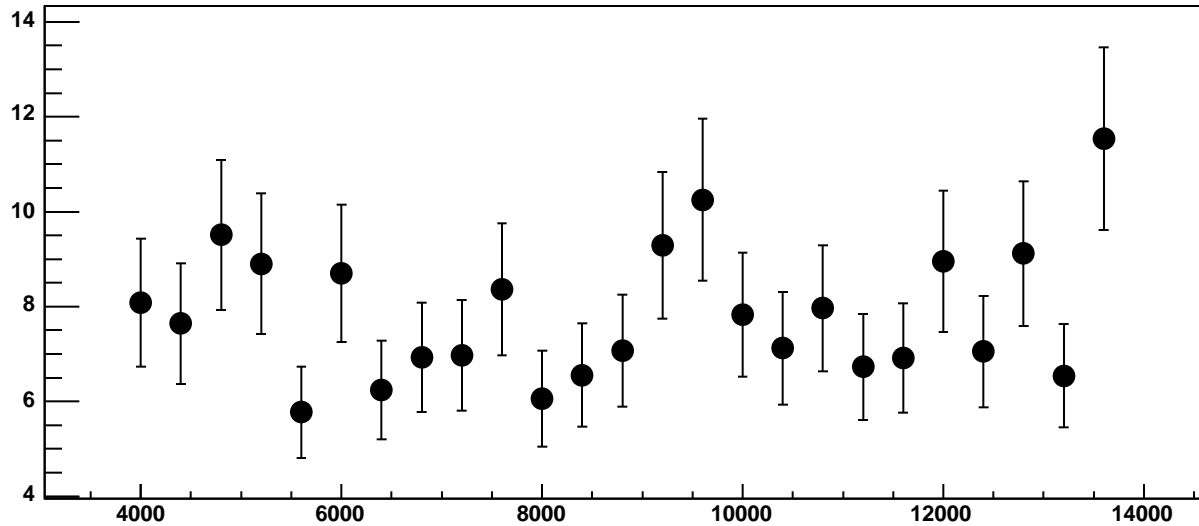
p0

720 ± 1.566

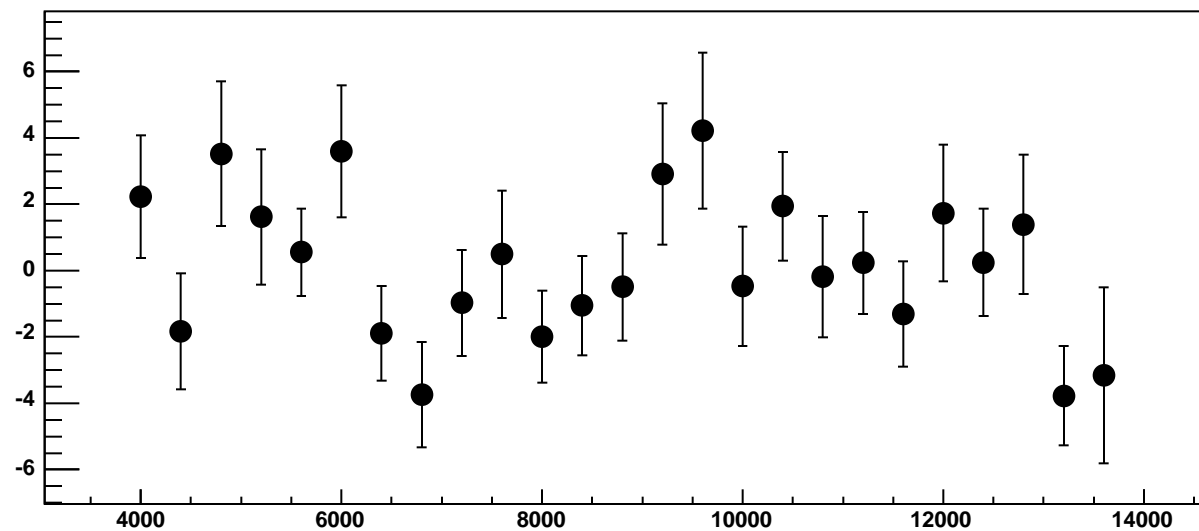
p1

0.0195 ± 0.0002052

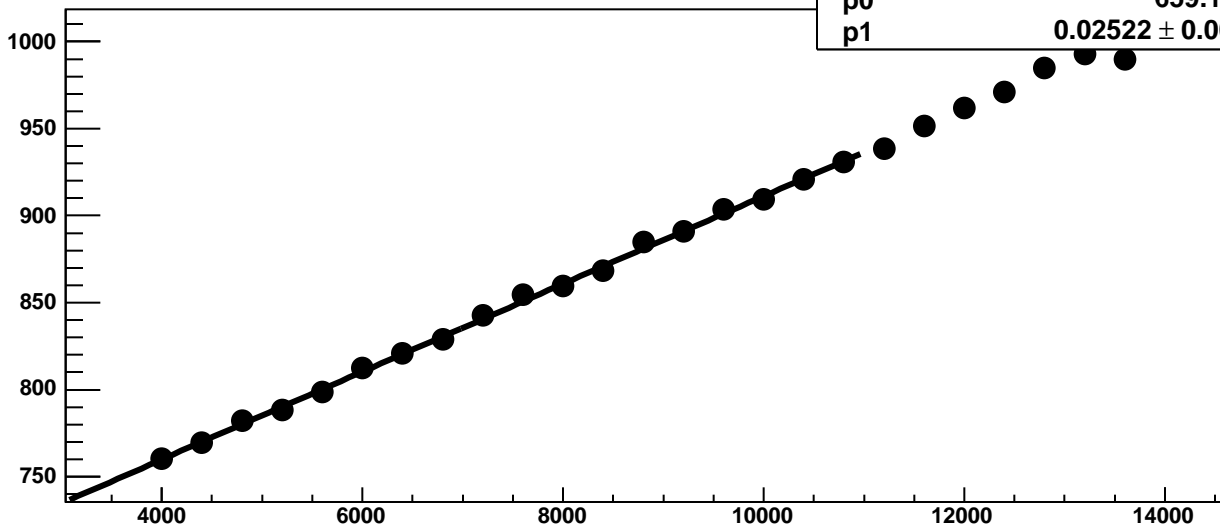
Chip 0, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

19.58 / 16

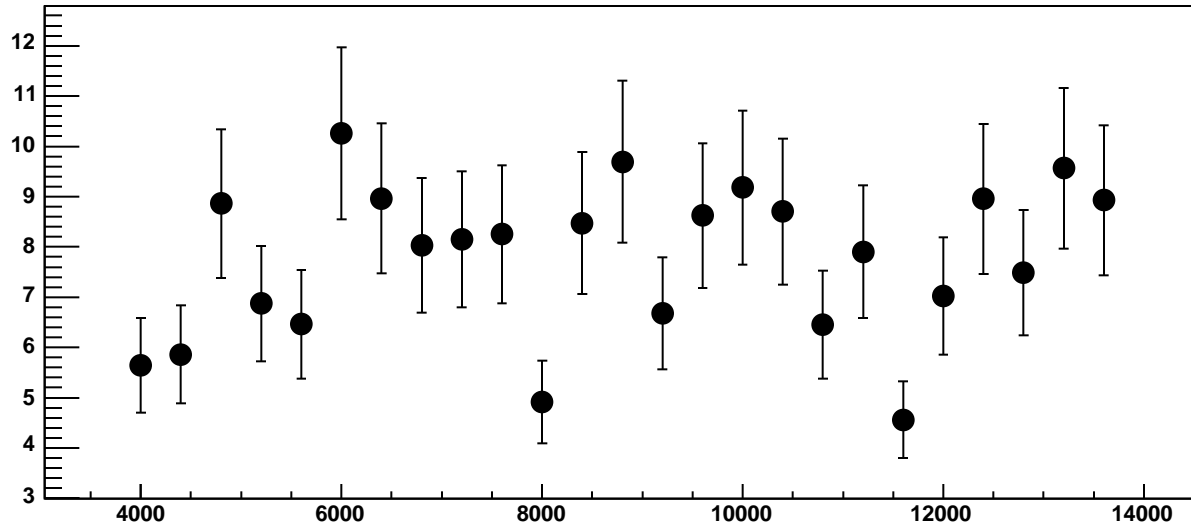
p0

659.1 ± 1.37

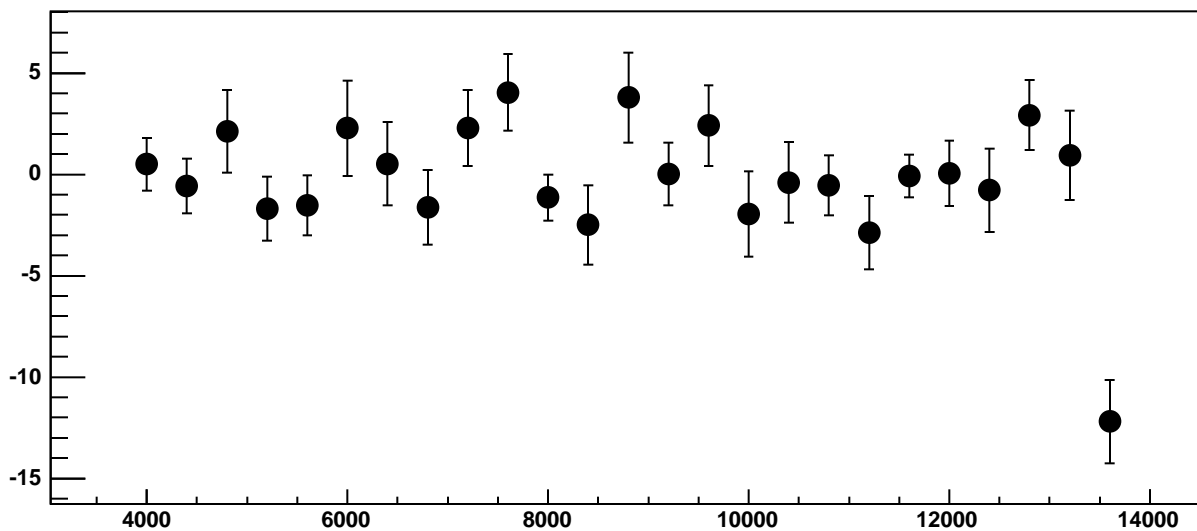
p1

0.02522 ± 0.0001828

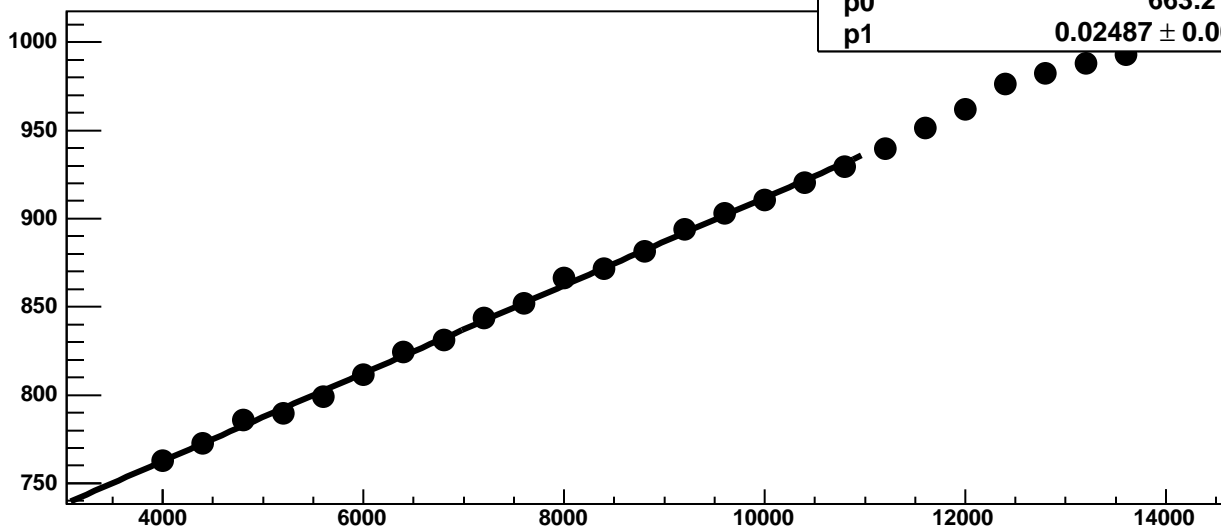
Chip 0, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

22.11 / 16

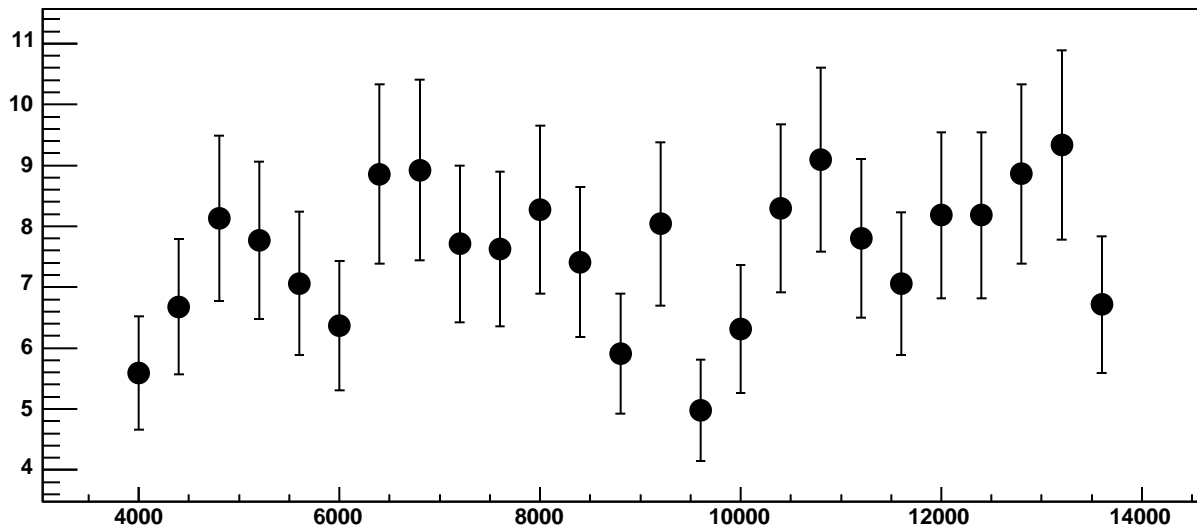
p0

663.2 ± 1.371

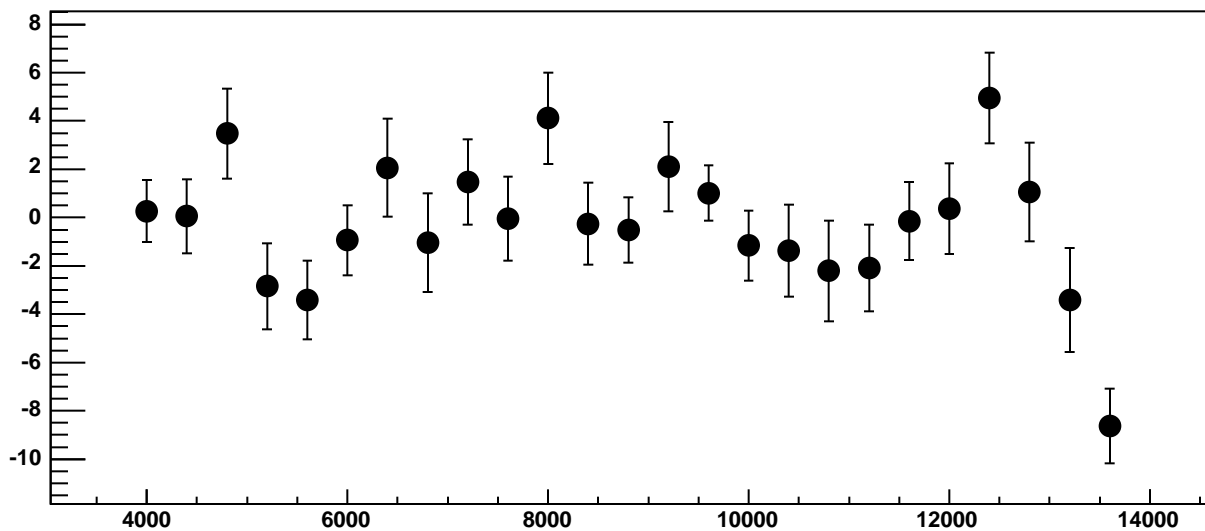
p1

0.02487 ± 0.0001785

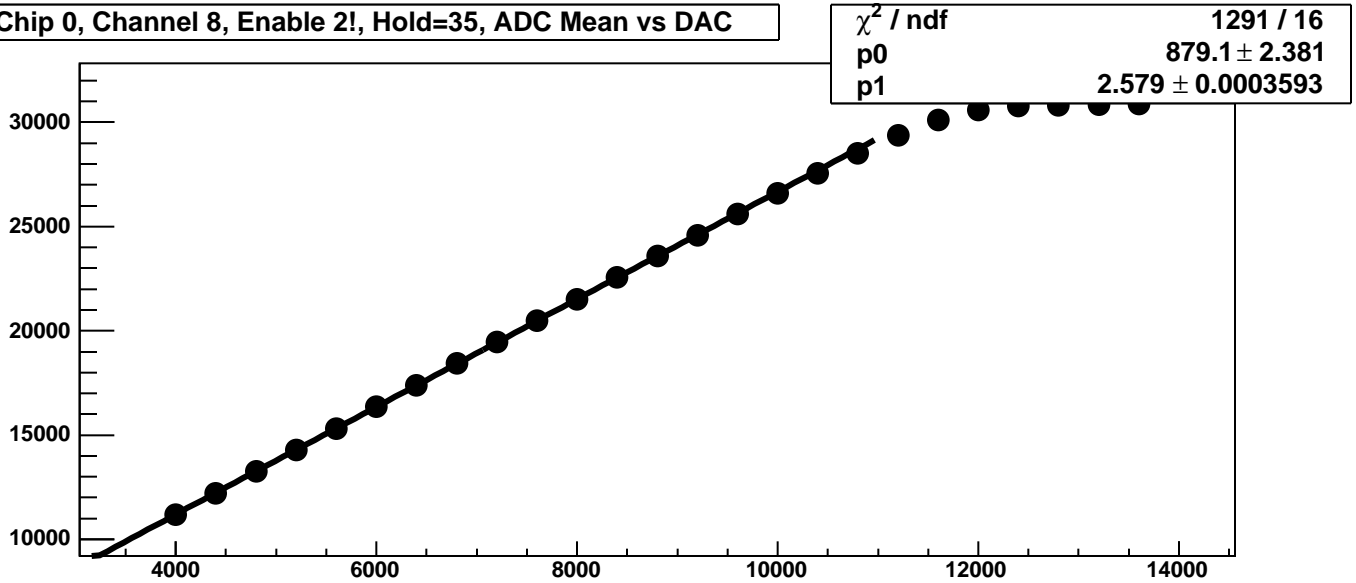
Chip 0, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



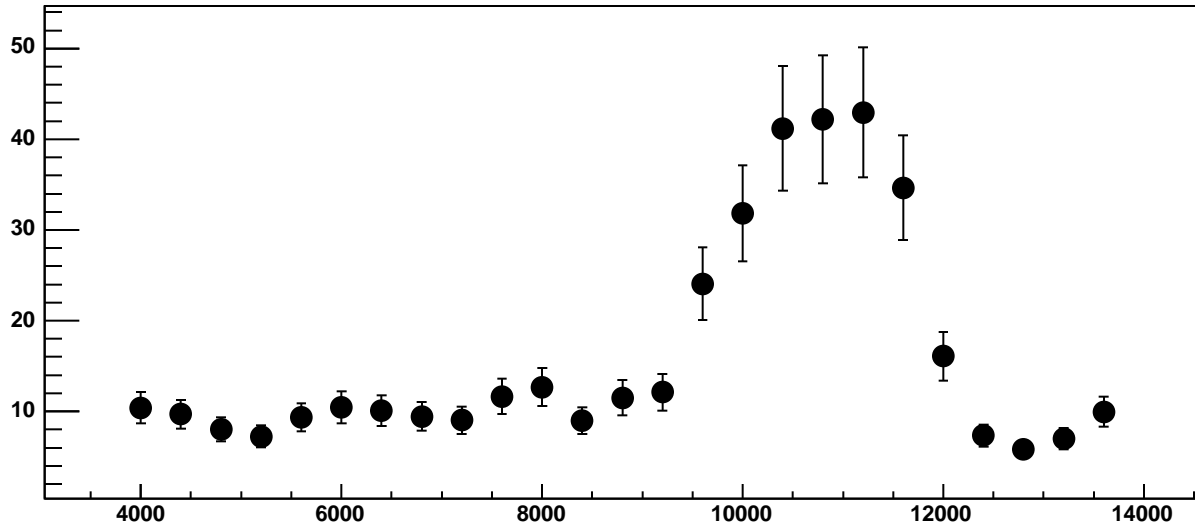
Chip 0, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



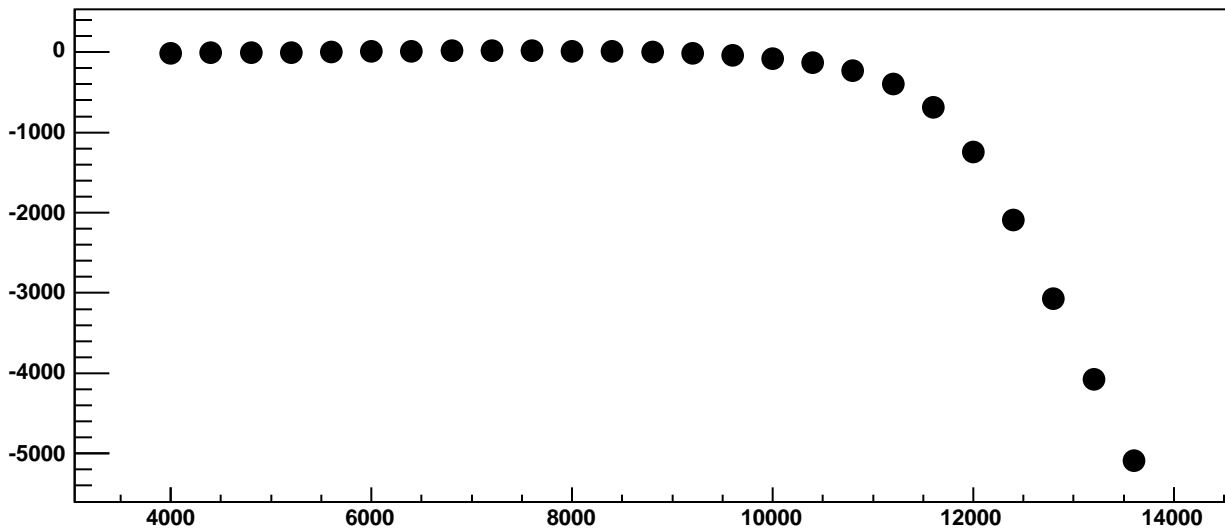
Chip 0, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC



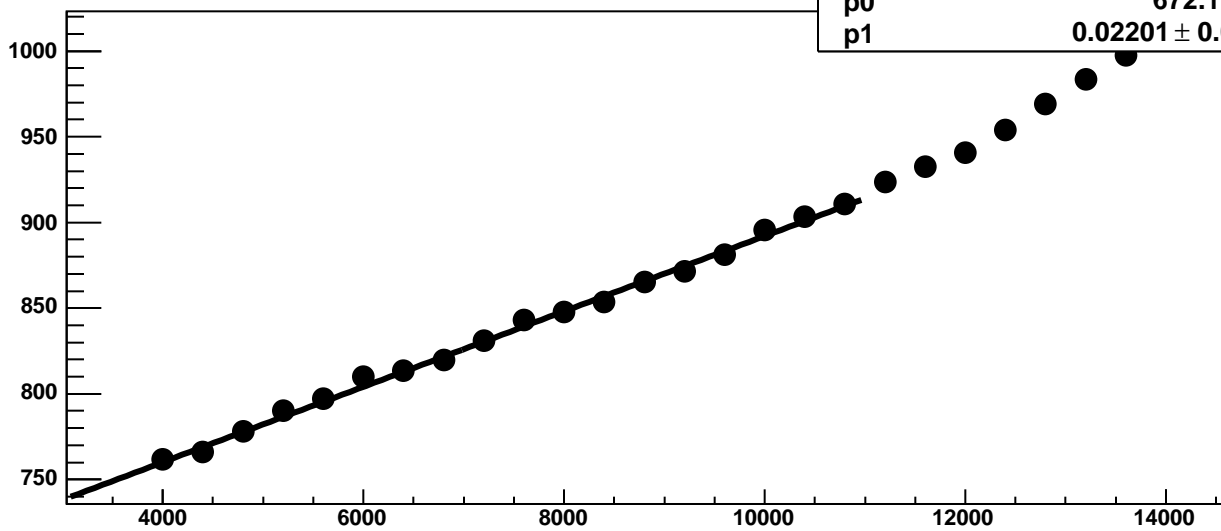
Chip 0, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

38.35 / 16

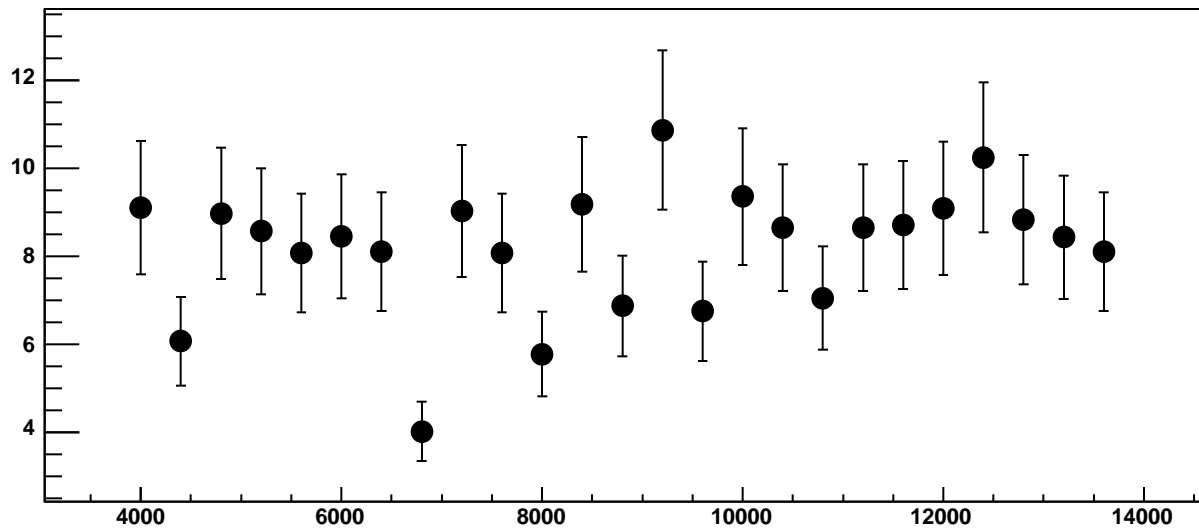
p0

672.1 ± 1.541

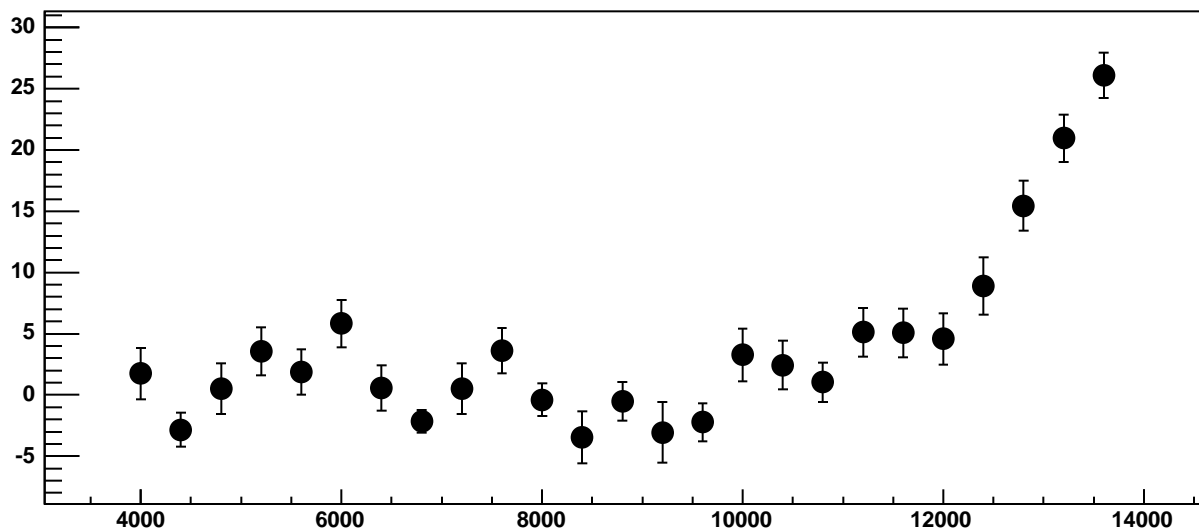
p1

0.02201 ± 0.000203

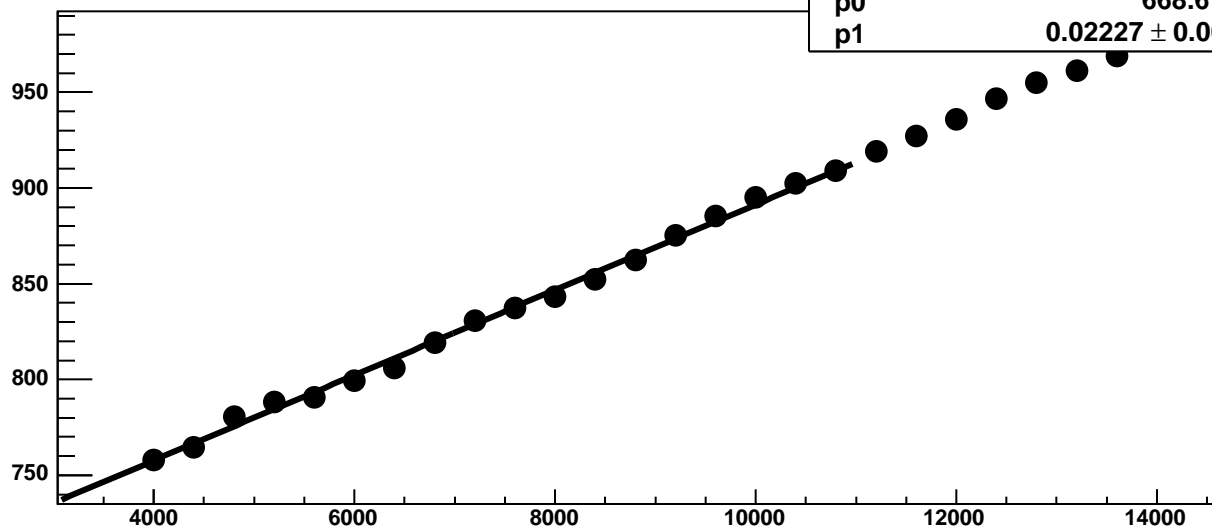
Chip 0, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

59.82 / 16

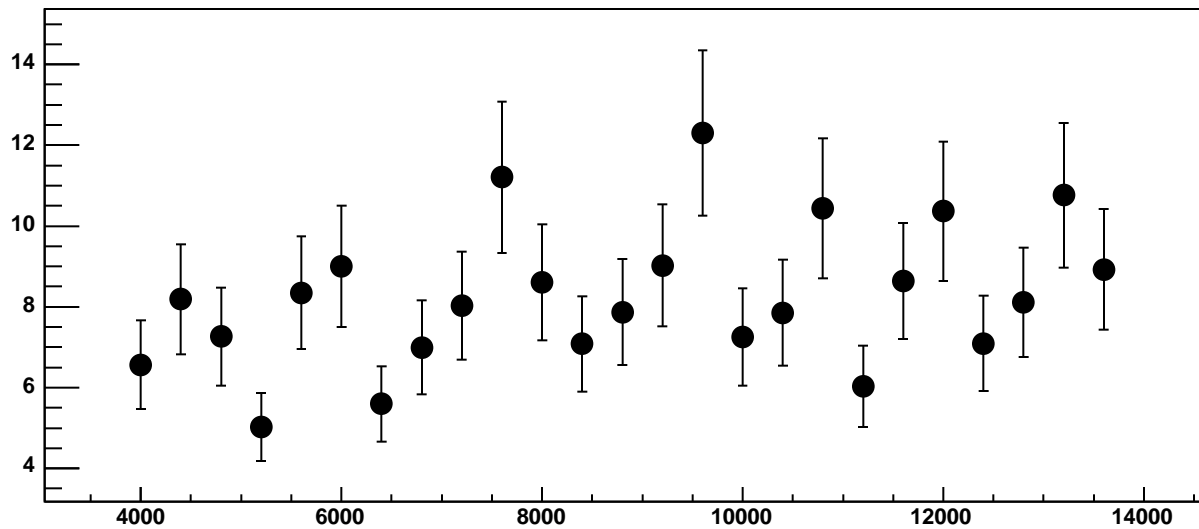
p0

668.6 ± 1.47

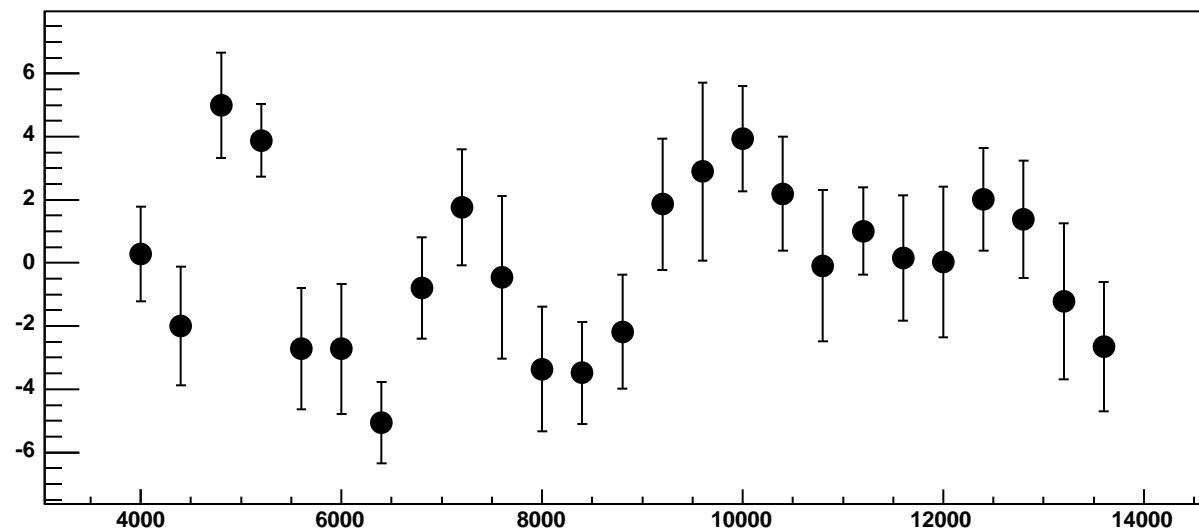
p1

0.02227 ± 0.0002023

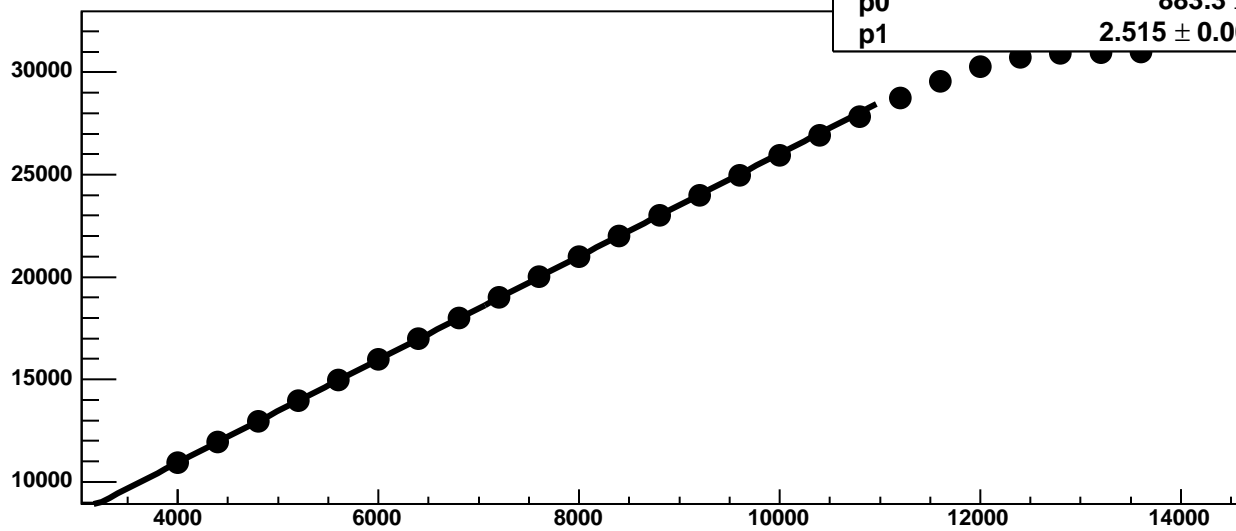
Chip 0, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



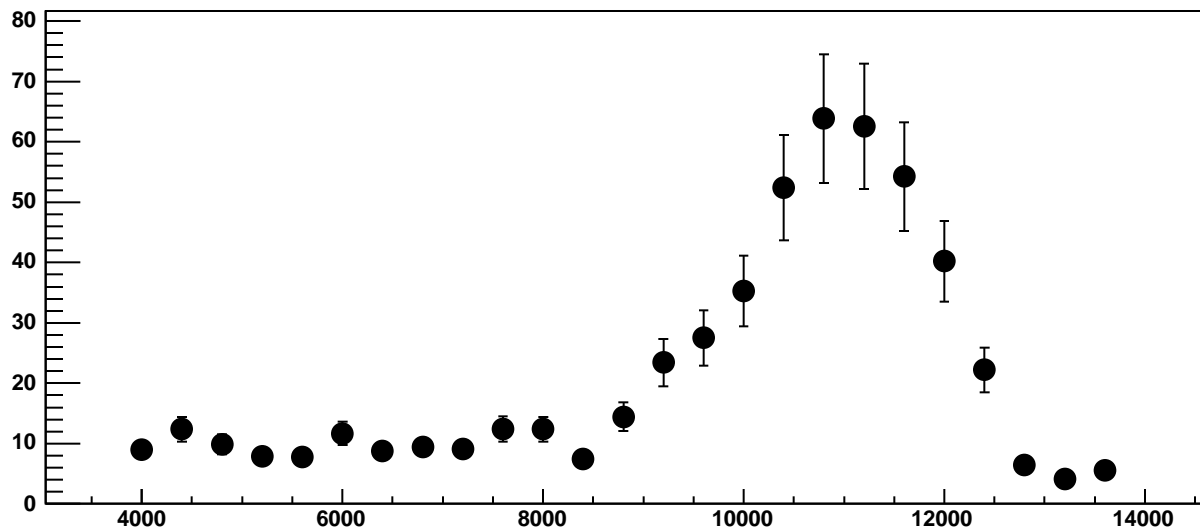
Chip 0, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



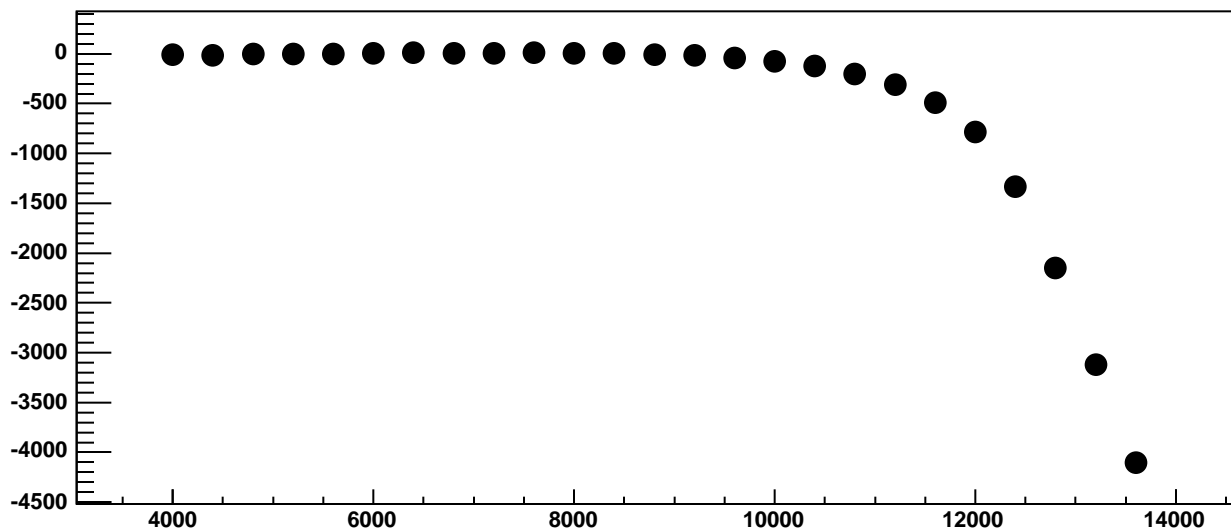
Chip 0, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



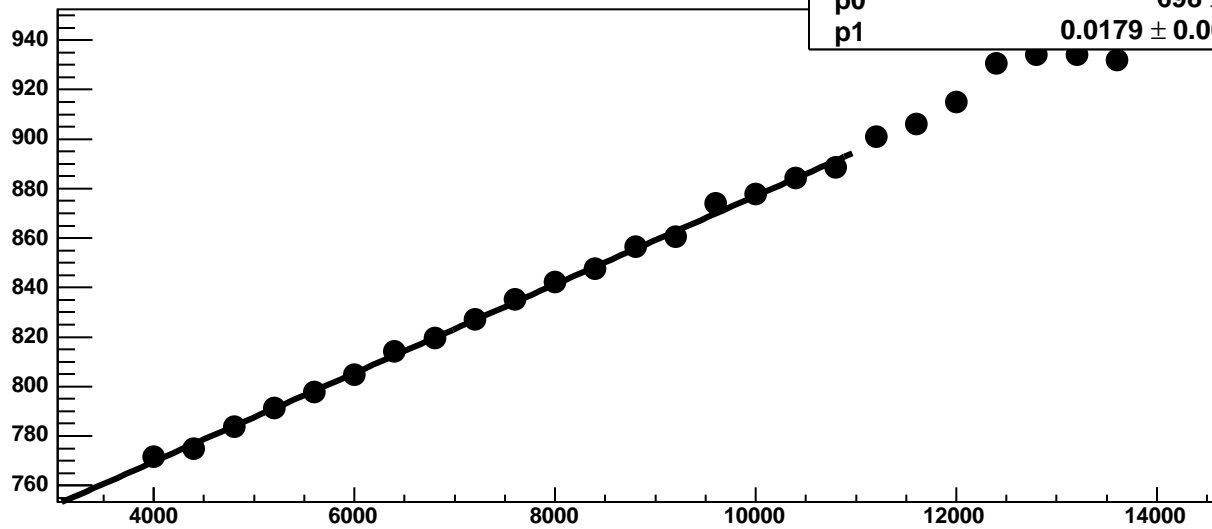
Chip 0, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



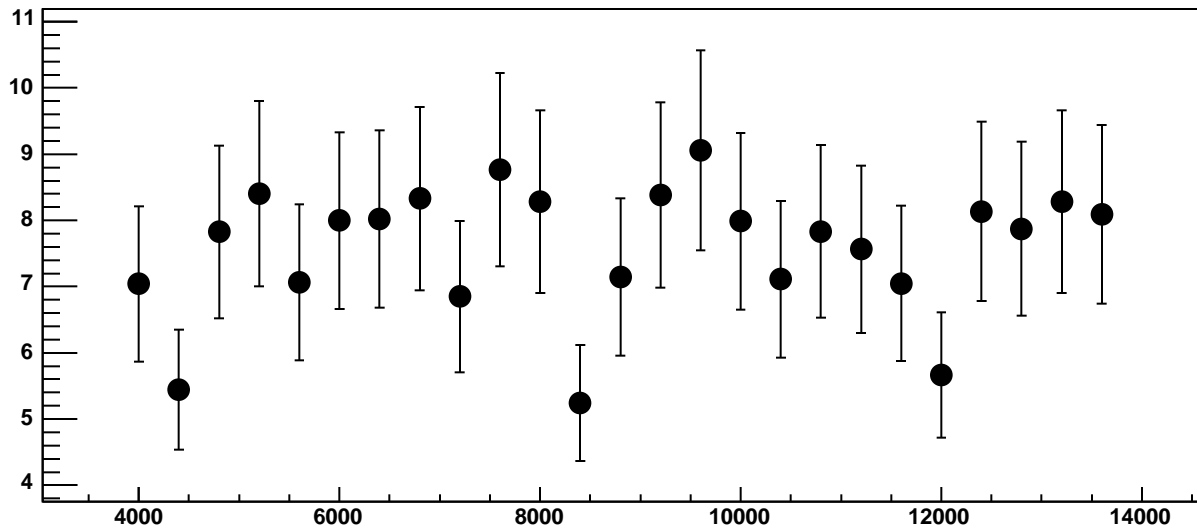
Chip 0, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC



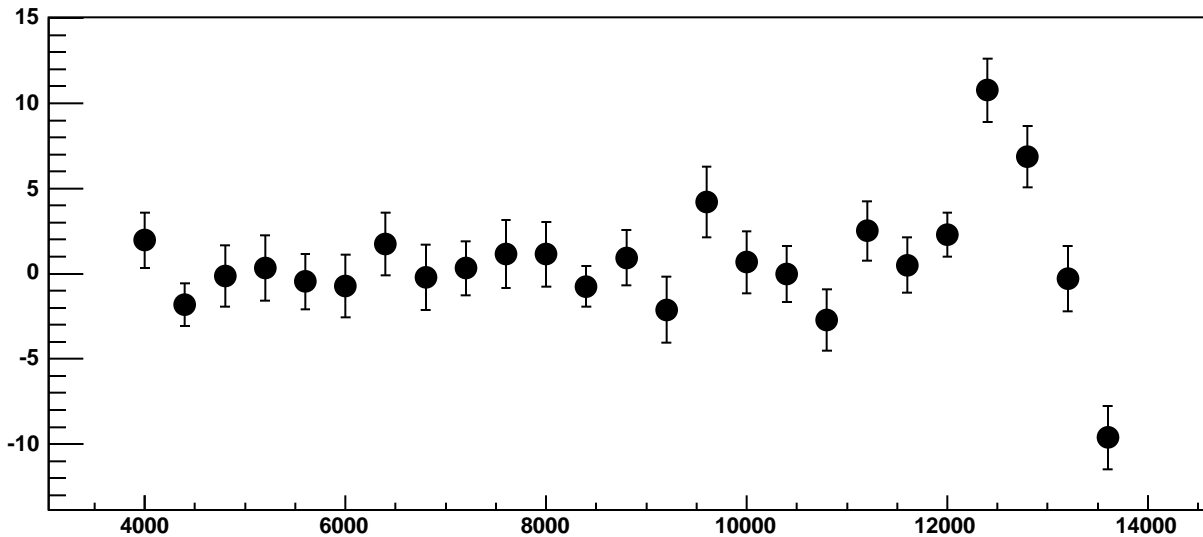
Chip 0, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



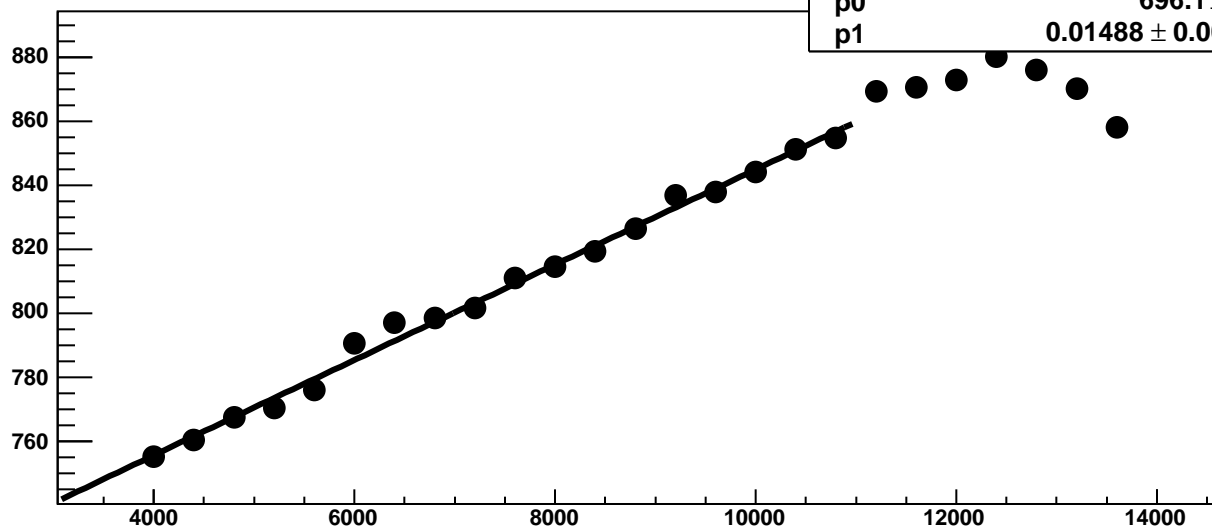
Chip 0, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



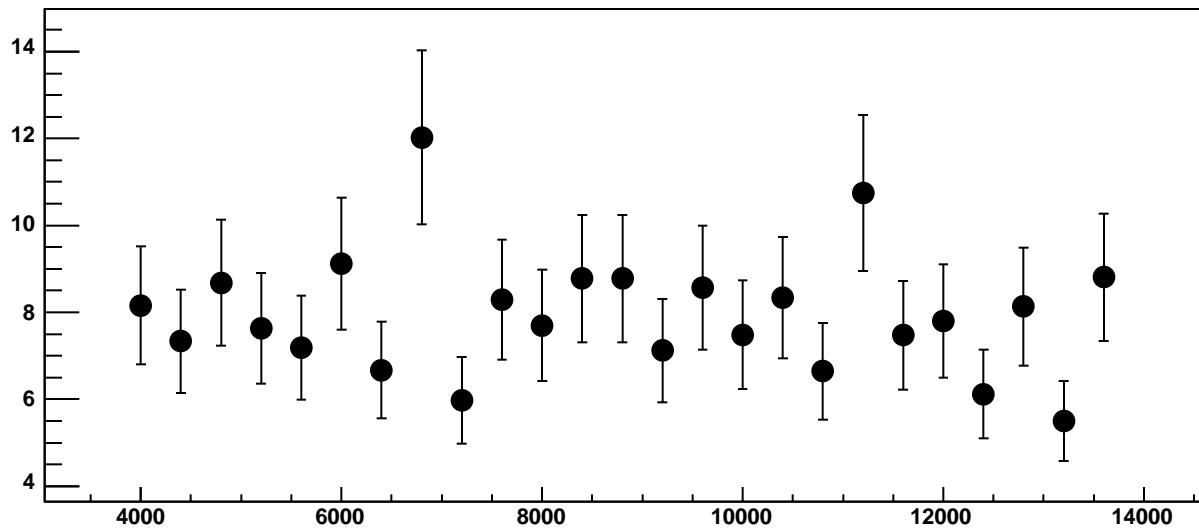
Chip 0, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



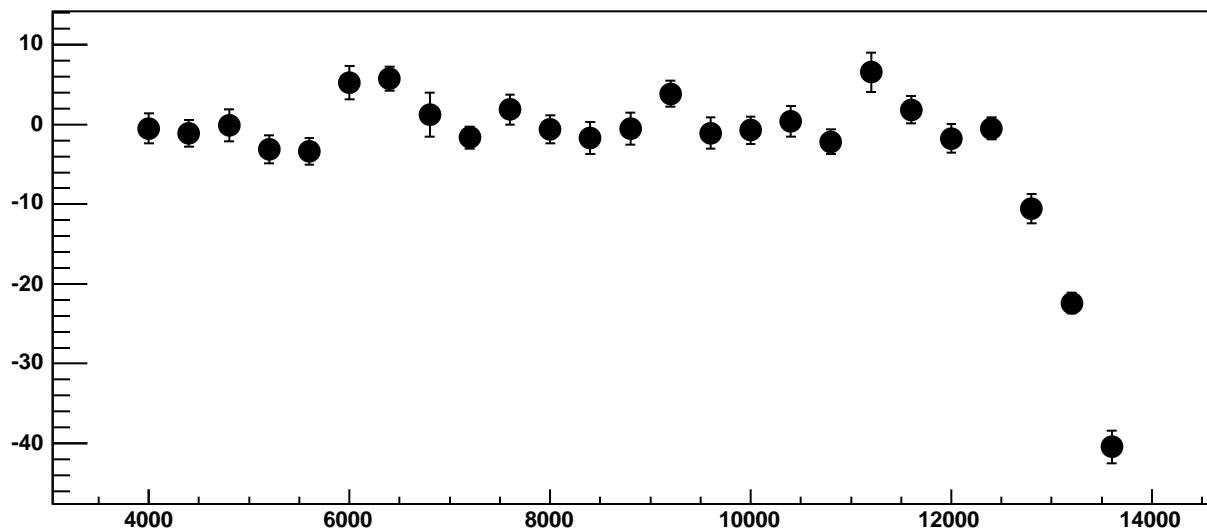
Chip 0, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



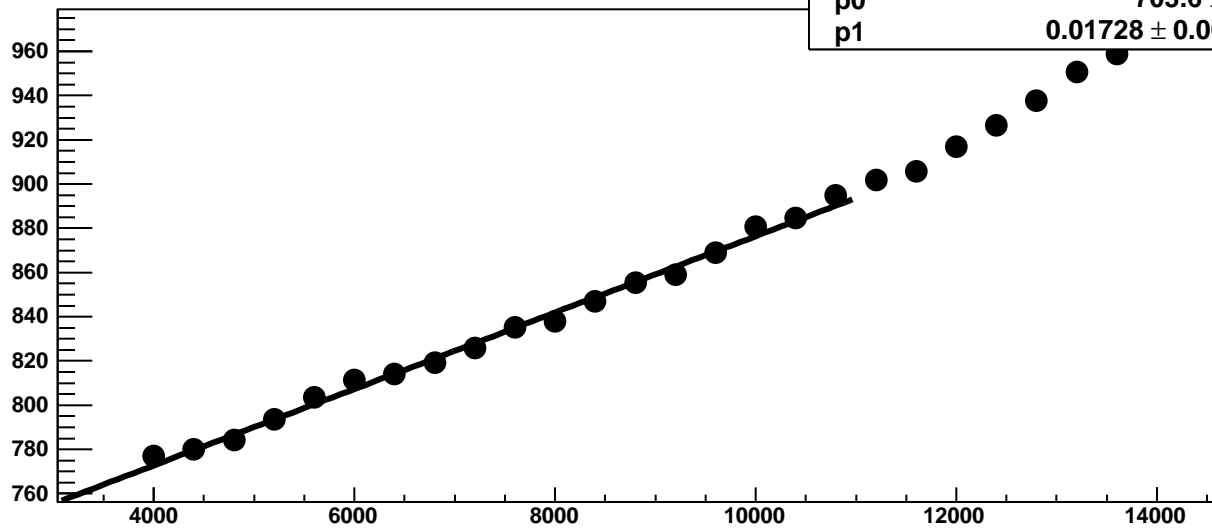
Chip 0, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

33.4 / 16

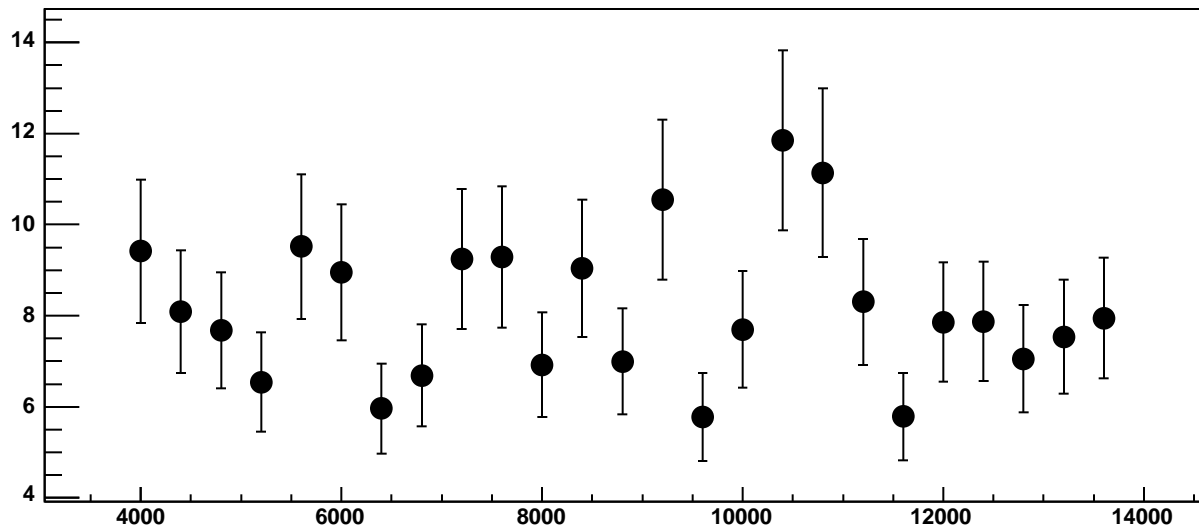
p0

703.6 ± 1.655

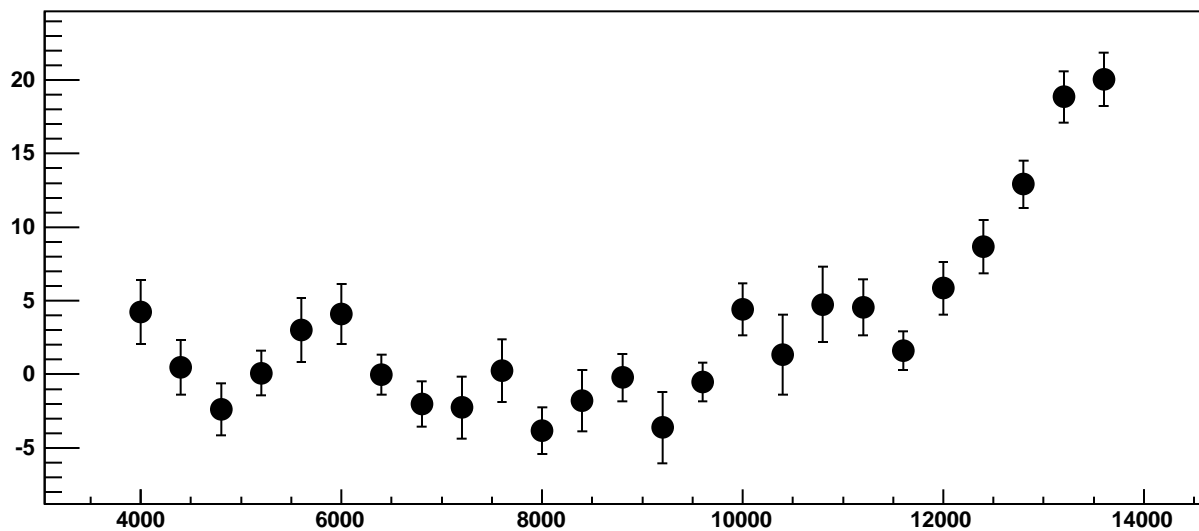
p1

0.01728 ± 0.0002192

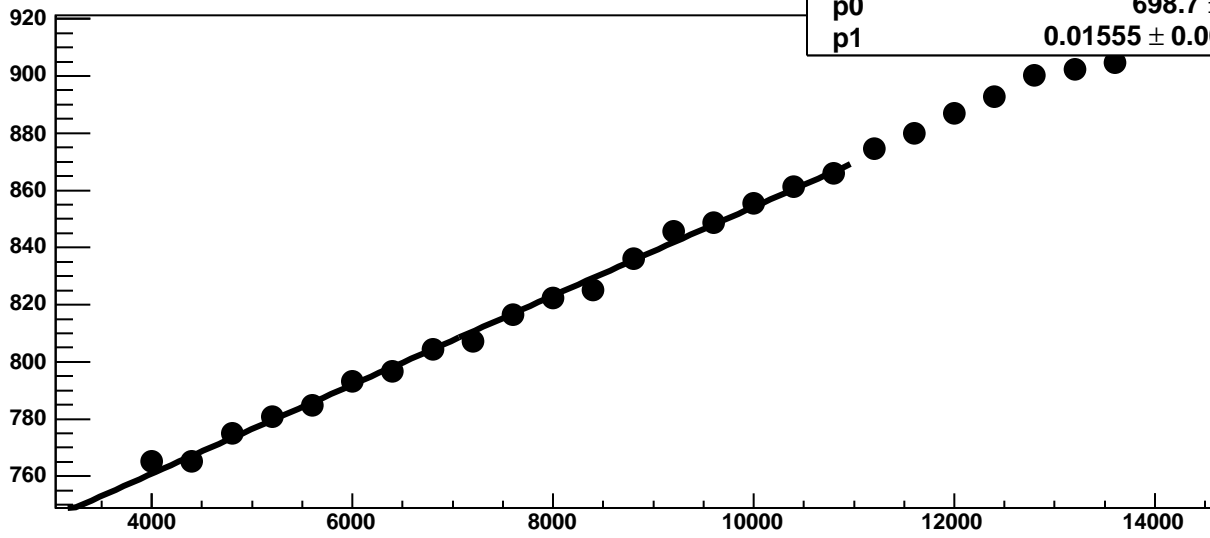
Chip 0, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

26.04 / 16

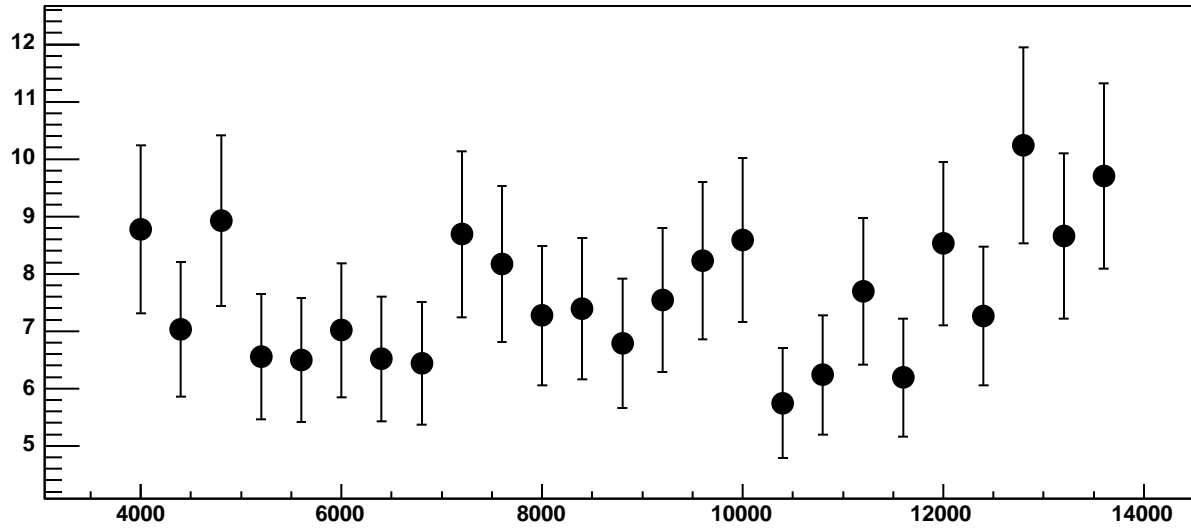
p0

698.7 ± 1.446

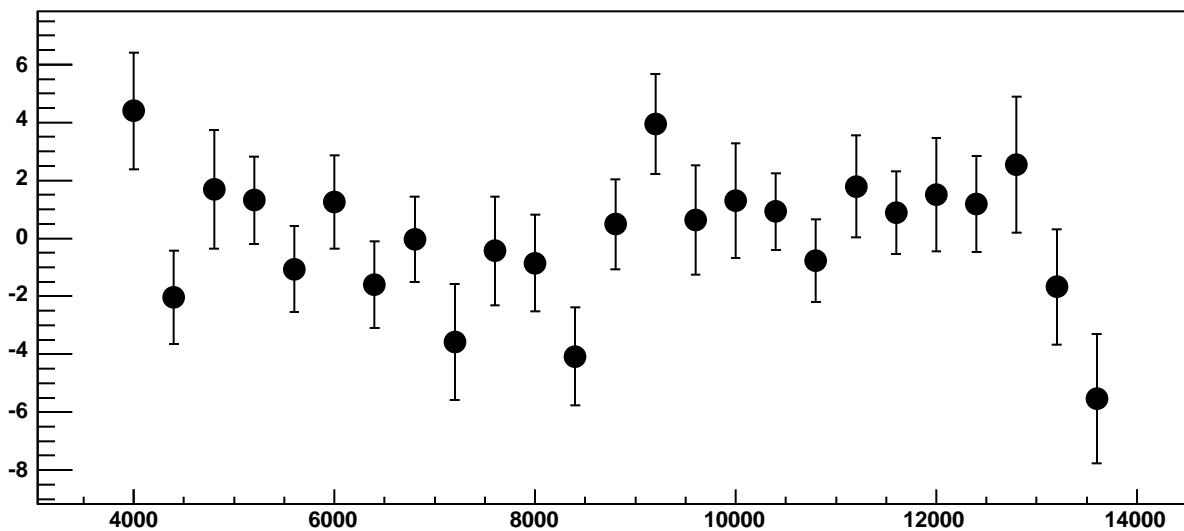
p1

0.01555 ± 0.0001853

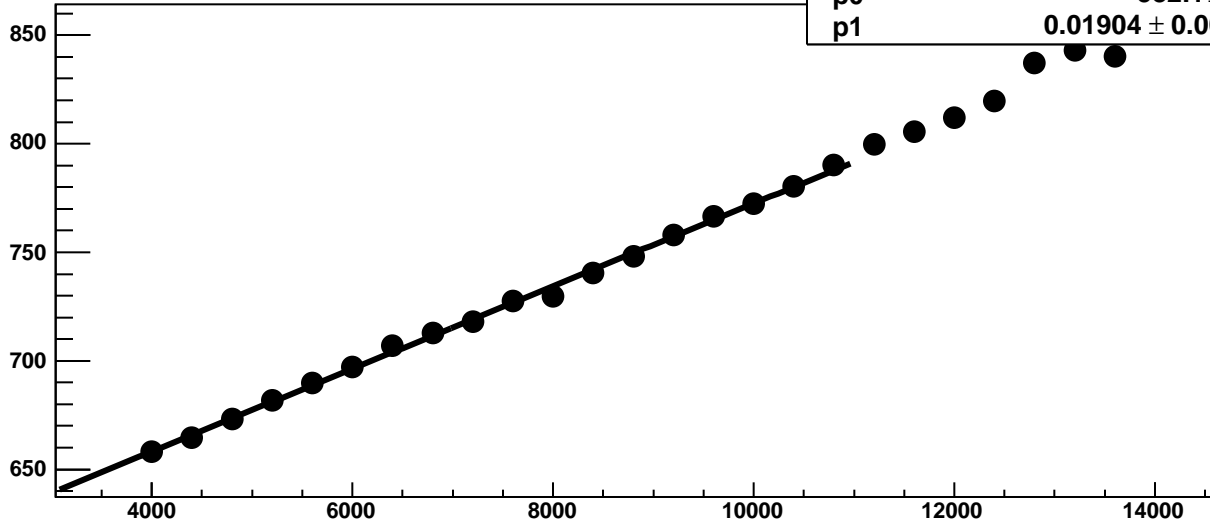
Chip 0, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

24.02 / 16

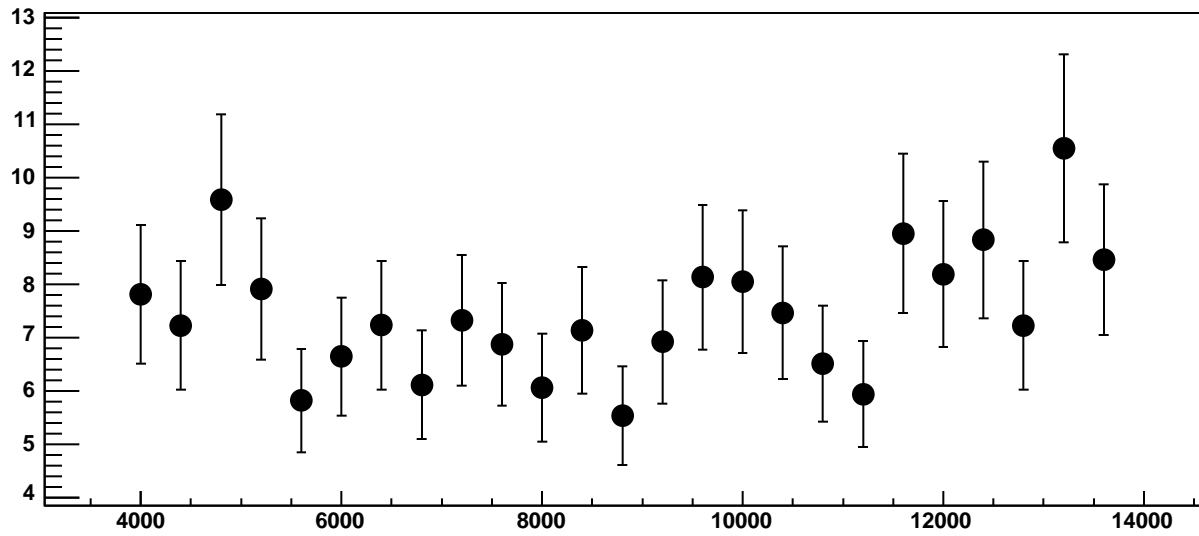
p0

582.1 ± 1.473

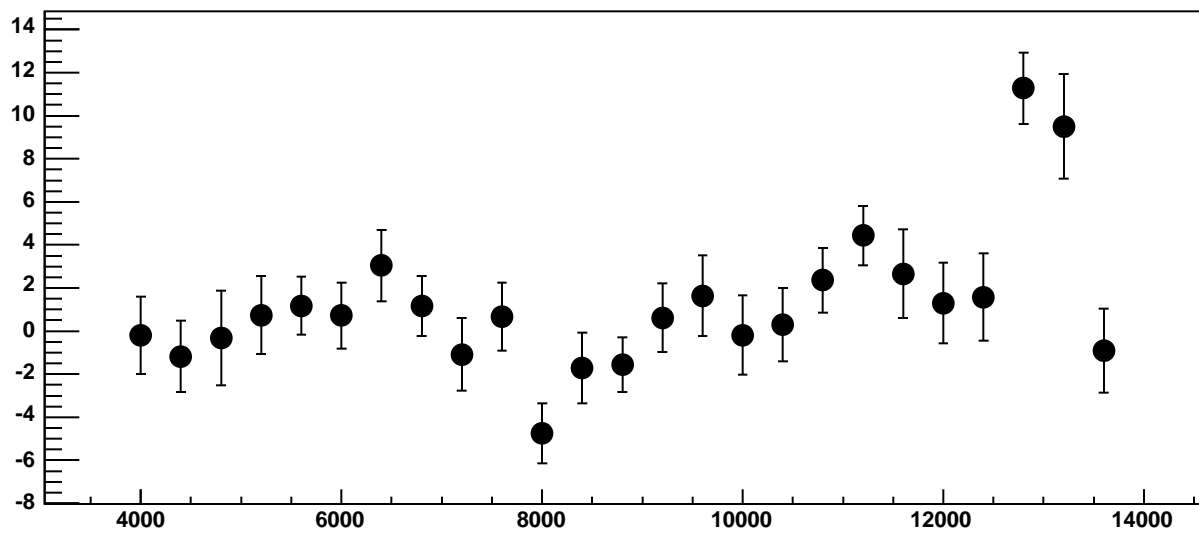
p1

0.01904 ± 0.0001903

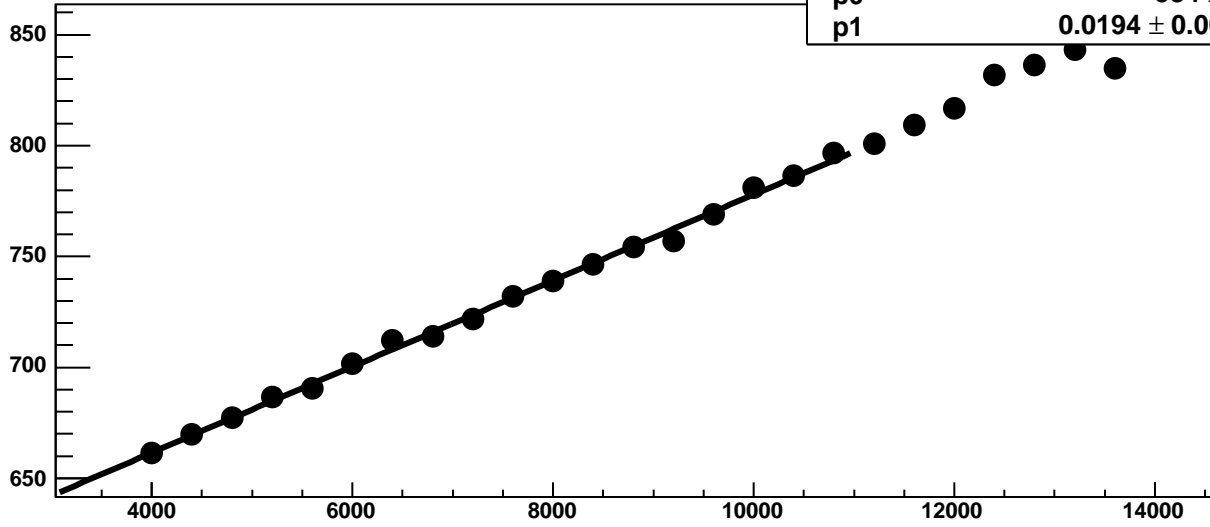
Chip 0, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

31.33 / 16

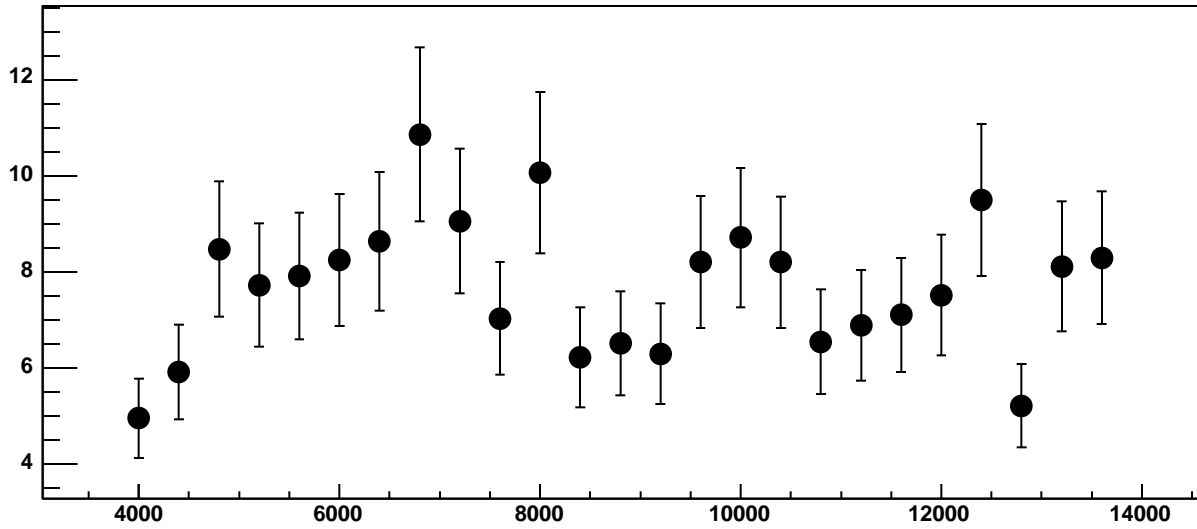
p0

584 ± 1.325

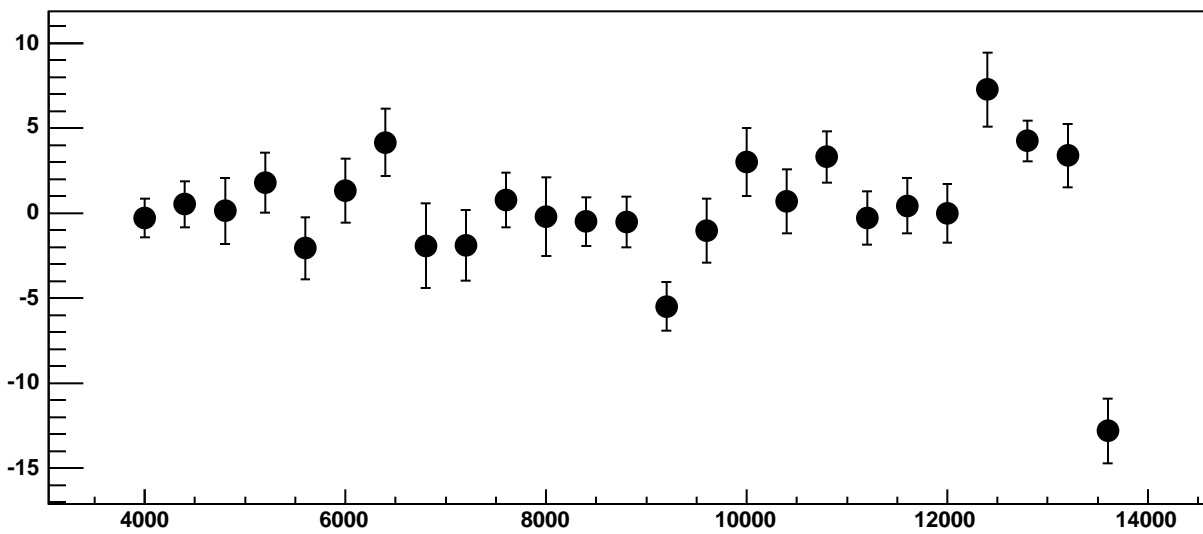
p1

0.0194 ± 0.0001753

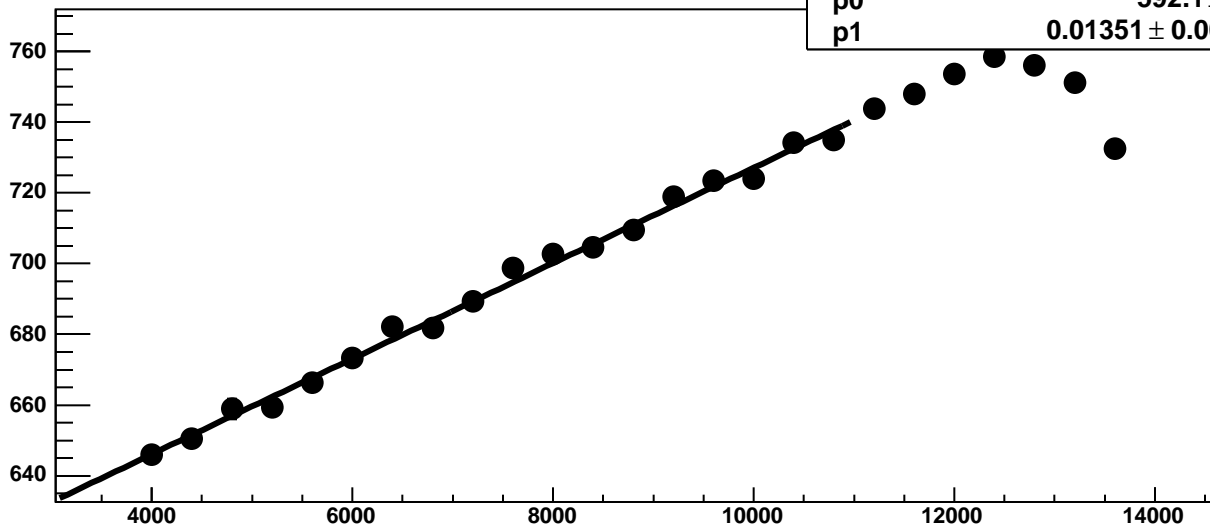
Chip 0, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



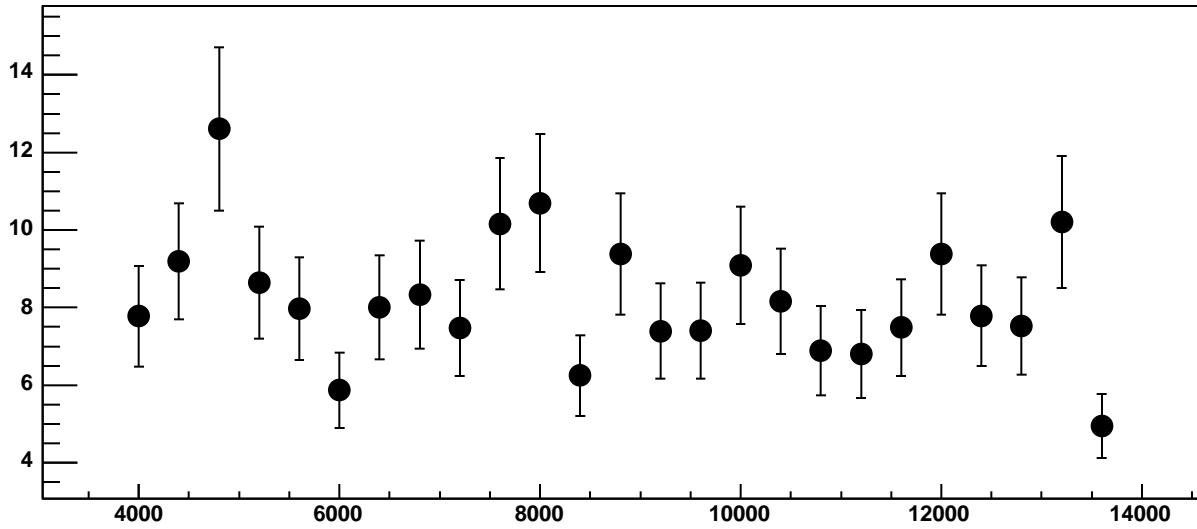
Chip 0, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



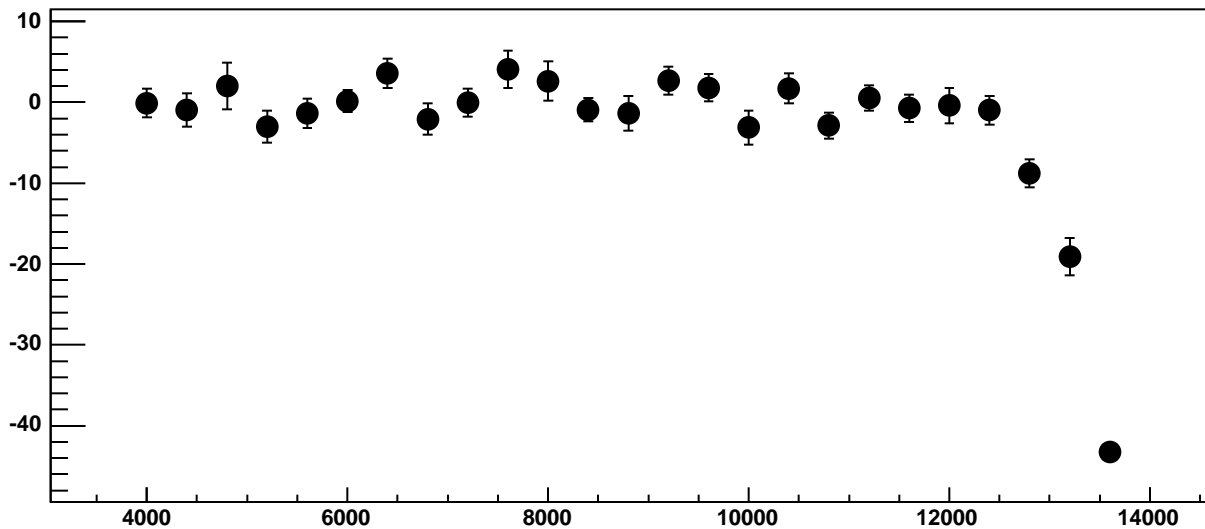
Chip 0, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



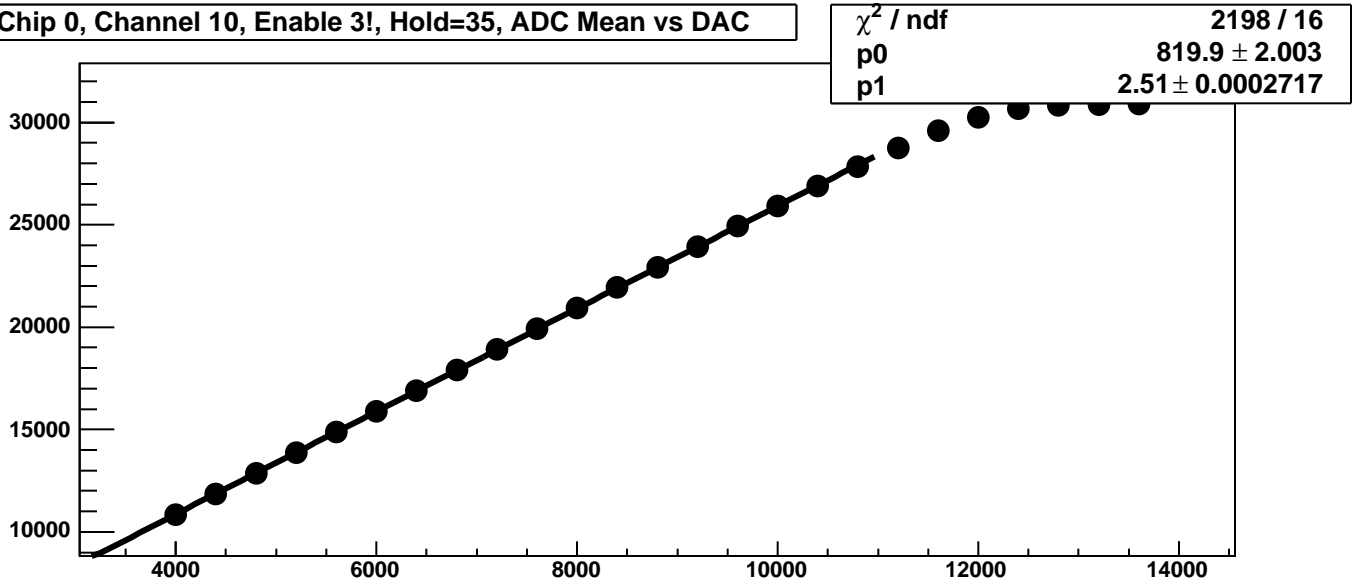
Chip 0, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



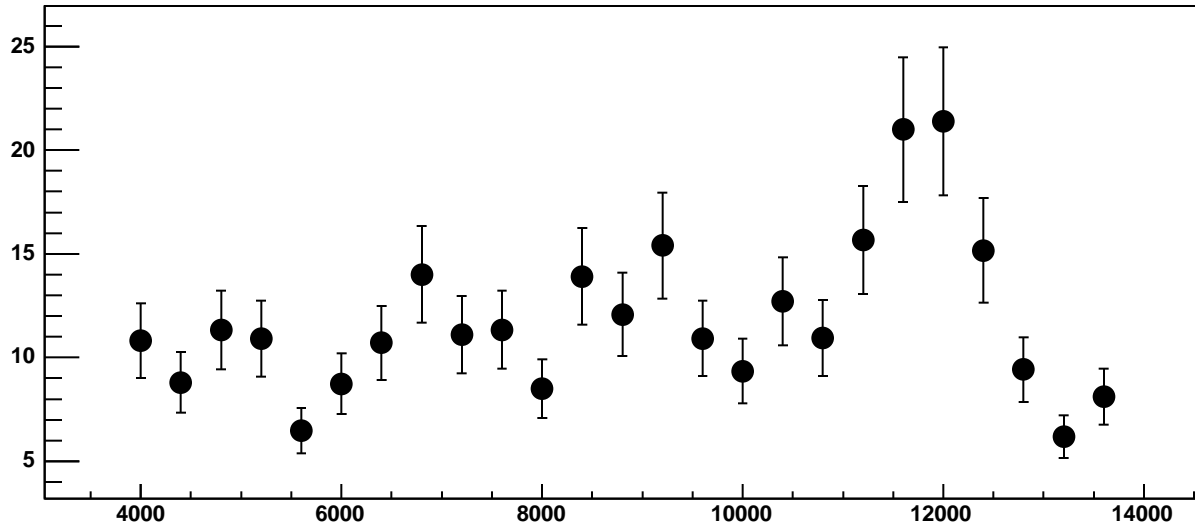
Chip 0, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



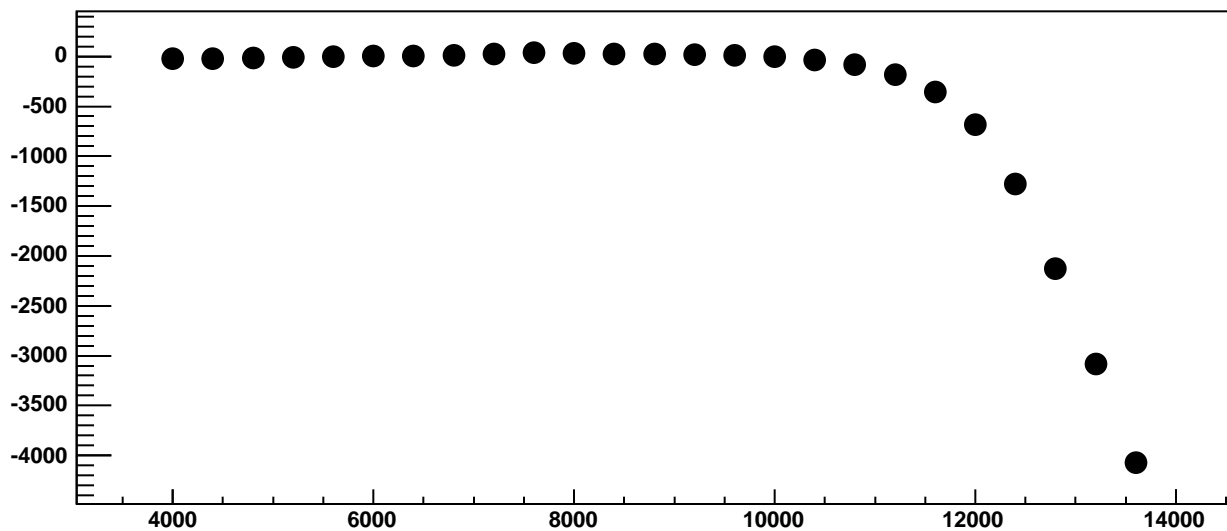
Chip 0, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC



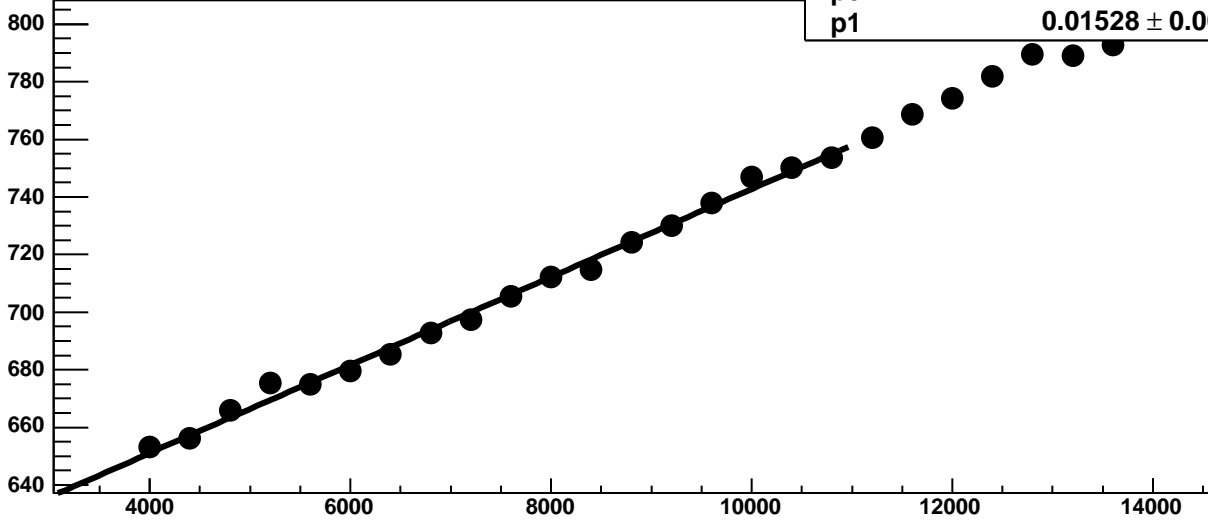
Chip 0, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



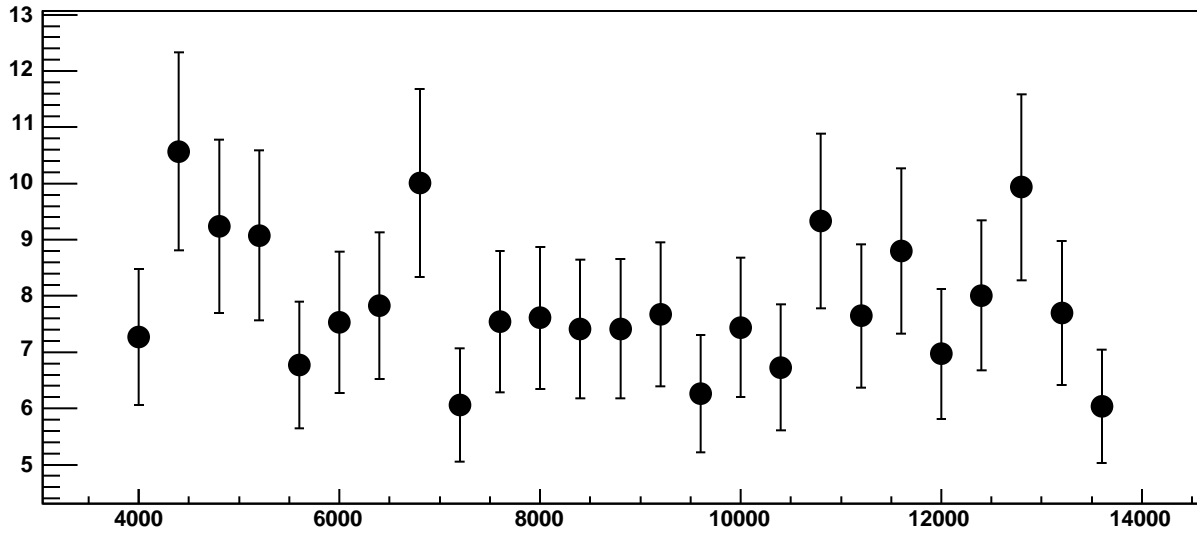
Chip 0, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC



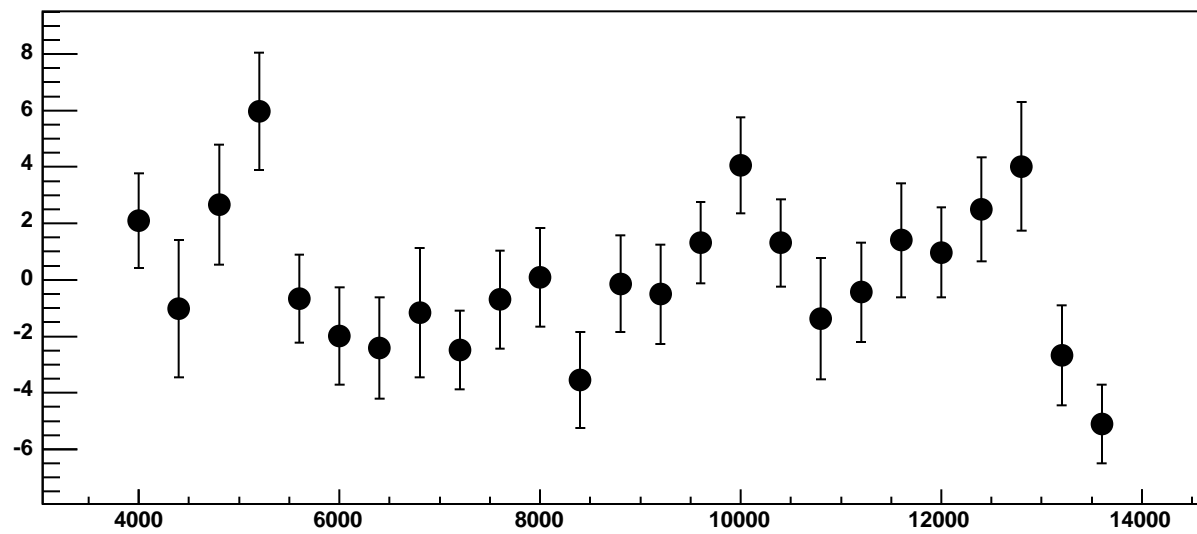
Chip 0, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



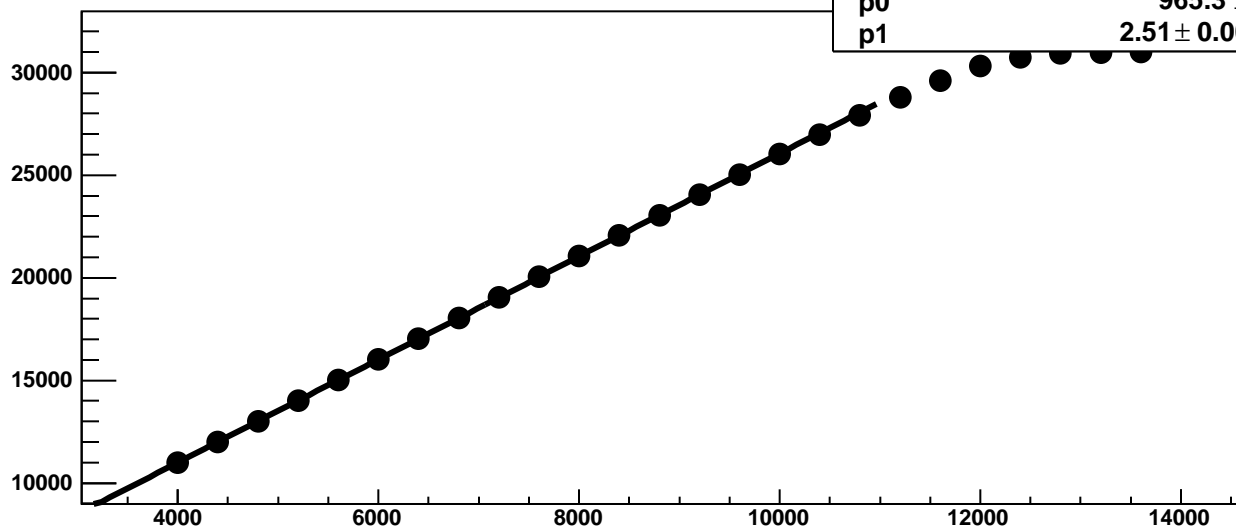
Chip 0, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

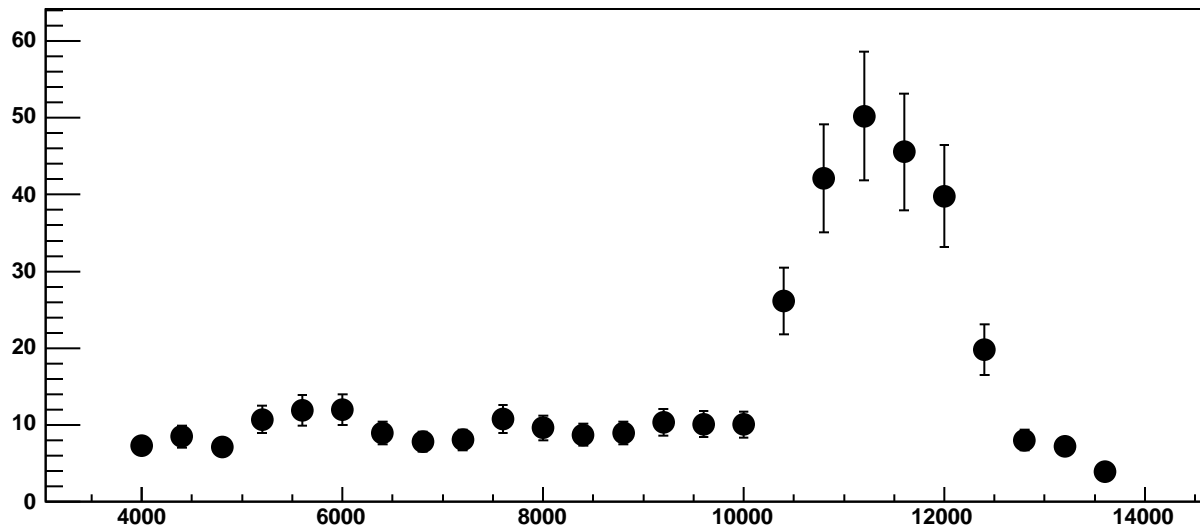


Chip 0, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC

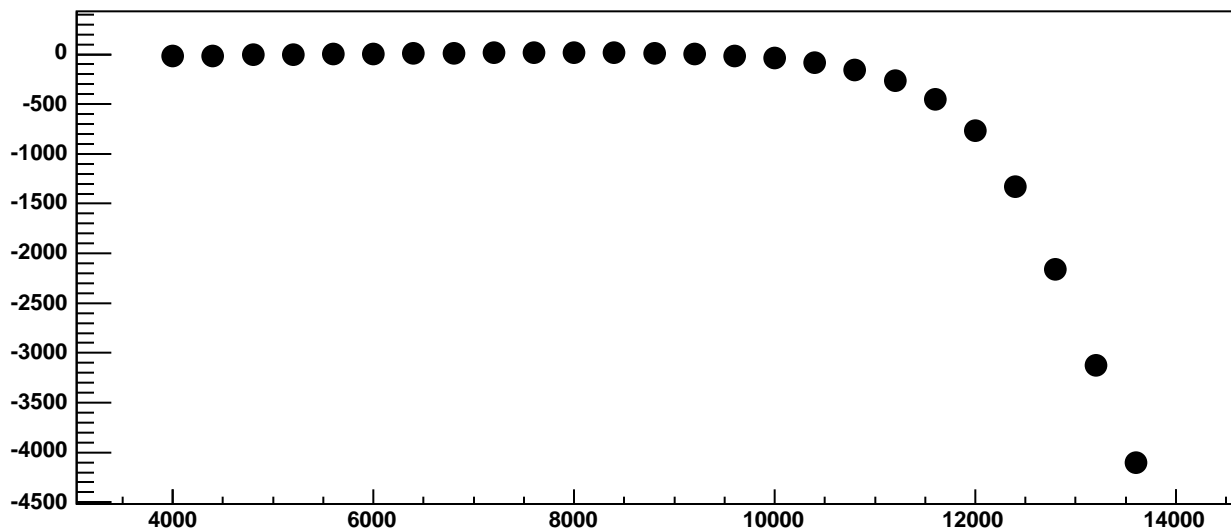


χ^2 / ndf 1266 / 16
p0 965.3 ± 1.935
p1 2.51 ± 0.0002732

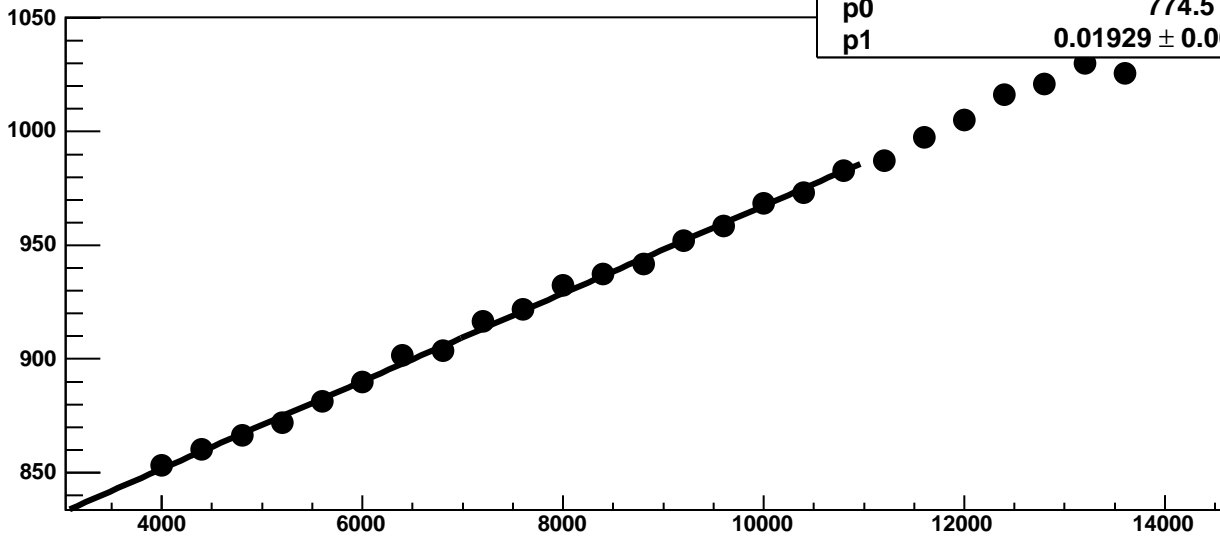
Chip 0, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC

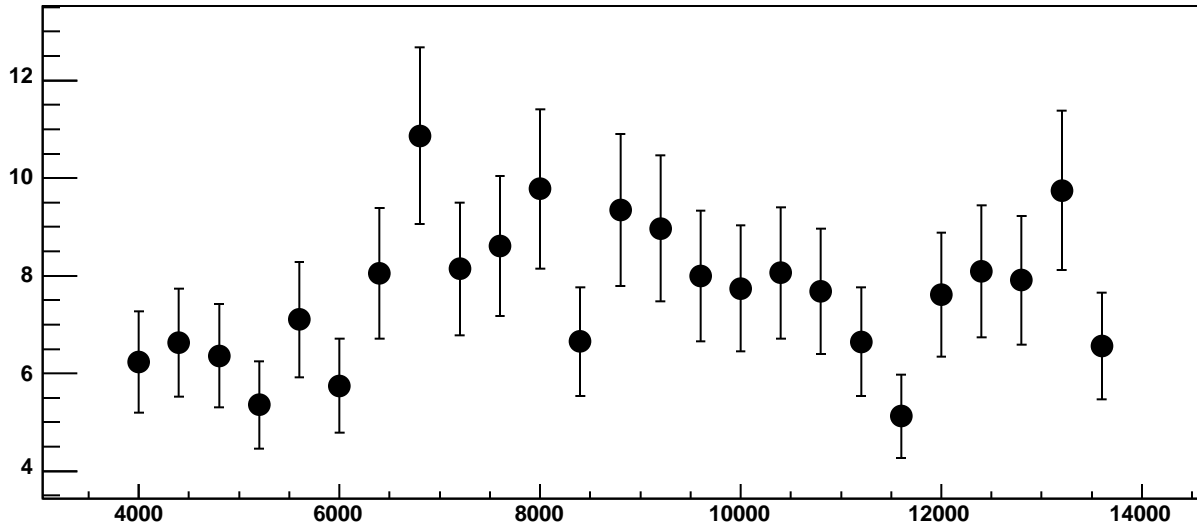


Chip 0, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

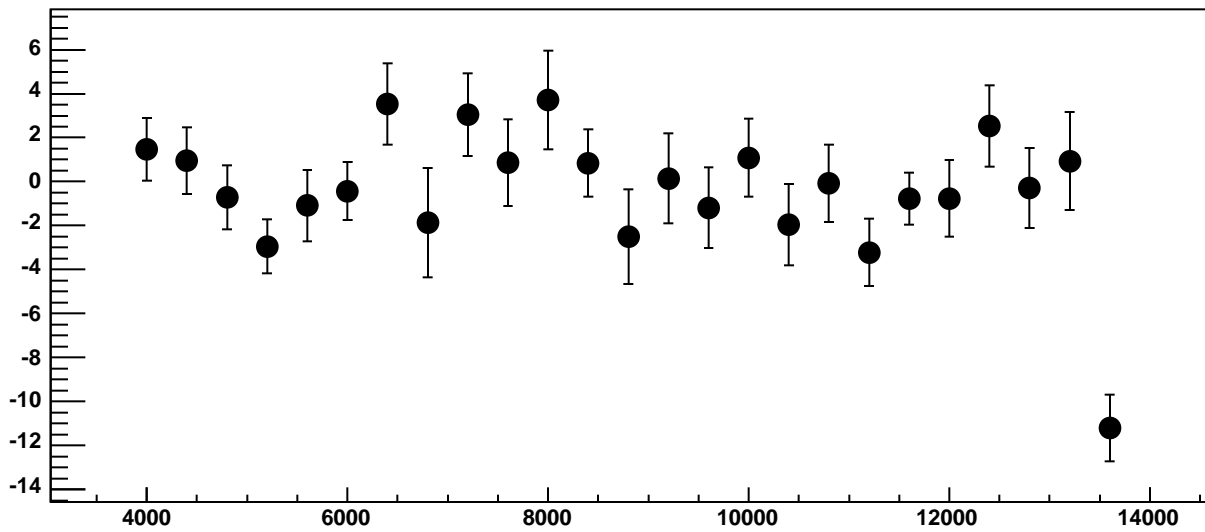


χ^2 / ndf 21.4 / 16
p0 774.5 ± 1.351
p1 0.01929 ± 0.0001856

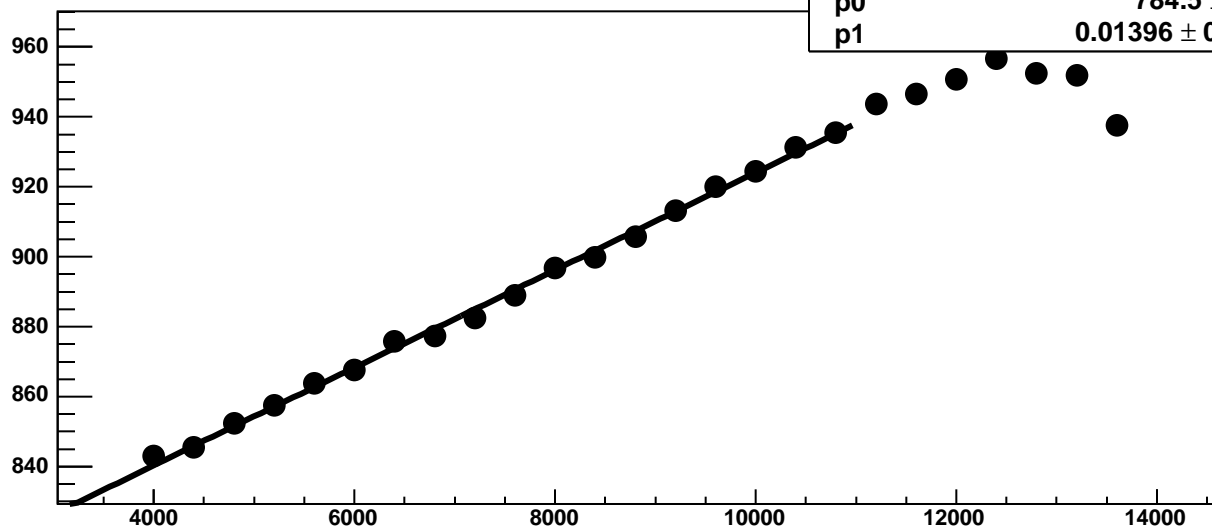
Chip 0, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



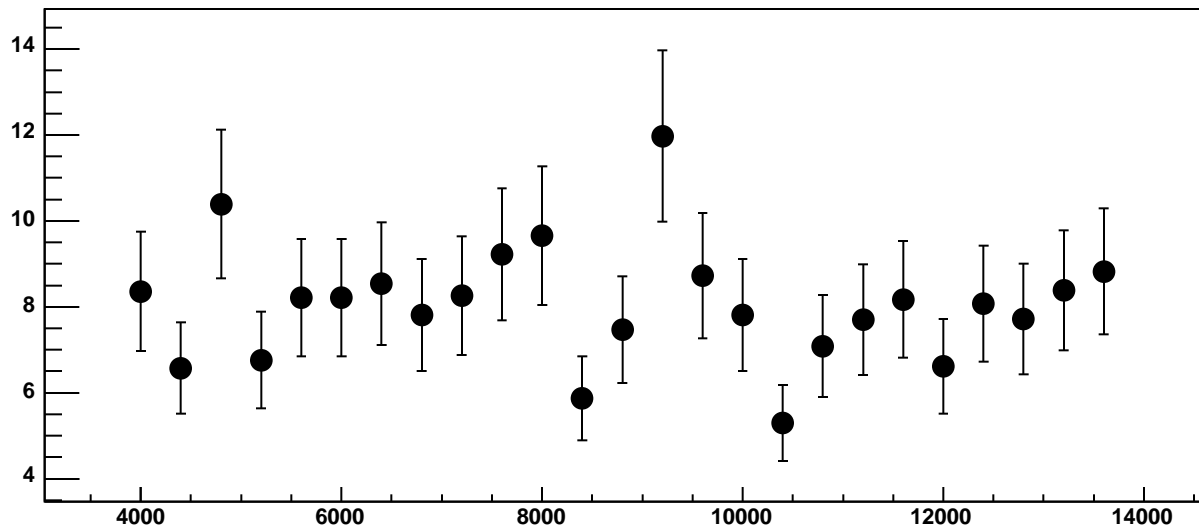
Chip 0, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



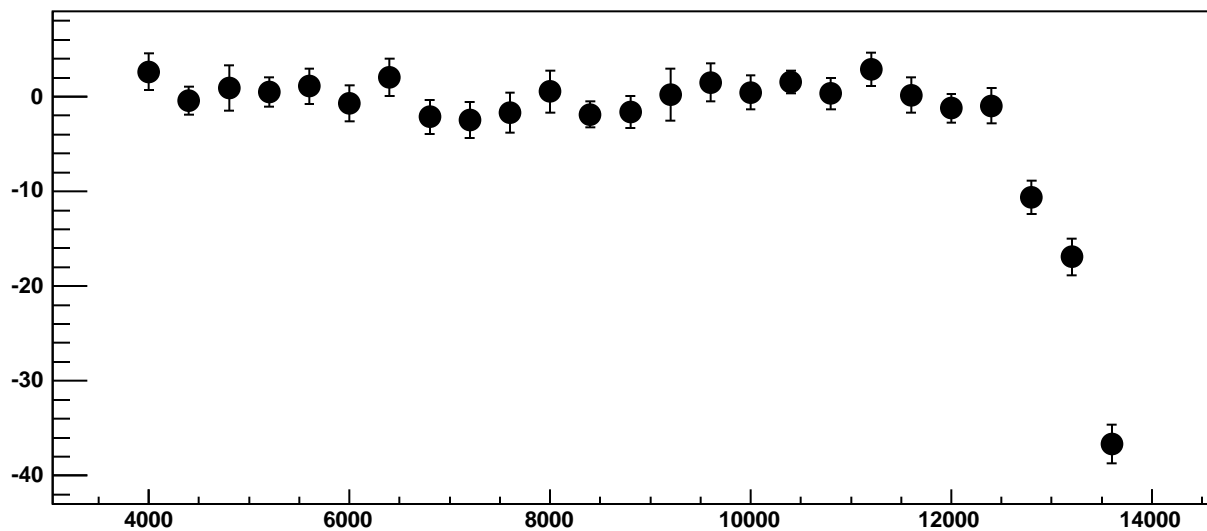
Chip 0, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



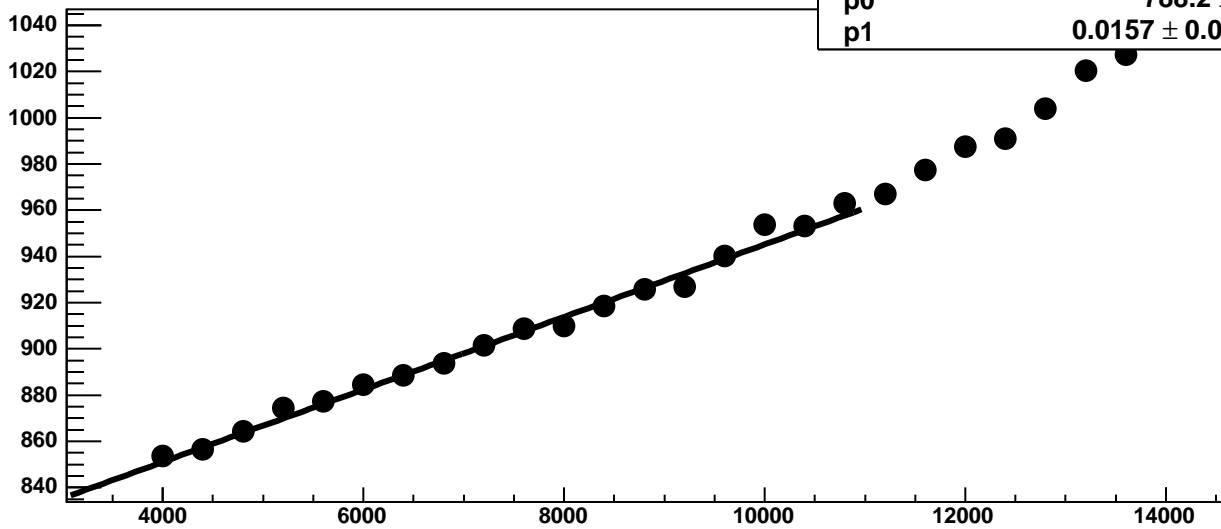
Chip 0, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



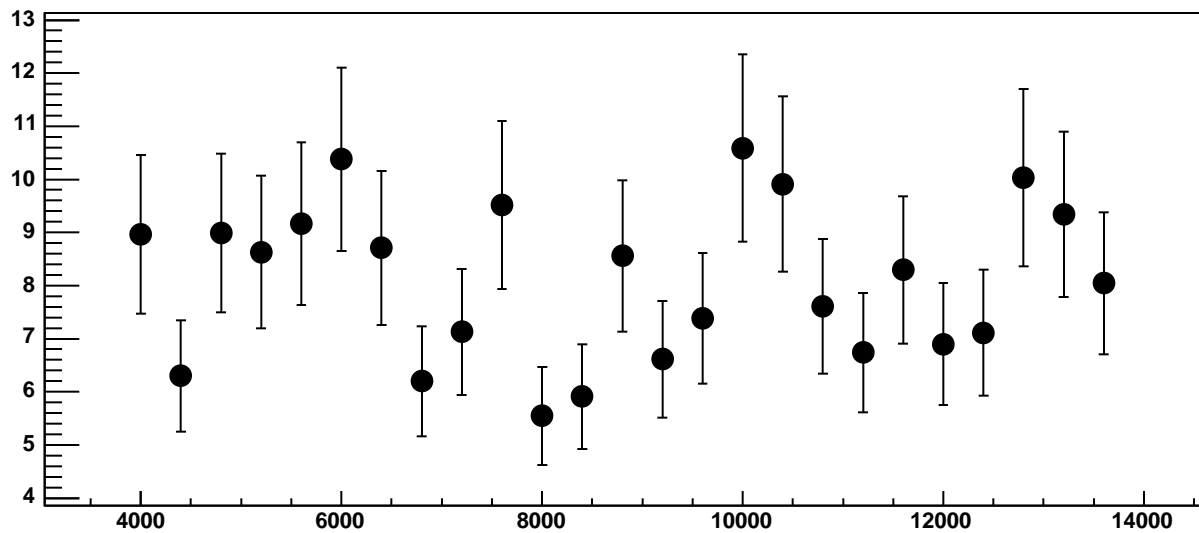
Chip 0, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



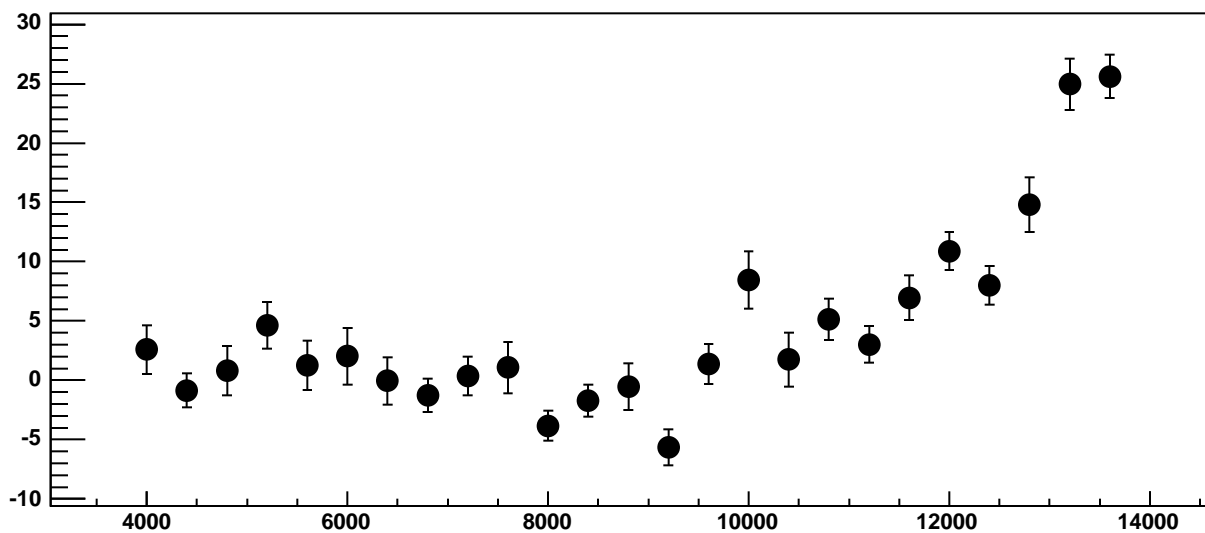
Chip 0, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC



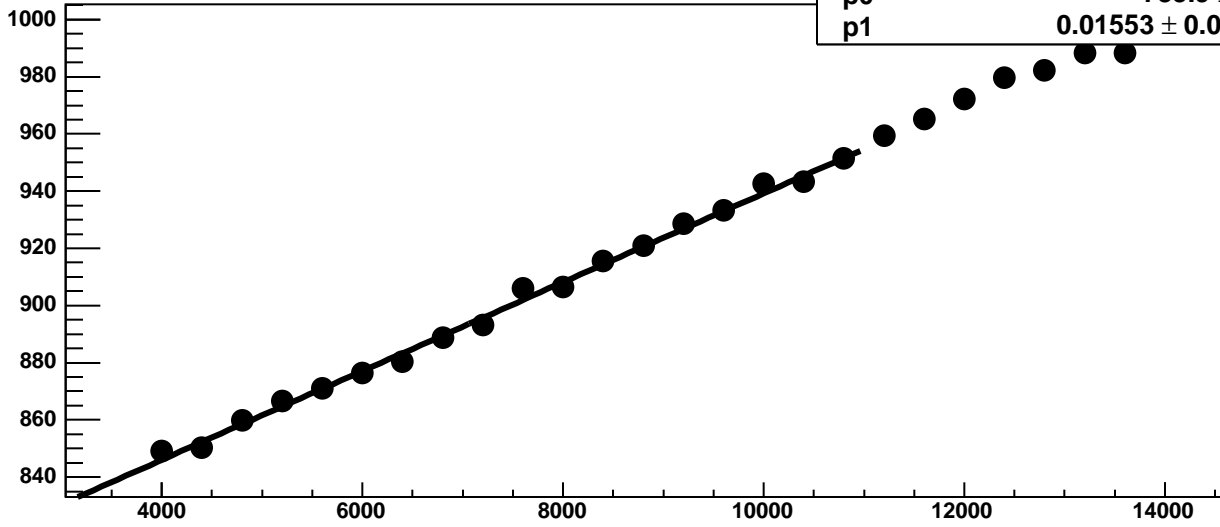
Chip 0, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

21.67 / 16

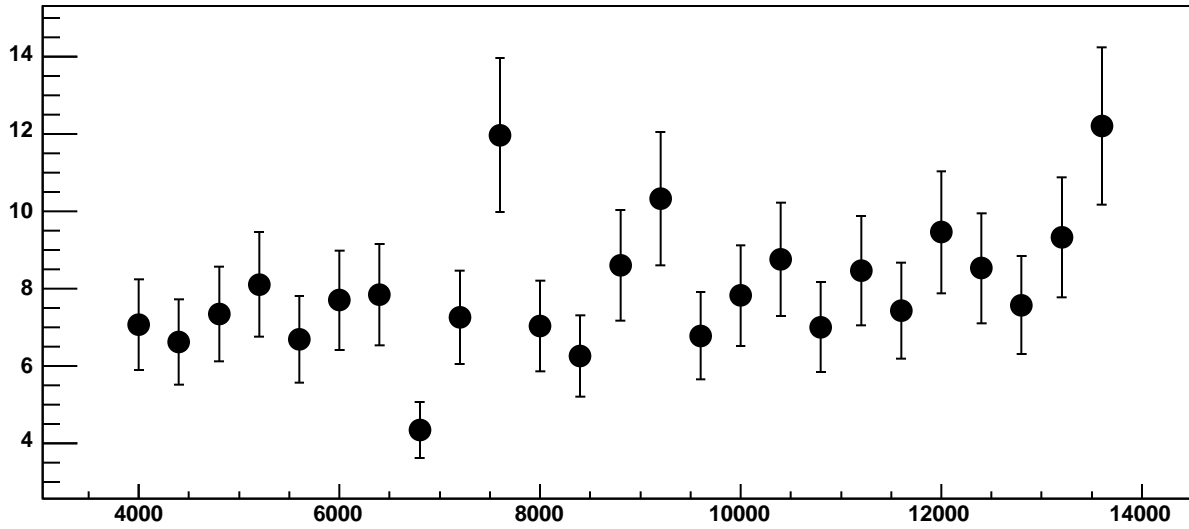
p0

783.9 ± 1.442

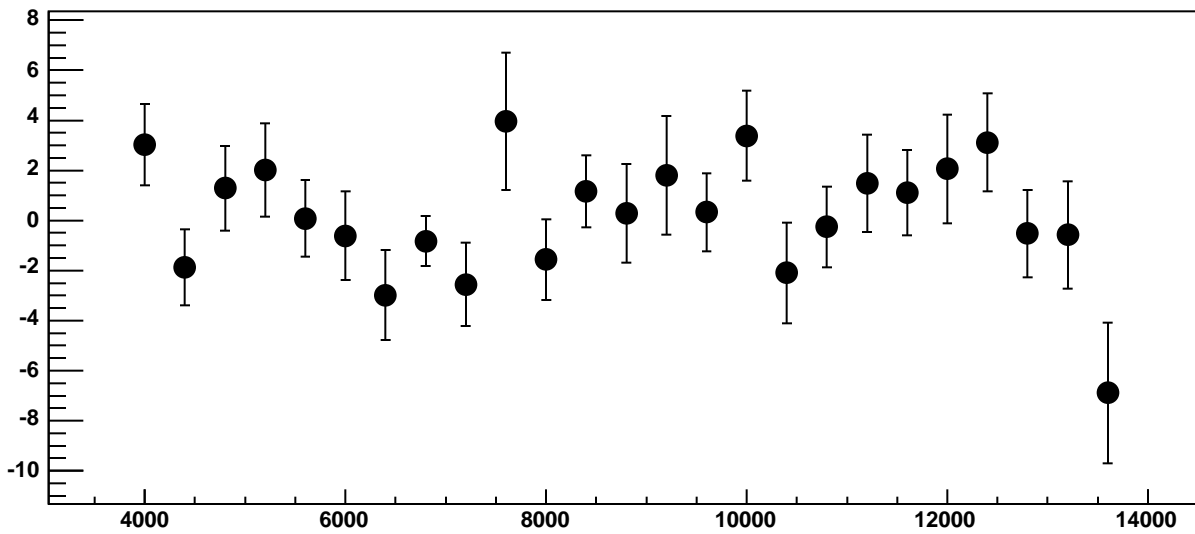
p1

0.01553 ± 0.0001921

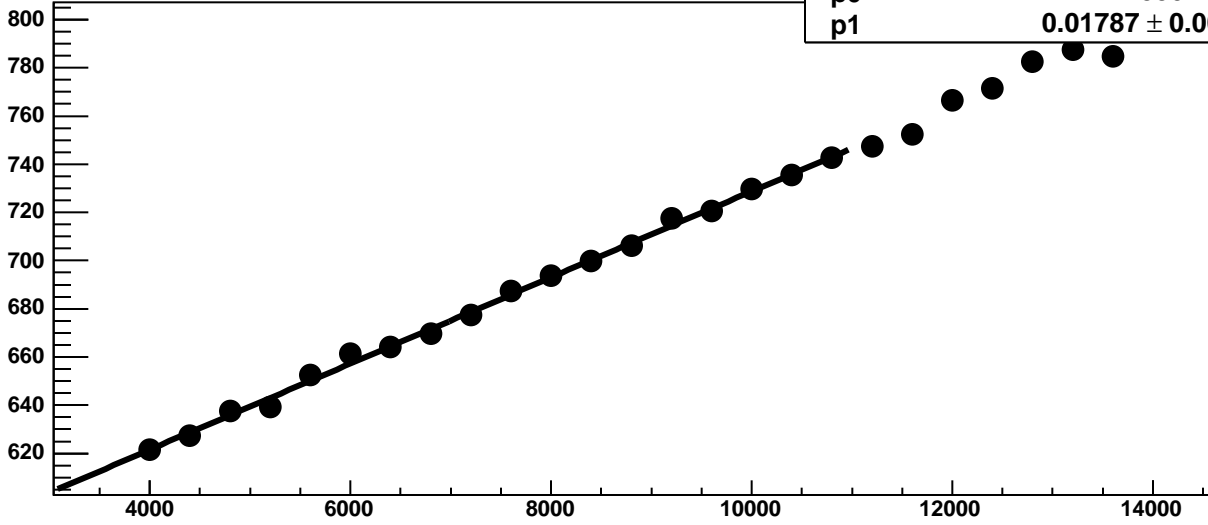
Chip 0, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

18.36 / 16

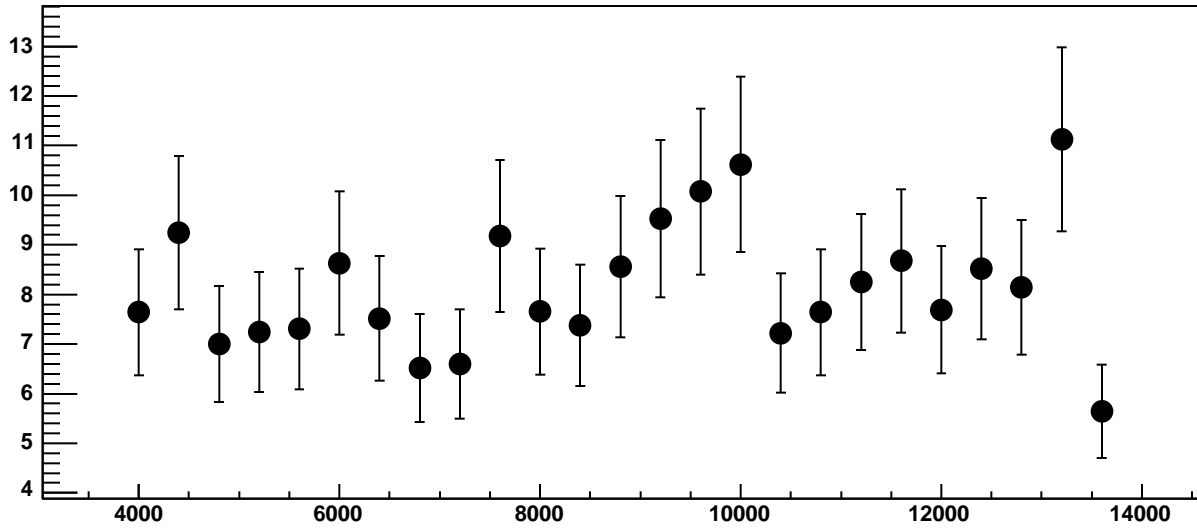
p0

550.1 ± 1.571

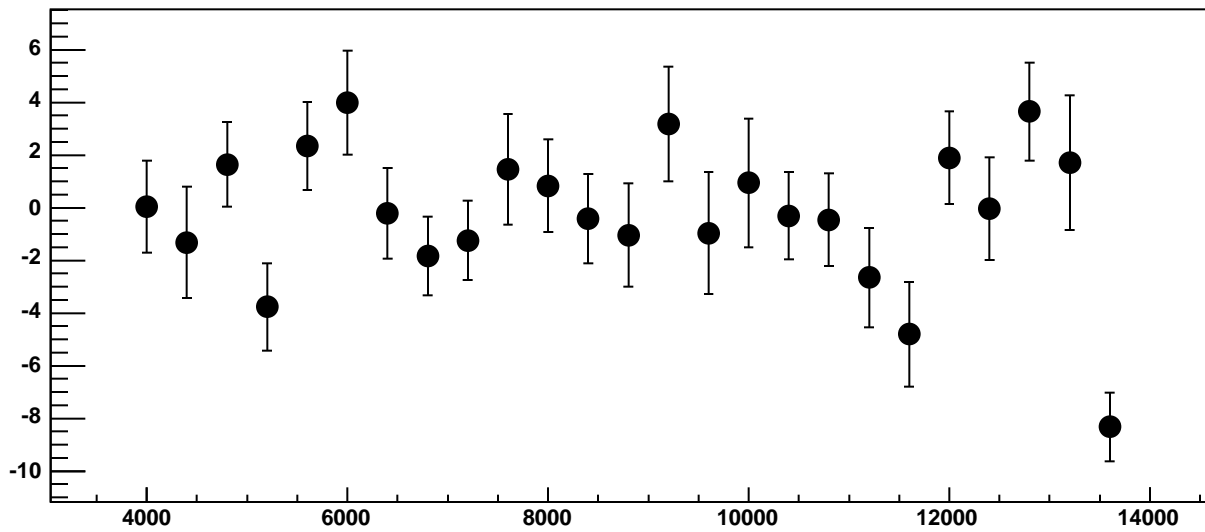
p1

0.01787 ± 0.0002089

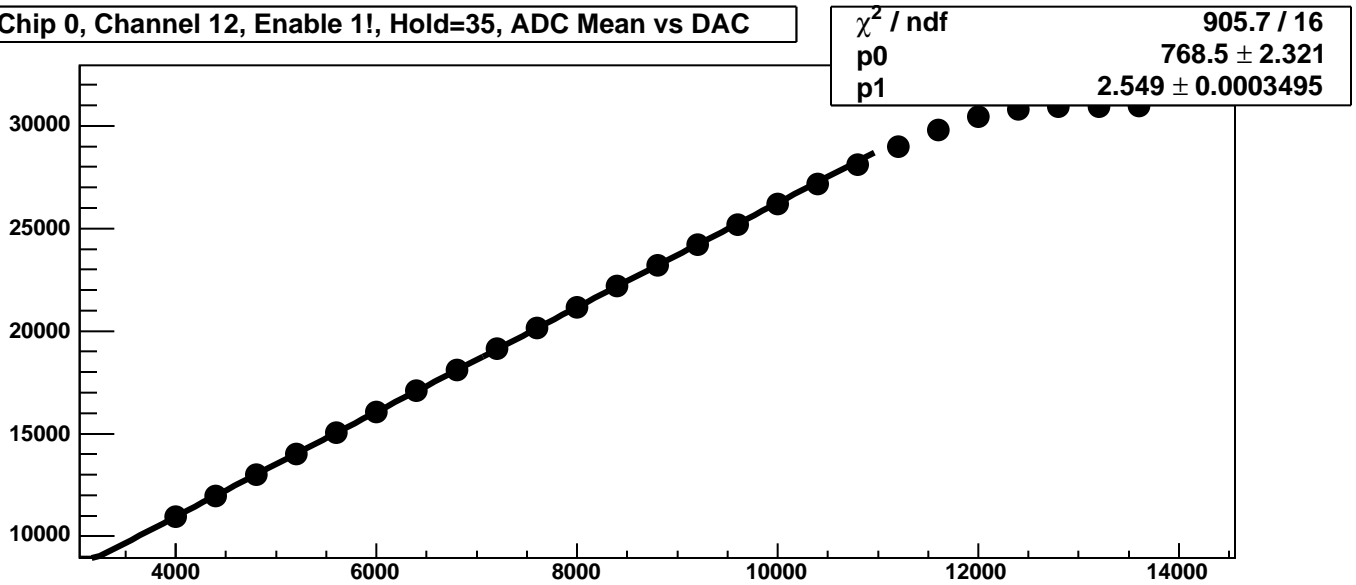
Chip 0, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



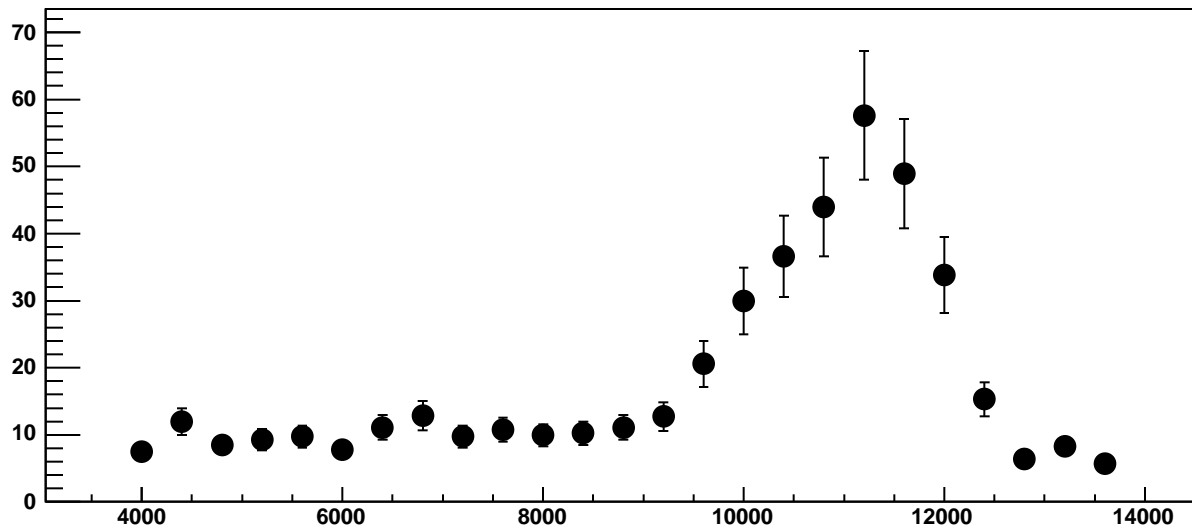
Chip 0, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



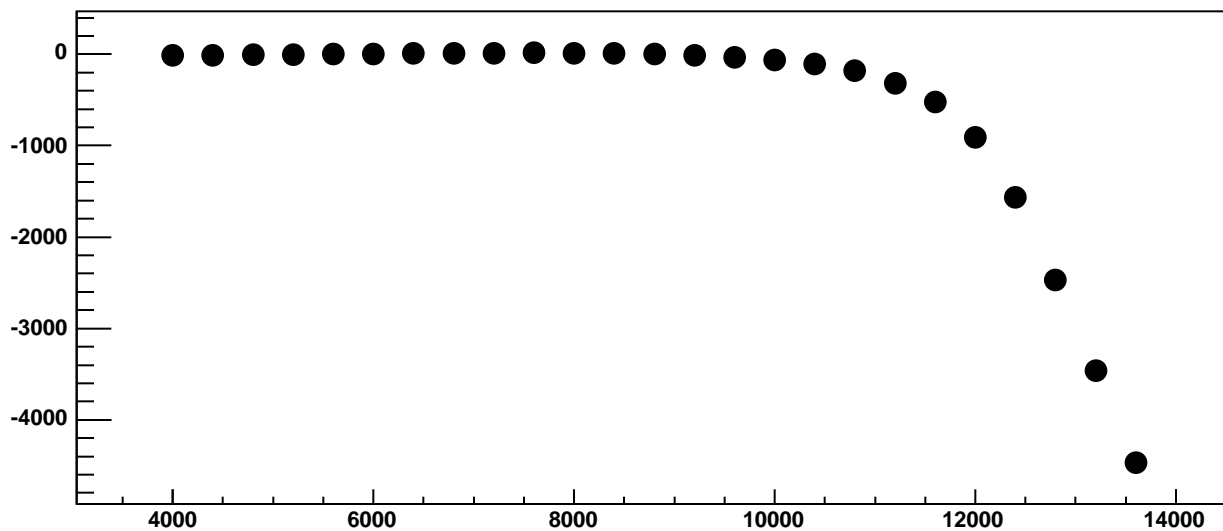
Chip 0, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC



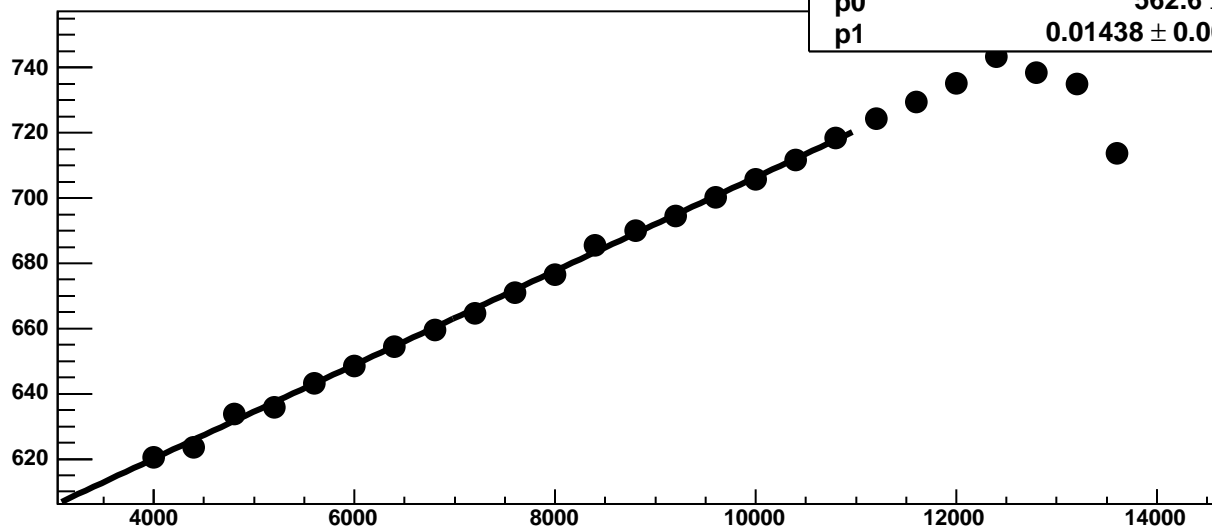
Chip 0, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



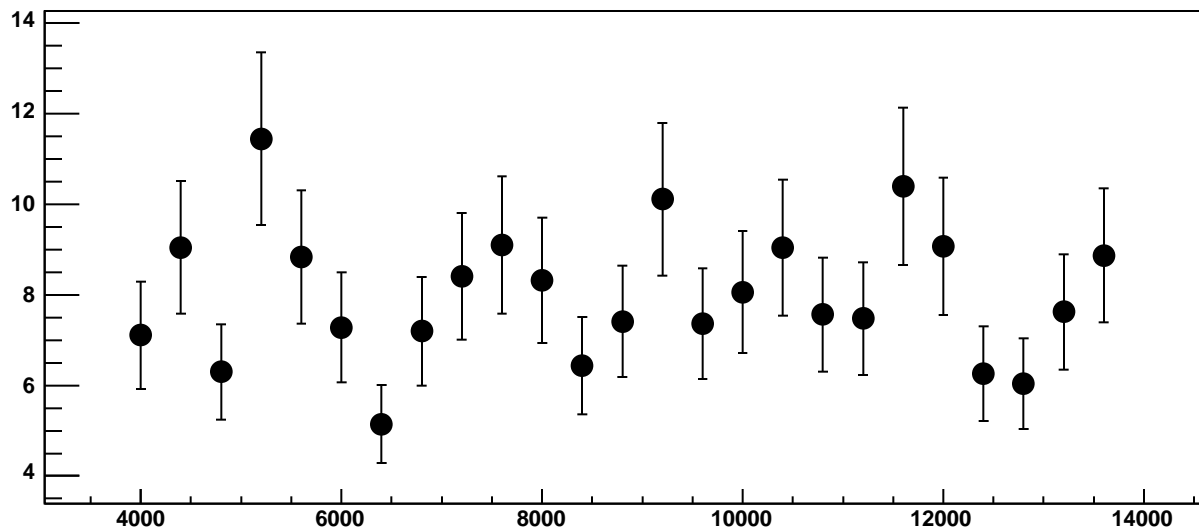
Chip 0, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC



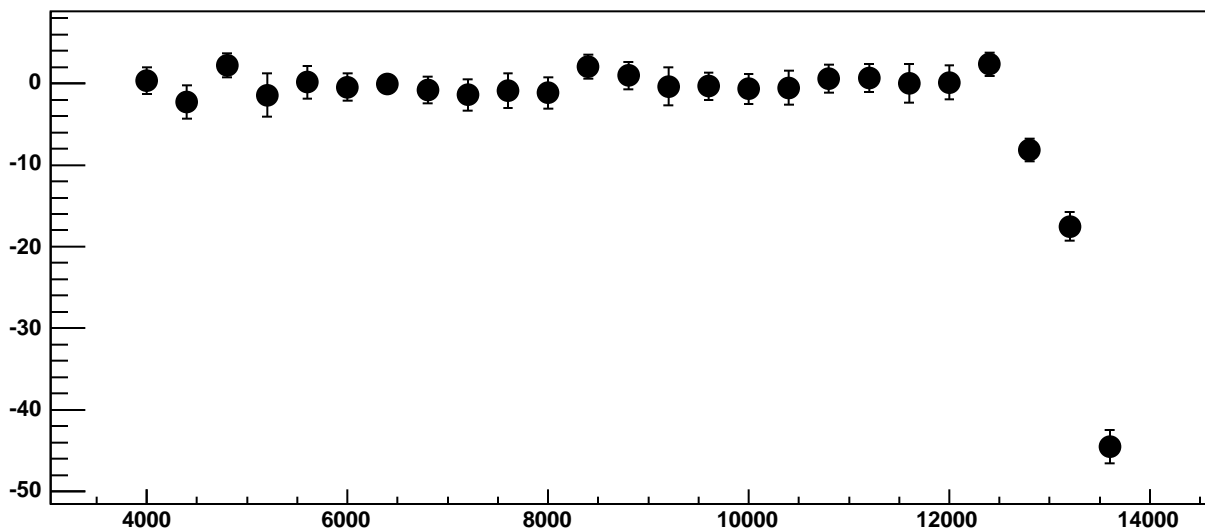
Chip 0, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



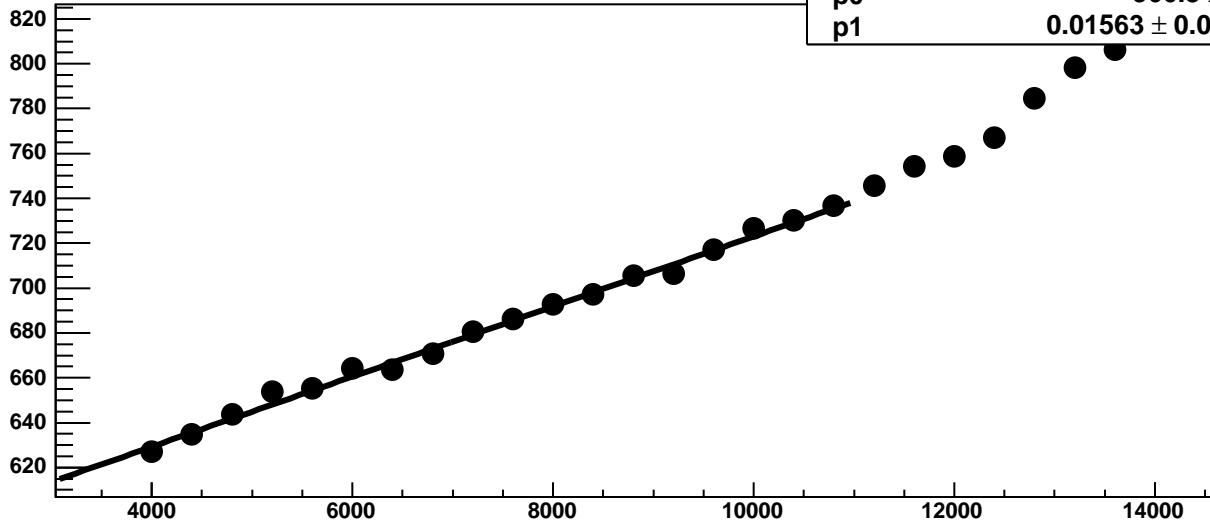
Chip 0, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

35.08 / 16

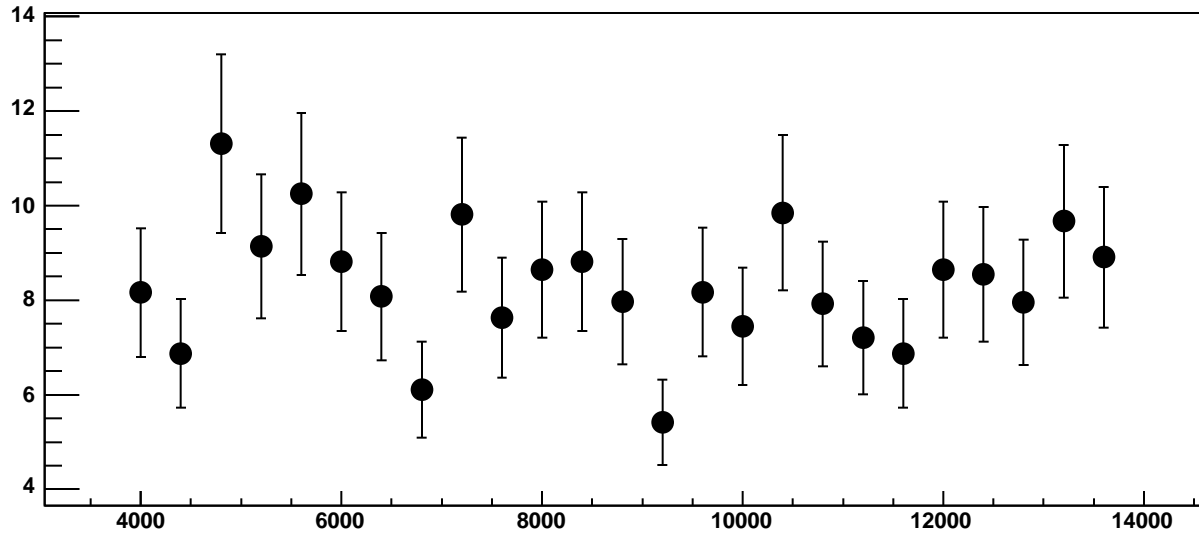
p0

566.8 ± 1.652

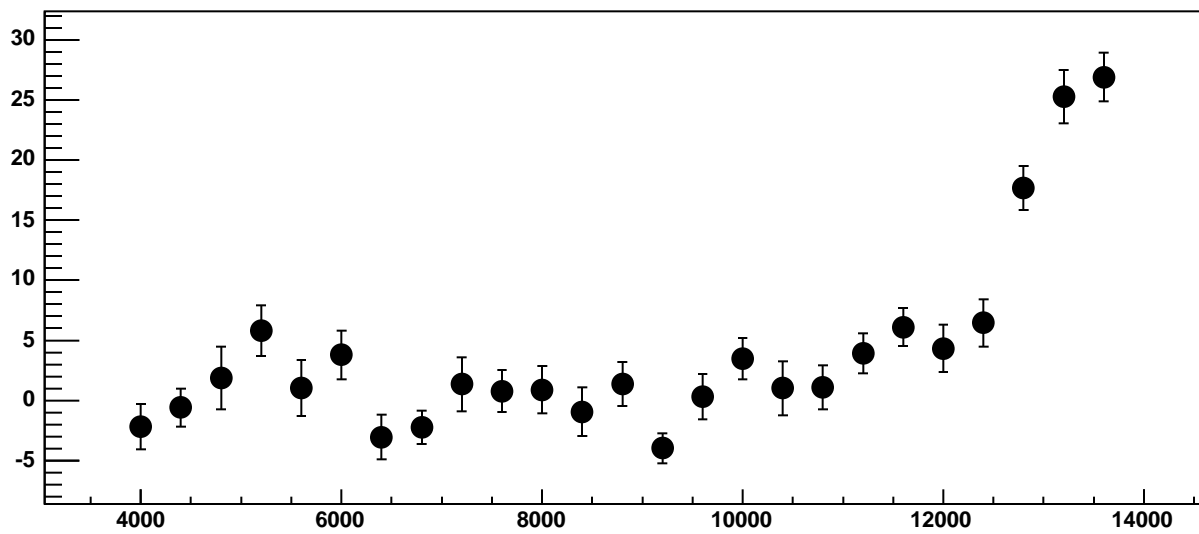
p1

0.01563 ± 0.0002112

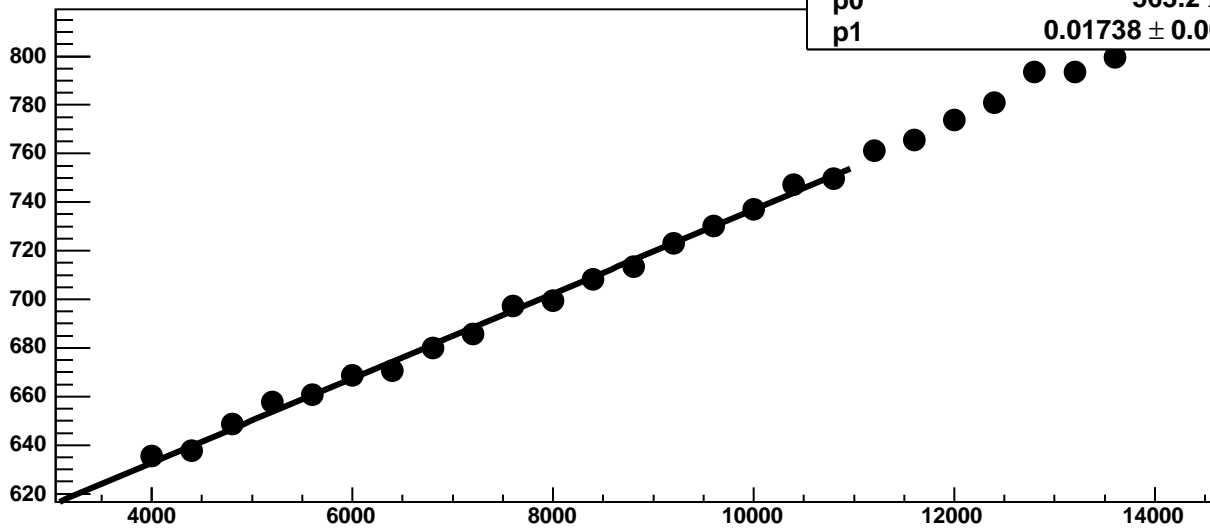
Chip 0, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

29.17 / 16

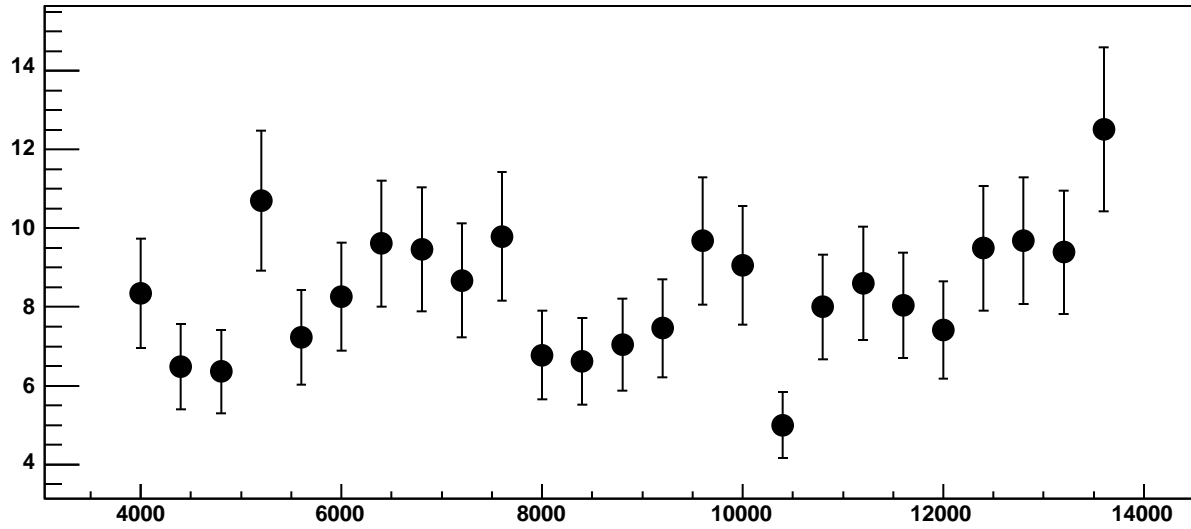
p0

563.2 ± 1.477

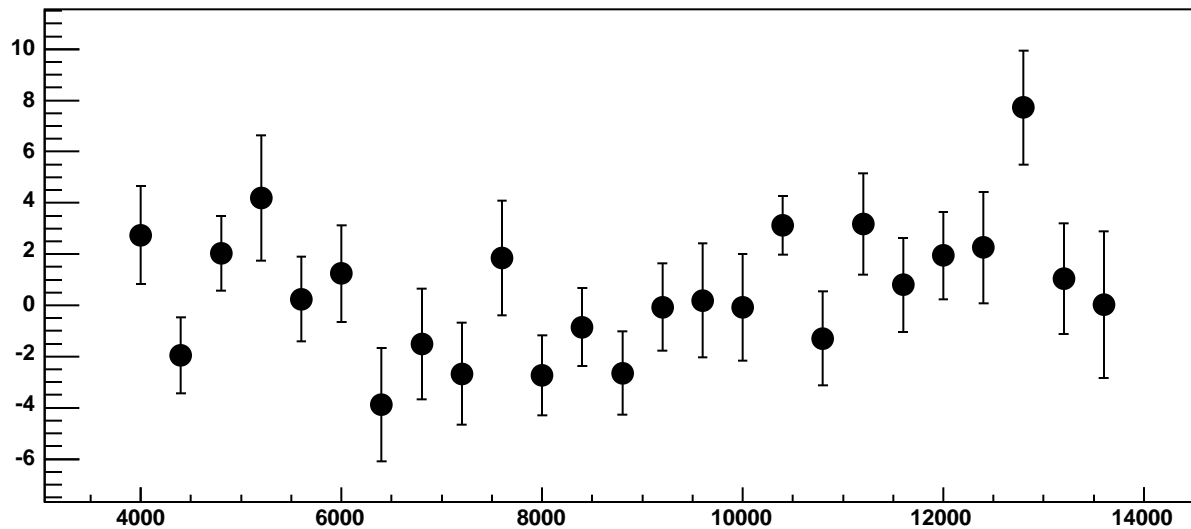
p1

0.01738 ± 0.0001878

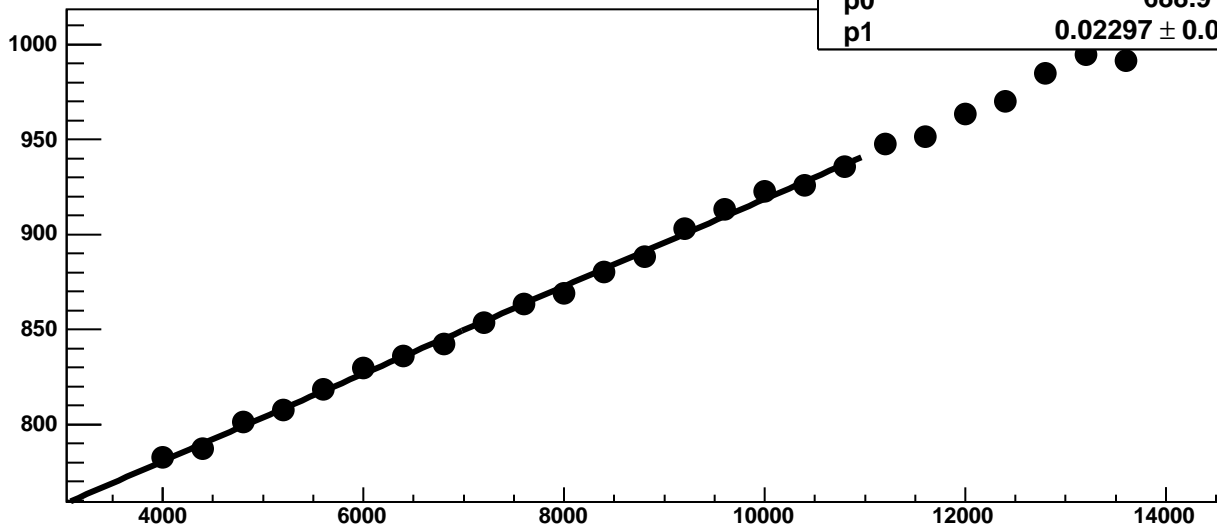
Chip 0, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

30.21 / 16

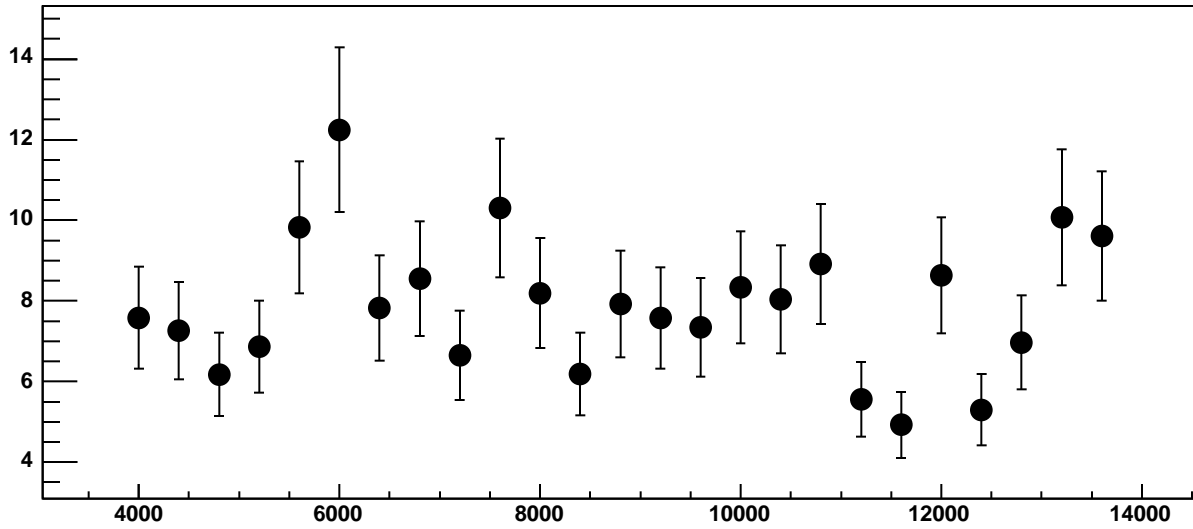
p0

688.9 ± 1.523

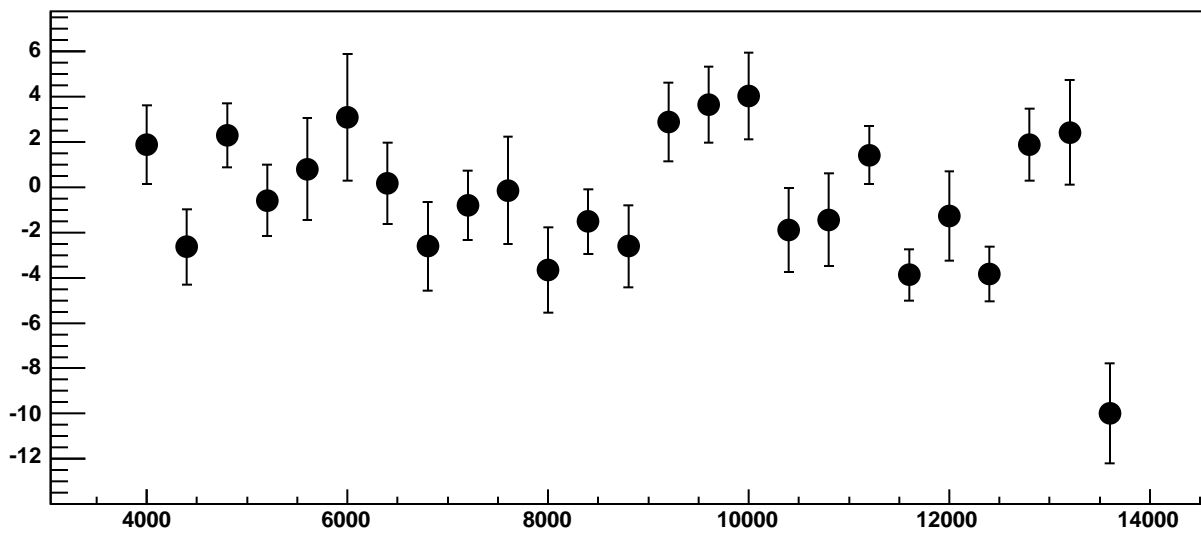
p1

0.02297 ± 0.0002005

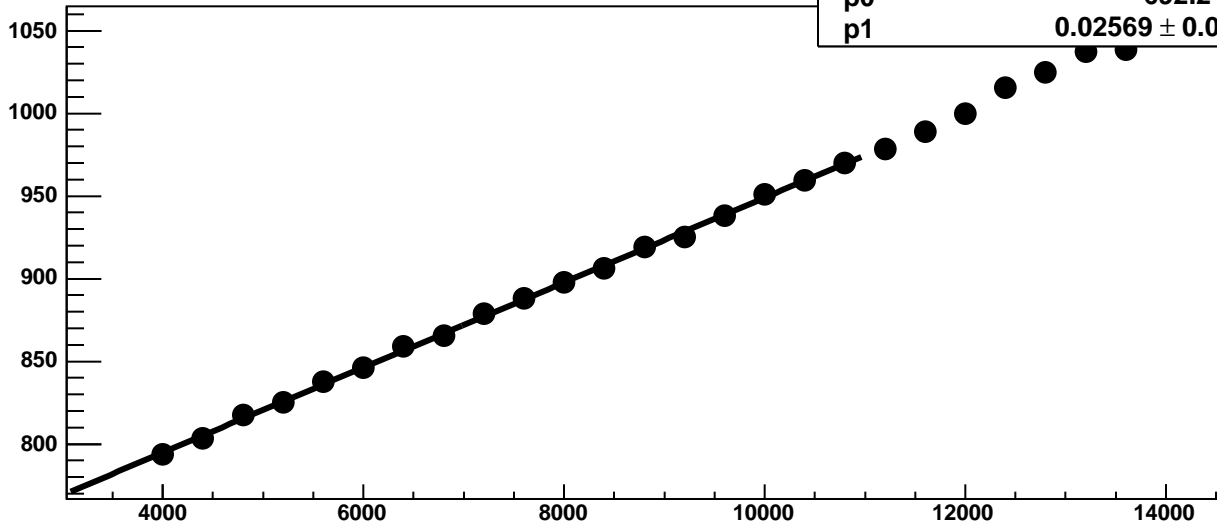
Chip 0, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

15.45 / 16

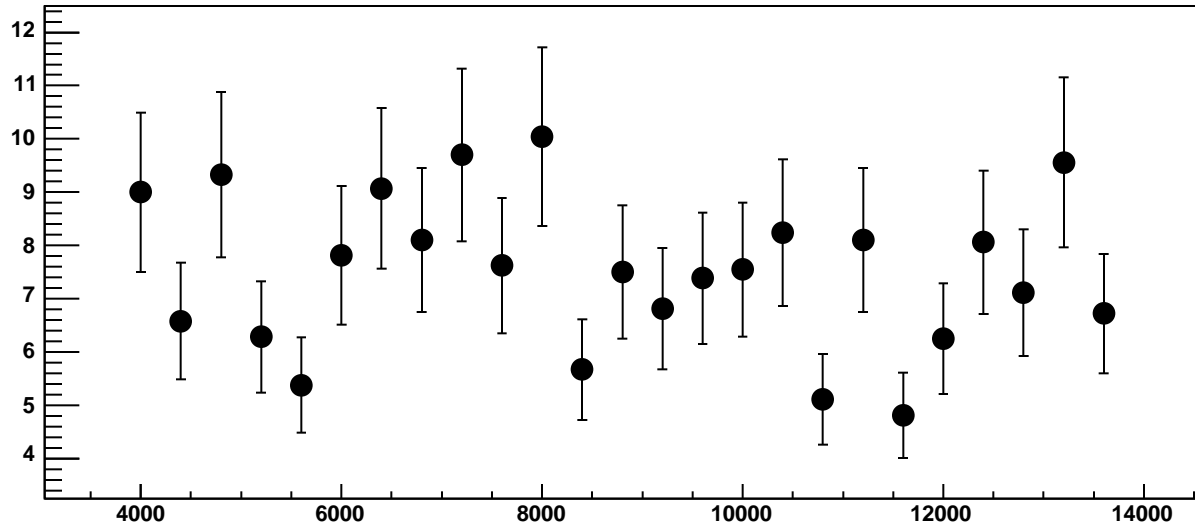
p0

692.2 ± 1.416

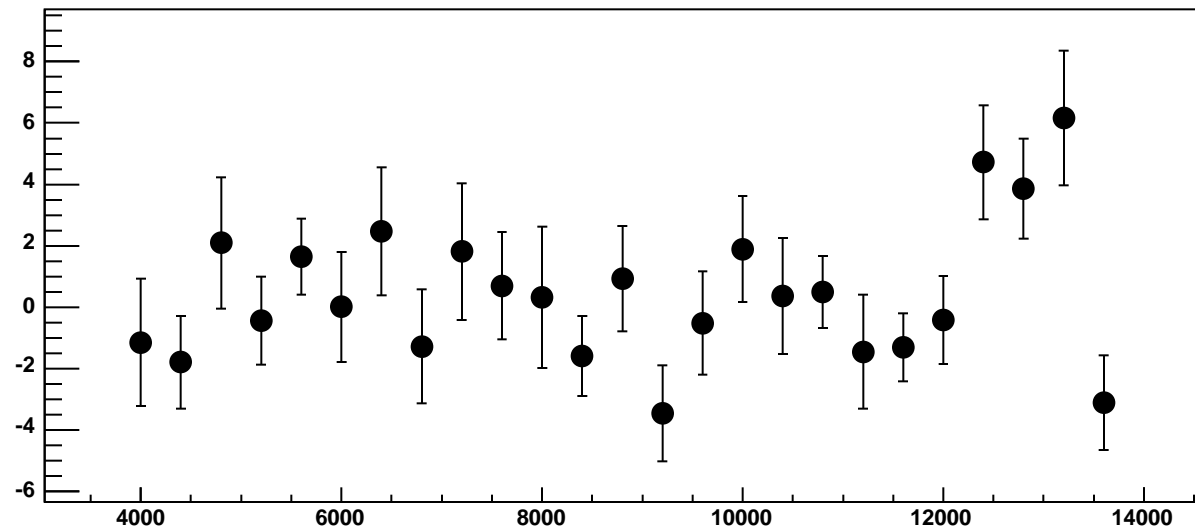
p1

0.02569 ± 0.0001799

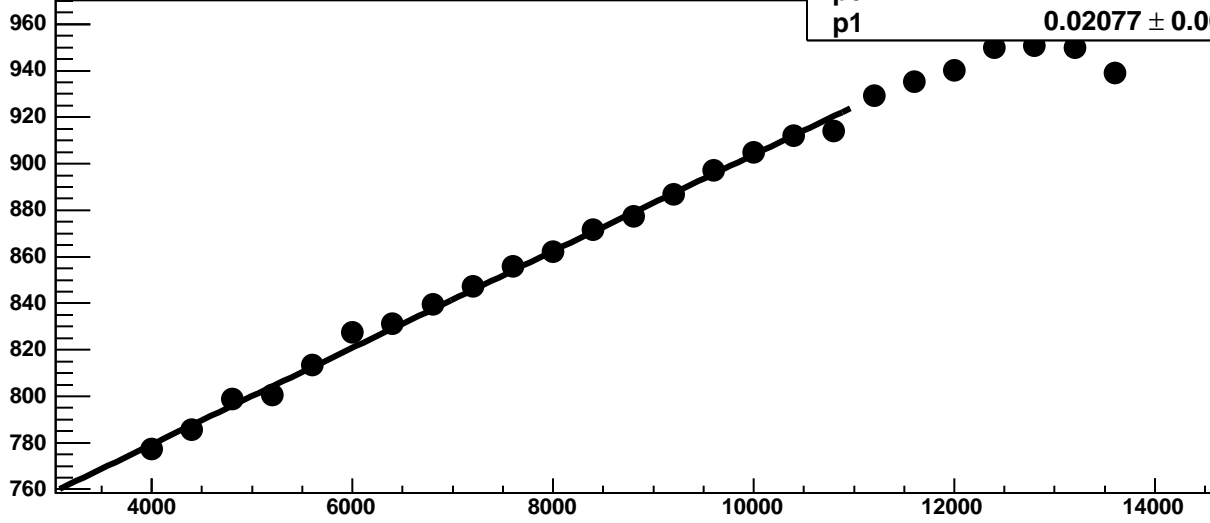
Chip 0, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC



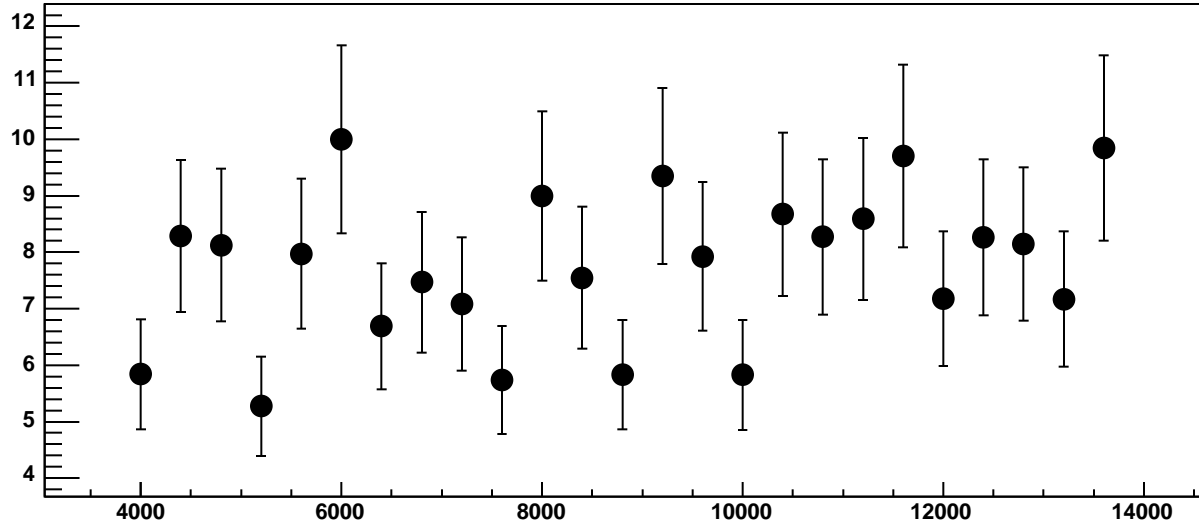
Chip 0, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC



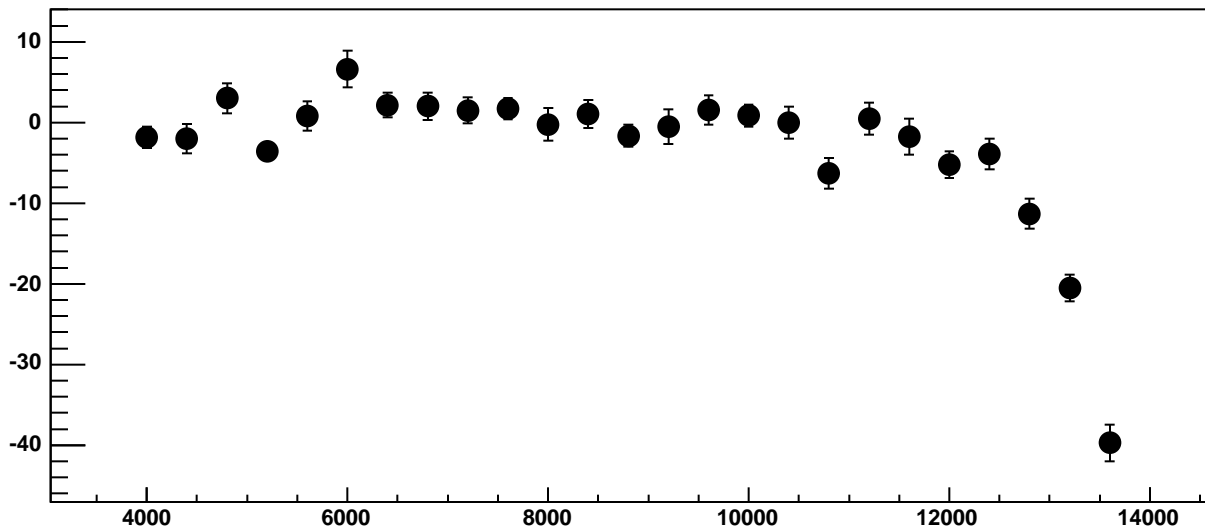
Chip 0, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



Chip 0, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC

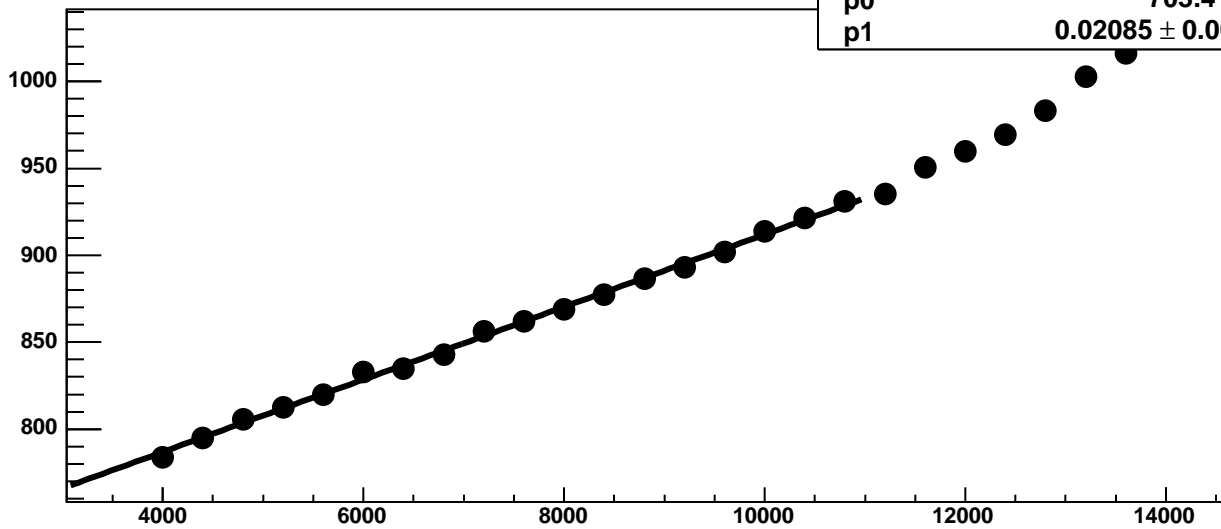


Chip 0, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

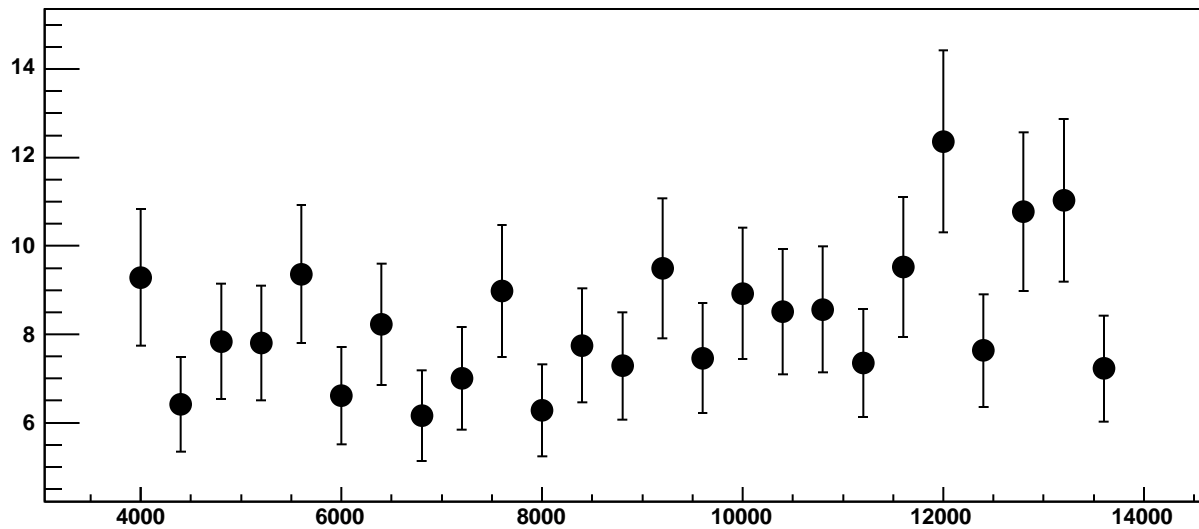


Chip 0, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

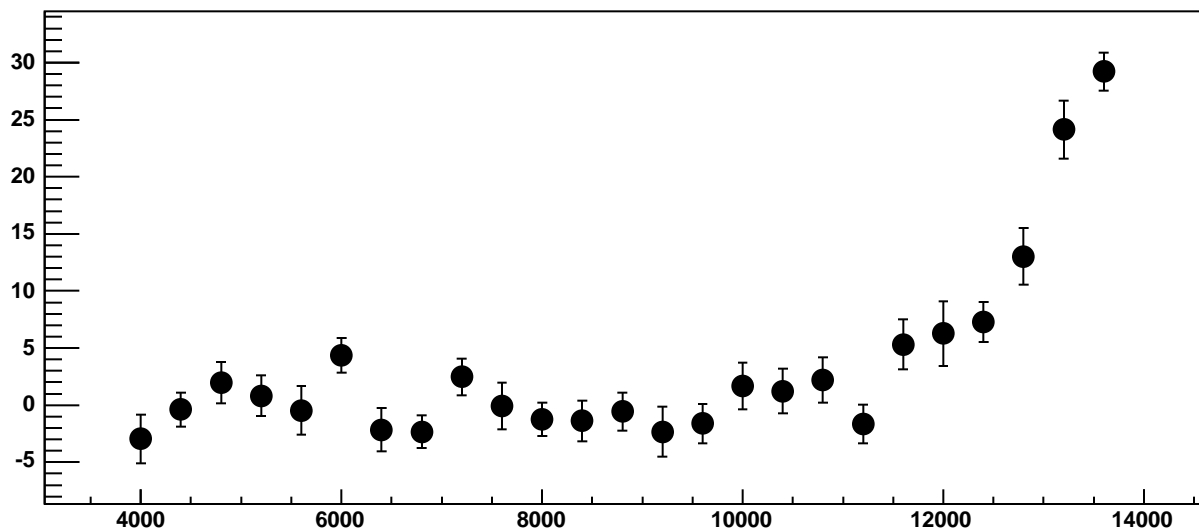
χ^2 / ndf 23.87 / 16
p0 703.4 ± 1.581
p1 0.02085 ± 0.0002097



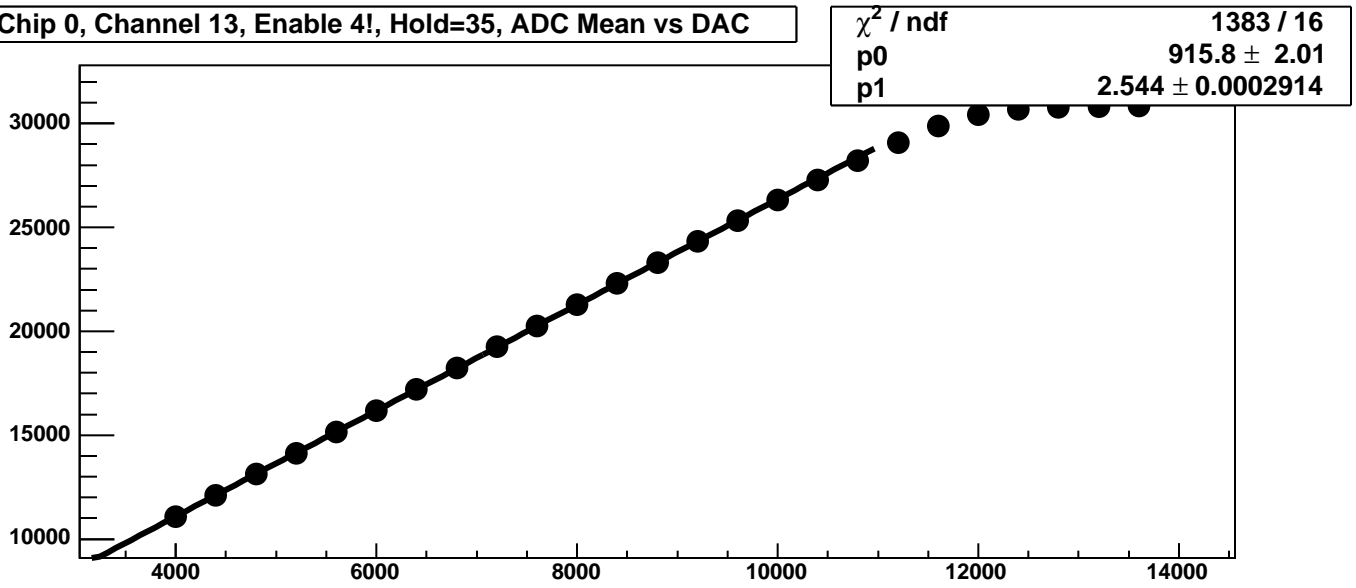
Chip 0, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



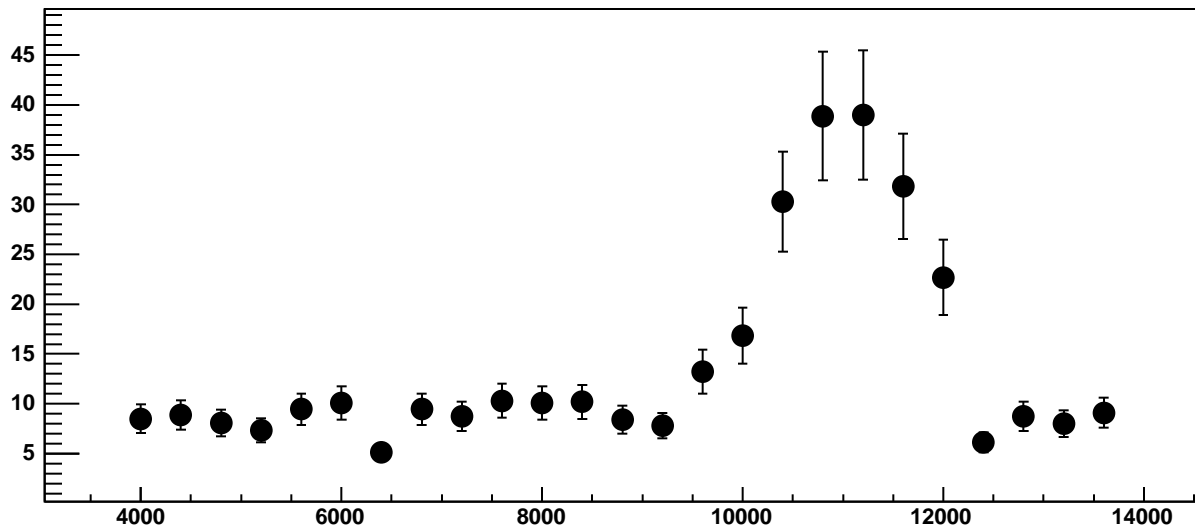
Chip 0, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



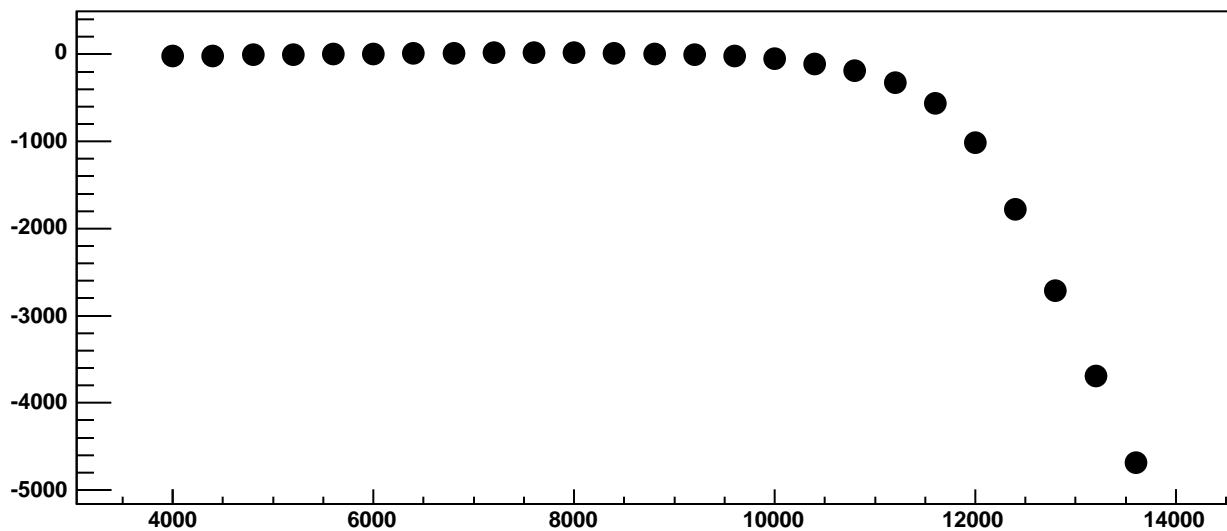
Chip 0, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC



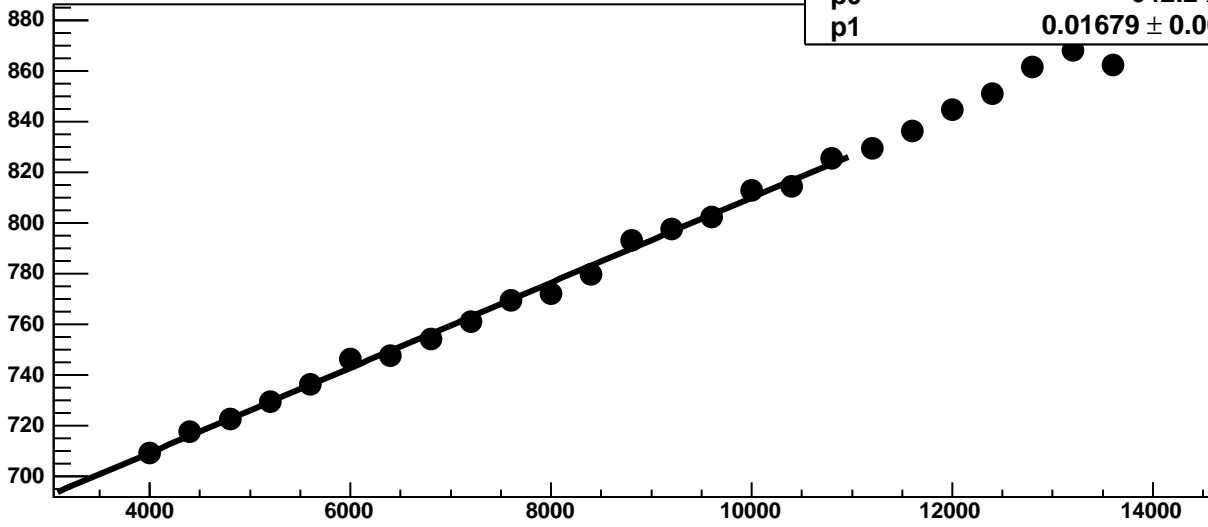
Chip 0, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC

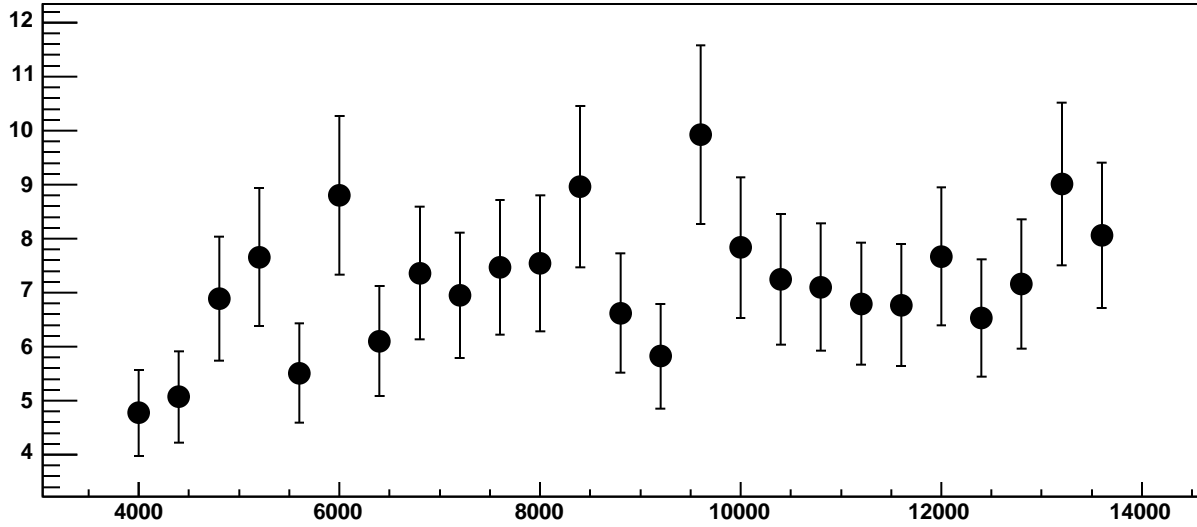


Chip 0, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

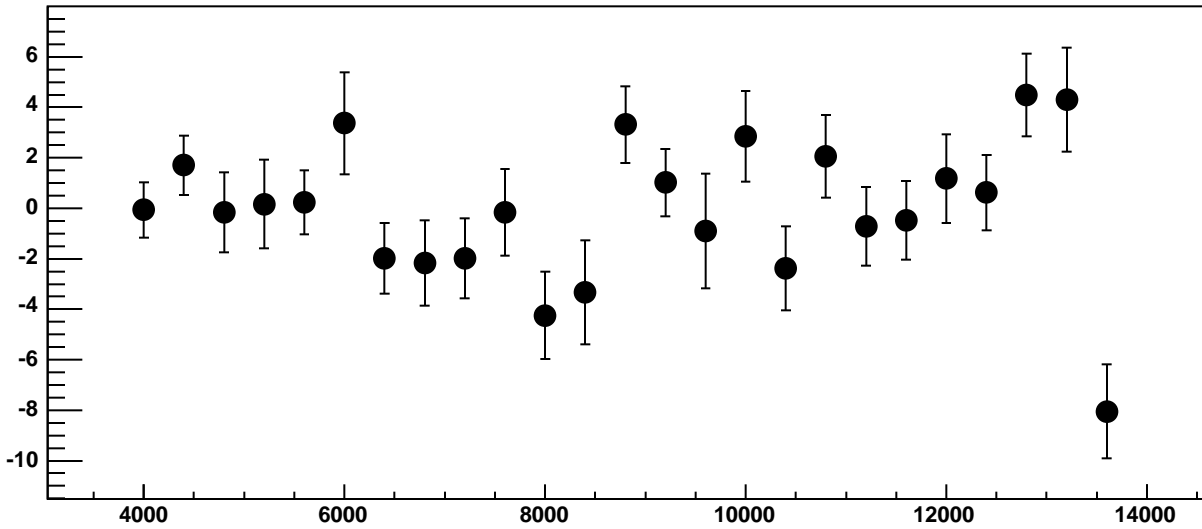


χ^2 / ndf 30.54 / 16
p0 642.2 ± 1.217
p1 0.01679 ± 0.0001669

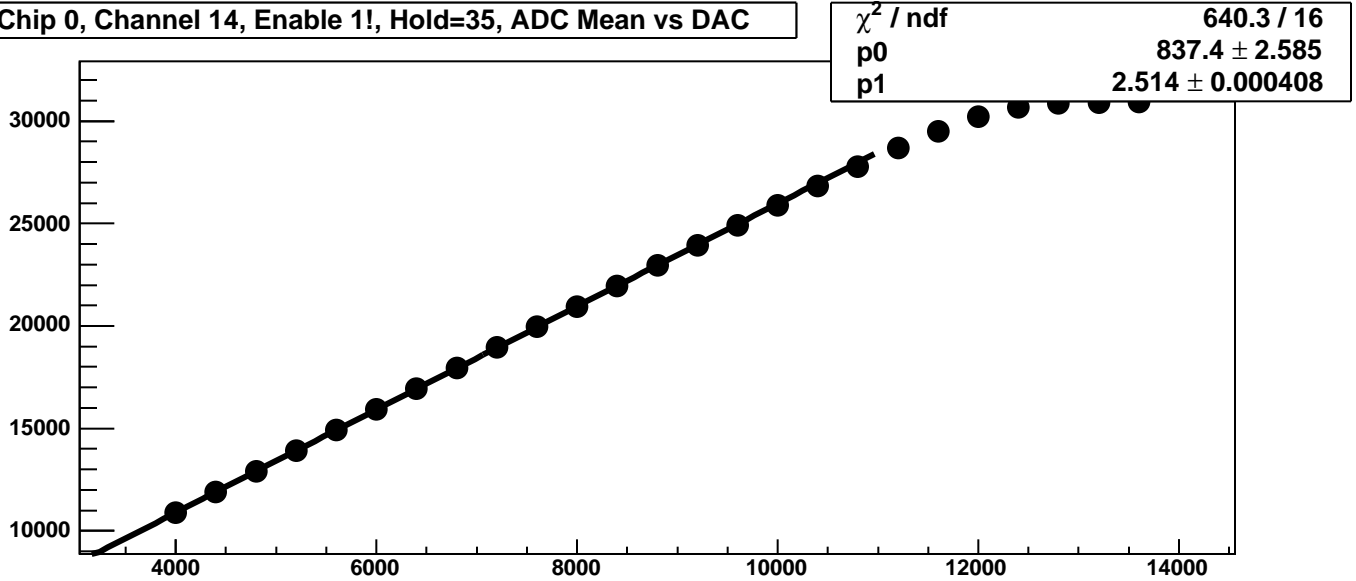
Chip 0, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



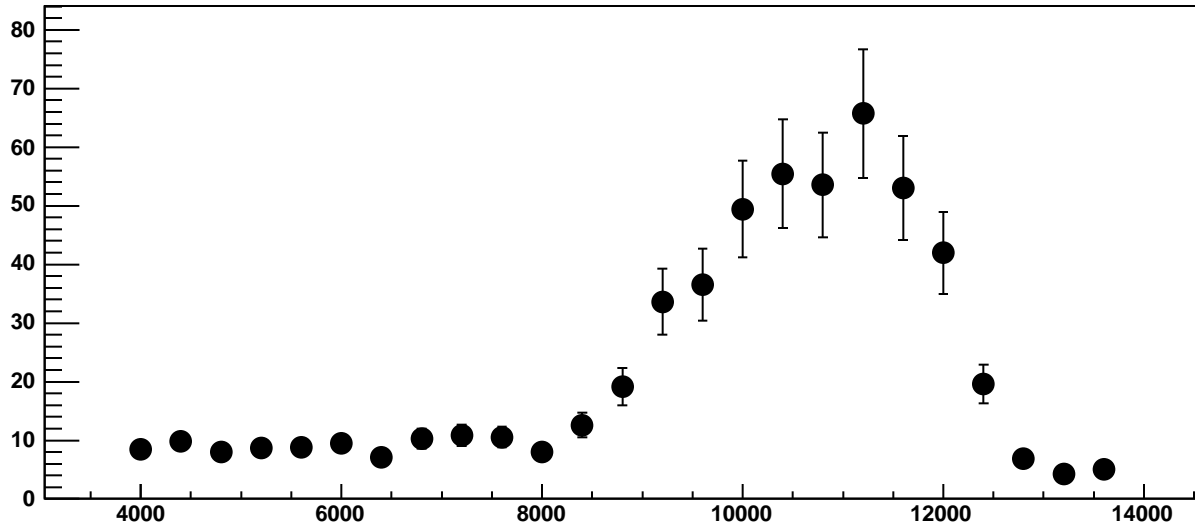
Chip 0, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



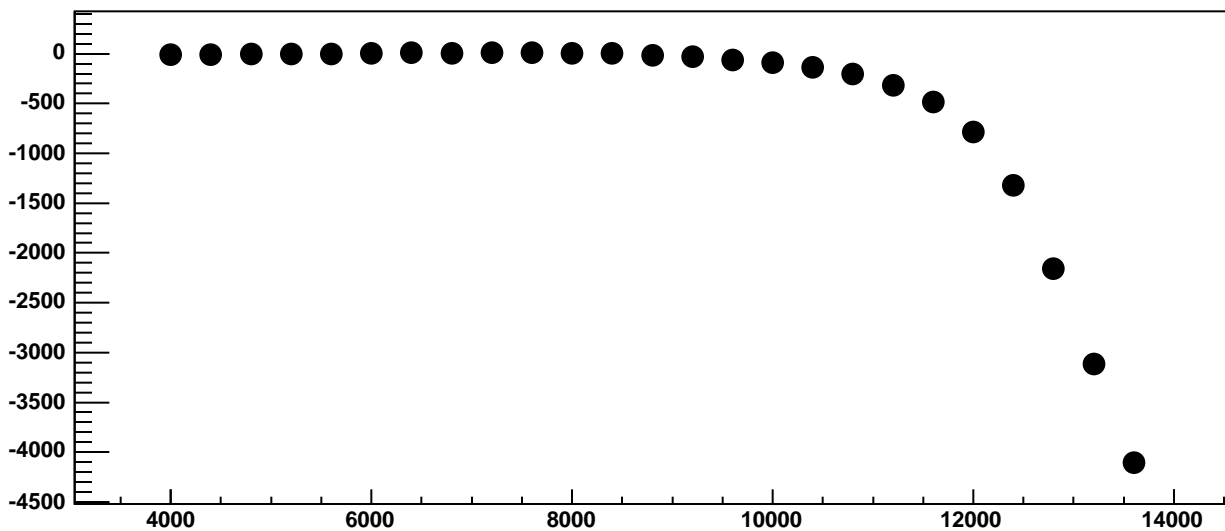
Chip 0, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC



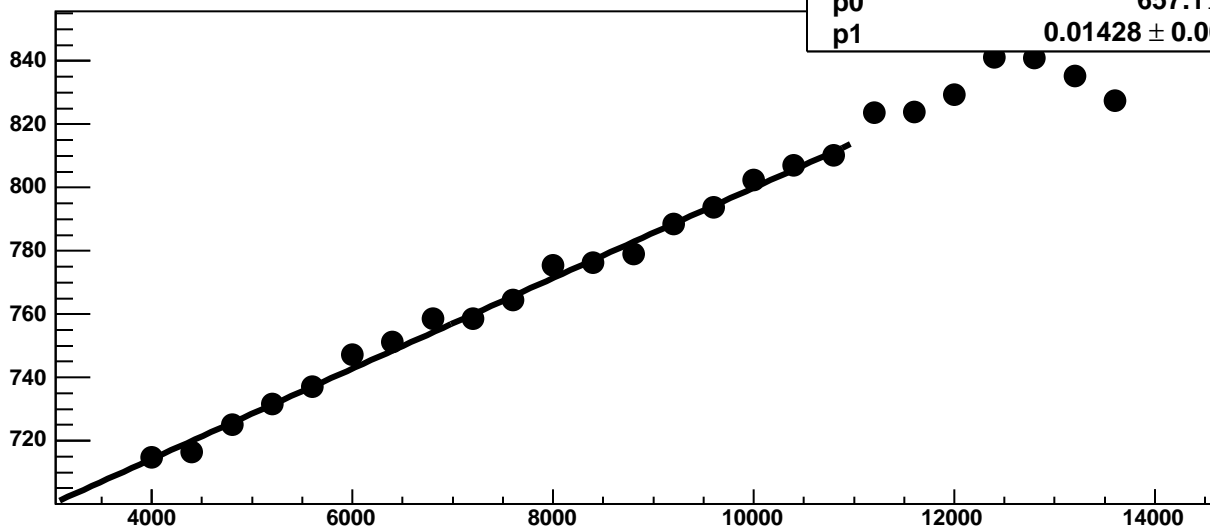
Chip 0, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



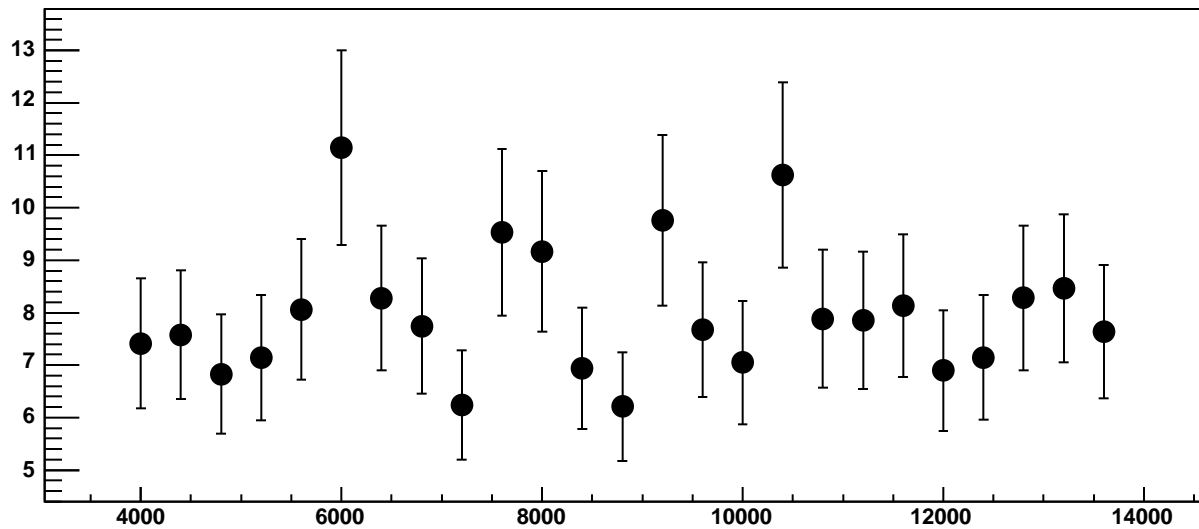
Chip 0, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC



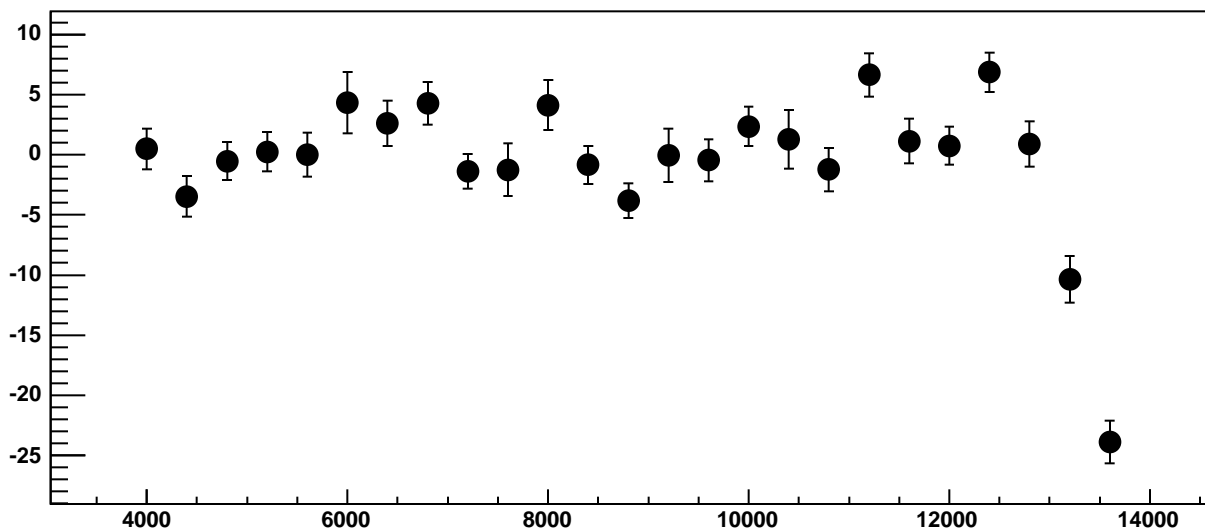
Chip 0, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



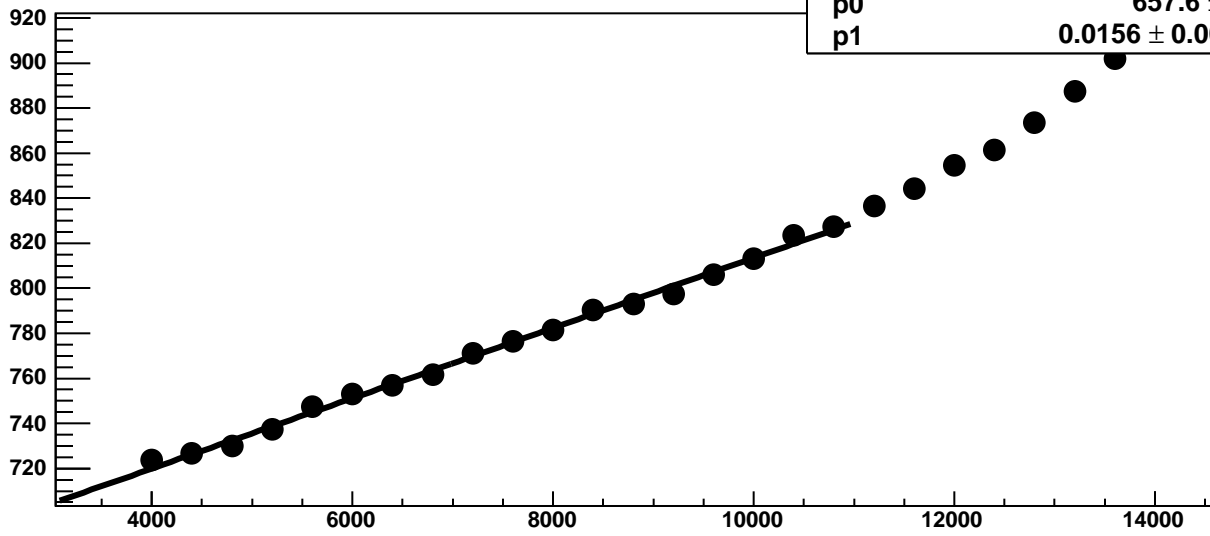
Chip 0, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

23.93 / 16

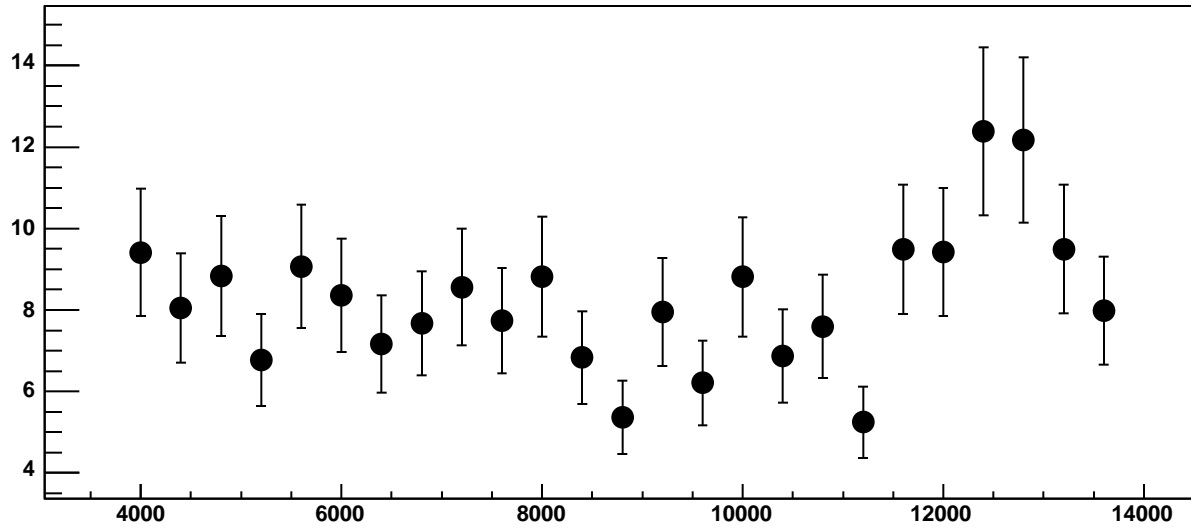
p0

657.6 ± 1.604

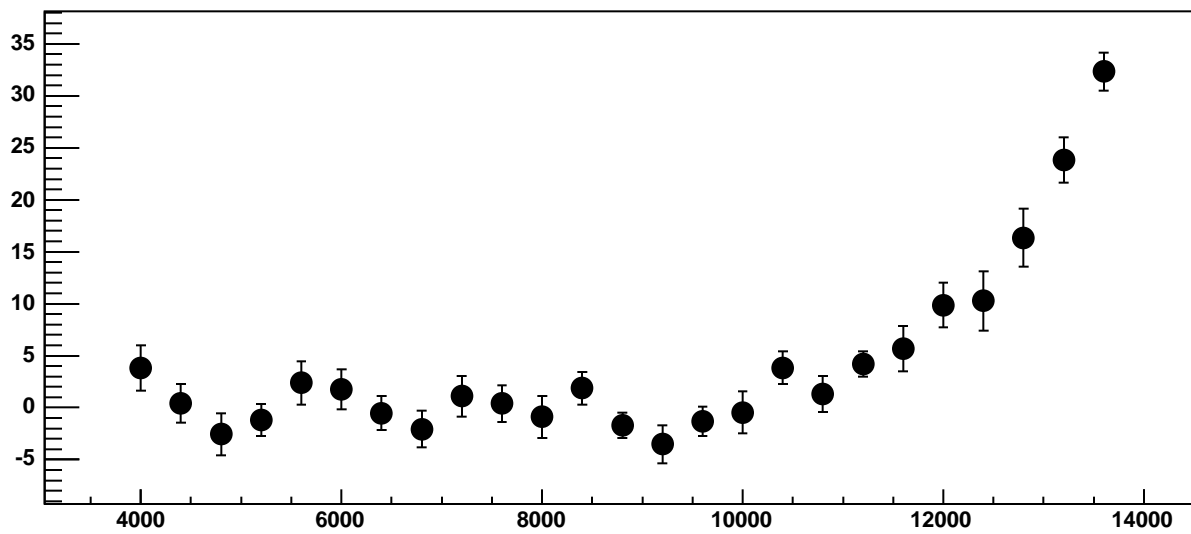
p1

0.0156 ± 0.0002025

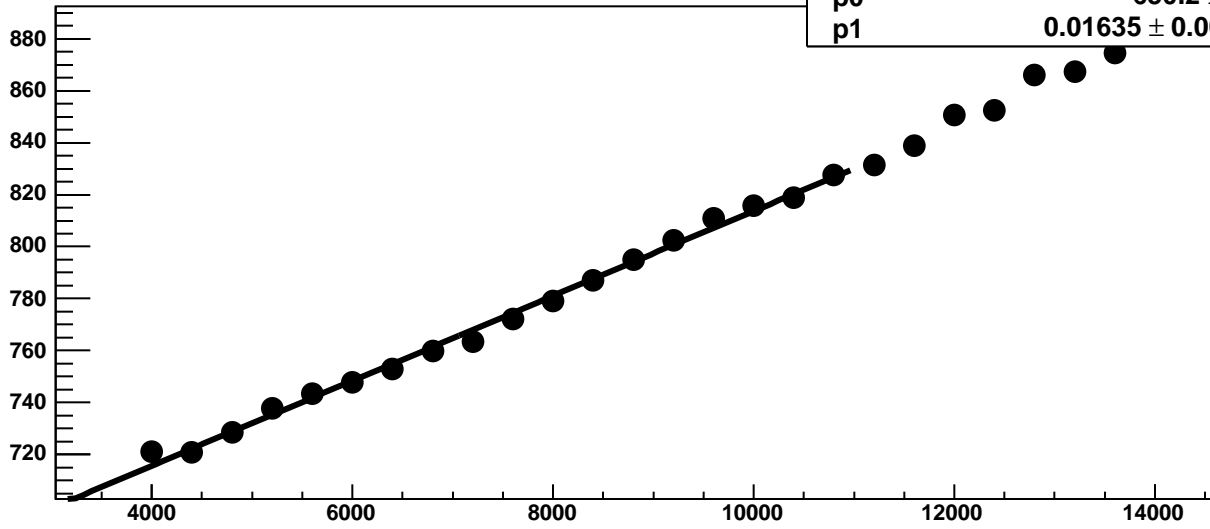
Chip 0, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



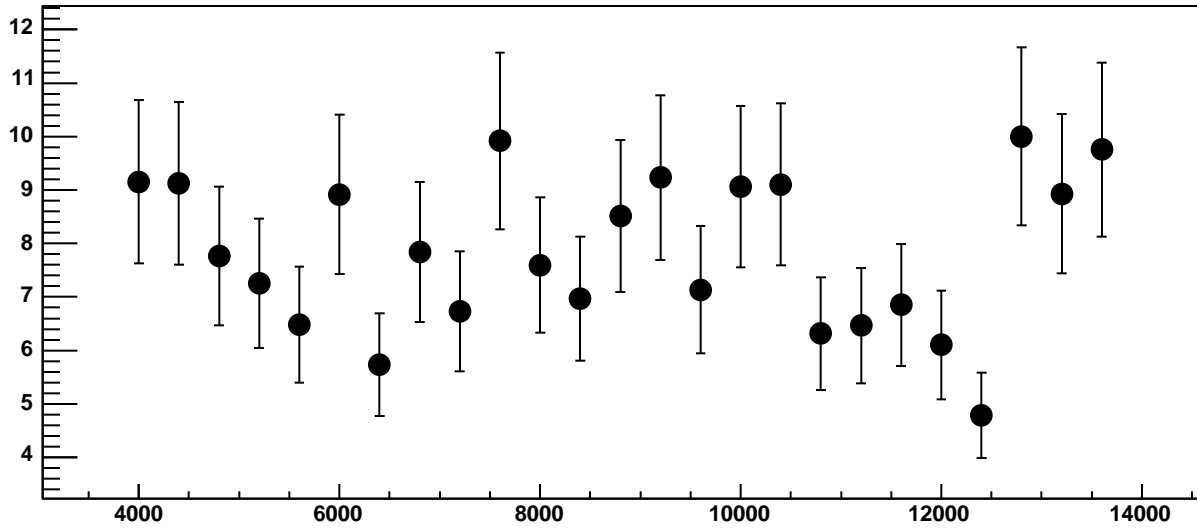
Chip 0, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



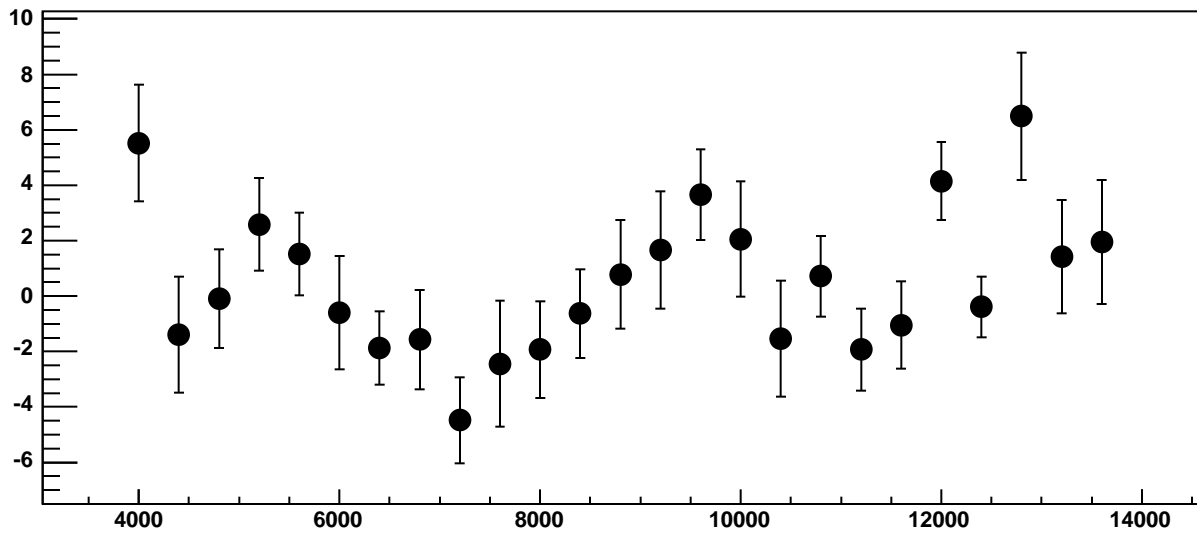
Chip 0, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC



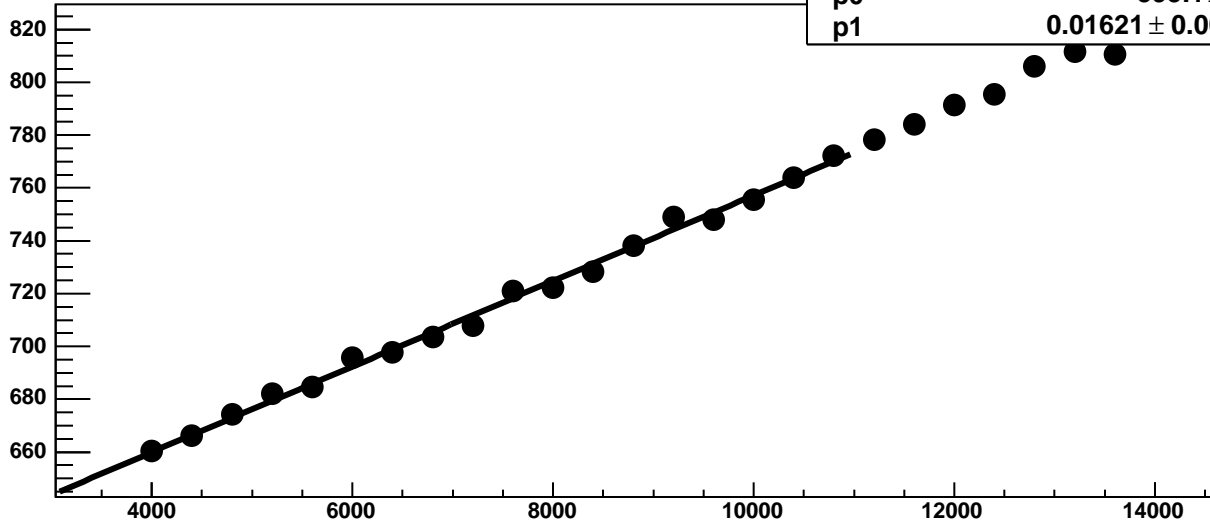
Chip 0, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

42.6 / 16

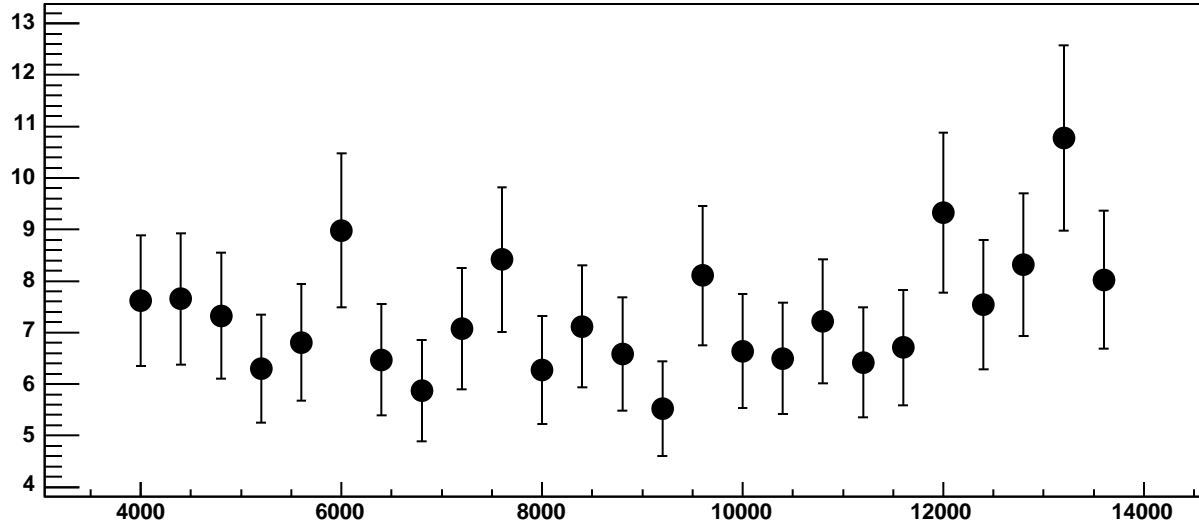
p0

595.1 ± 1.425

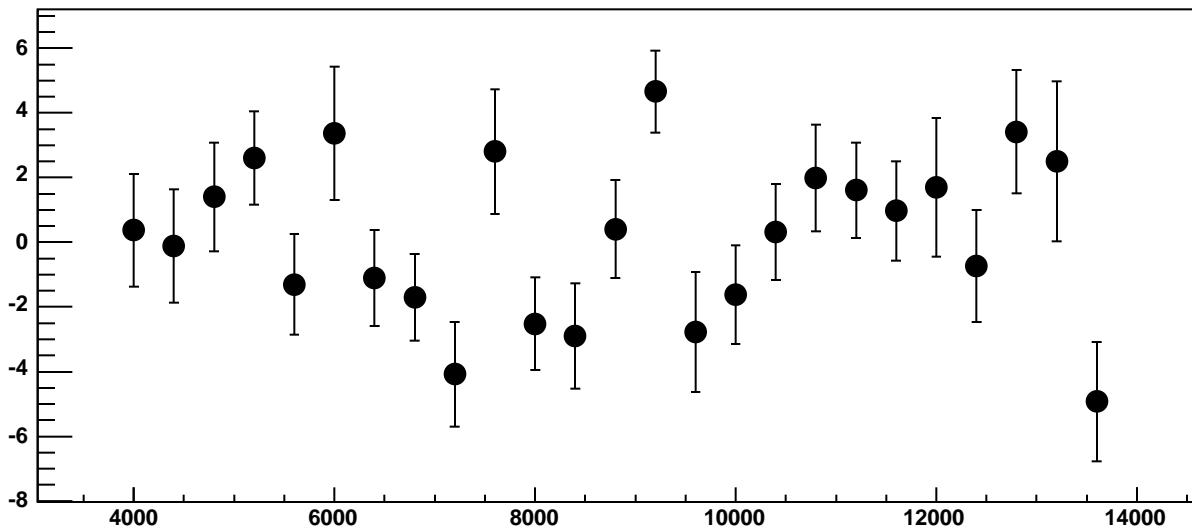
p1

0.01621 ± 0.0001832

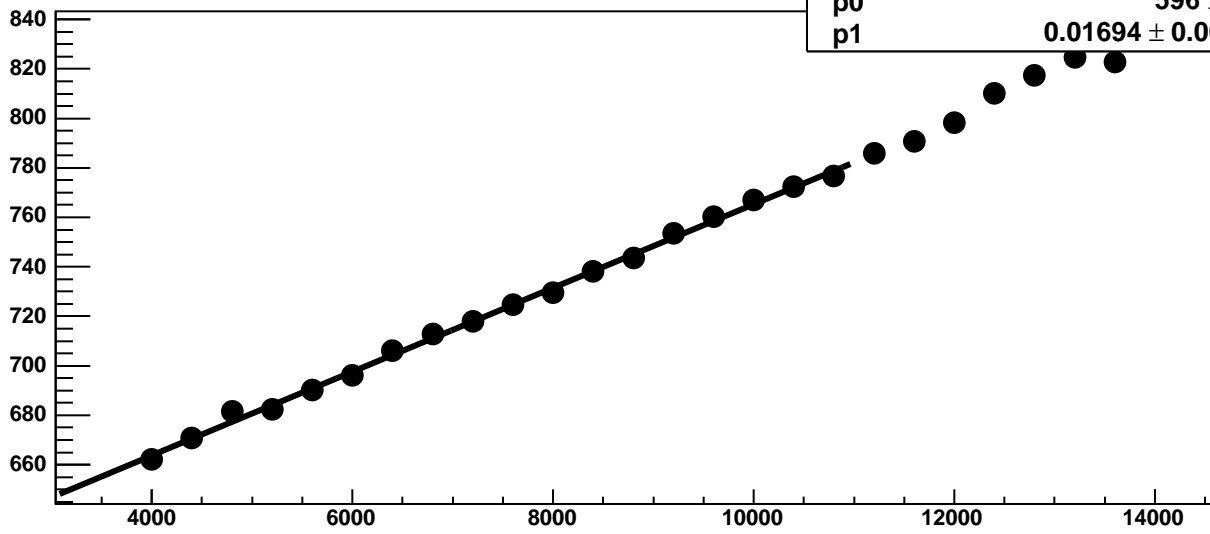
Chip 0, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

14.8 / 16

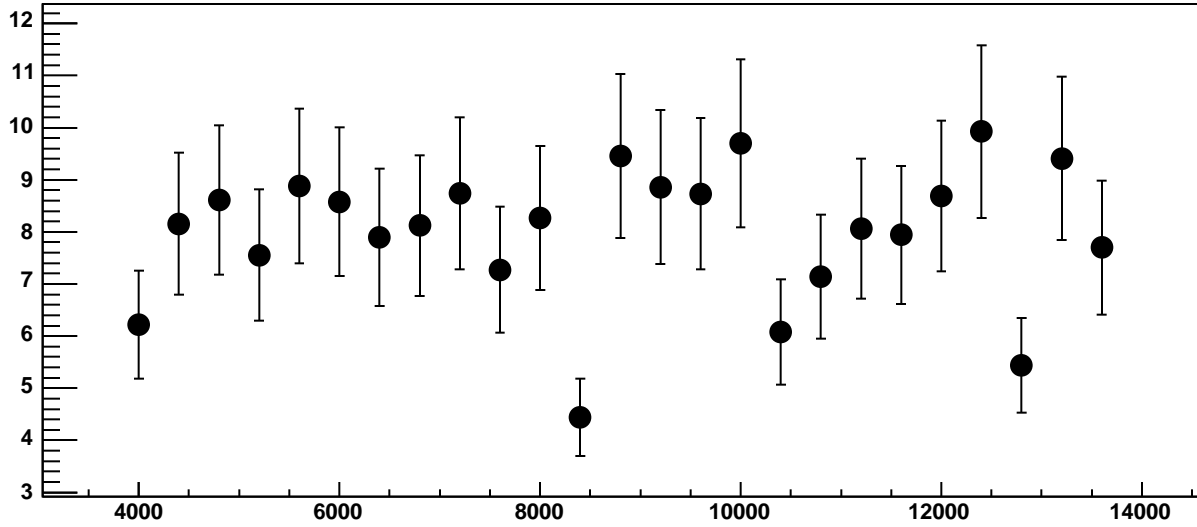
p0

596 ± 1.497

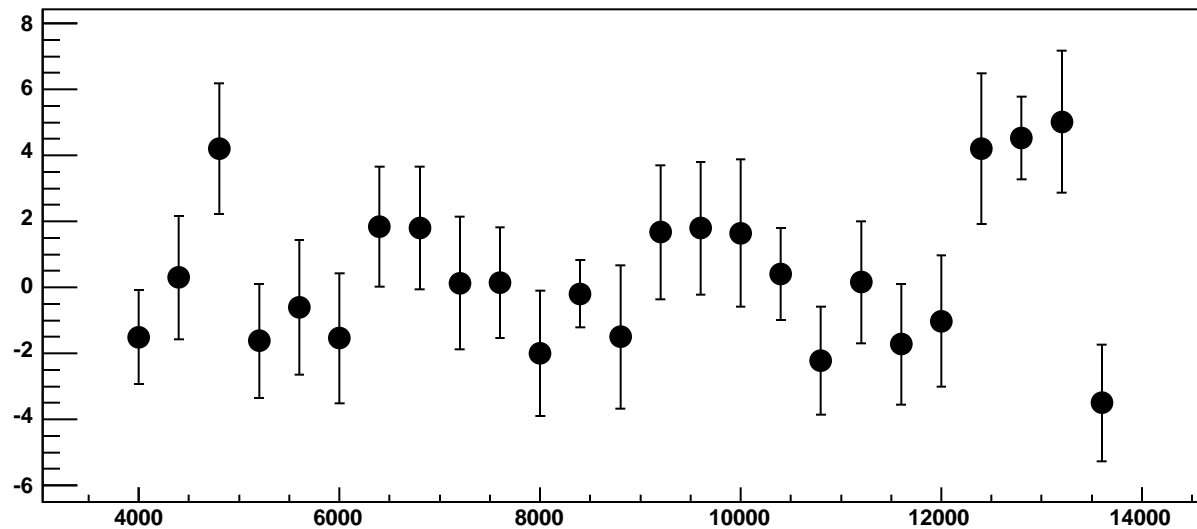
p1

0.01694 ± 0.0001924

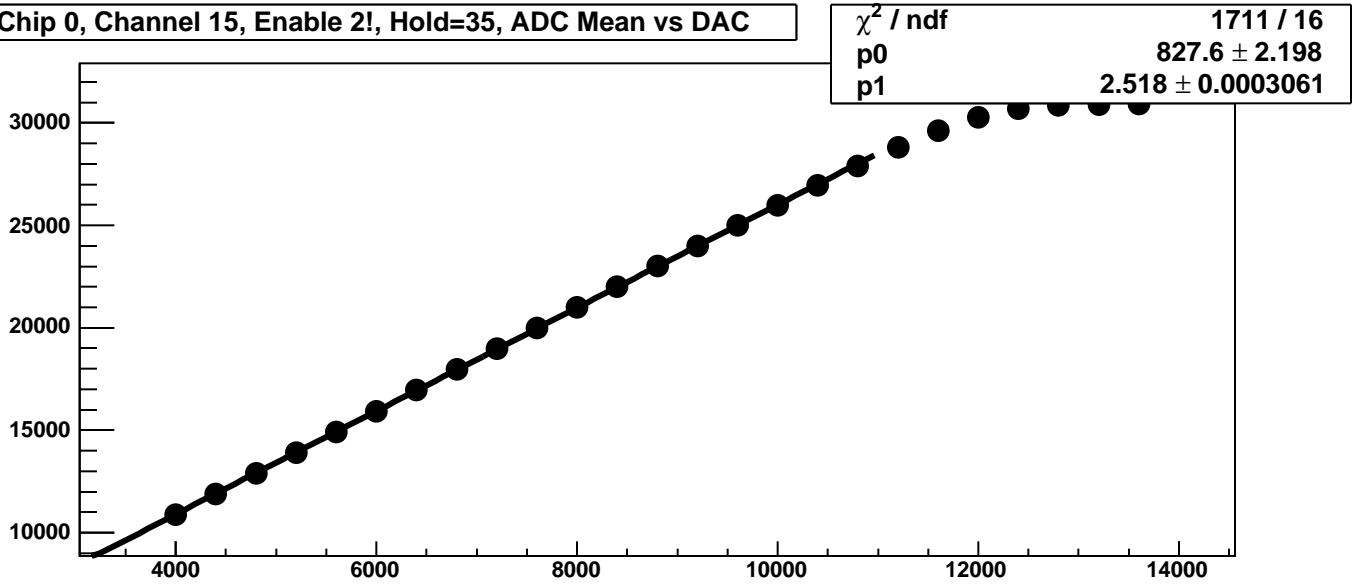
Chip 0, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



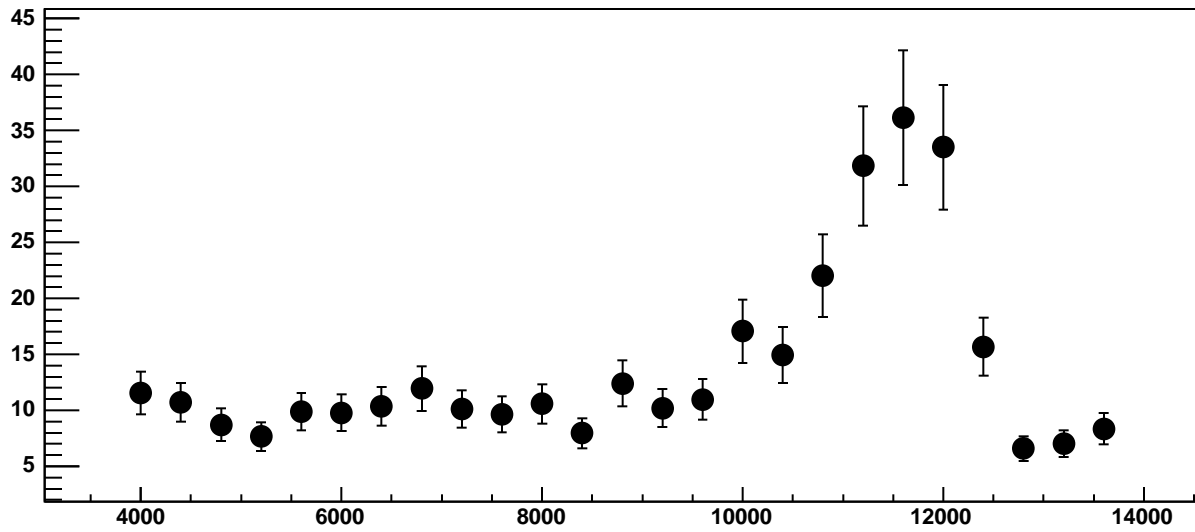
Chip 0, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



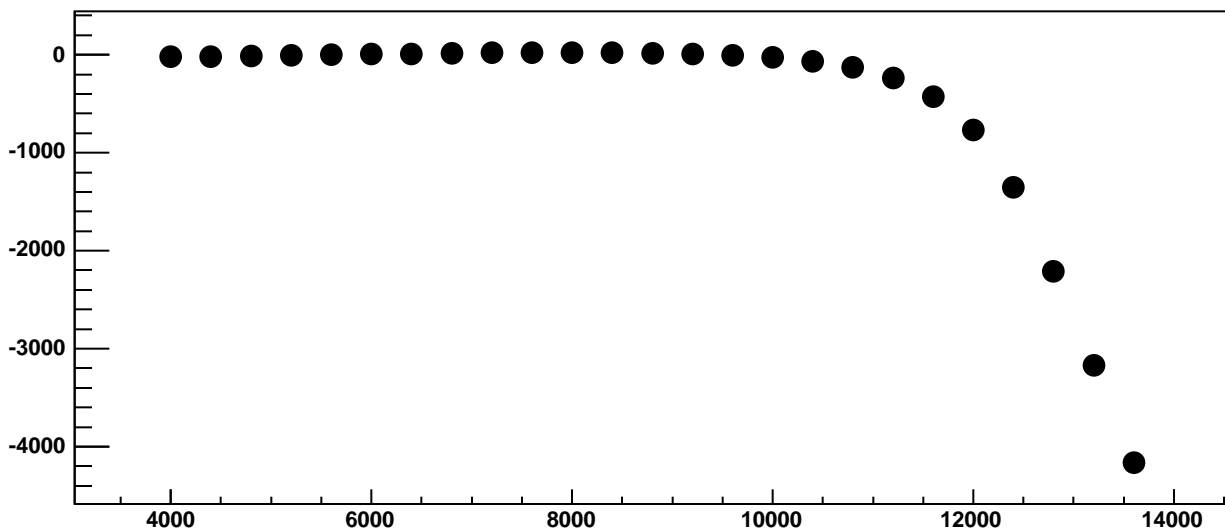
Chip 0, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC



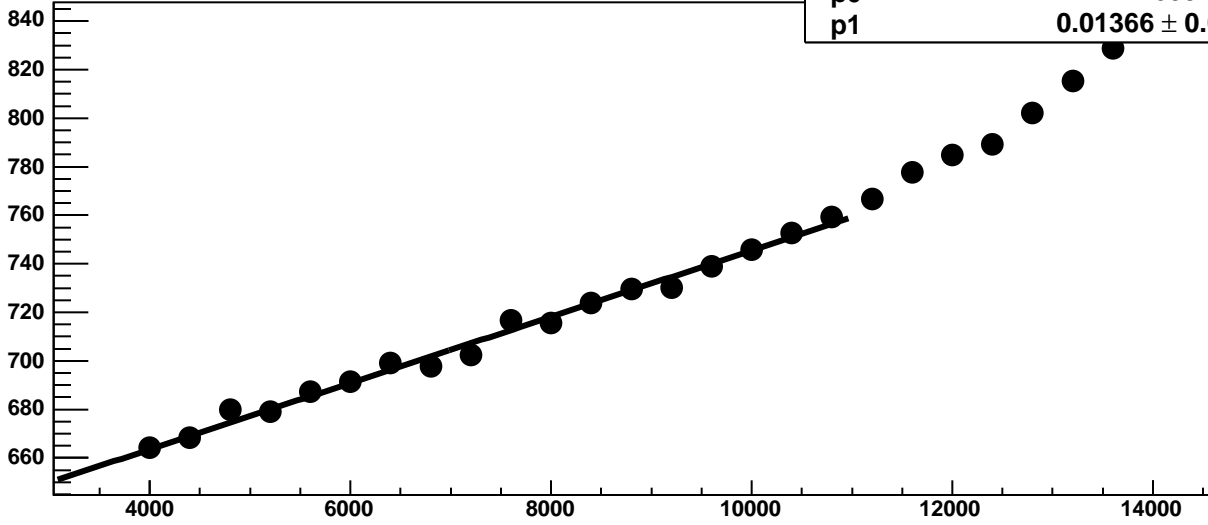
Chip 0, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

40.29 / 16

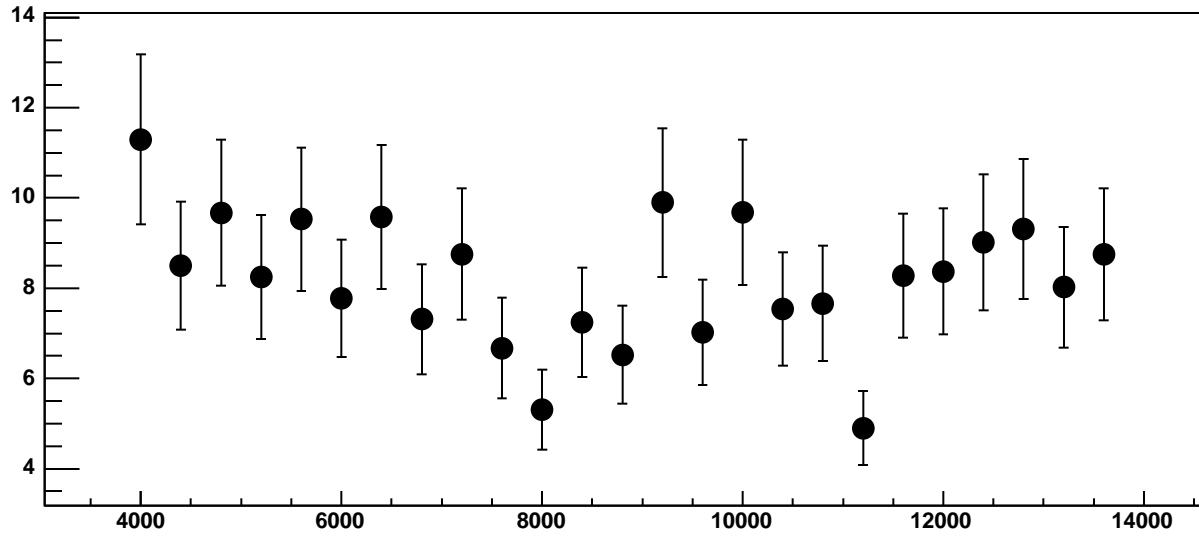
p0

609 ± 1.765

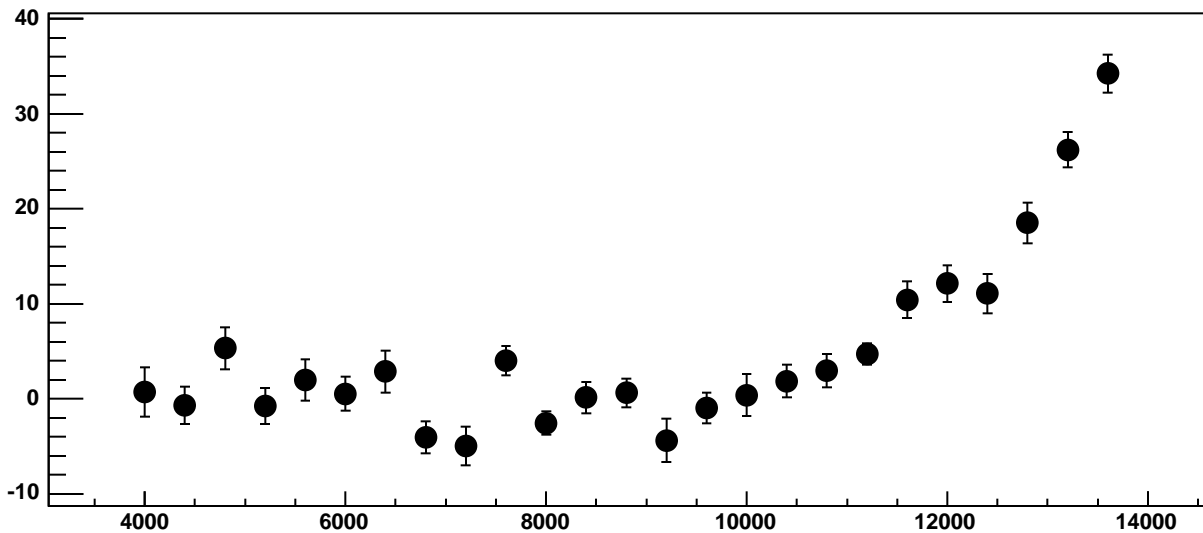
p1

0.01366 ± 0.000223

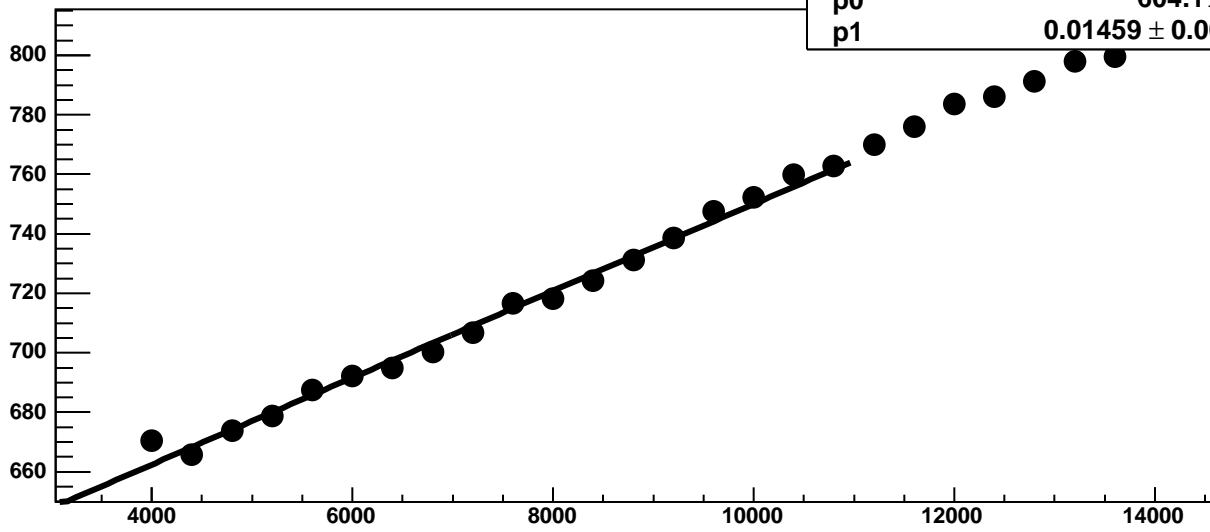
Chip 0, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

48.69 / 16

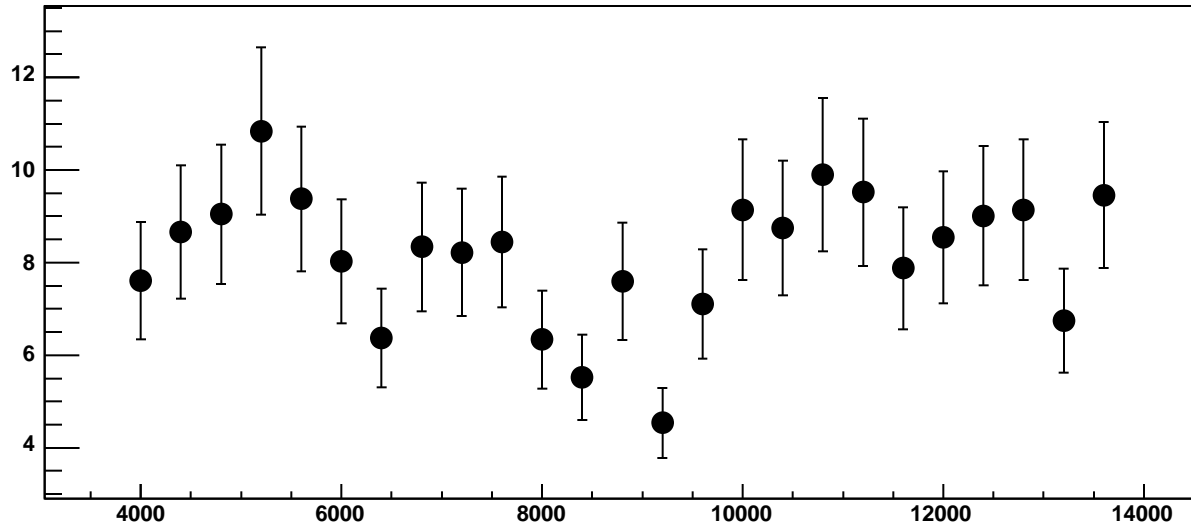
p0

604.1 ± 1.676

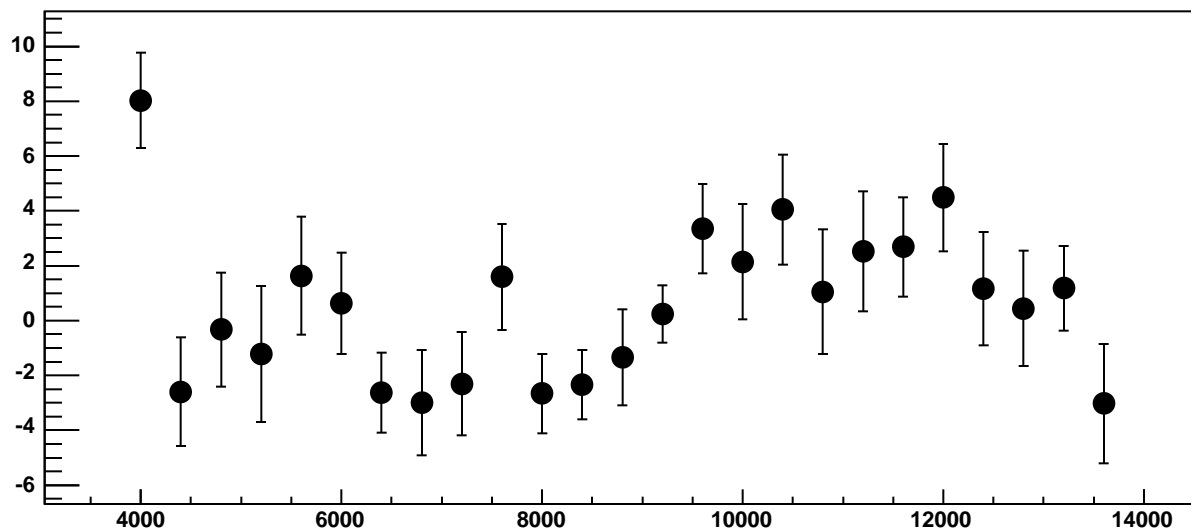
p1

0.01459 ± 0.0002125

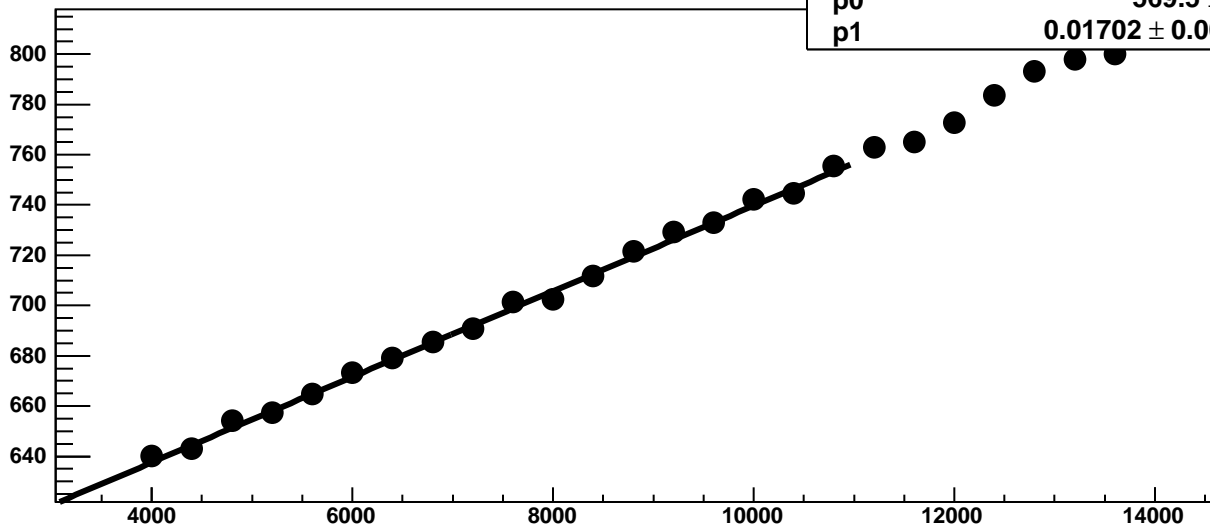
Chip 0, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



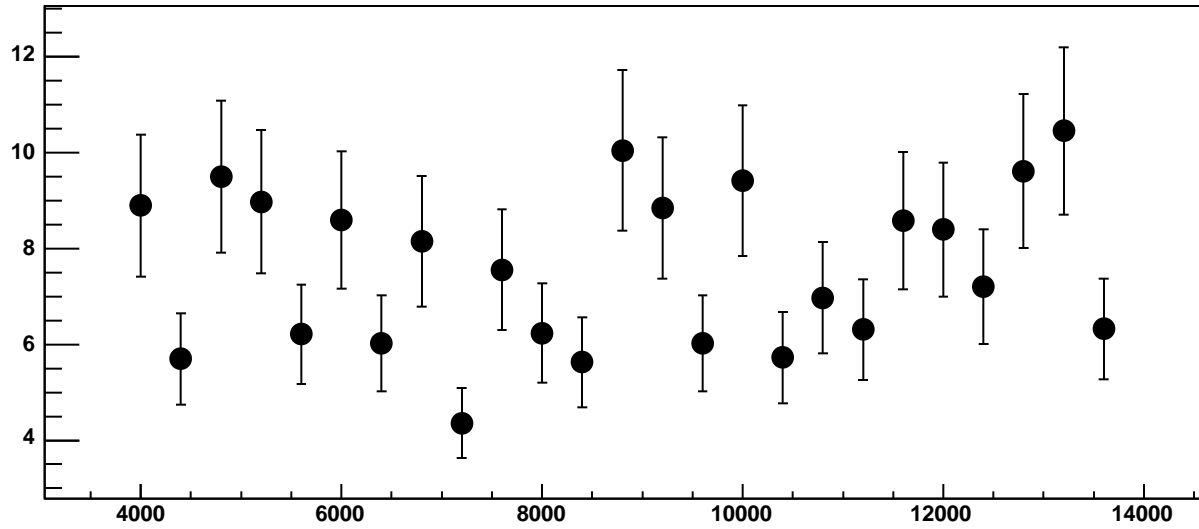
Chip 0, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



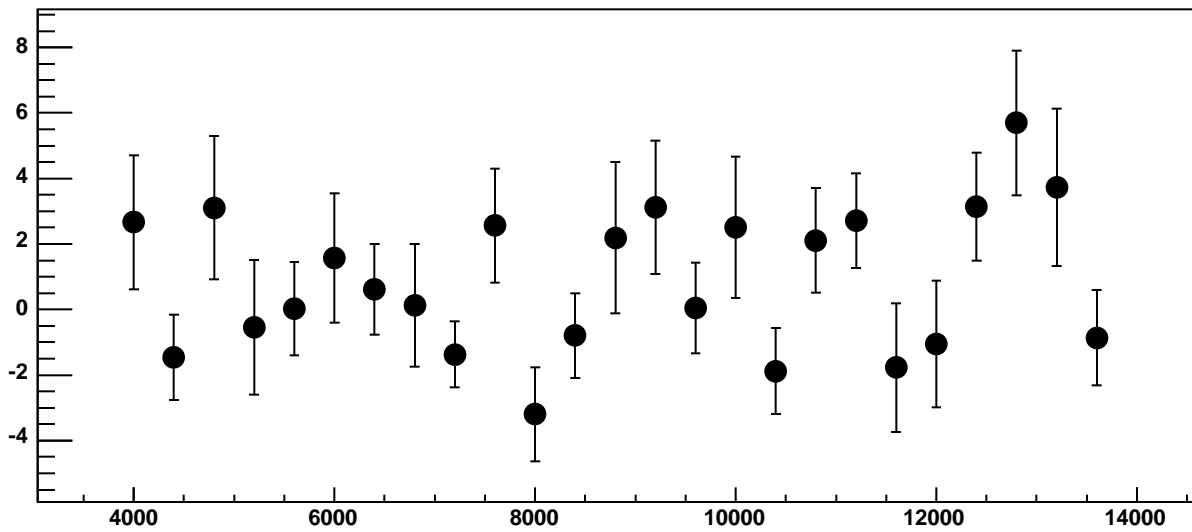
Chip 0, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



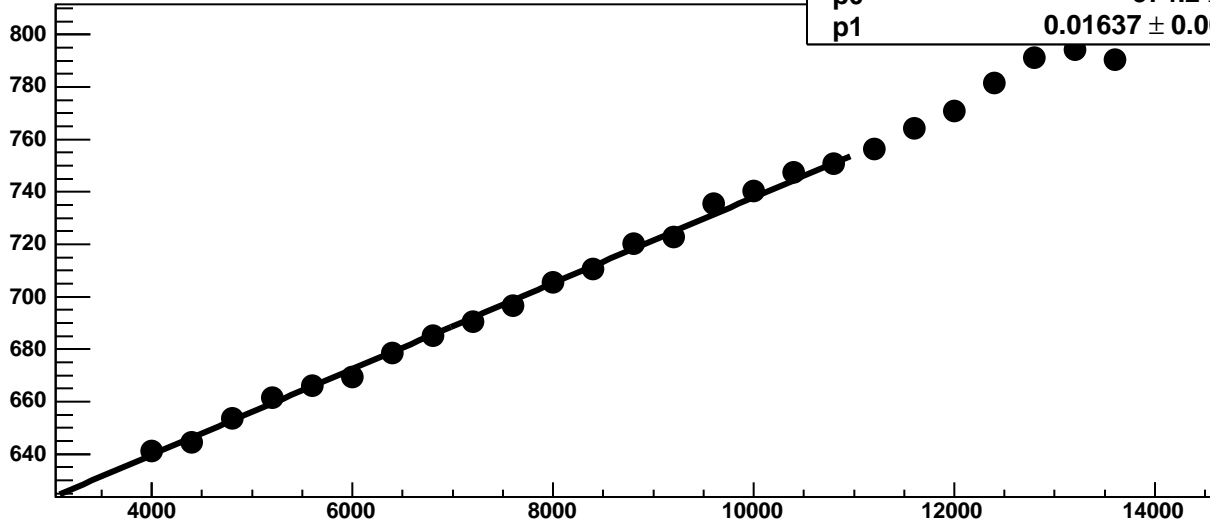
Chip 0, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



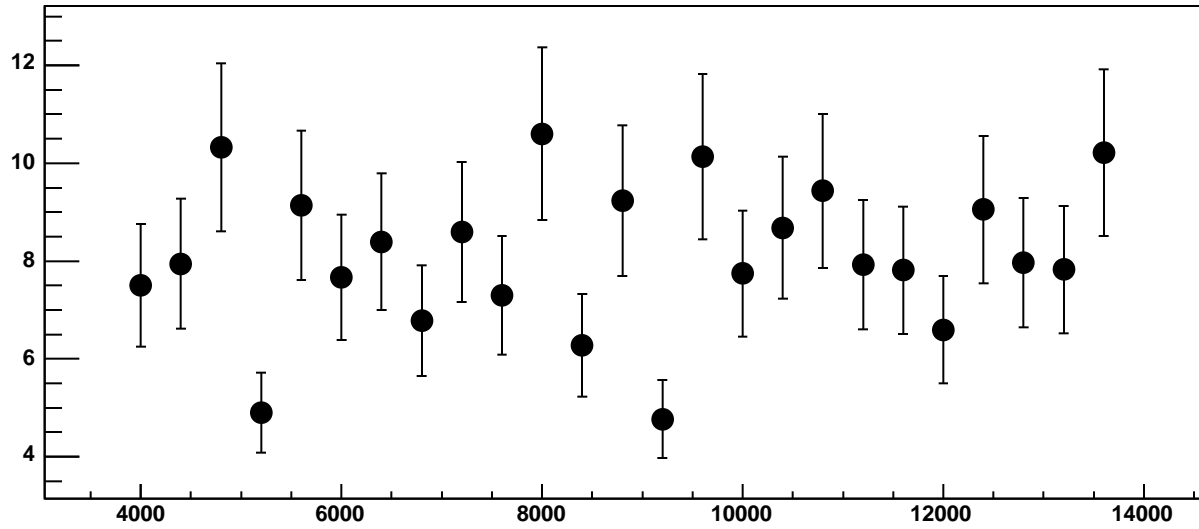
Chip 0, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



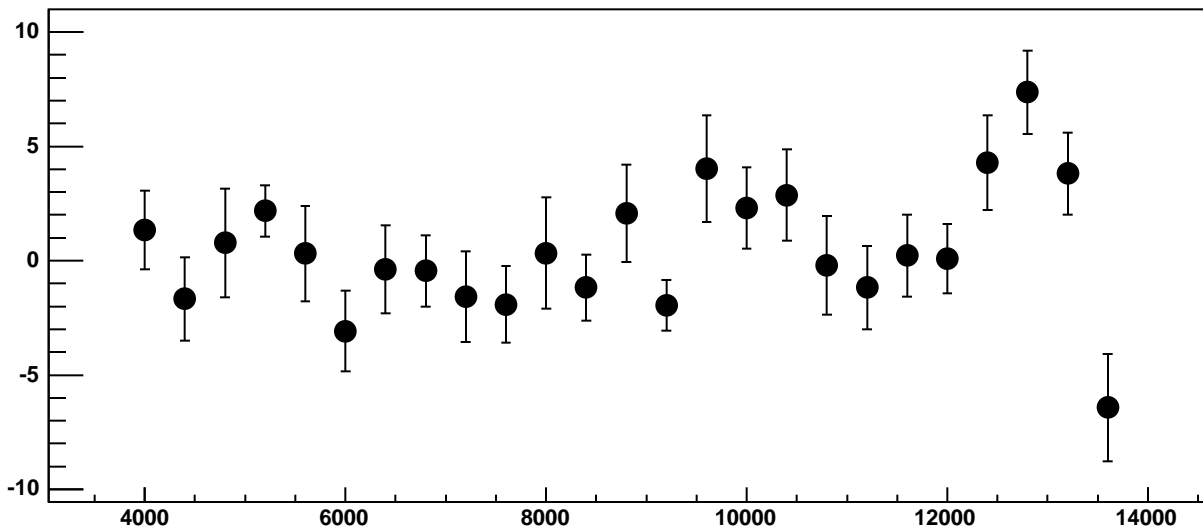
Chip 0, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



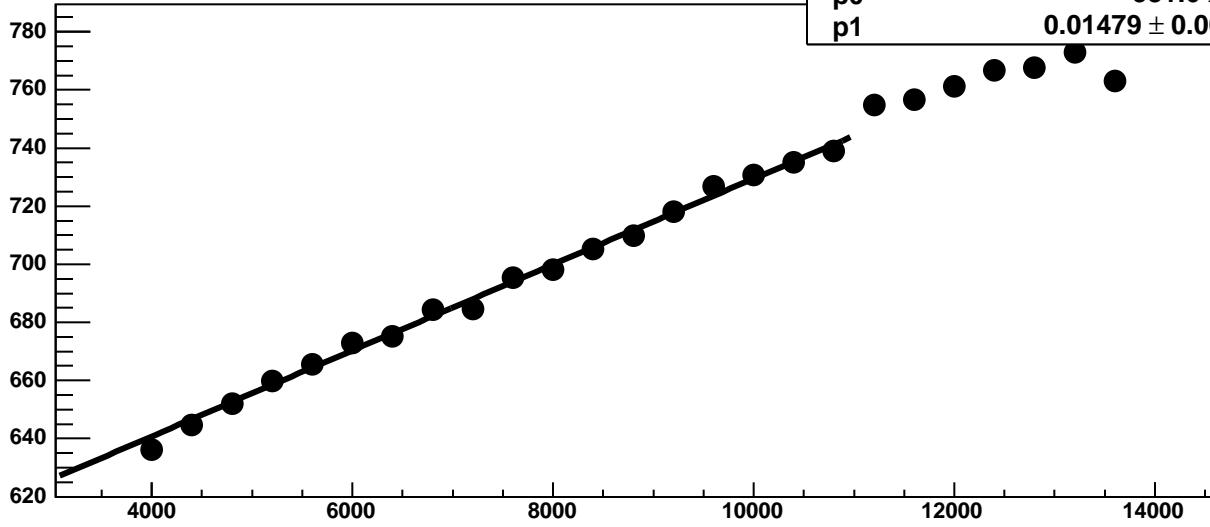
Chip 0, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



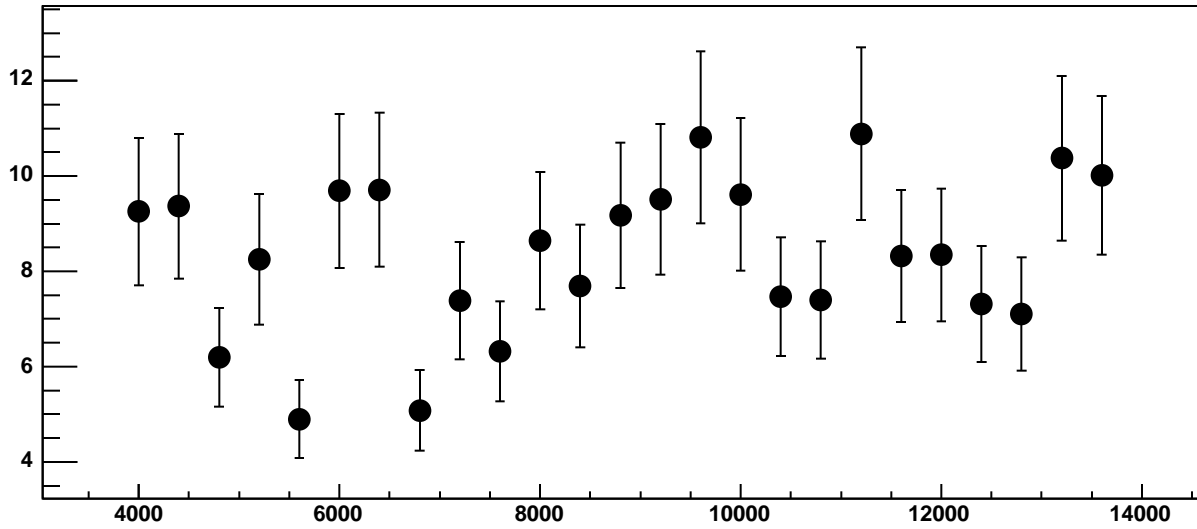
Chip 0, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



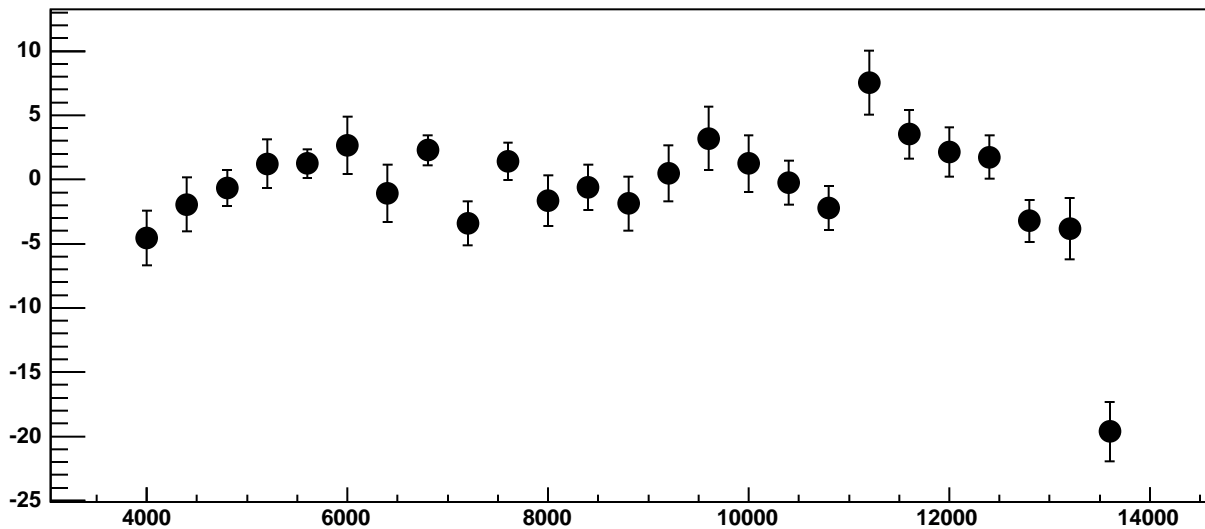
Chip 0, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC



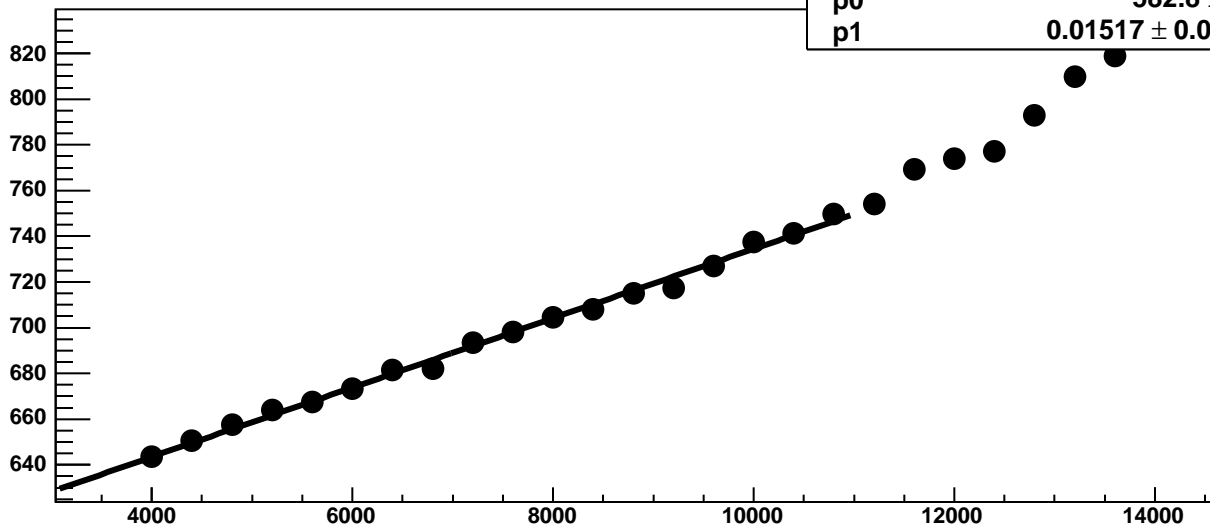
Chip 0, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

30.04 / 16

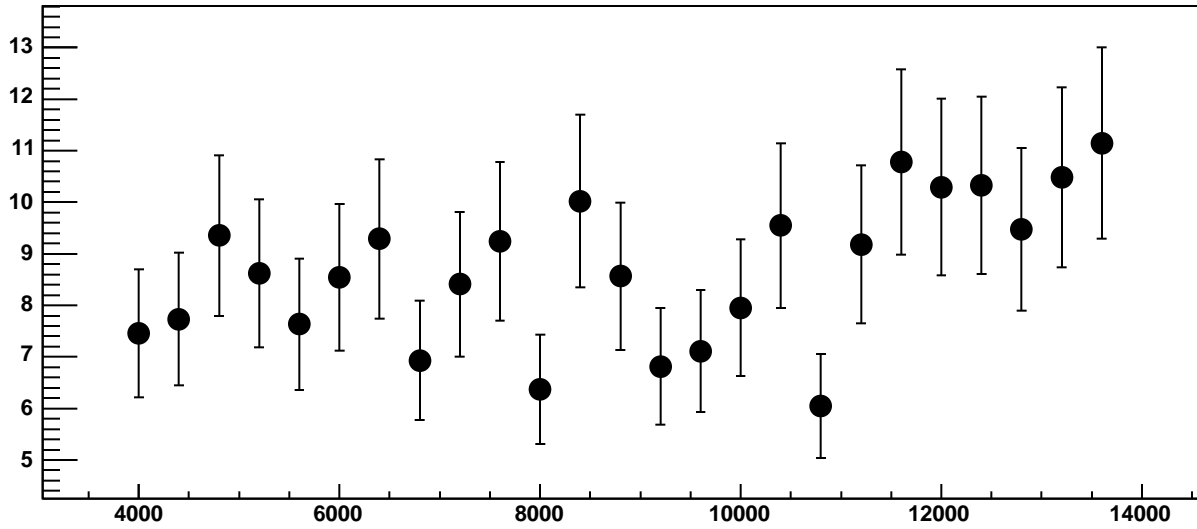
p0

582.8 ± 1.564

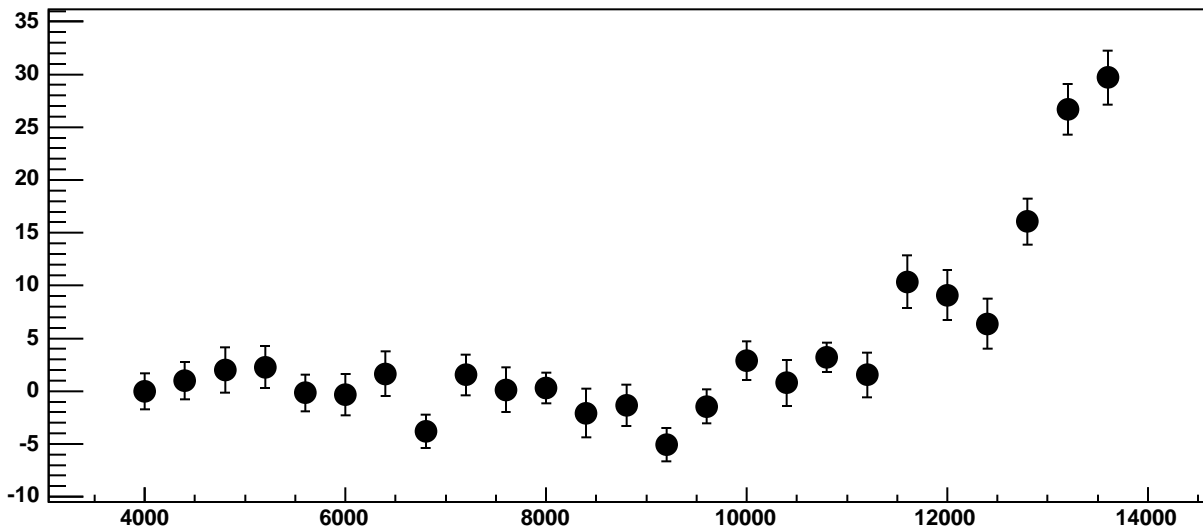
p1

0.01517 ± 0.0001991

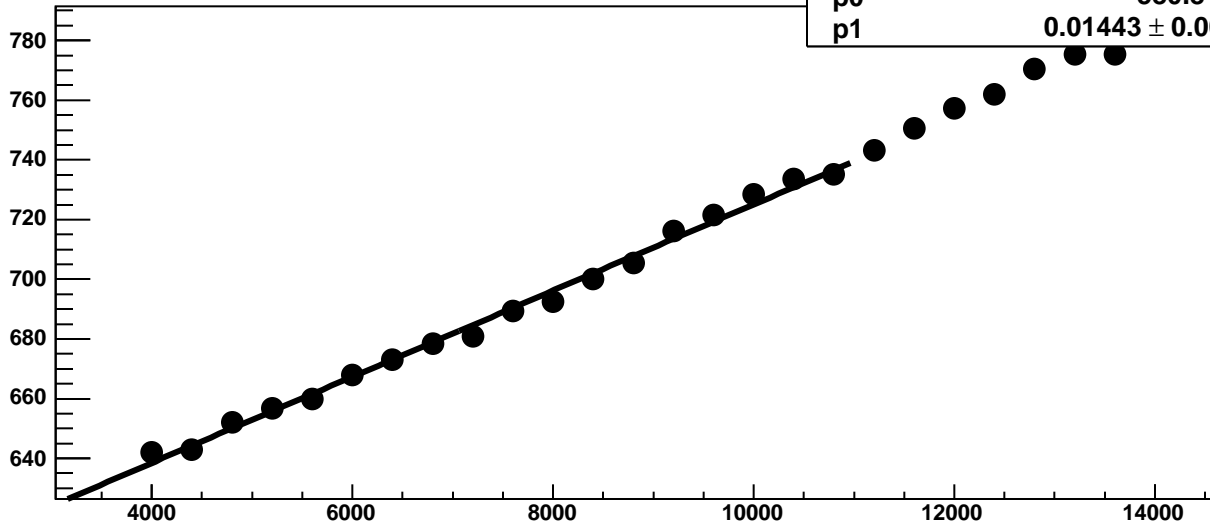
Chip 0, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

27.77 / 16

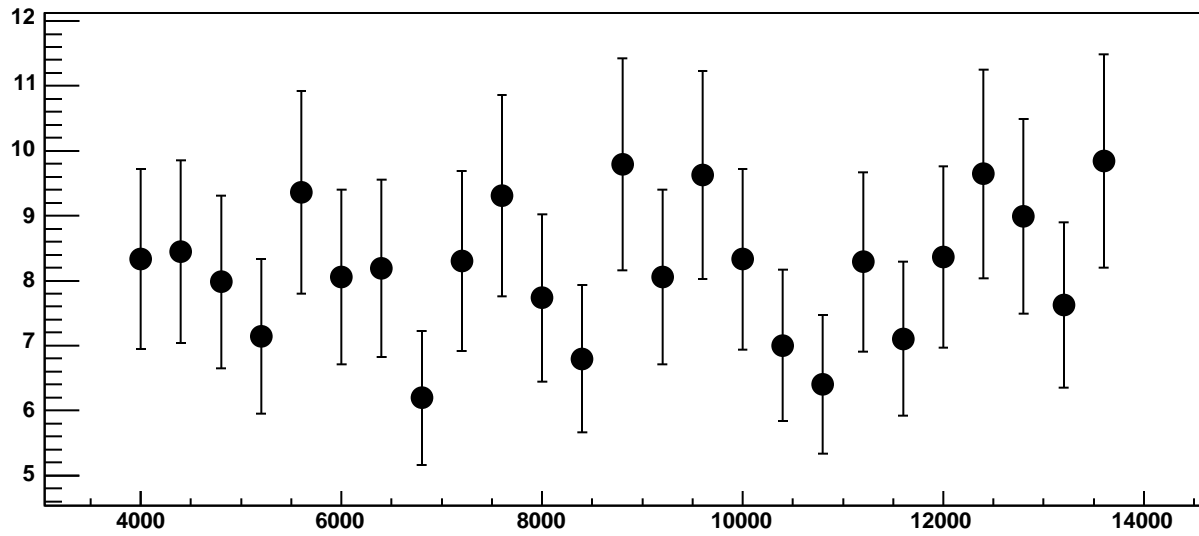
p0

580.8 ± 1.571

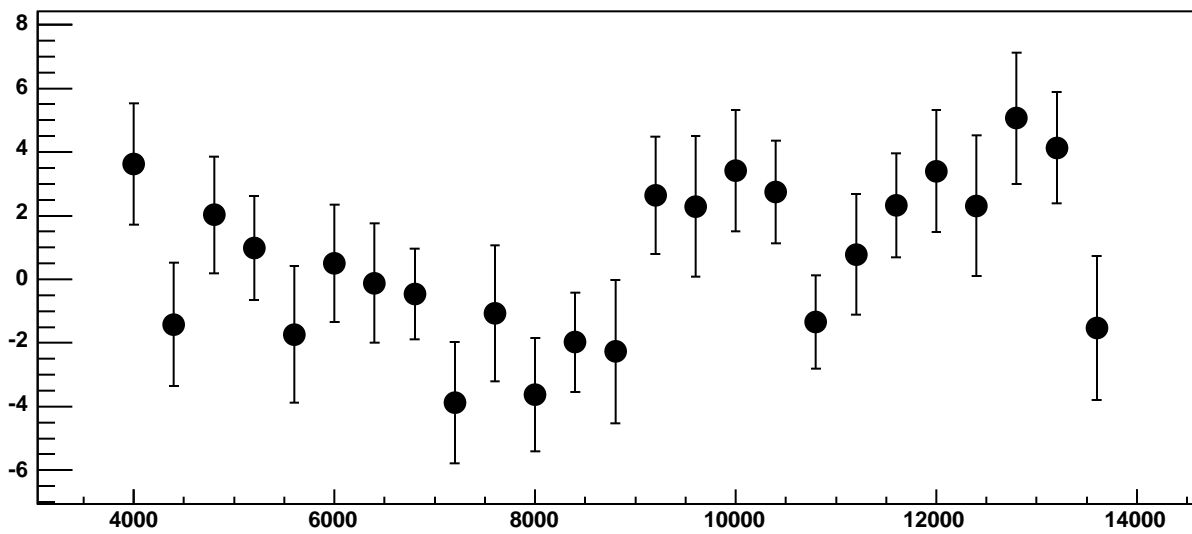
p1

0.01443 ± 0.0002015

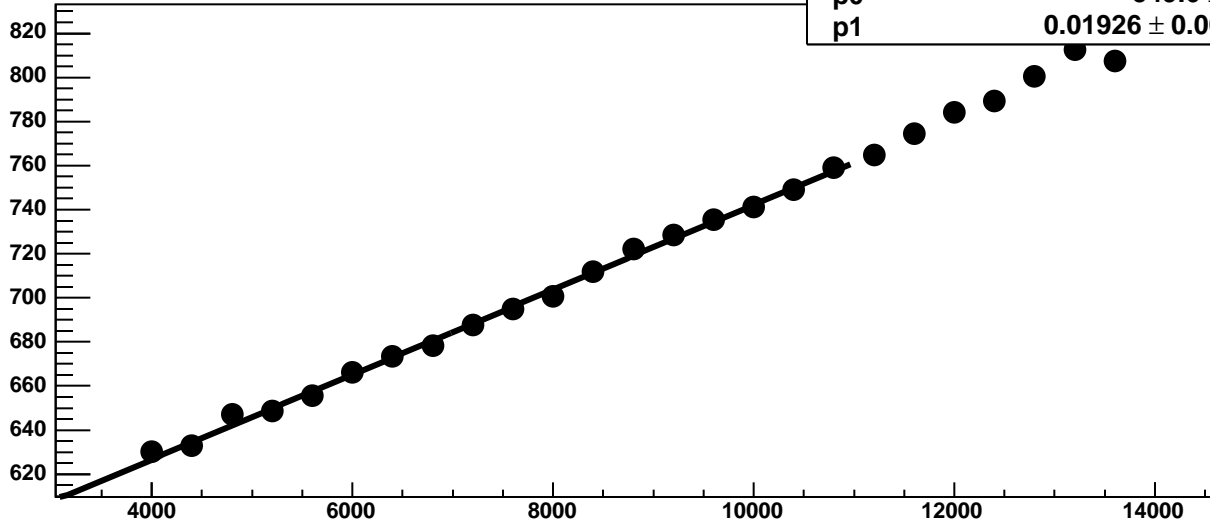
Chip 0, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

20.09 / 16

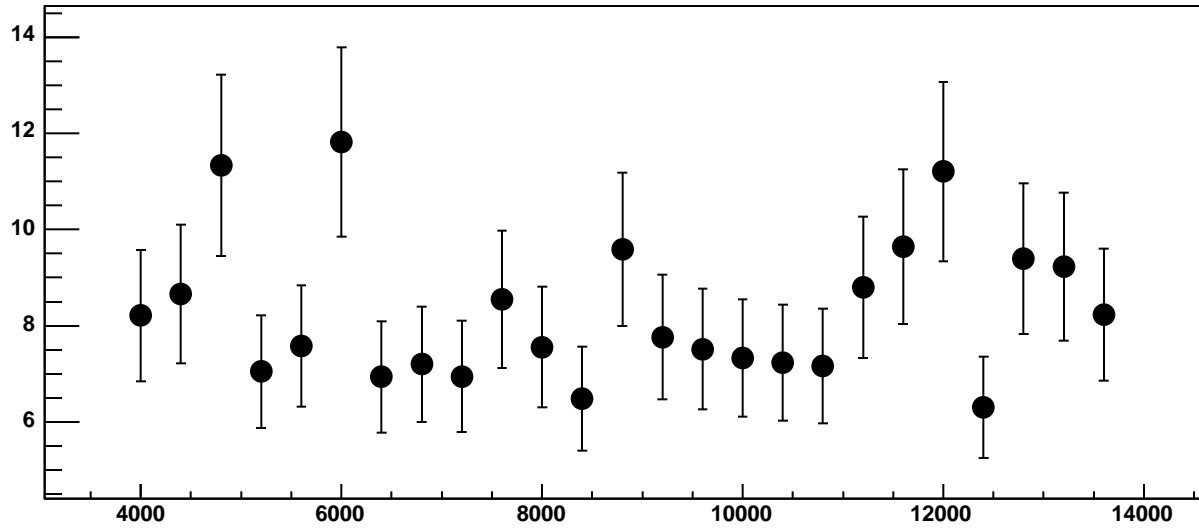
p0

549.6 ± 1.619

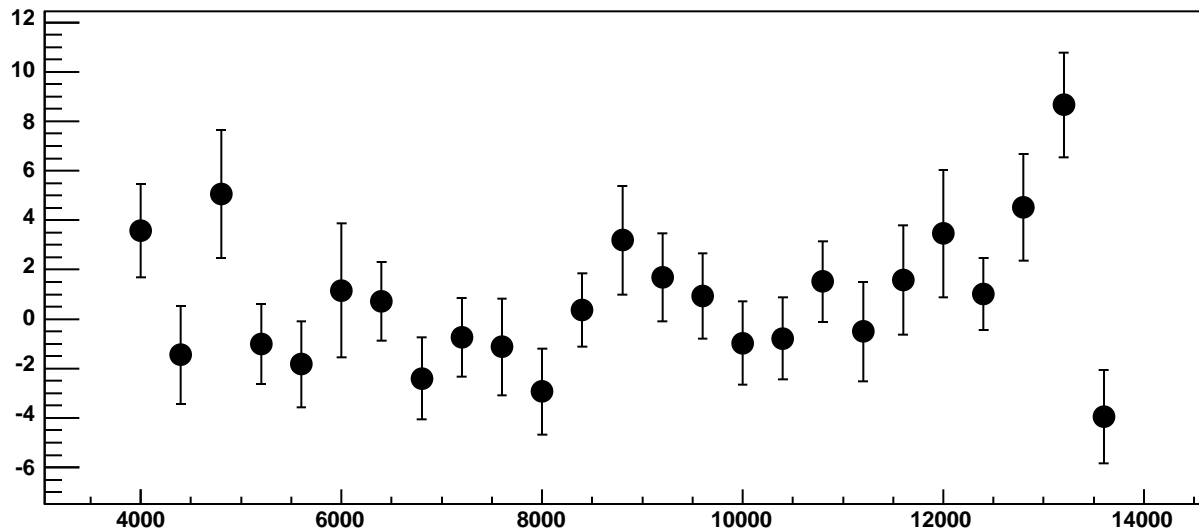
p1

0.01926 ± 0.0002057

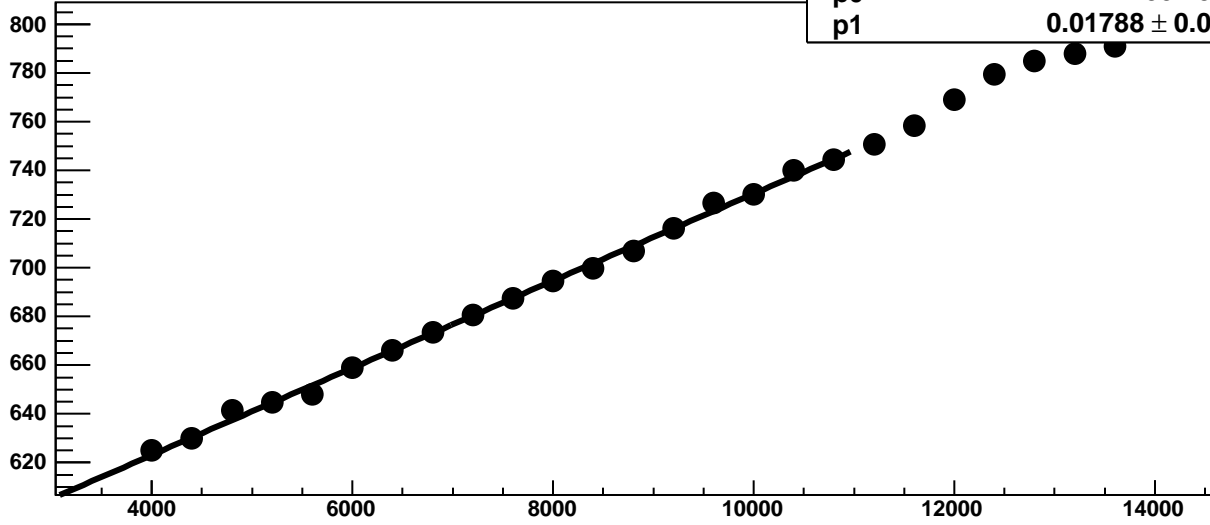
Chip 0, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

18.81 / 16

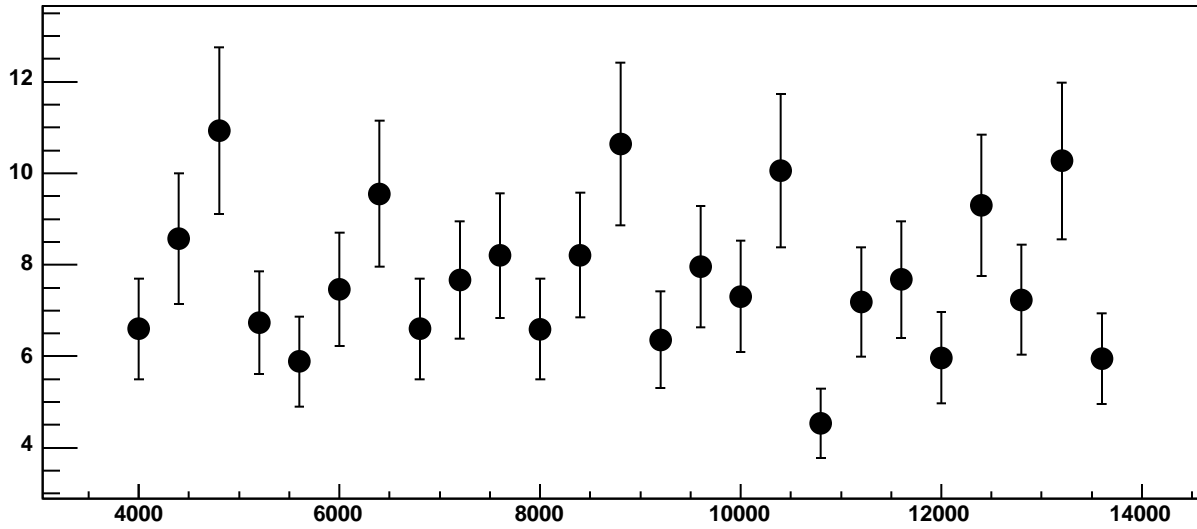
p0

551.5 ± 1.41

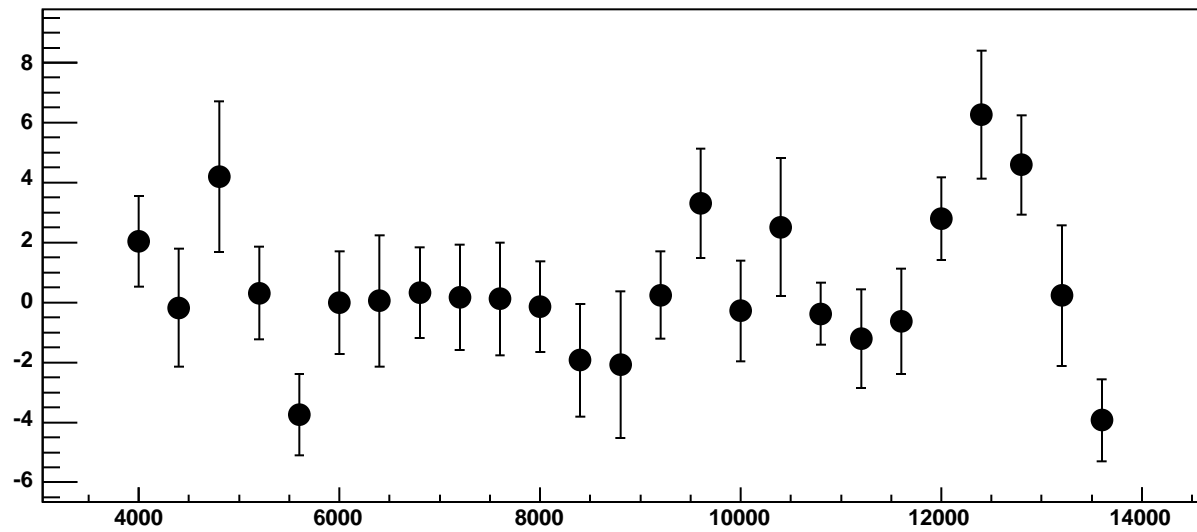
p1

0.01788 ± 0.0001781

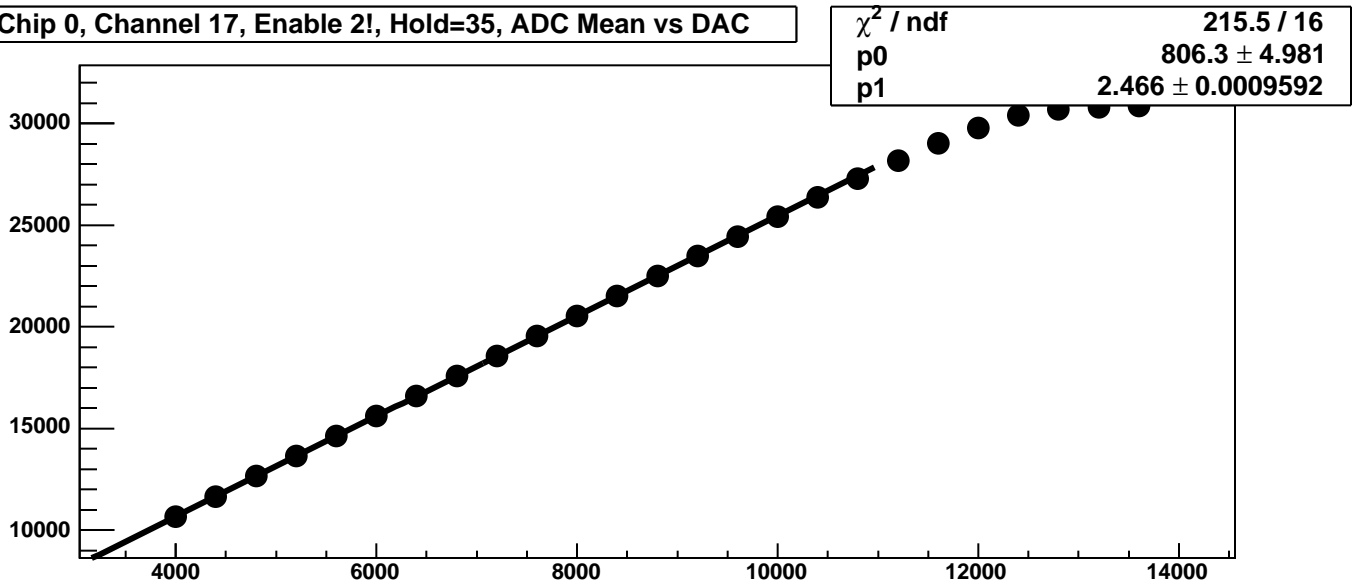
Chip 0, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



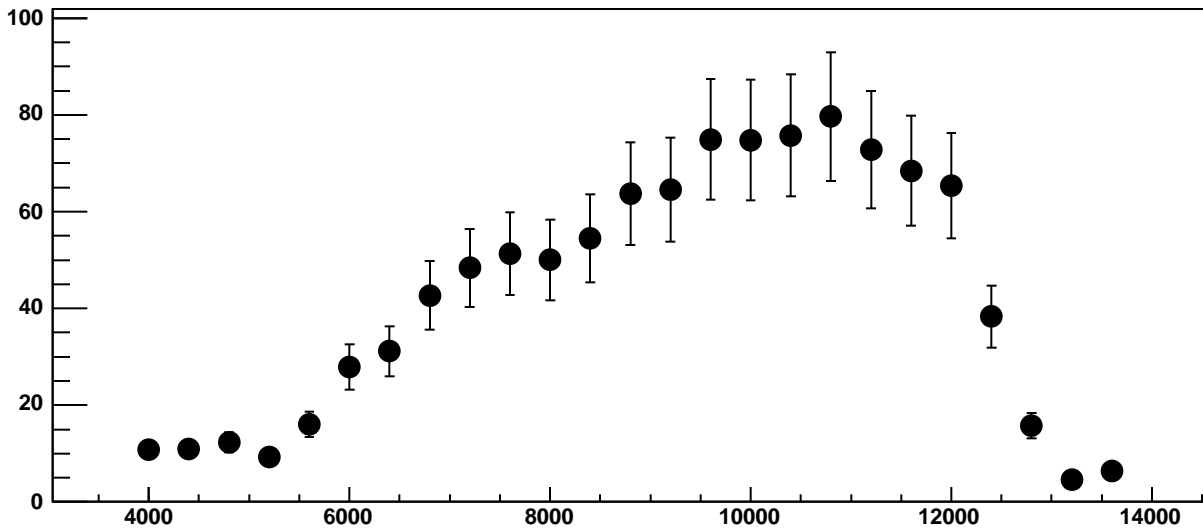
Chip 0, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



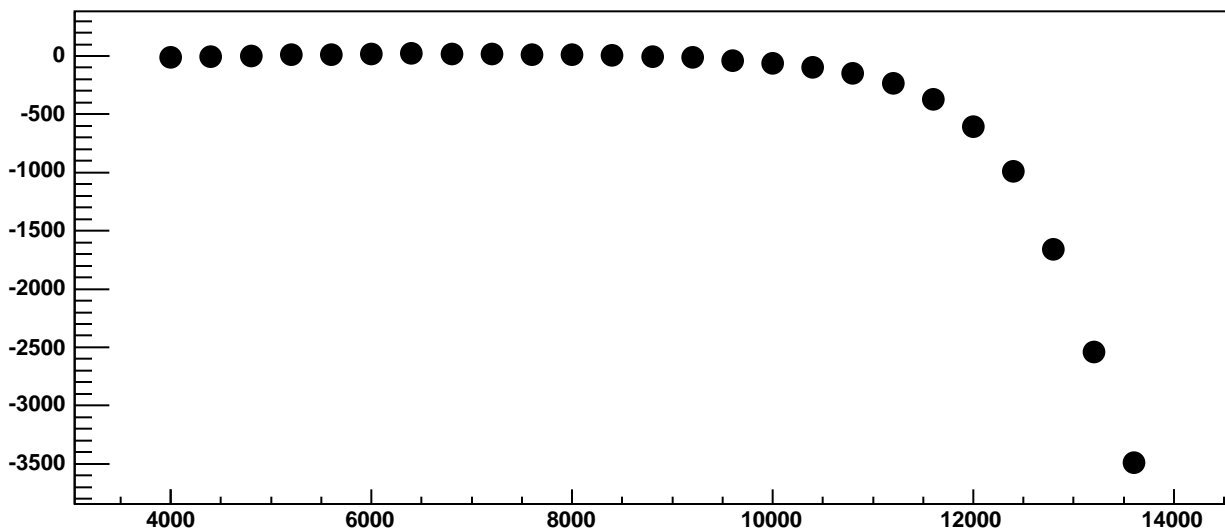
Chip 0, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC



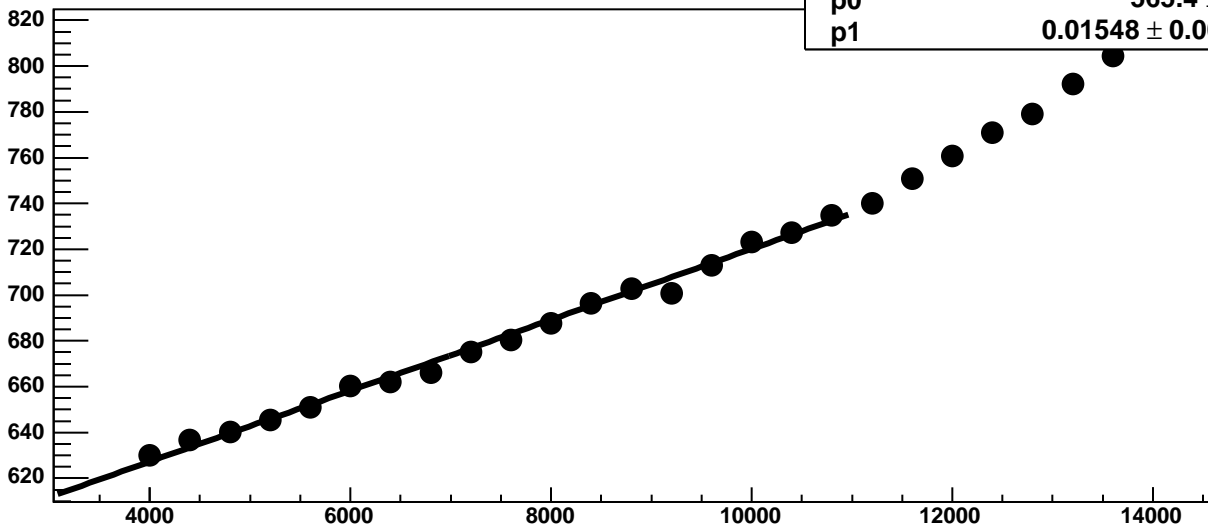
Chip 0, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

36.19 / 16

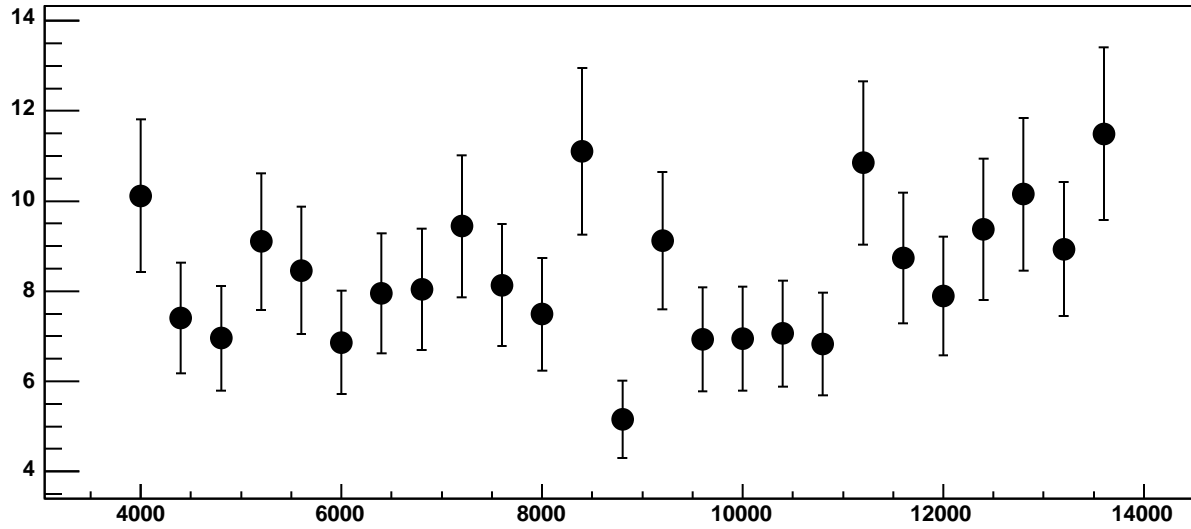
p0

565.4 ± 1.559

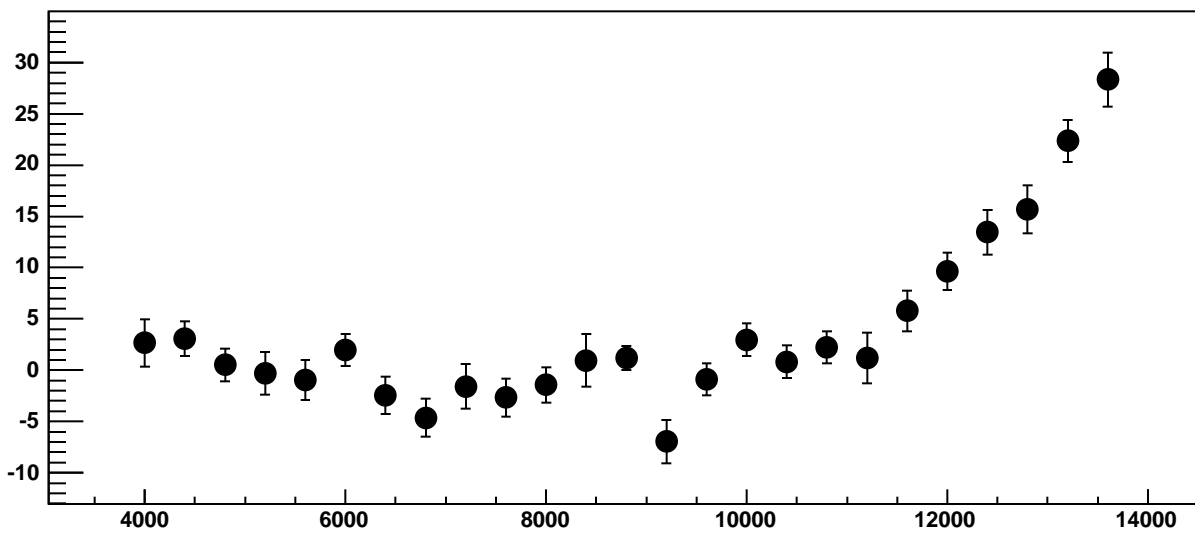
p1

0.01548 ± 0.0001966

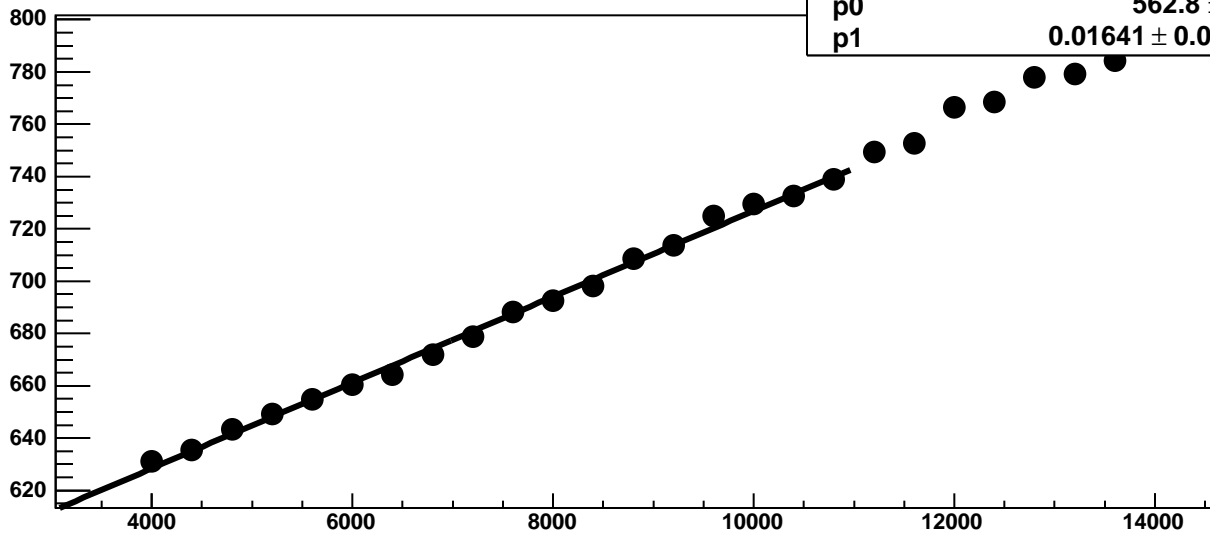
Chip 0, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

23.47 / 16

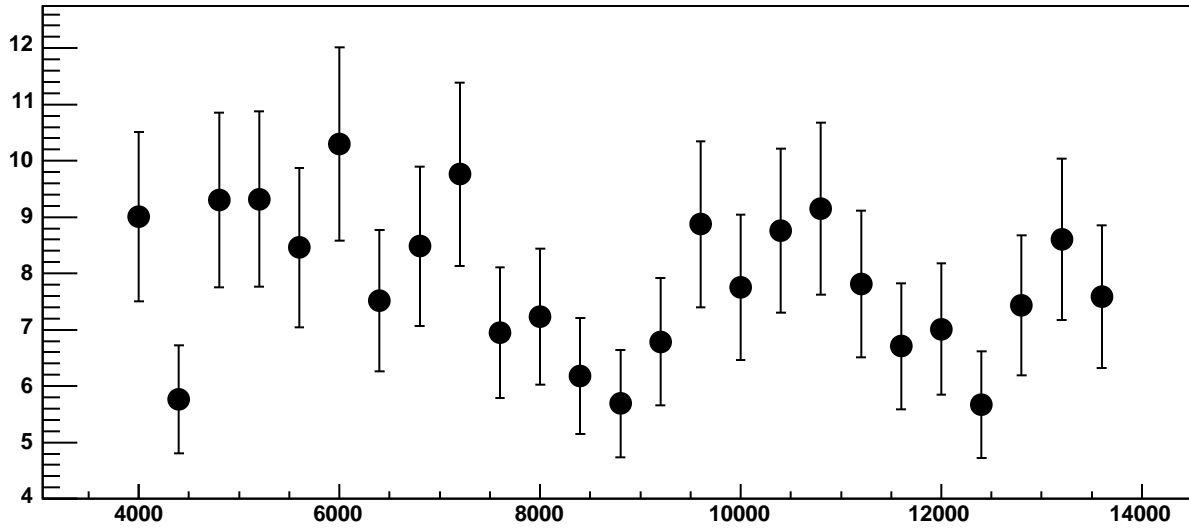
p0

562.8 ± 1.602

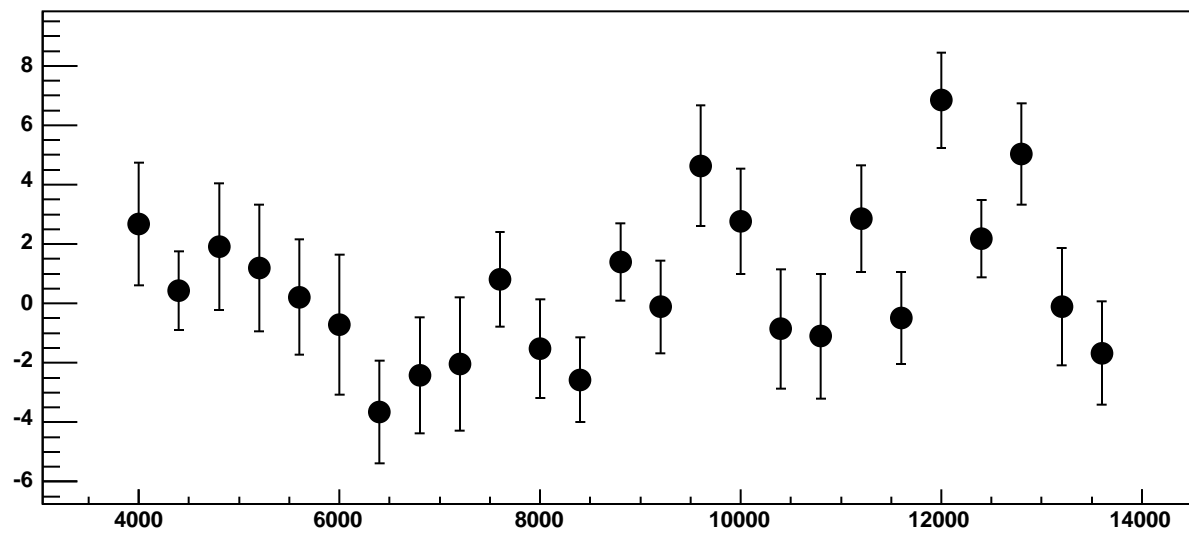
p1

0.01641 ± 0.0002071

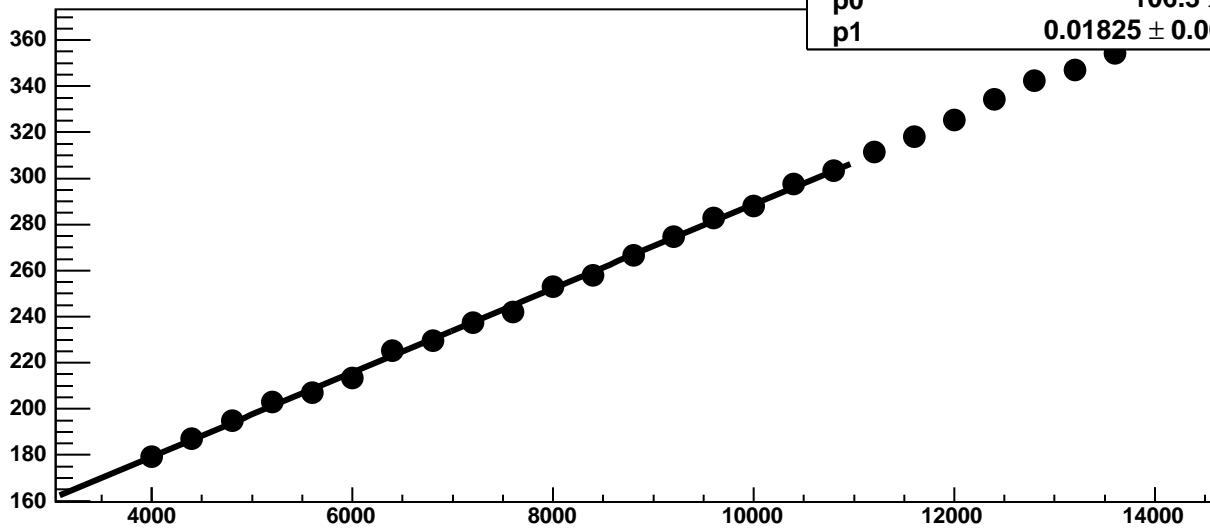
Chip 0, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

13.44 / 16

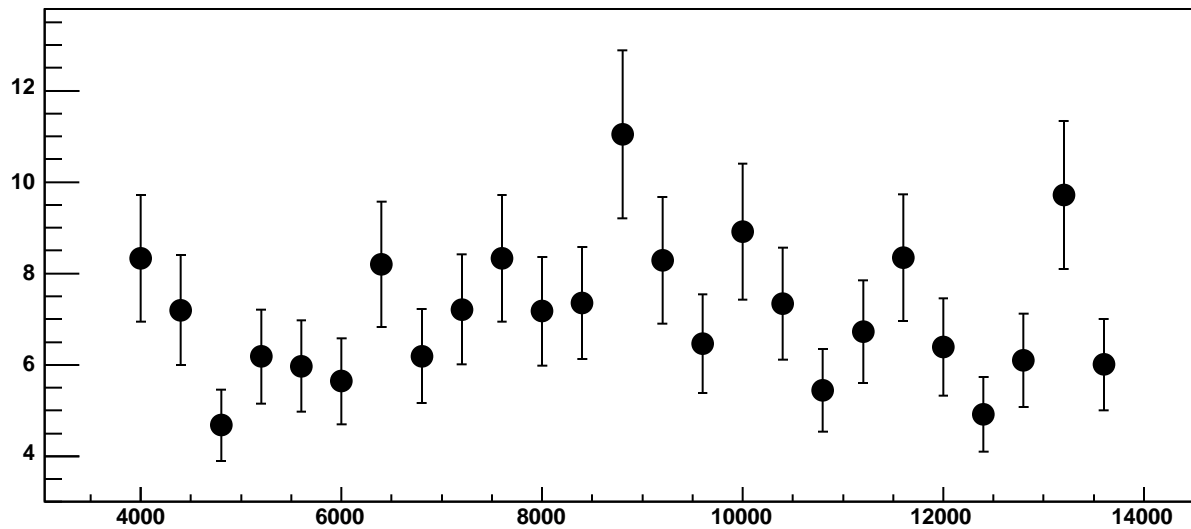
p0

106.3 ± 1.287

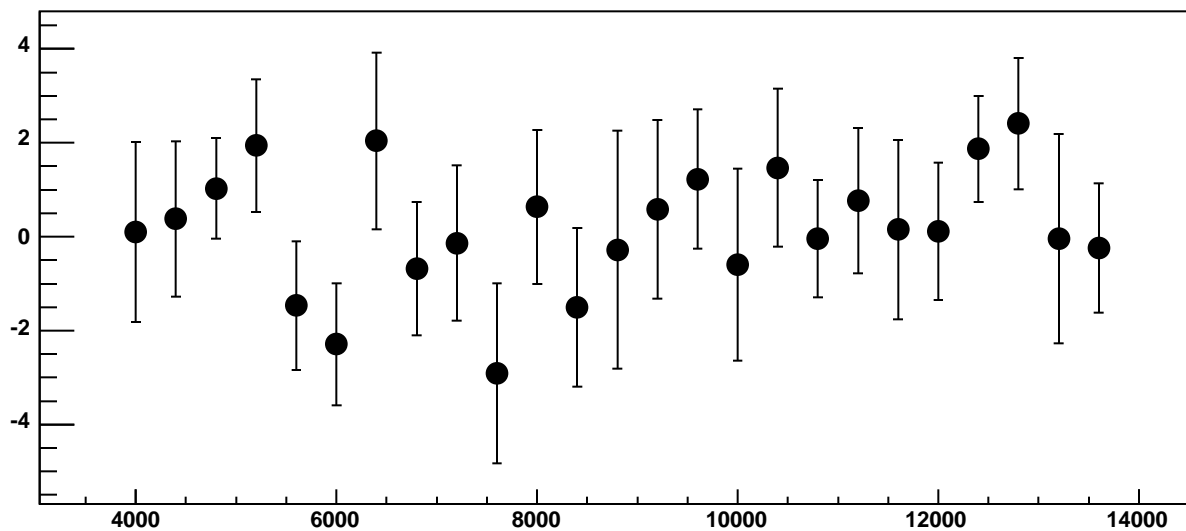
p1

0.01825 ± 0.0001718

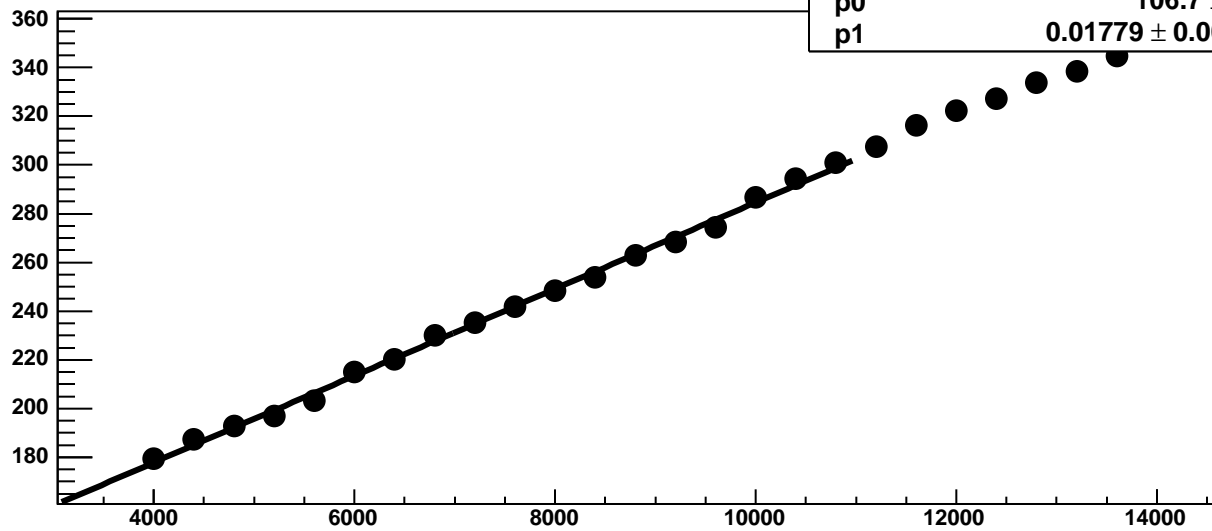
Chip 1, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

26.09 / 16

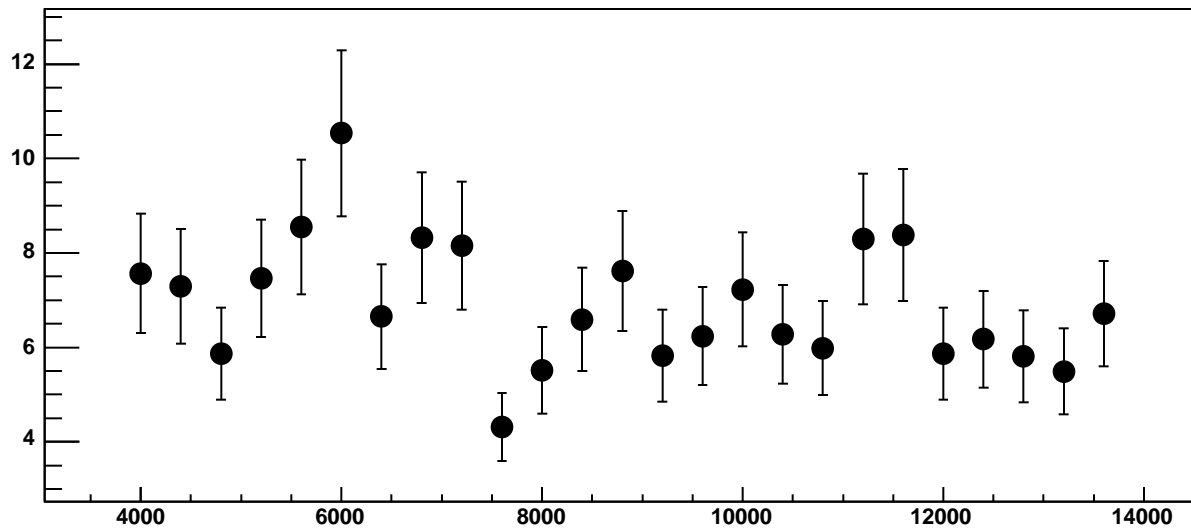
p0

106.7 ± 1.398

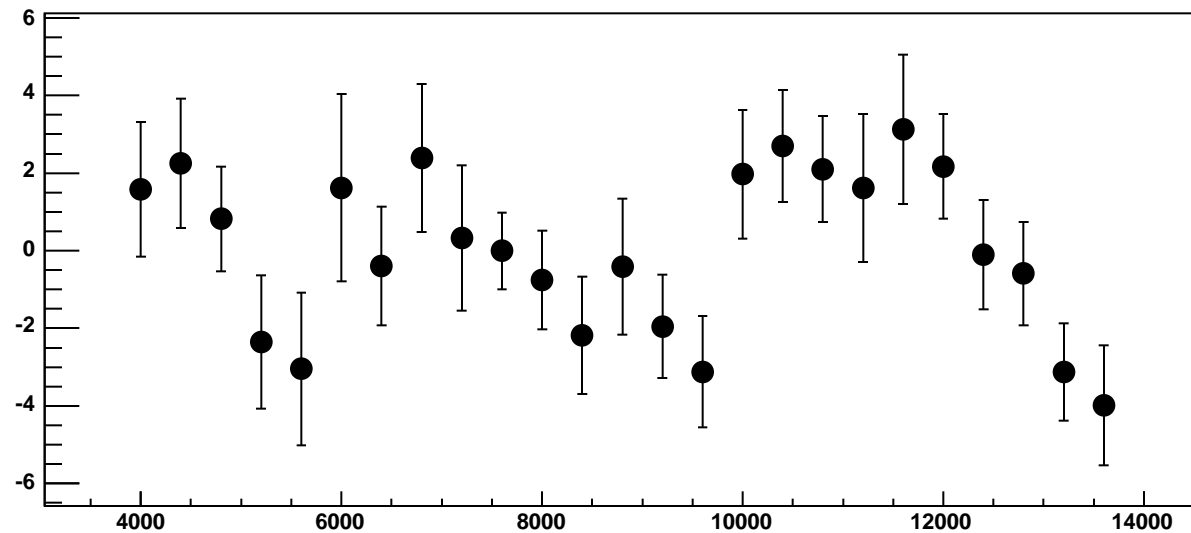
p1

0.01779 ± 0.0001765

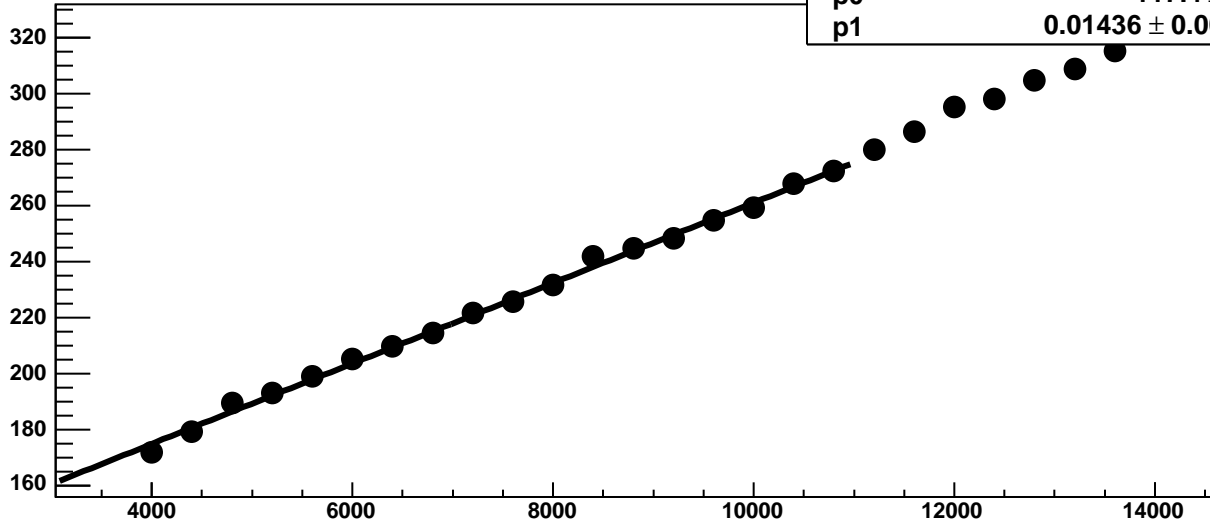
Chip 1, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



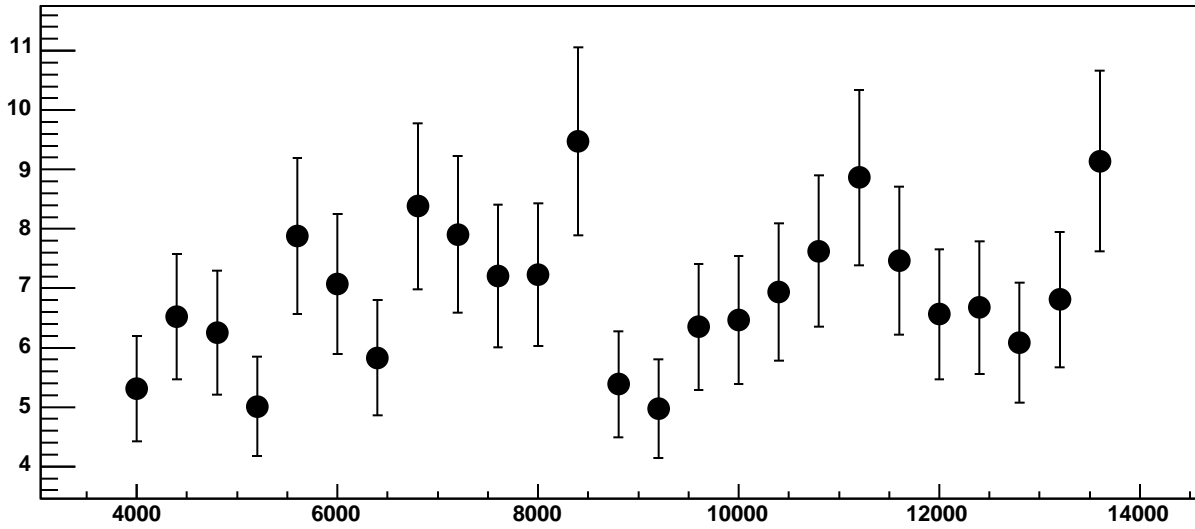
Chip 1, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



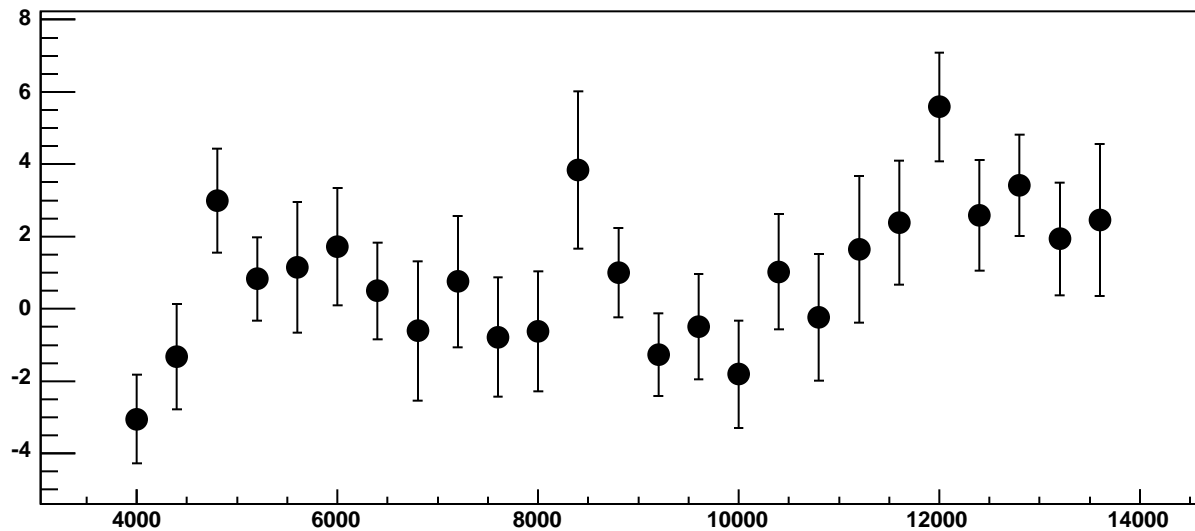
Chip 1, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



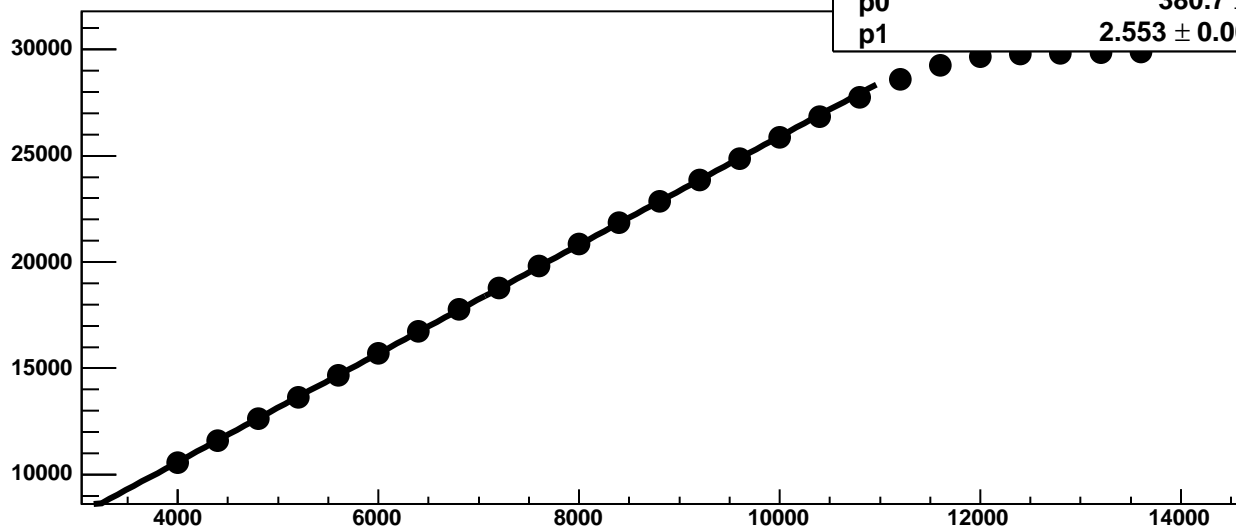
Chip 1, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

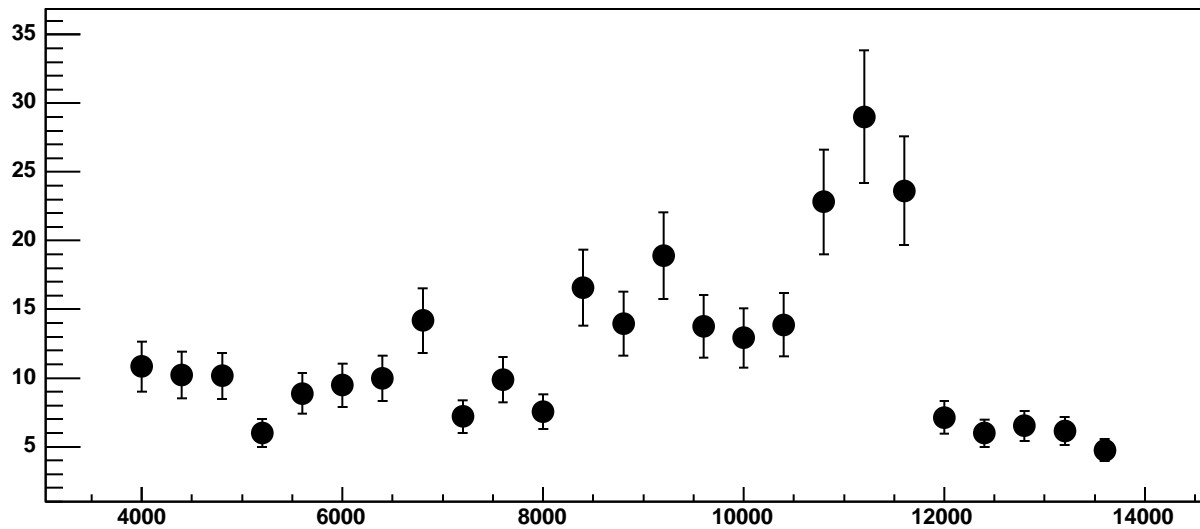


Chip 1, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC

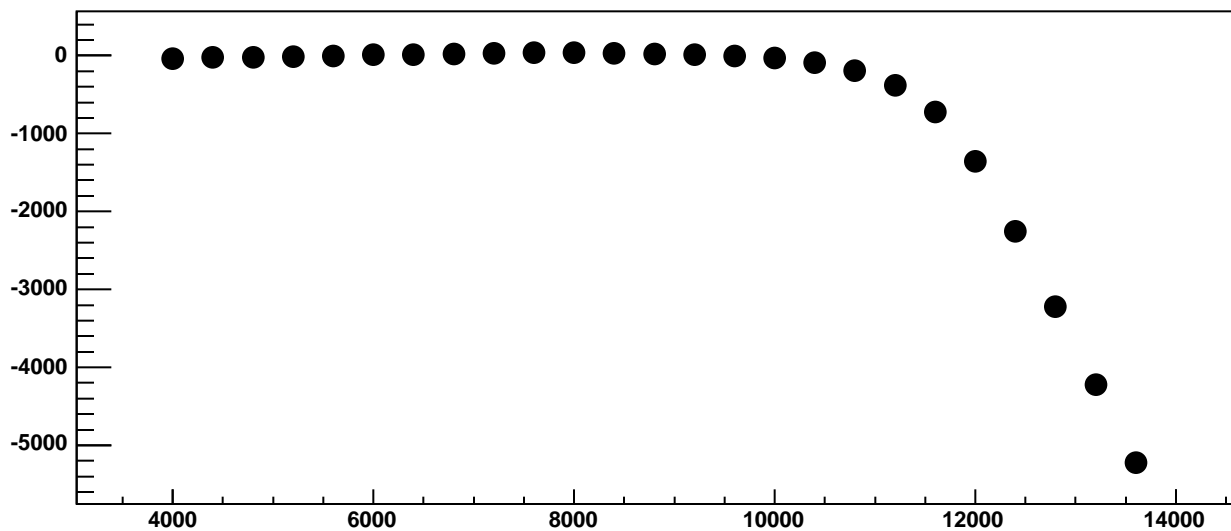


χ^2 / ndf 4028 / 16
p0 380.7 ± 2.165
p1 2.553 ± 0.0003128

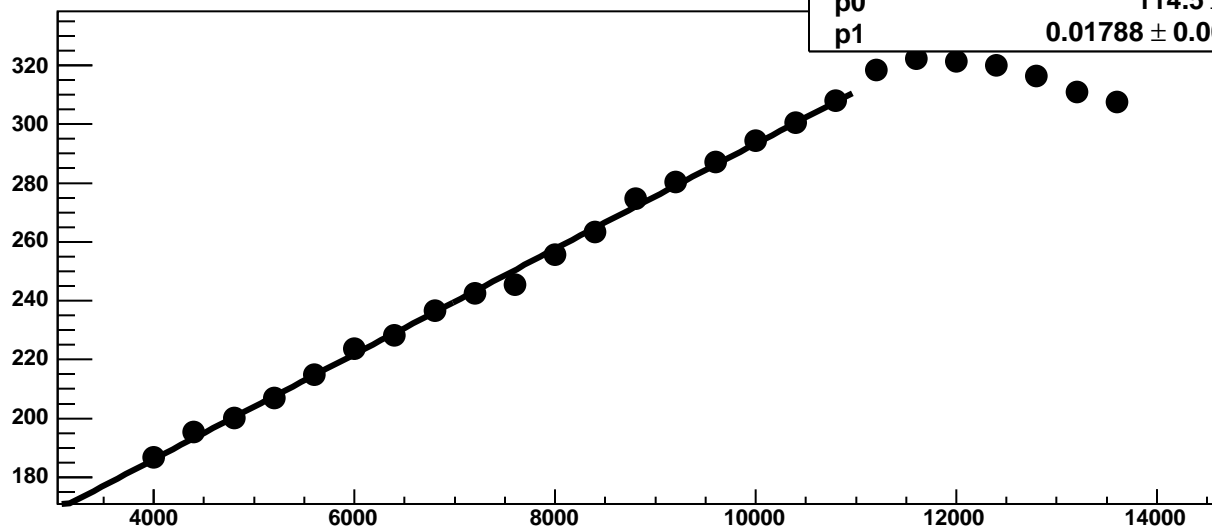
Chip 1, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



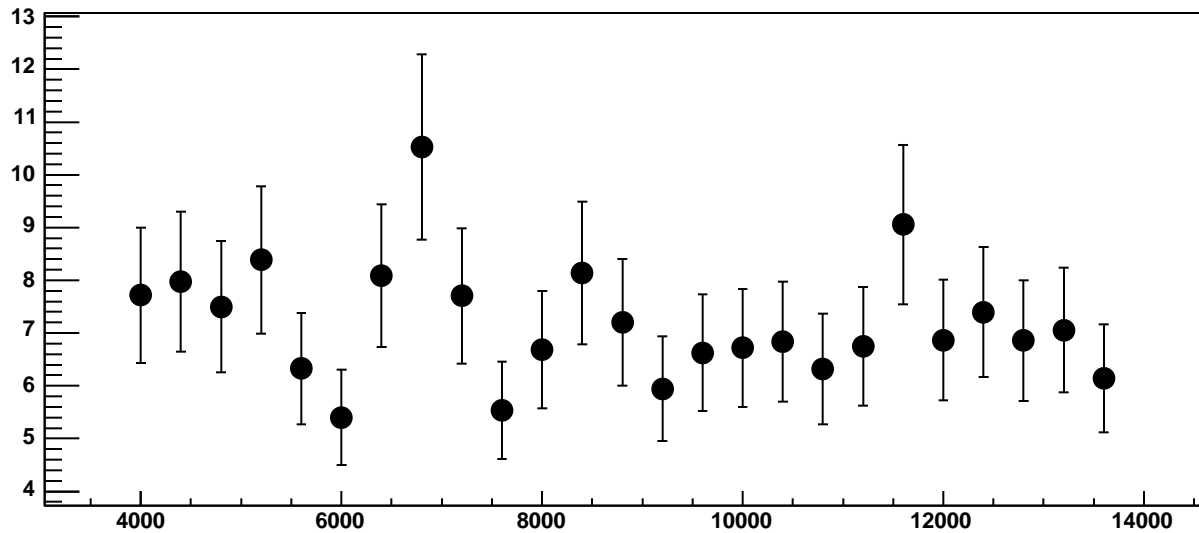
Chip 1, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC



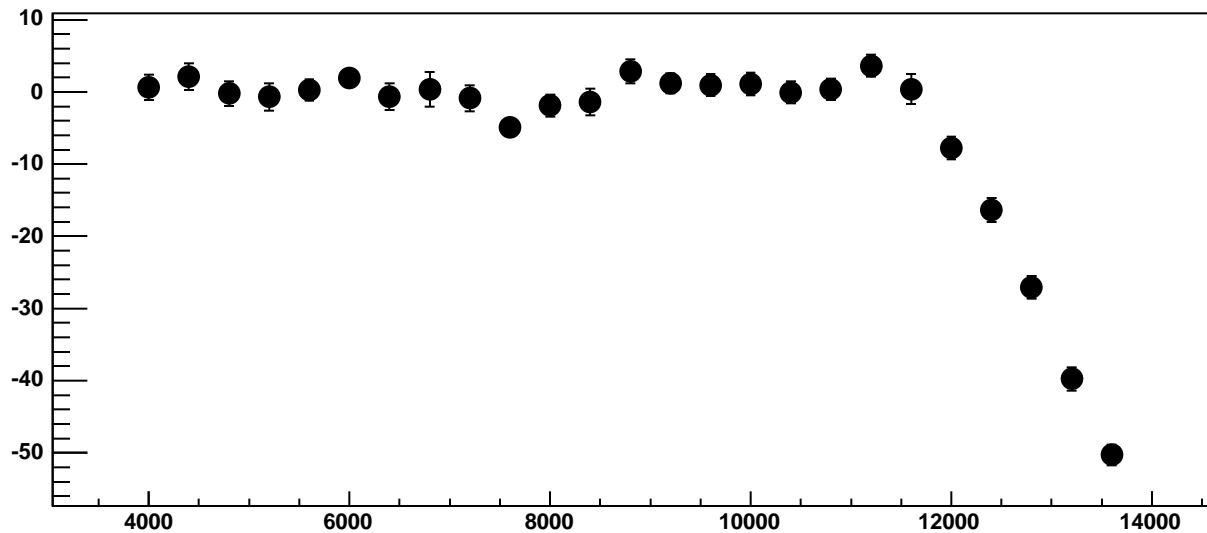
Chip 1, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC



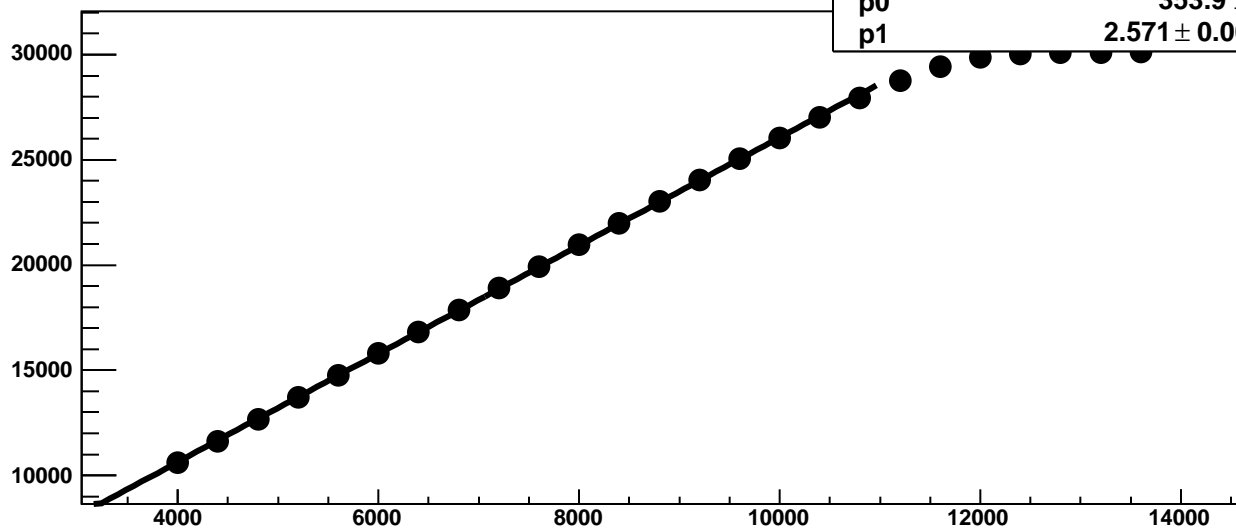
Chip 1, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



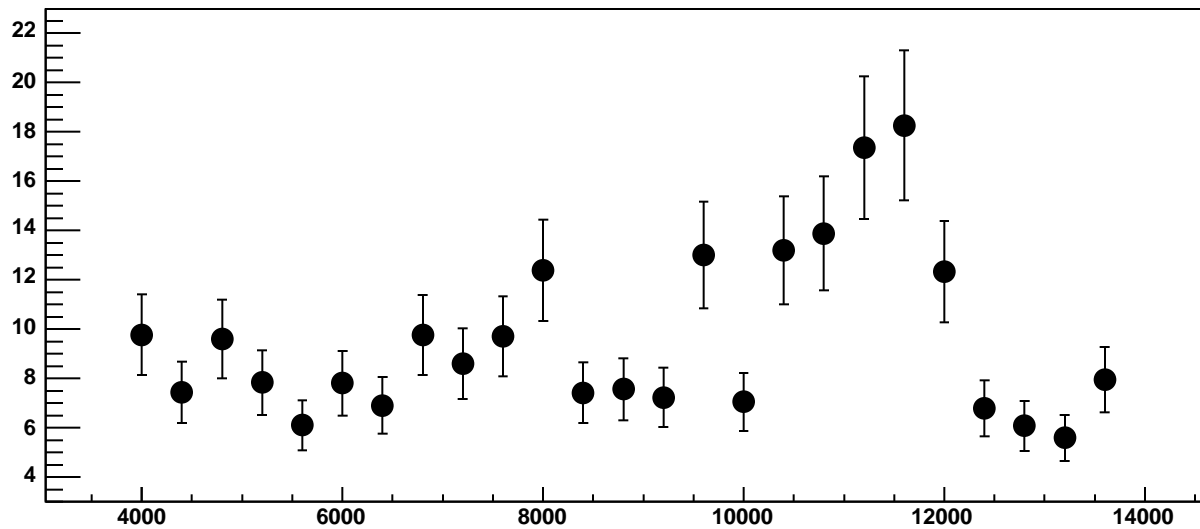
Chip 1, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



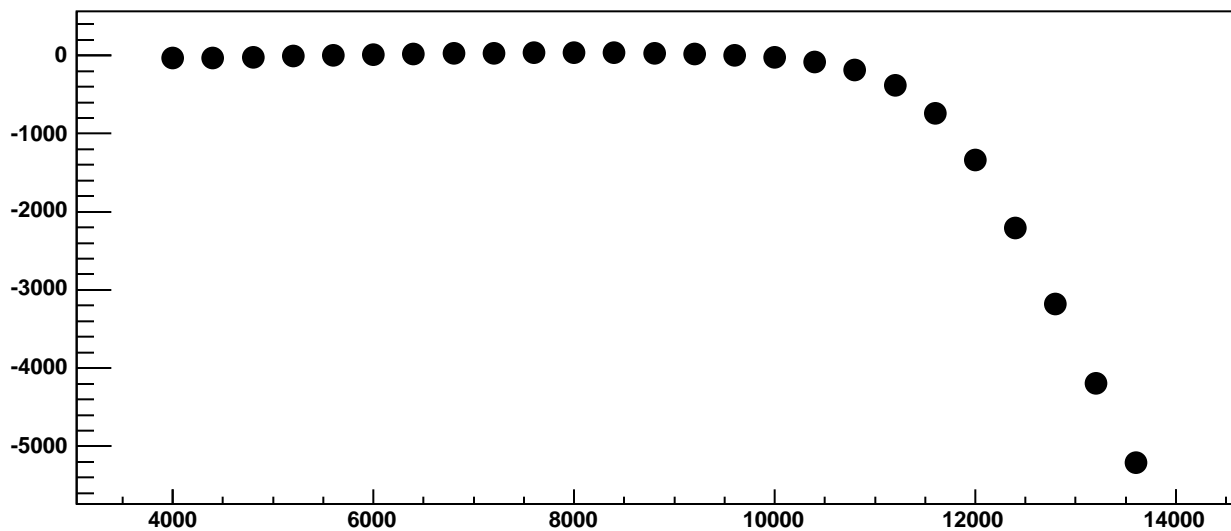
Chip 1, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



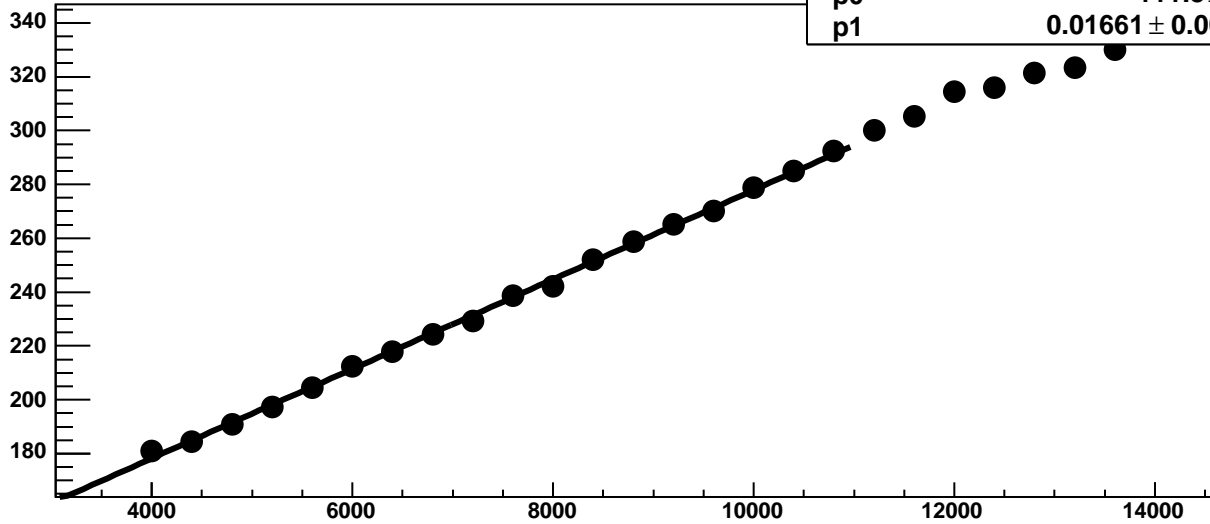
Chip 1, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC

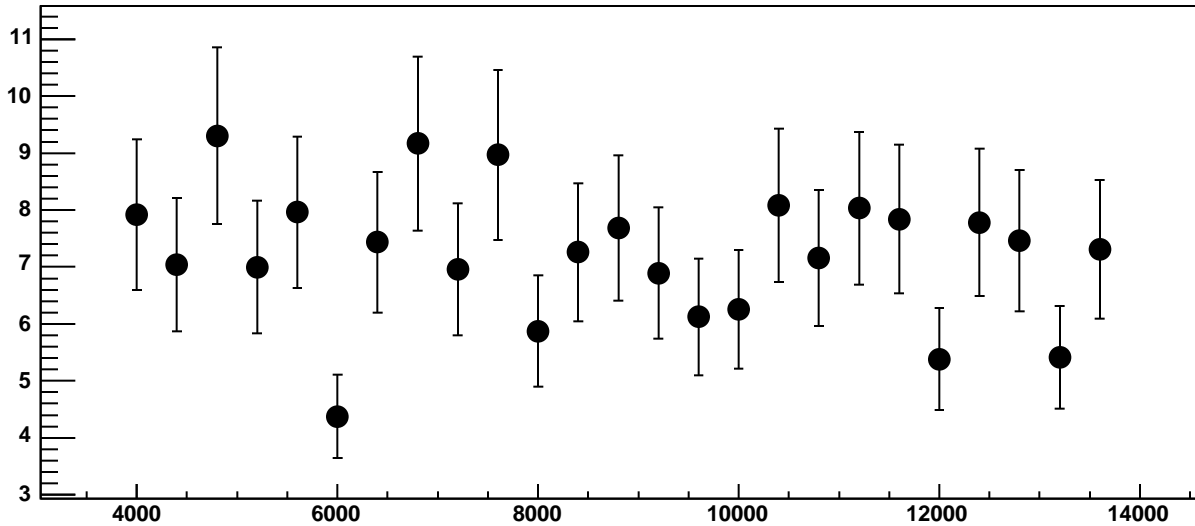


Chip 1, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

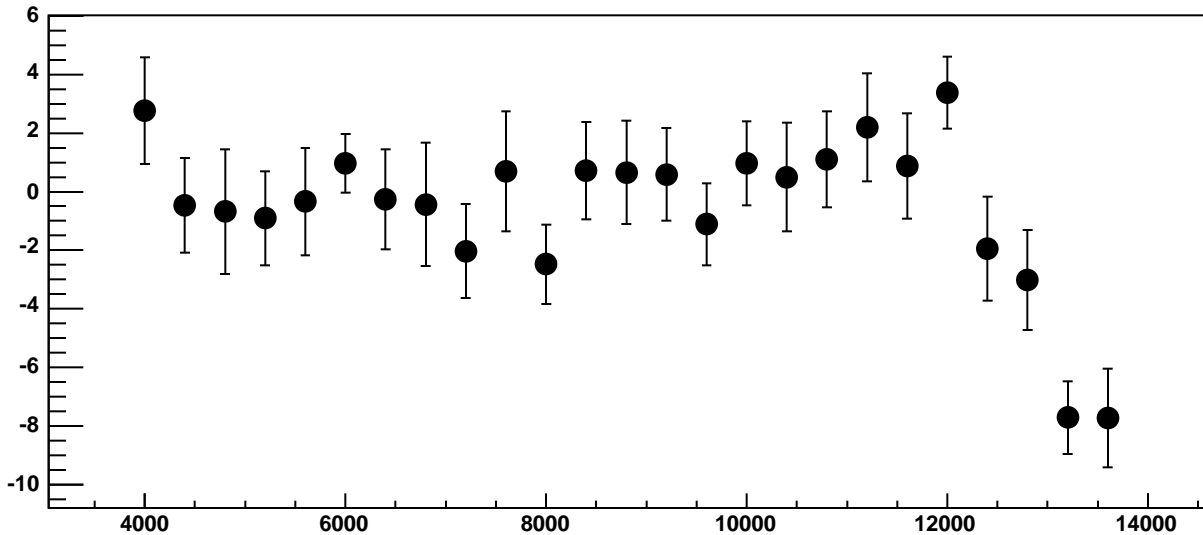


χ^2 / ndf 11.03 / 16
p0 111.8 ± 1.438
p1 0.01661 ± 0.0001864

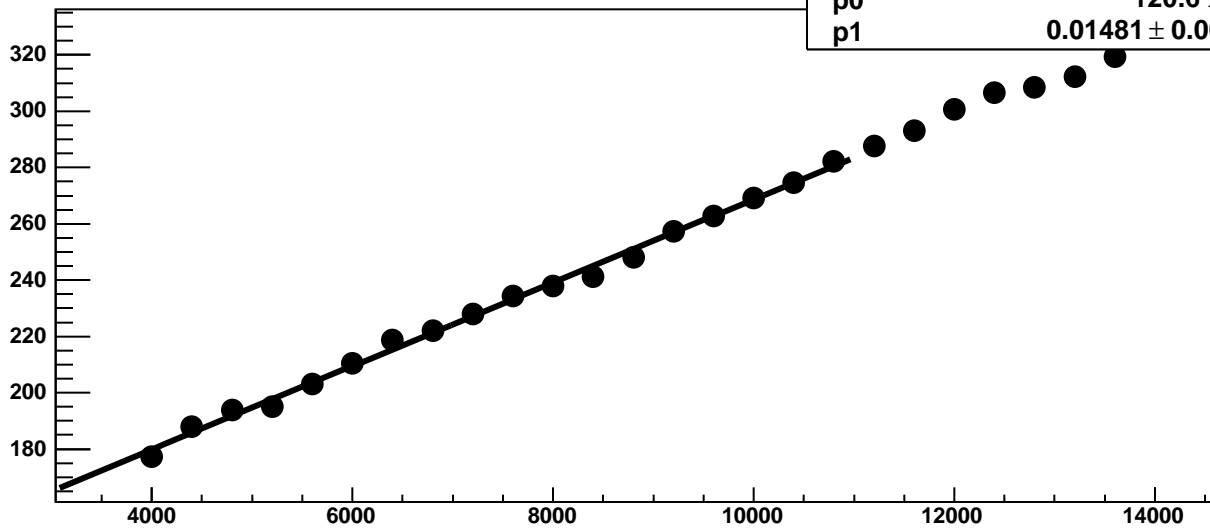
Chip 1, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

25.32 / 16

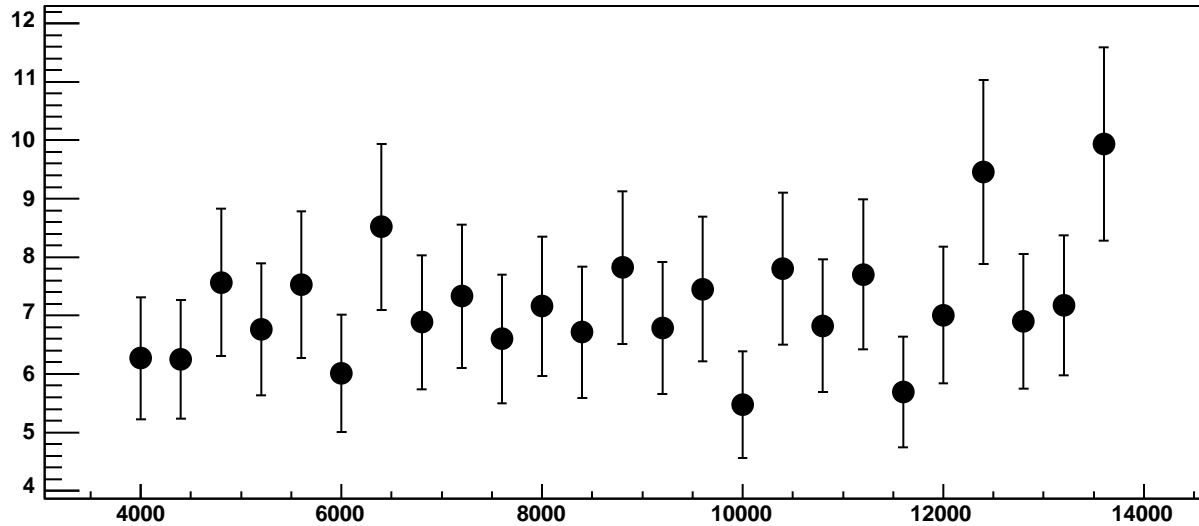
p0

120.6 ± 1.336

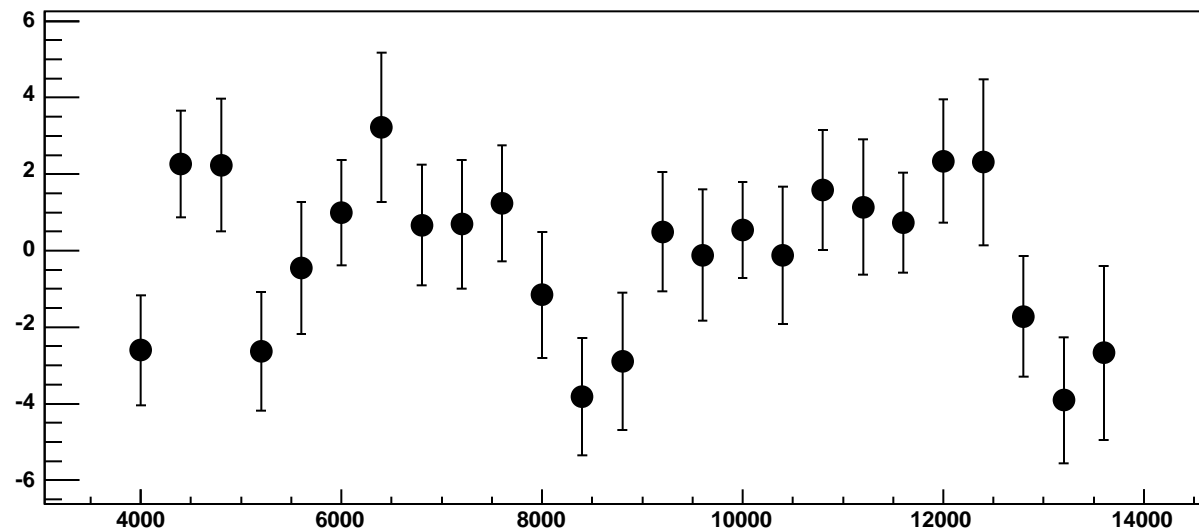
p1

0.01481 ± 0.0001742

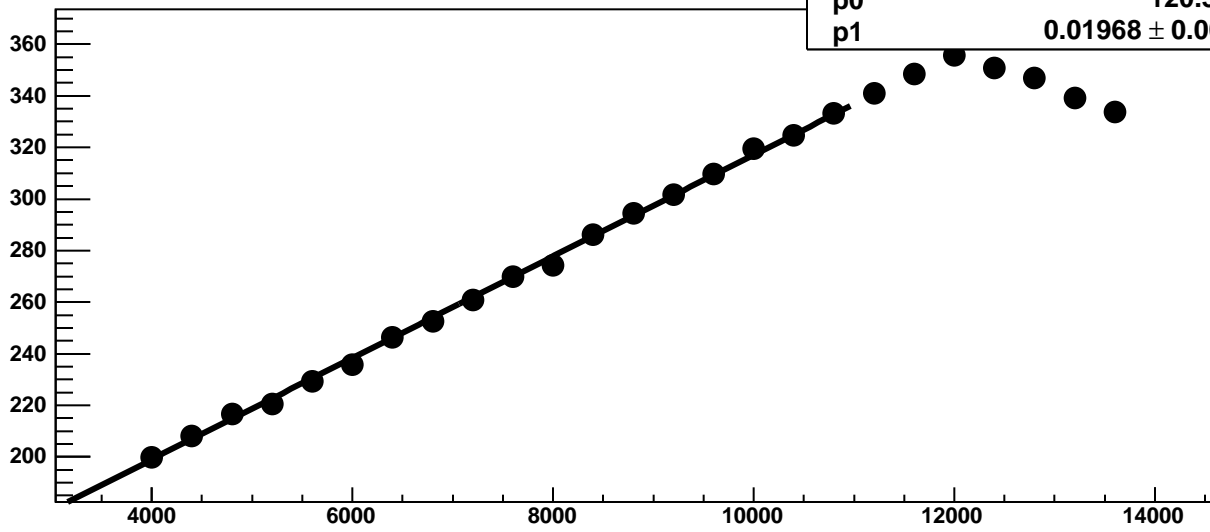
Chip 1, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



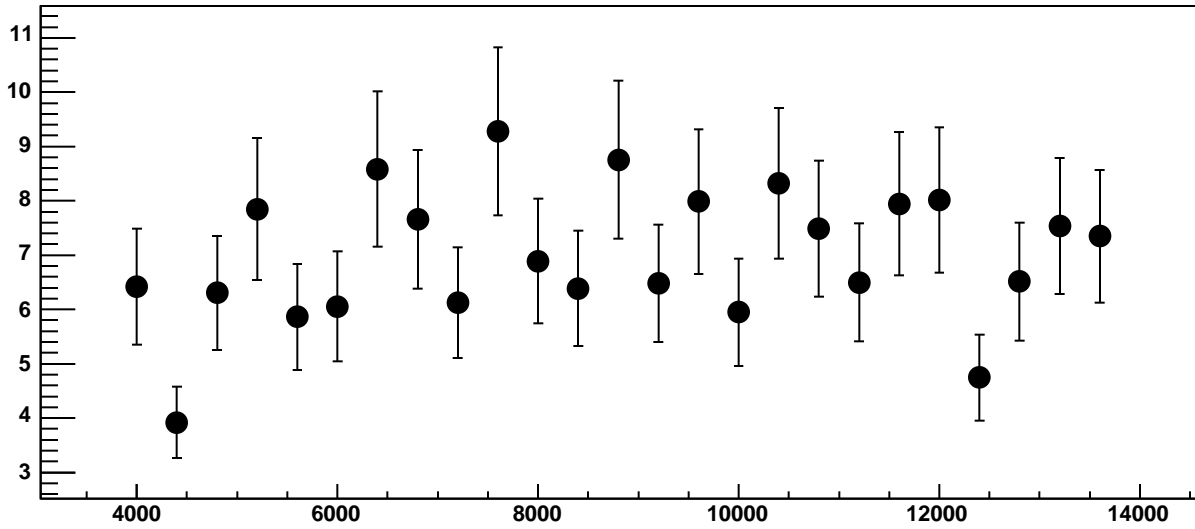
Chip 1, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



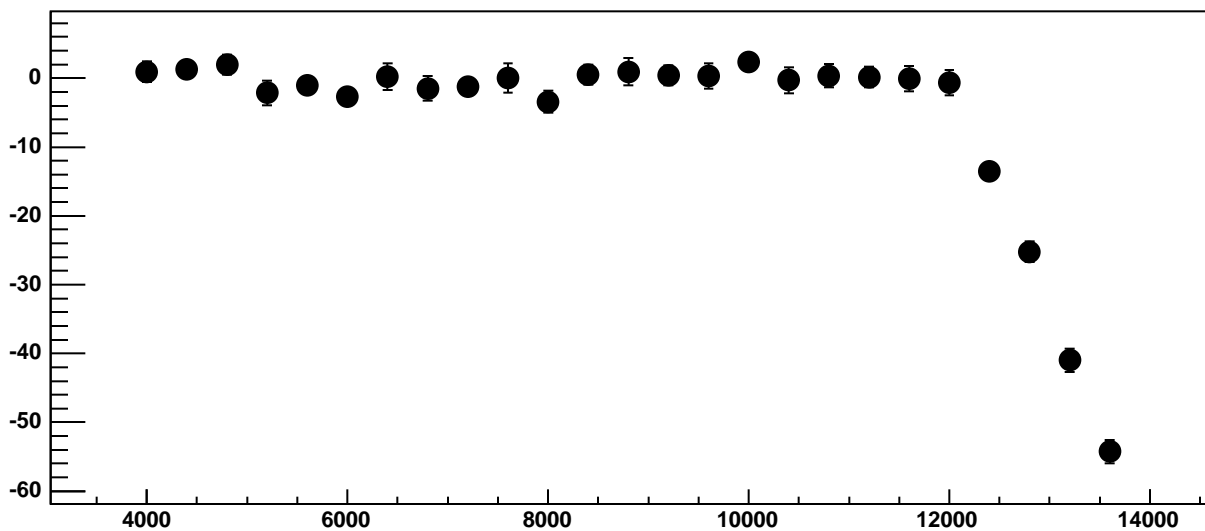
Chip 1, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC



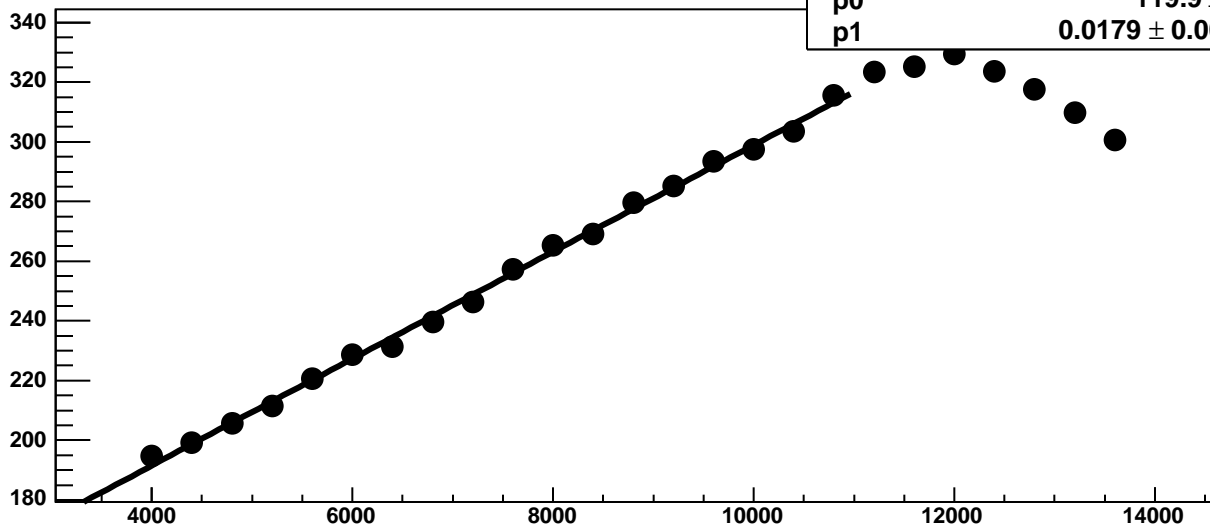
Chip 1, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



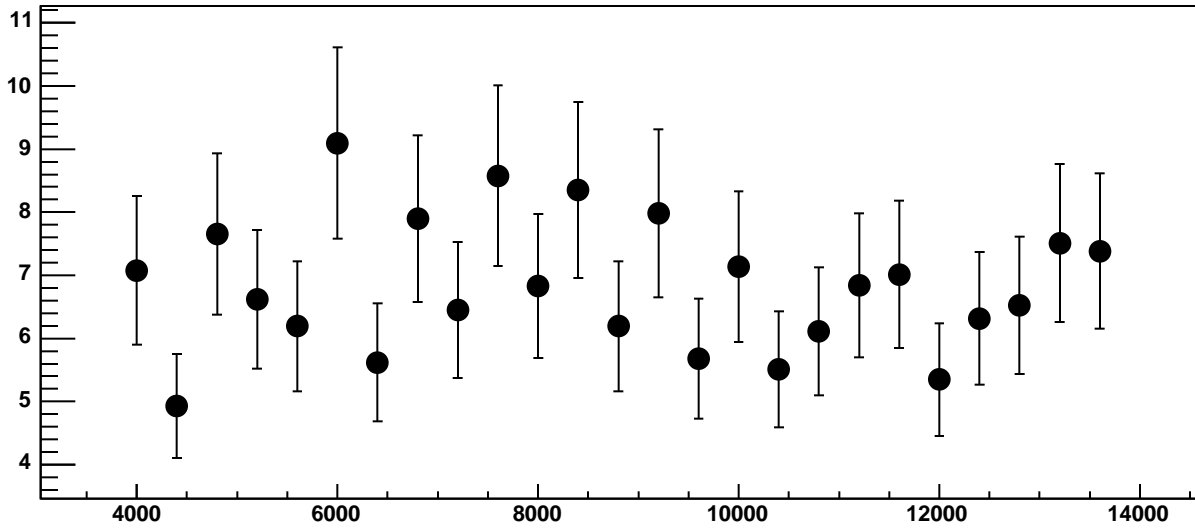
Chip 1, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC



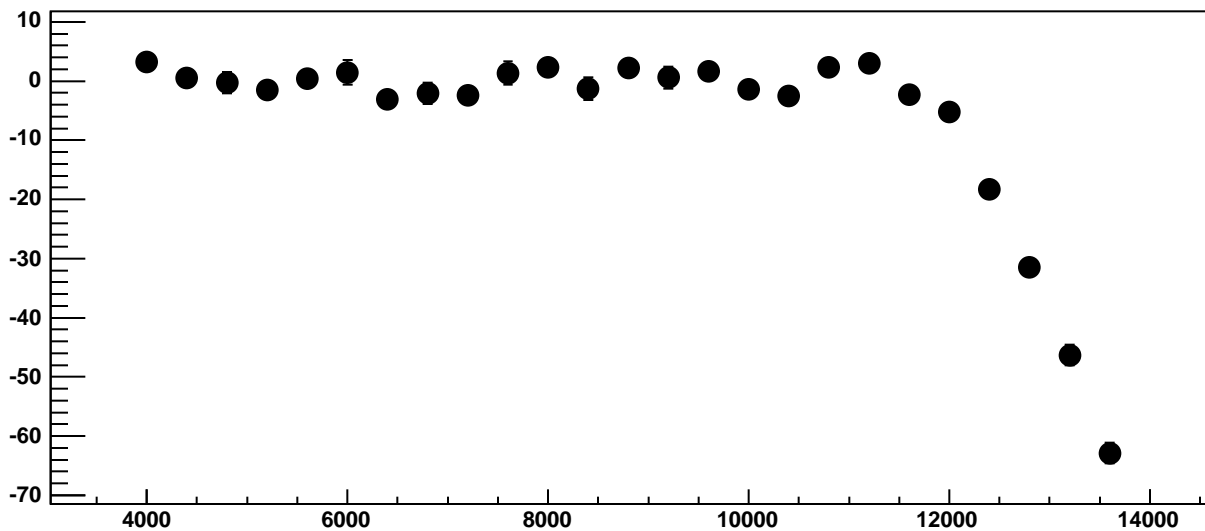
Chip 1, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



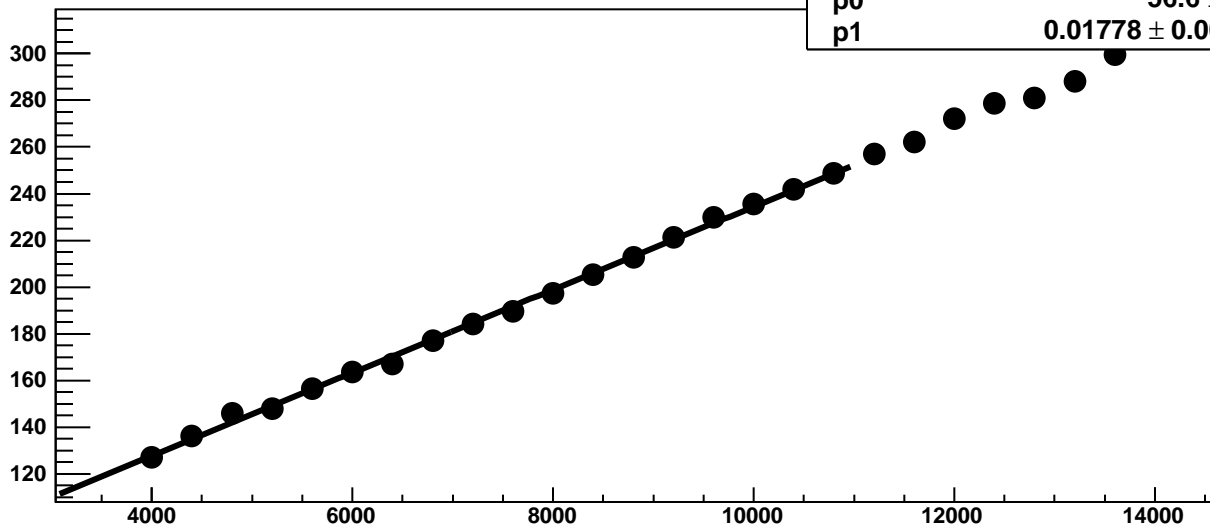
Chip 1, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



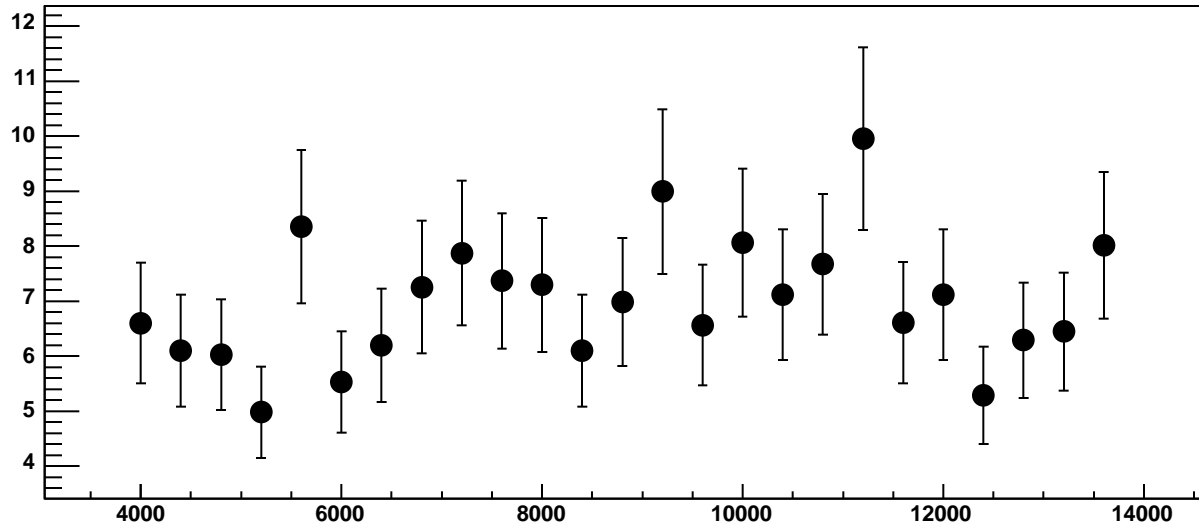
Chip 1, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



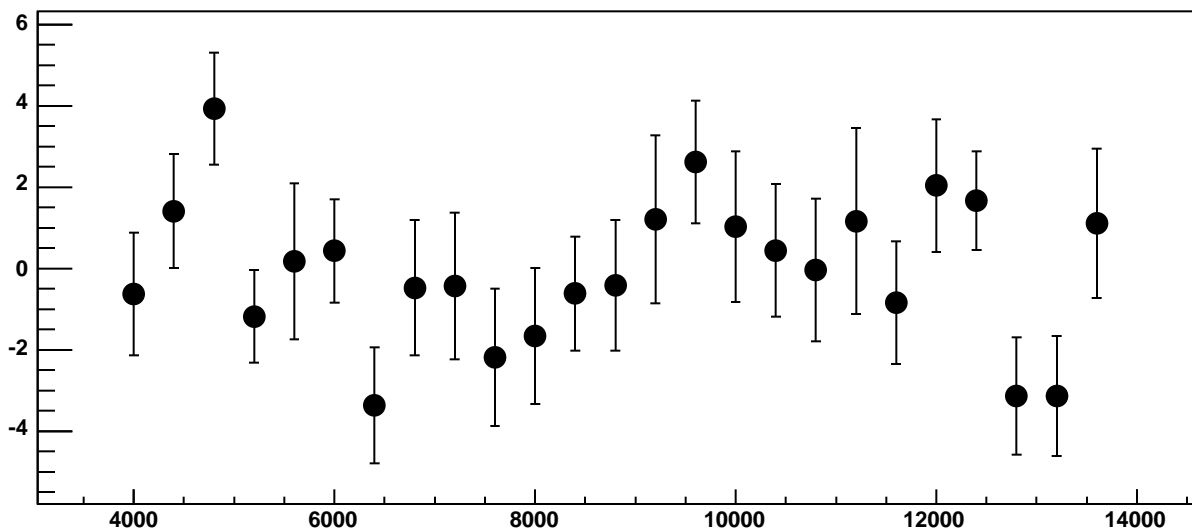
Chip 1, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC



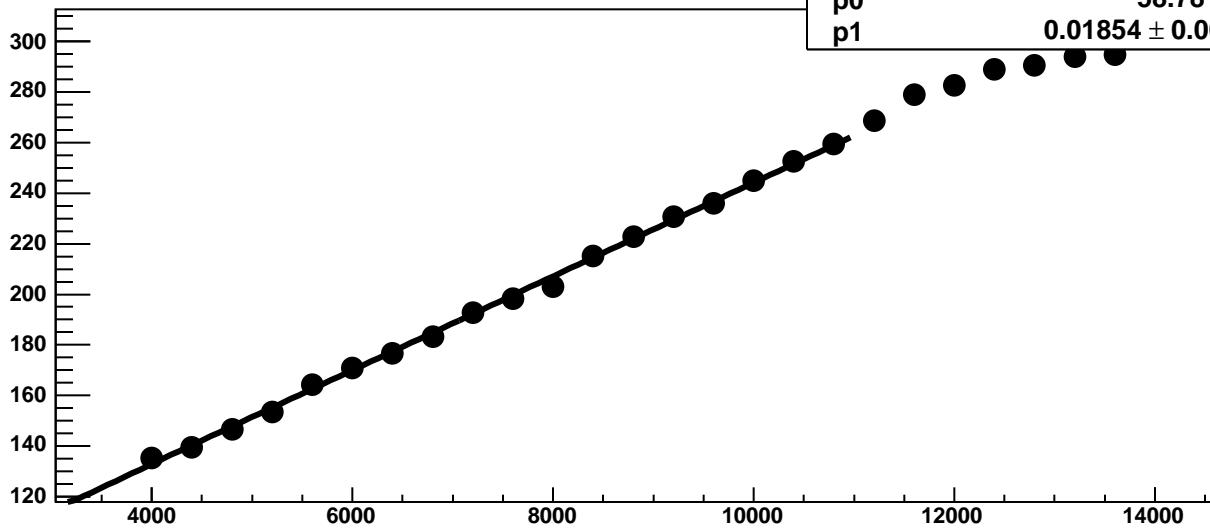
Chip 1, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



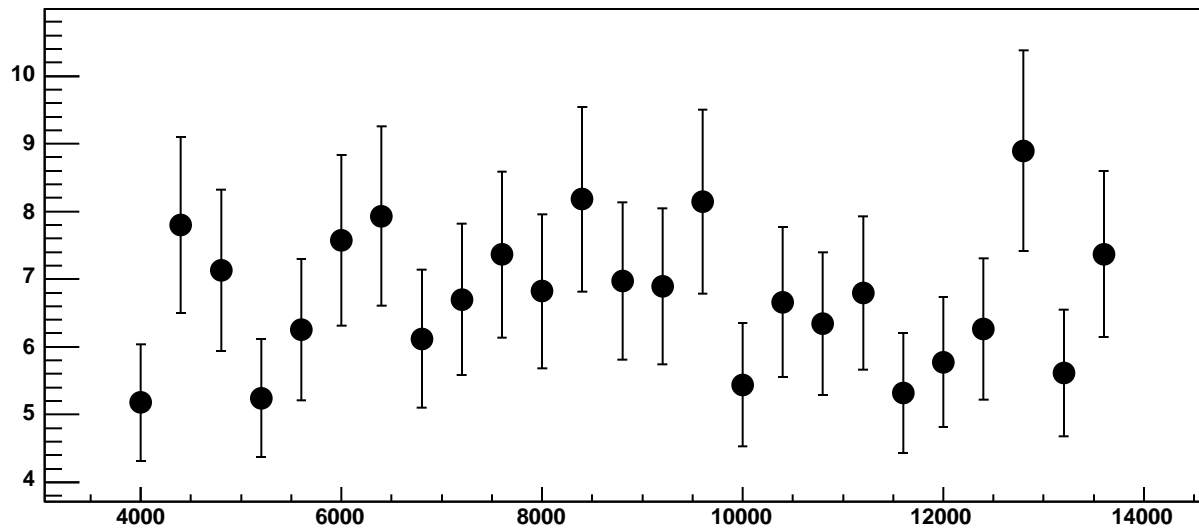
Chip 1, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC



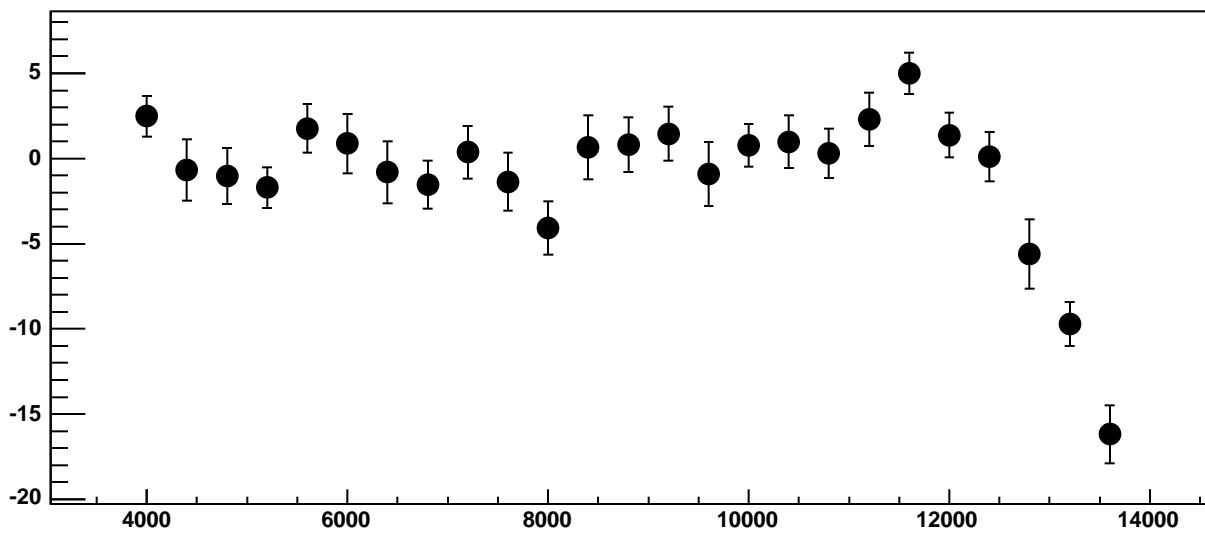
Chip 1, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



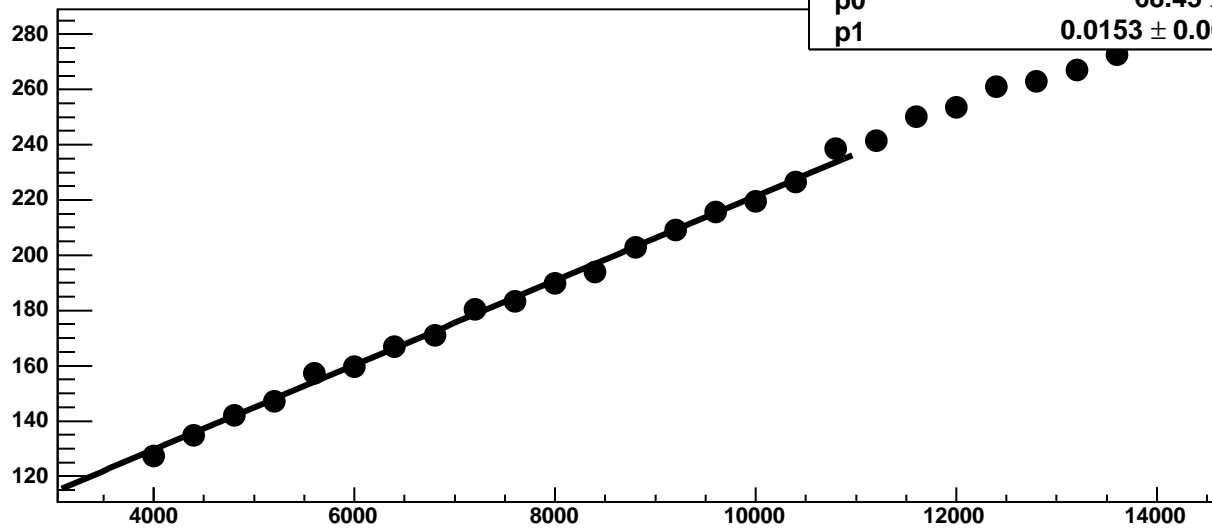
Chip 1, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



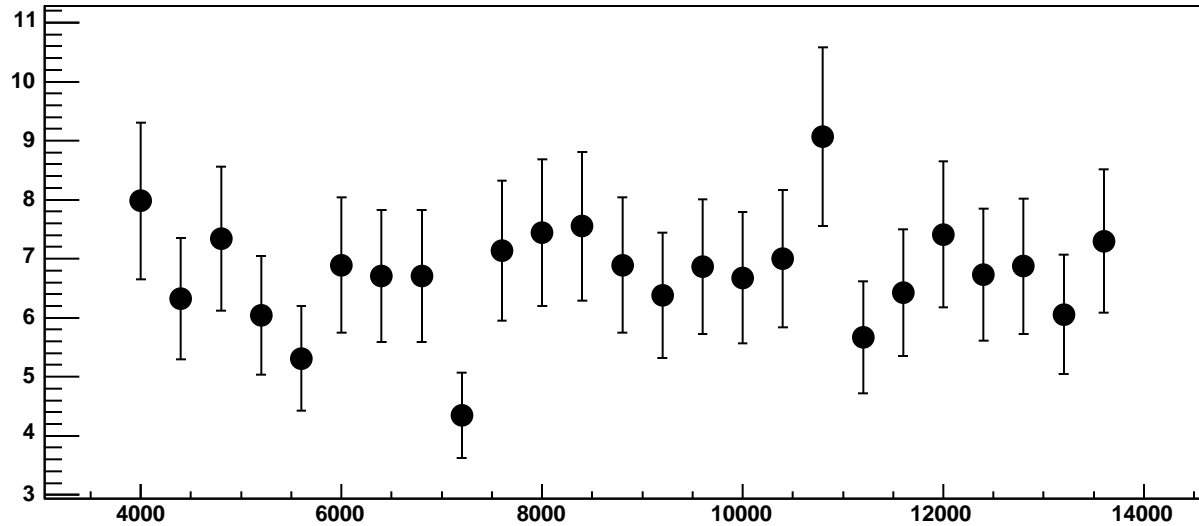
Chip 1, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



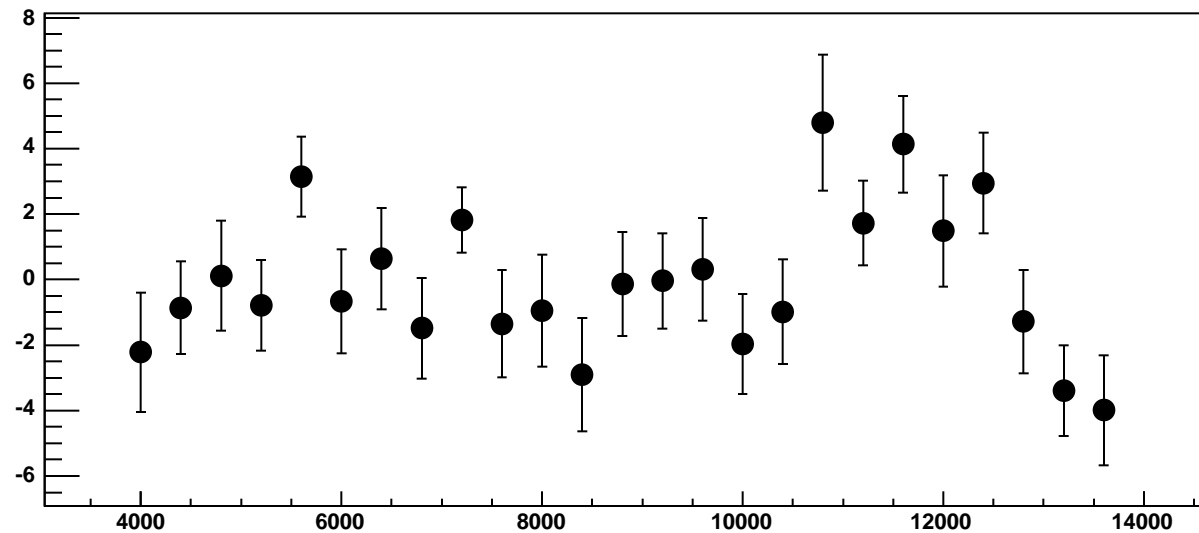
Chip 1, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



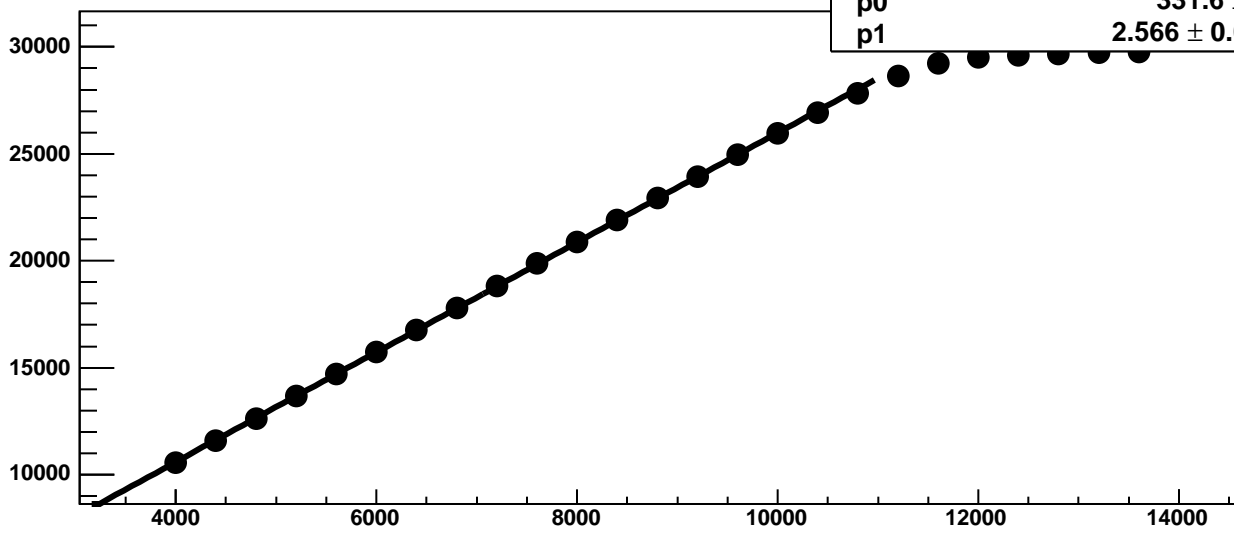
Chip 1, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



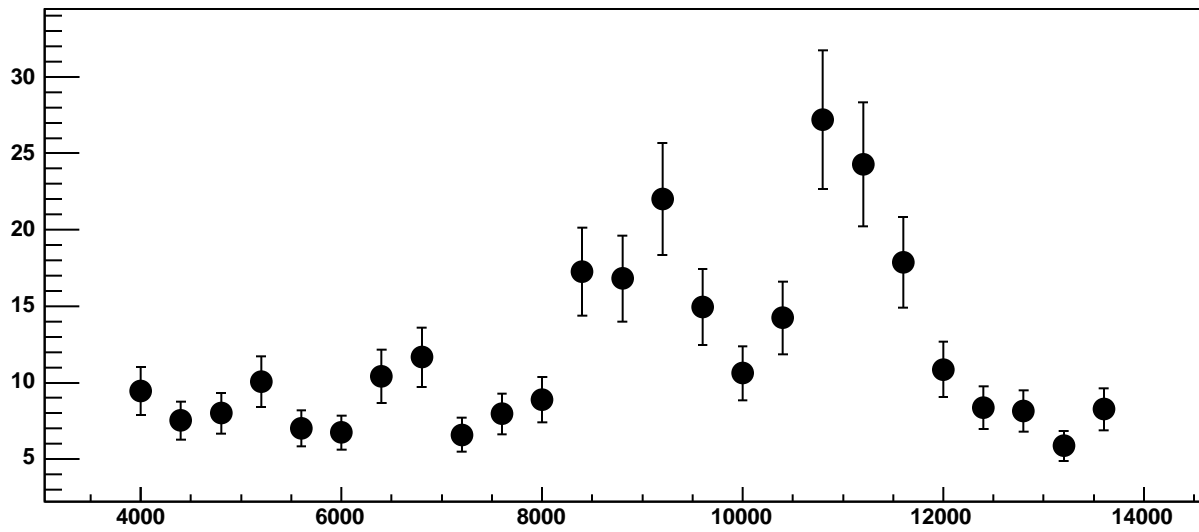
Chip 1, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



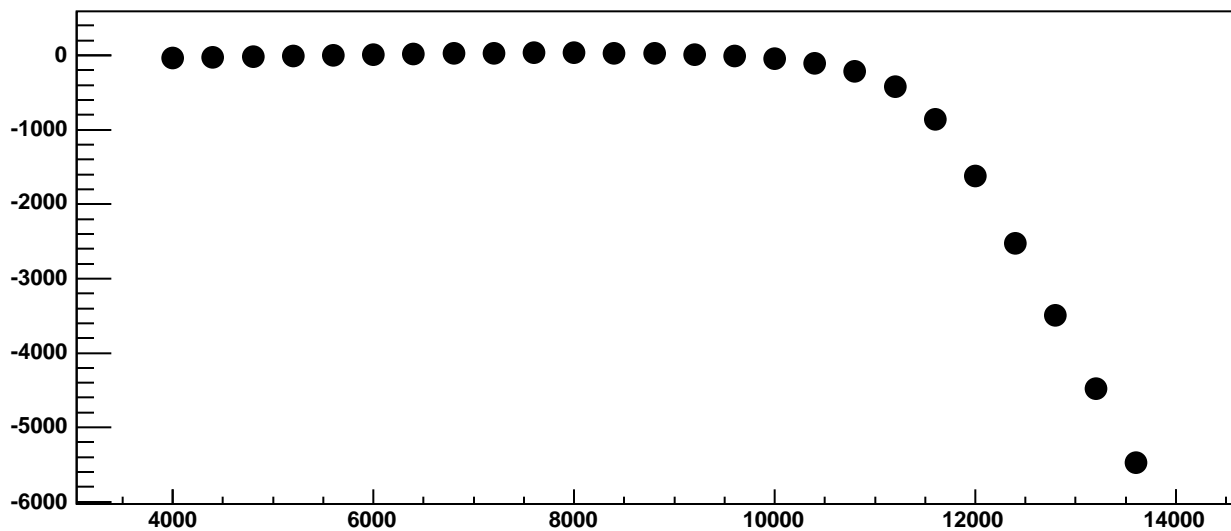
Chip 1, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC



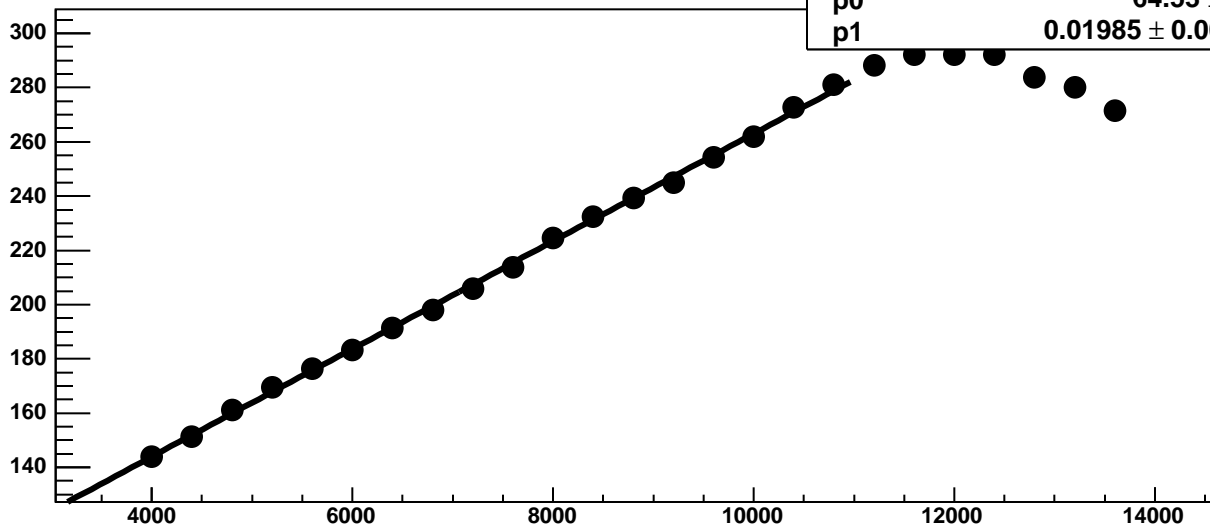
Chip 1, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



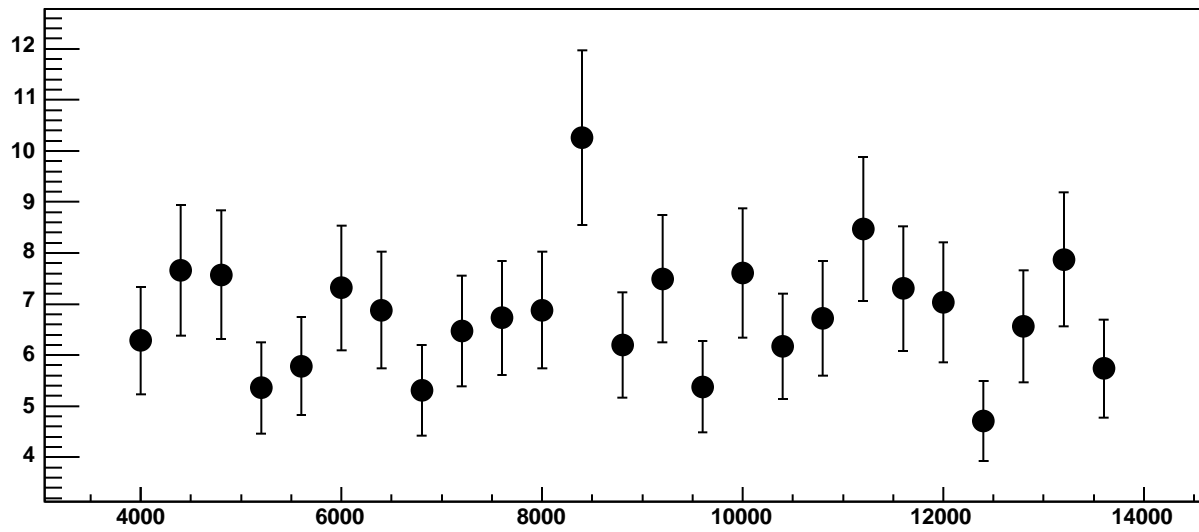
Chip 1, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC



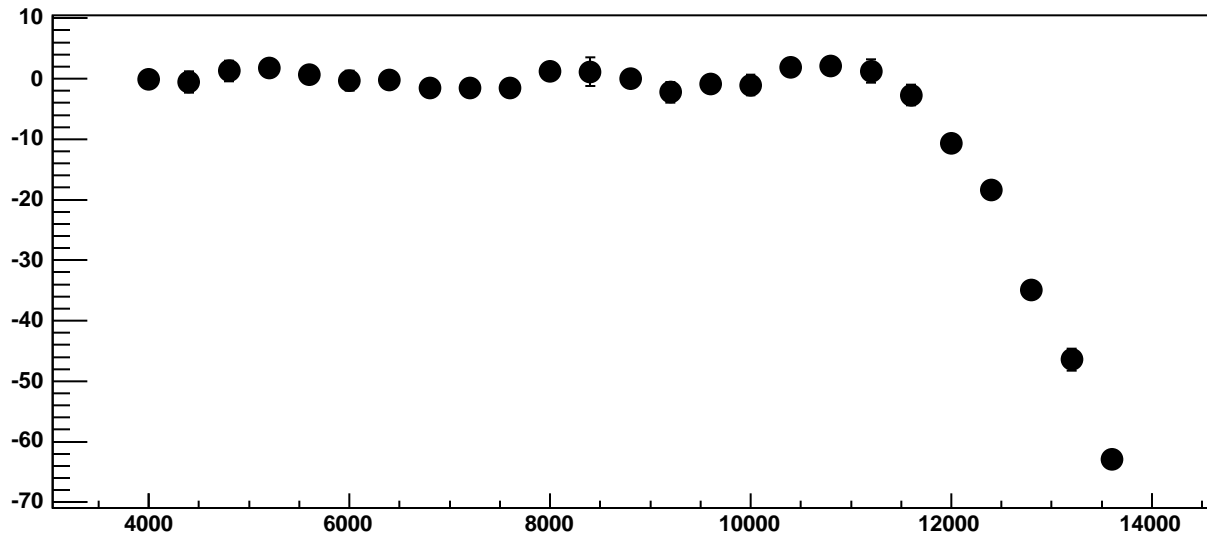
Chip 1, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC



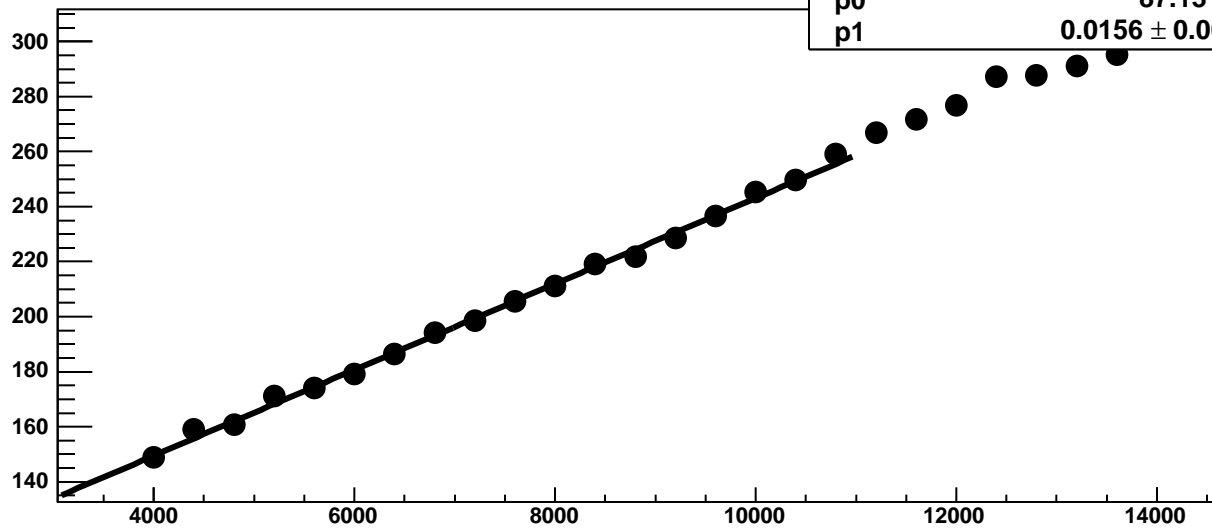
Chip 1, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



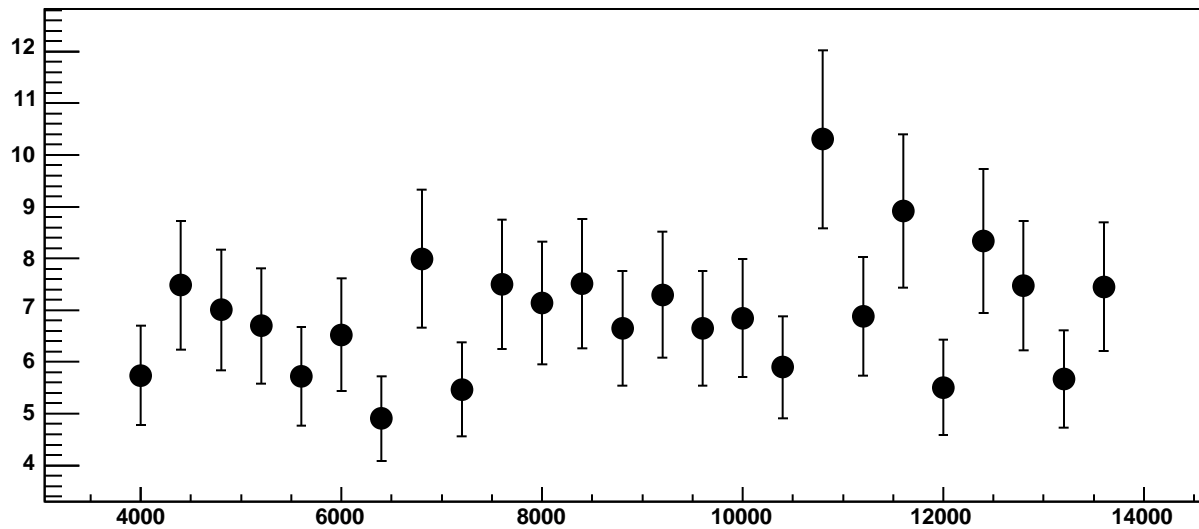
Chip 1, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



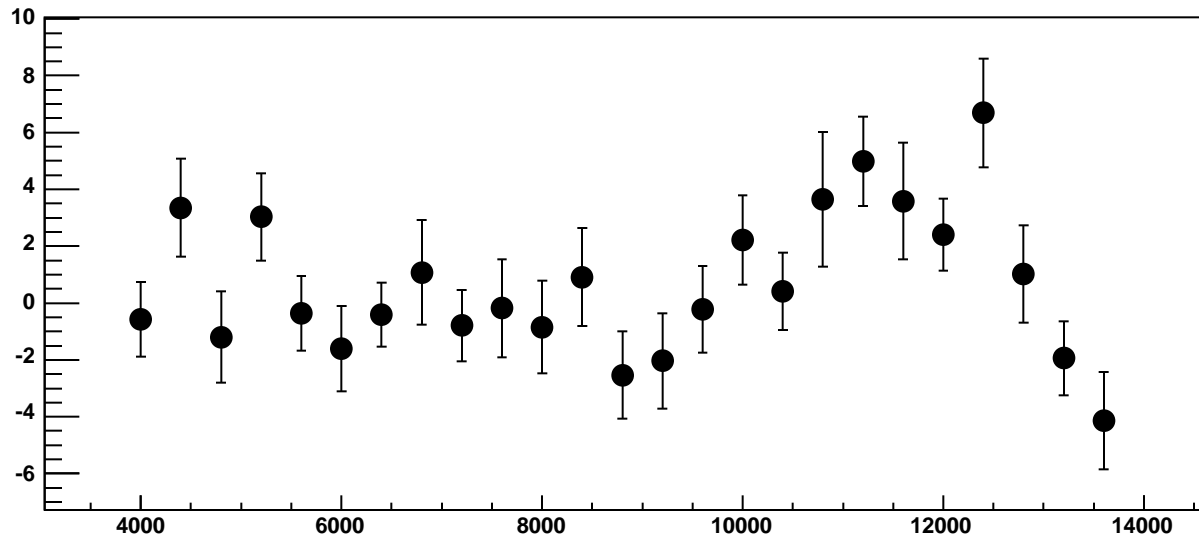
Chip 1, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



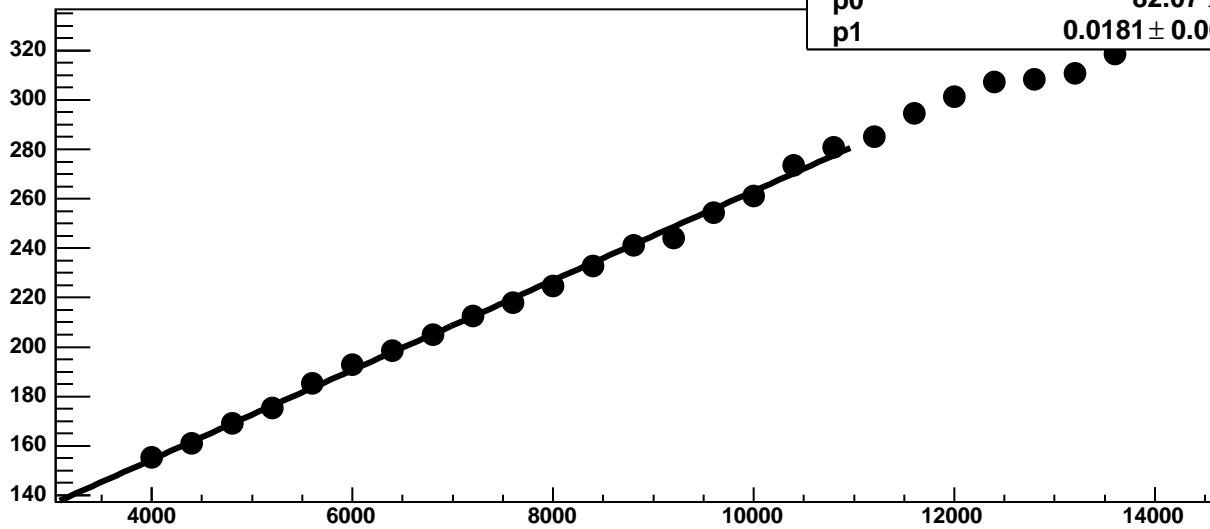
Chip 1, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



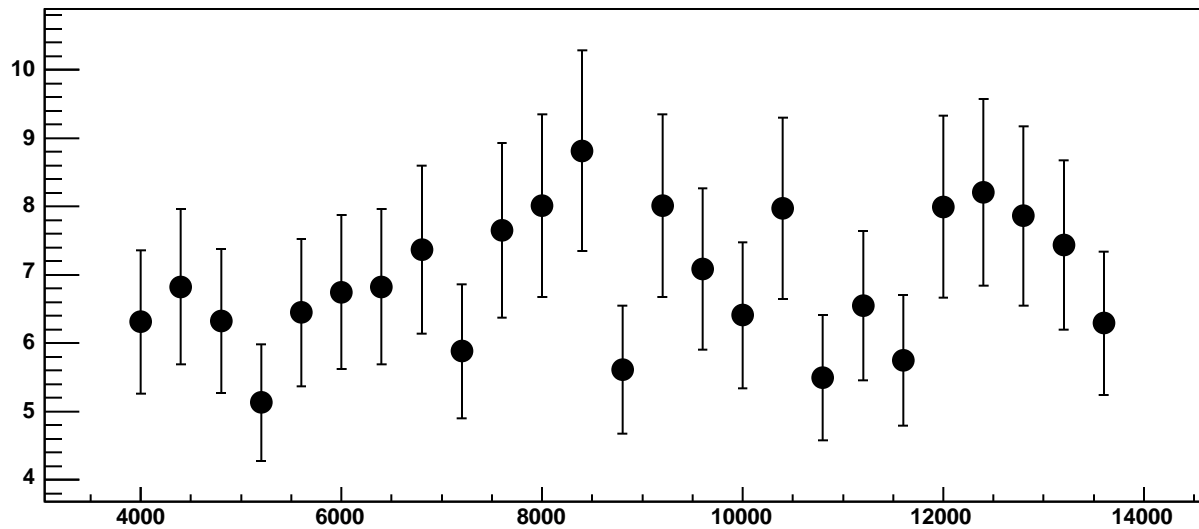
Chip 1, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



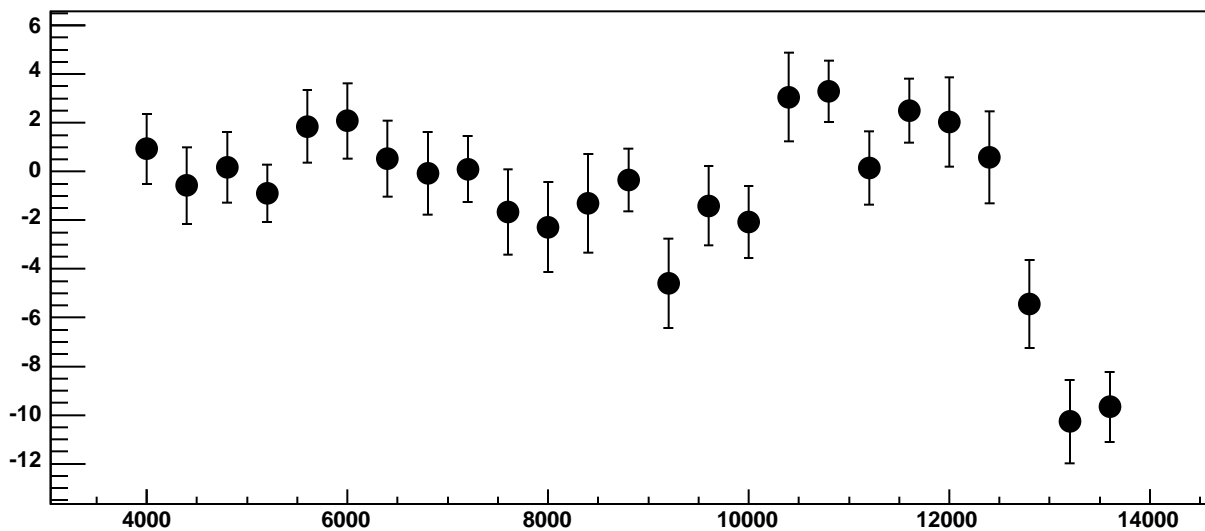
Chip 1, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC



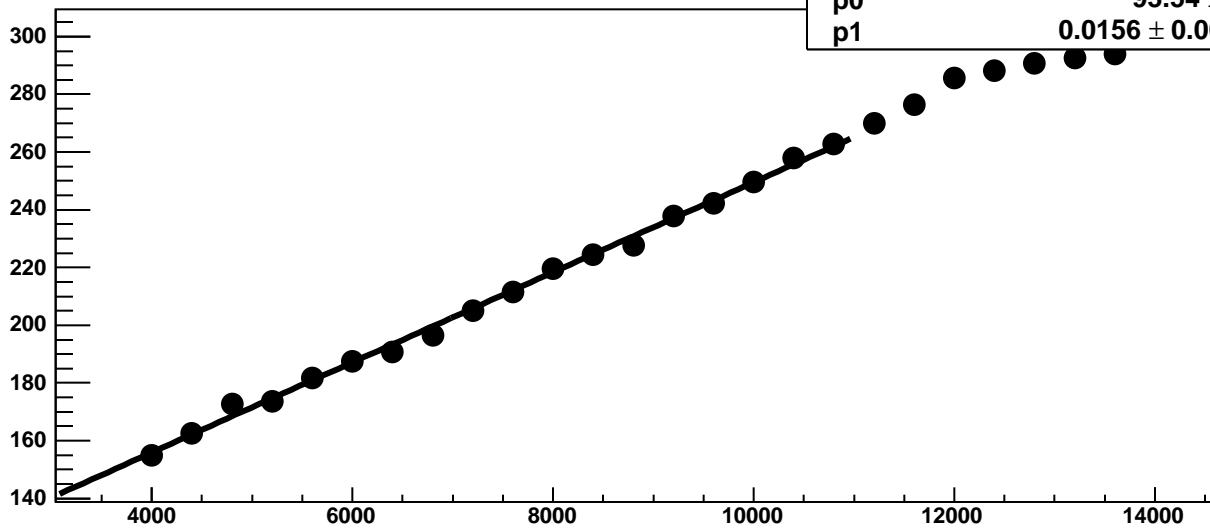
Chip 1, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



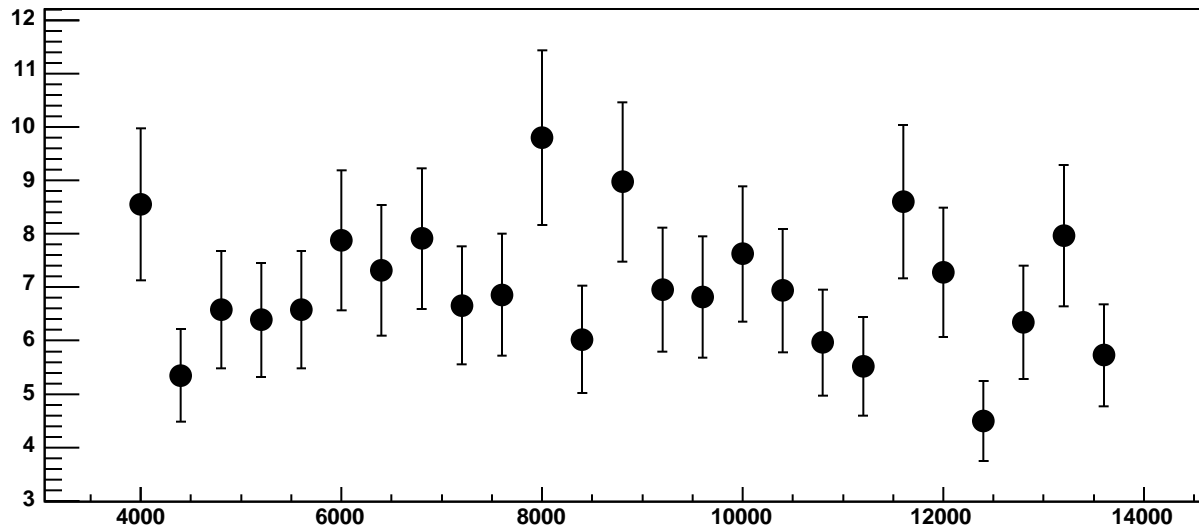
Chip 1, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC



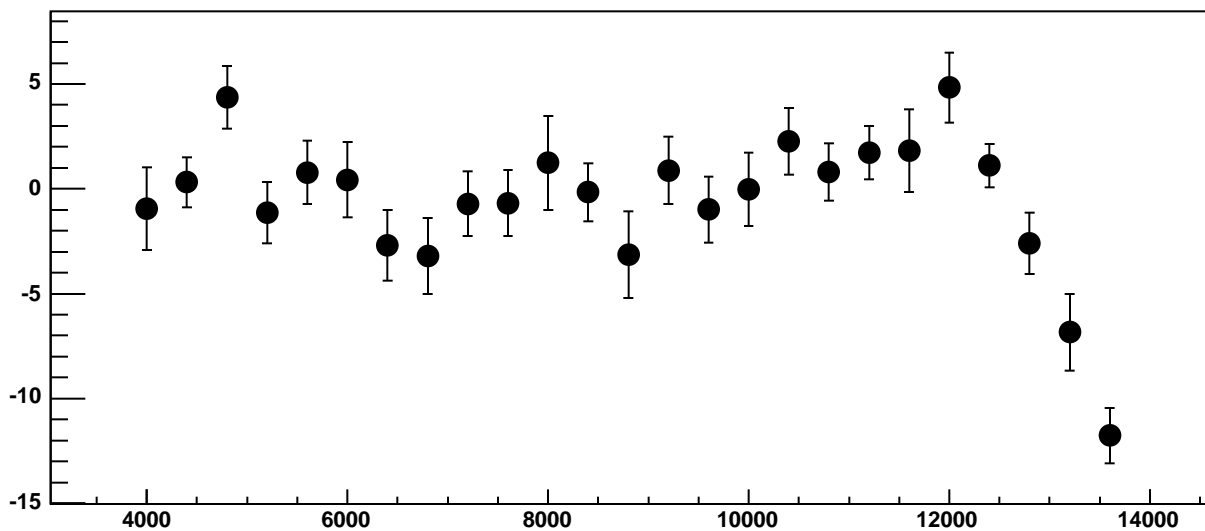
Chip 1, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



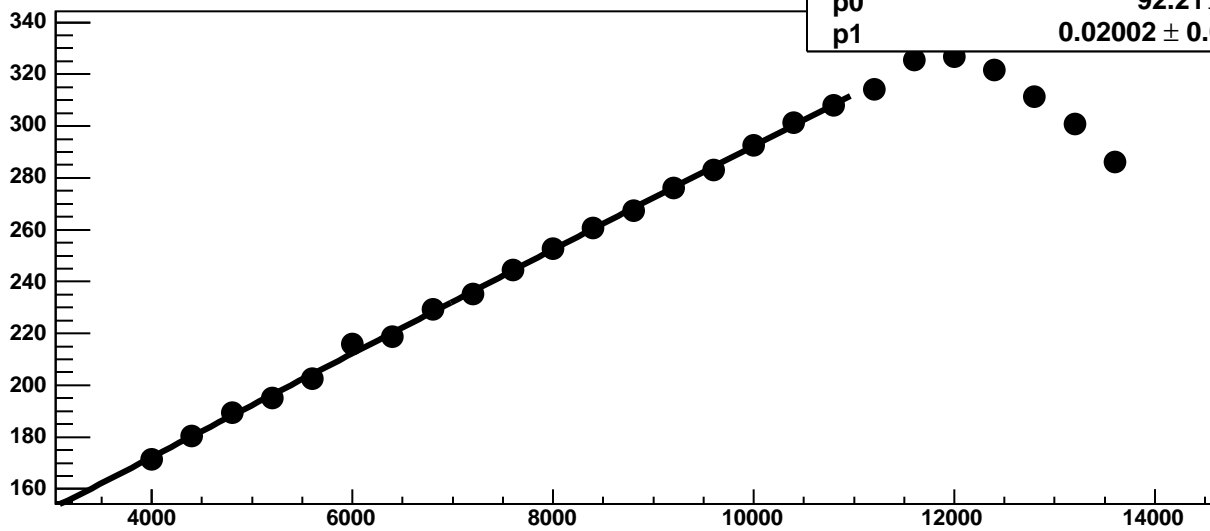
Chip 1, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



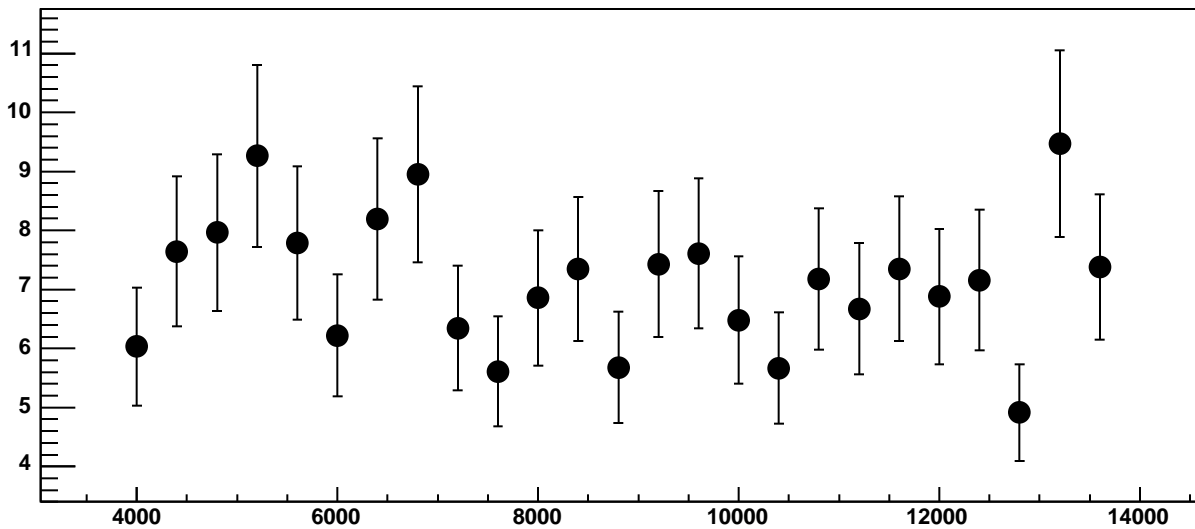
Chip 1, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC



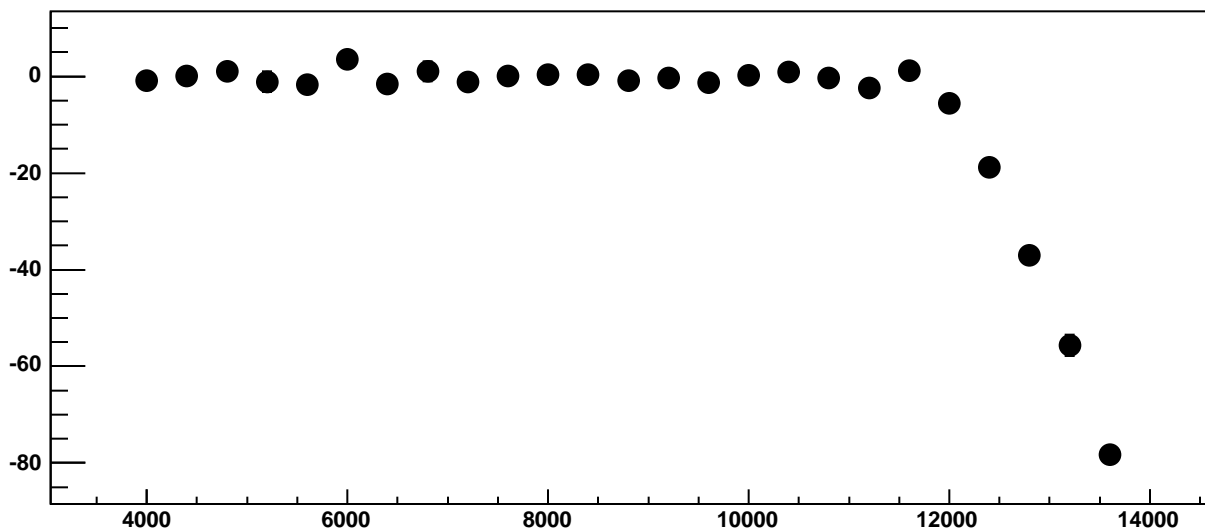
Chip 1, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC



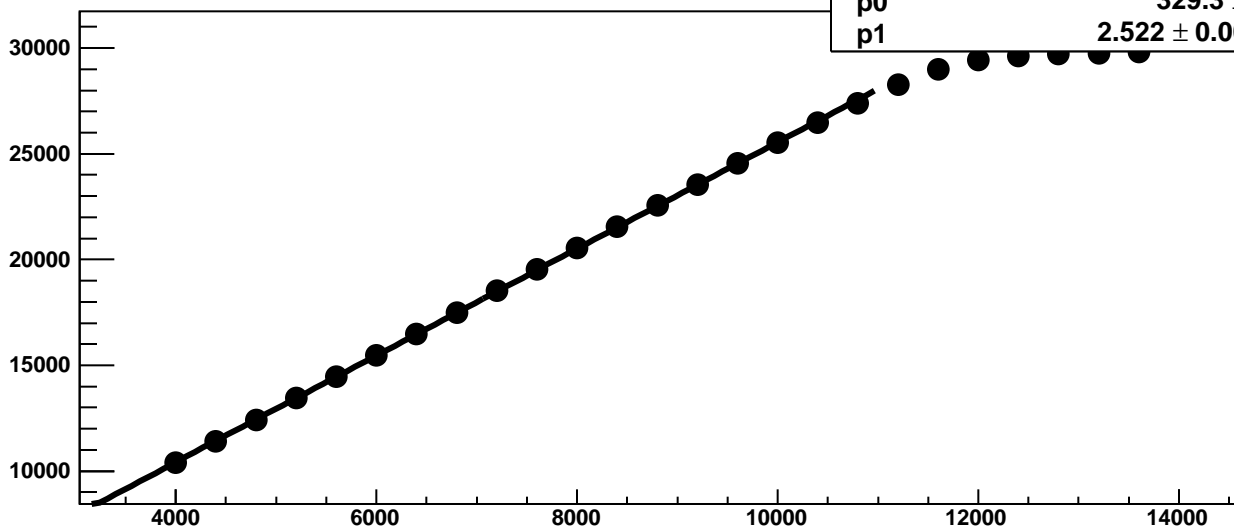
Chip 1, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



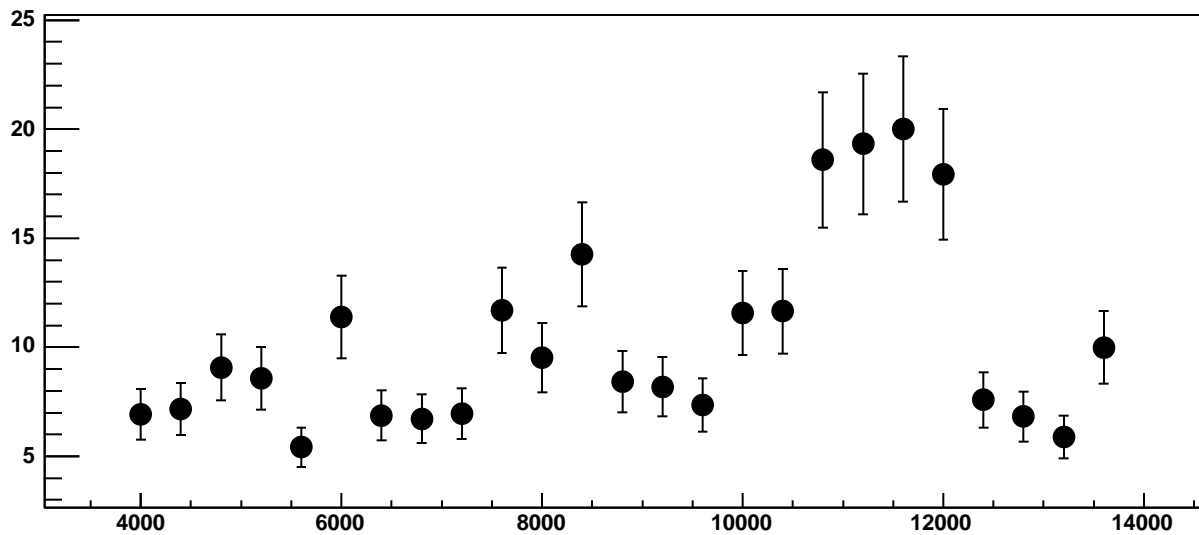
Chip 1, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



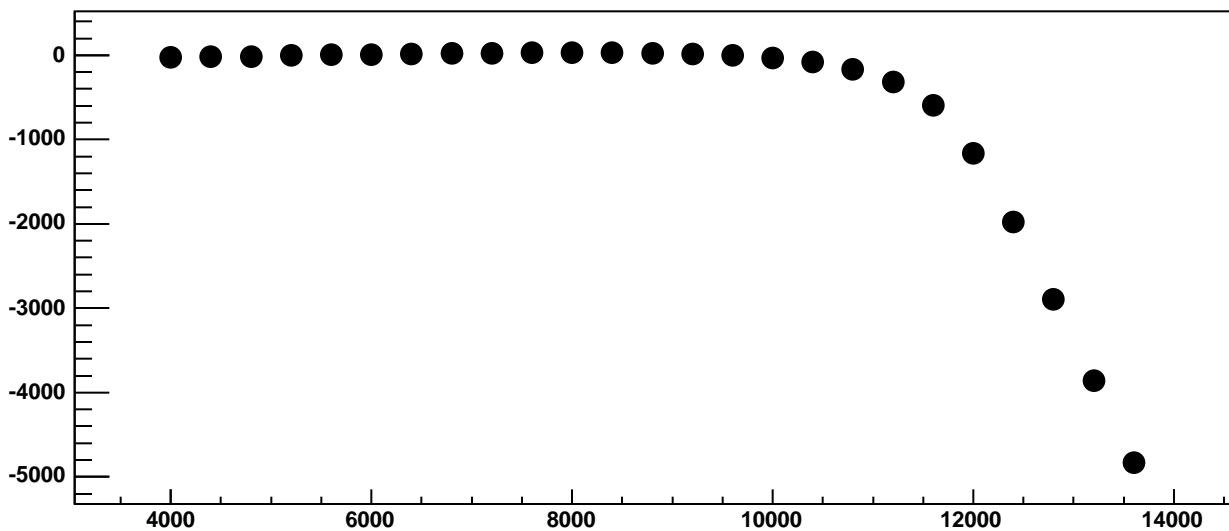
Chip 1, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC



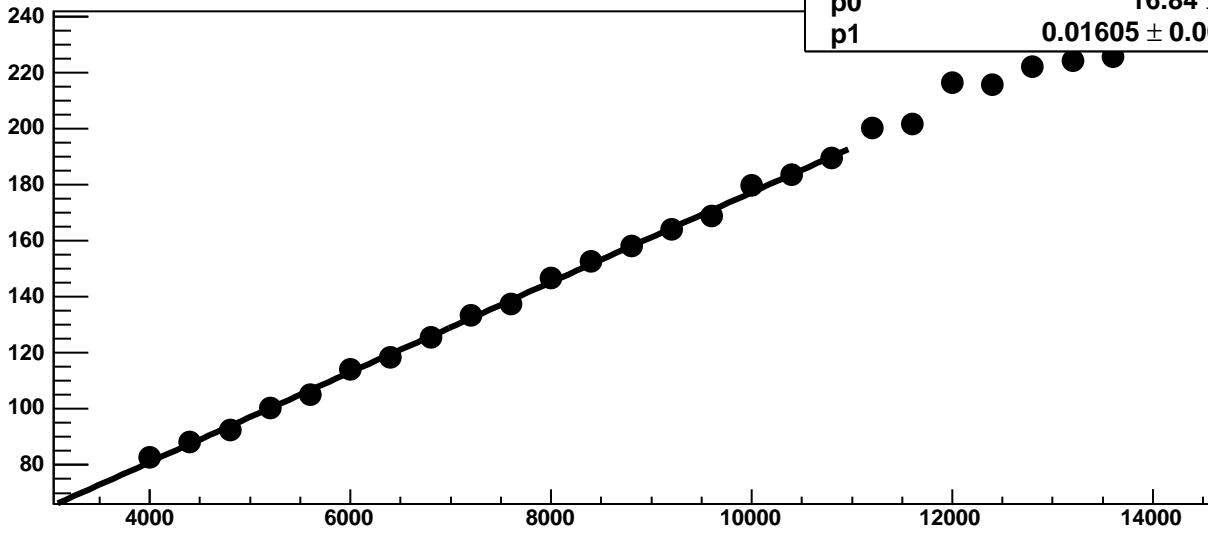
Chip 1, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



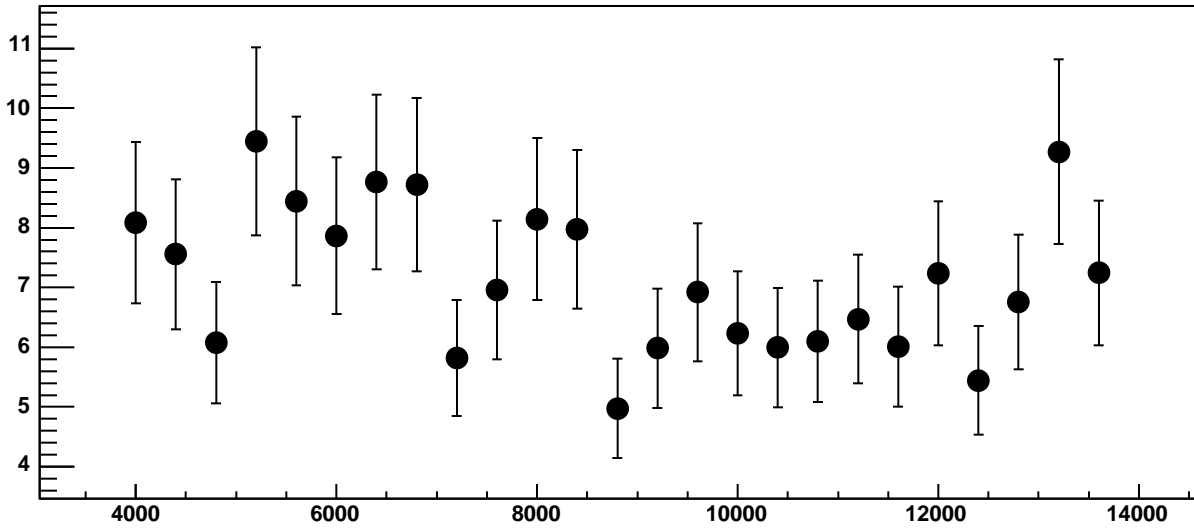
Chip 1, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC



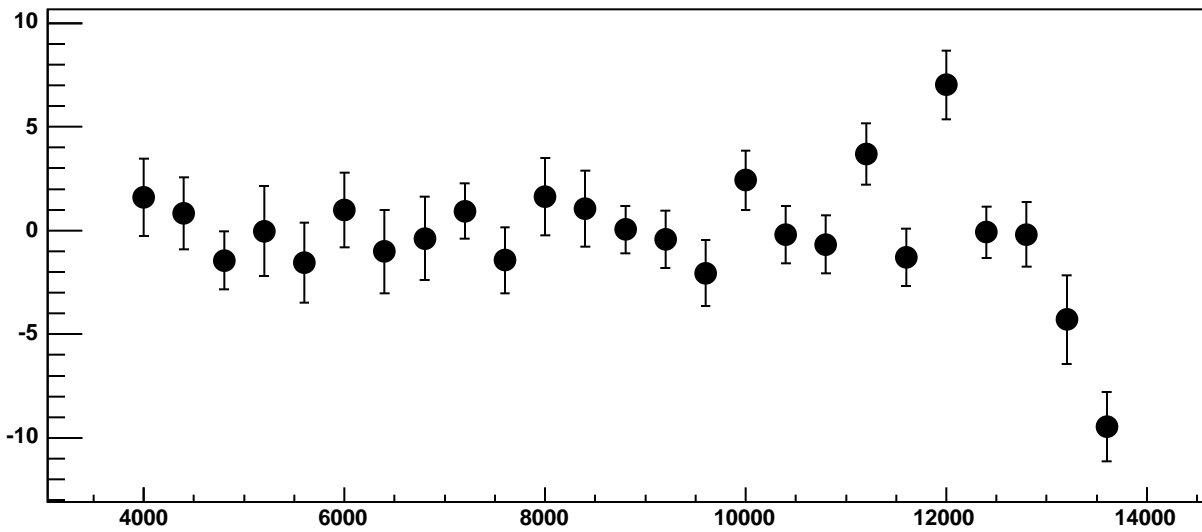
Chip 1, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



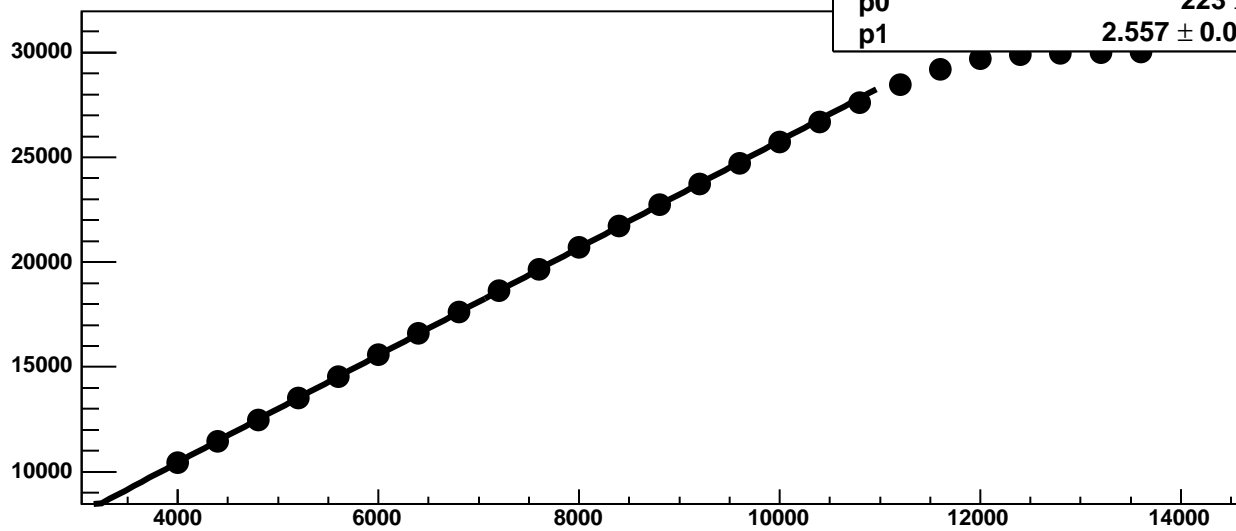
Chip 1, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



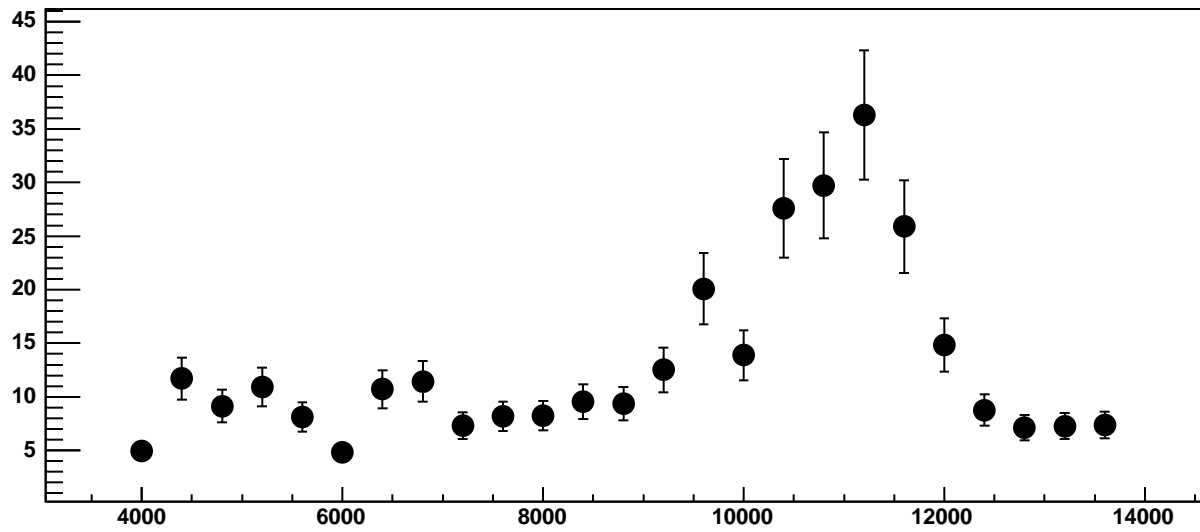
Chip 1, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



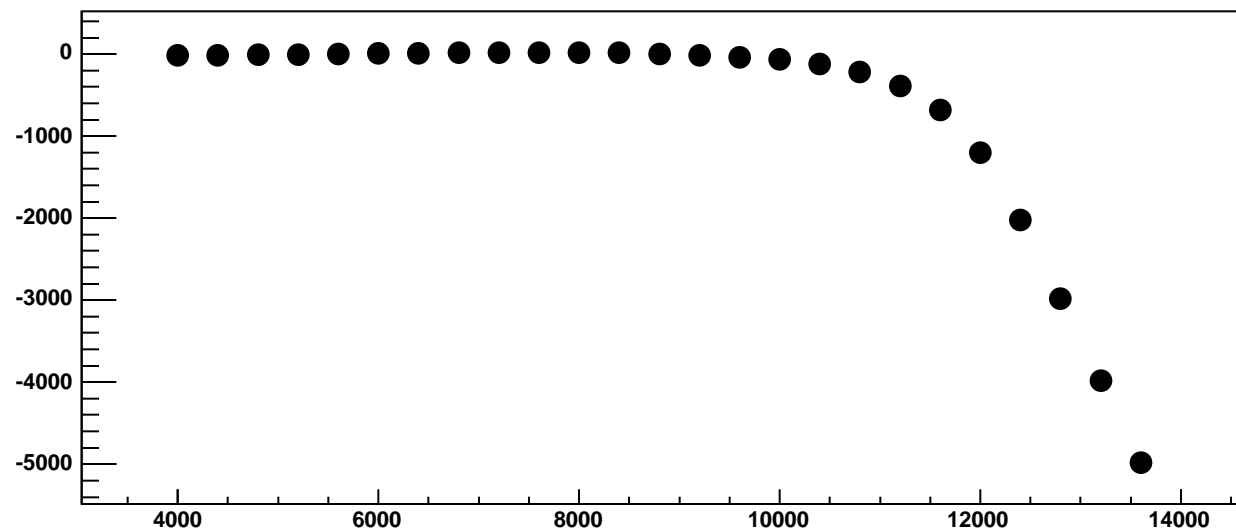
Chip 1, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC



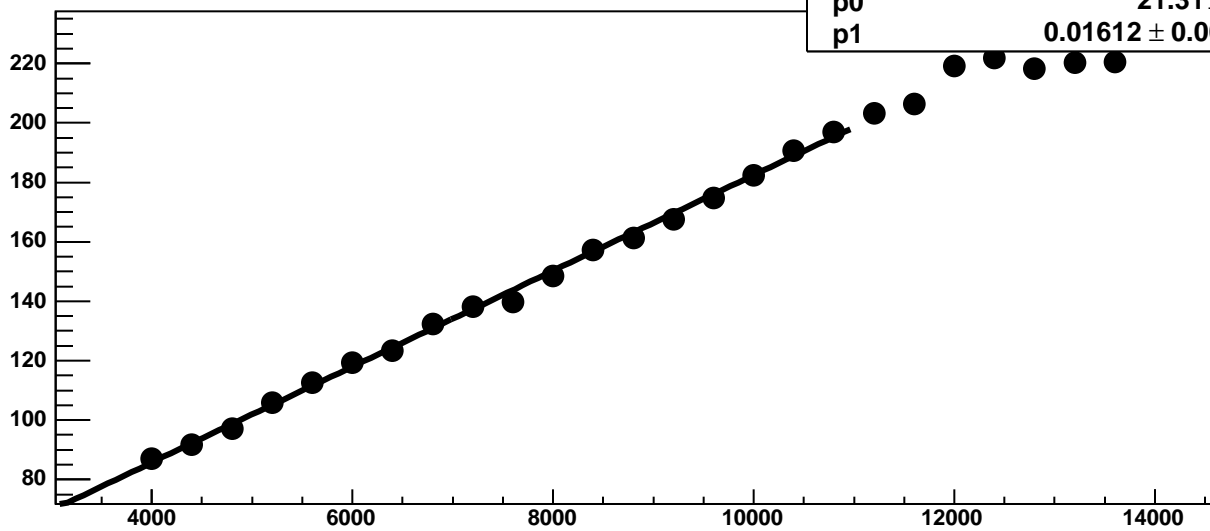
Chip 1, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



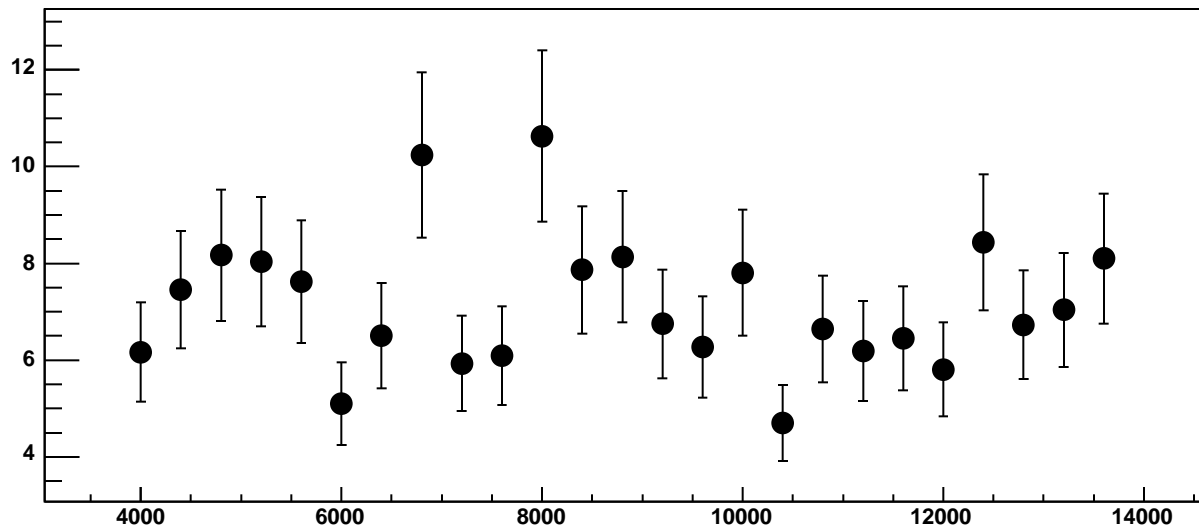
Chip 1, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC



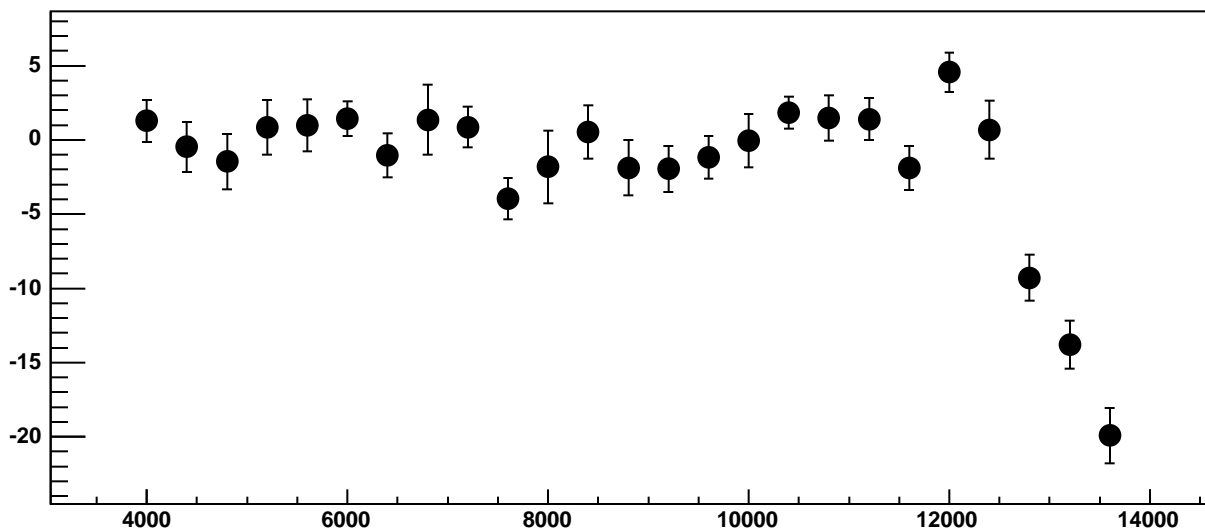
Chip 1, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



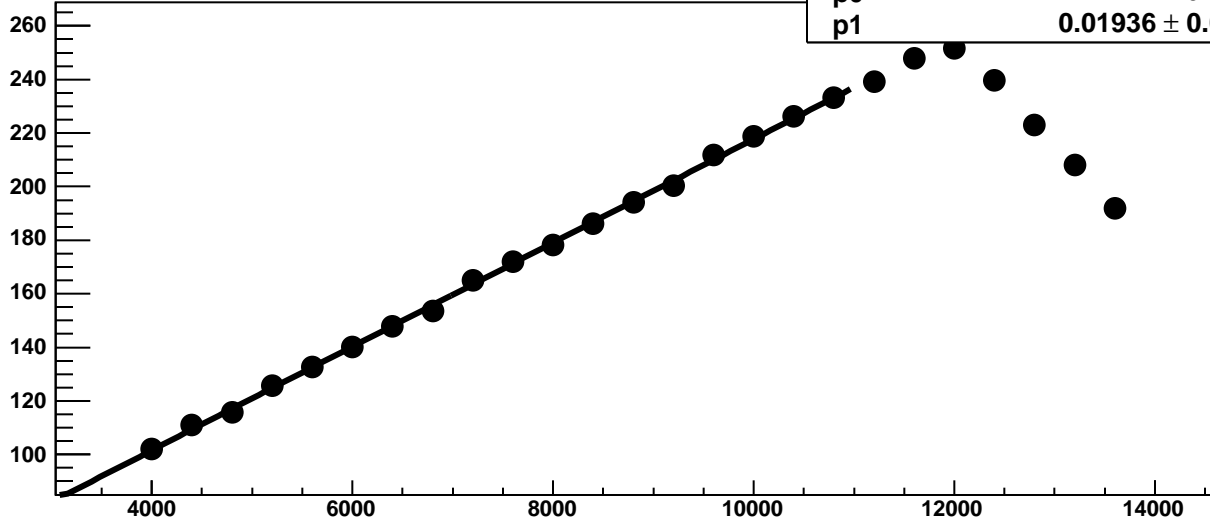
Chip 1, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



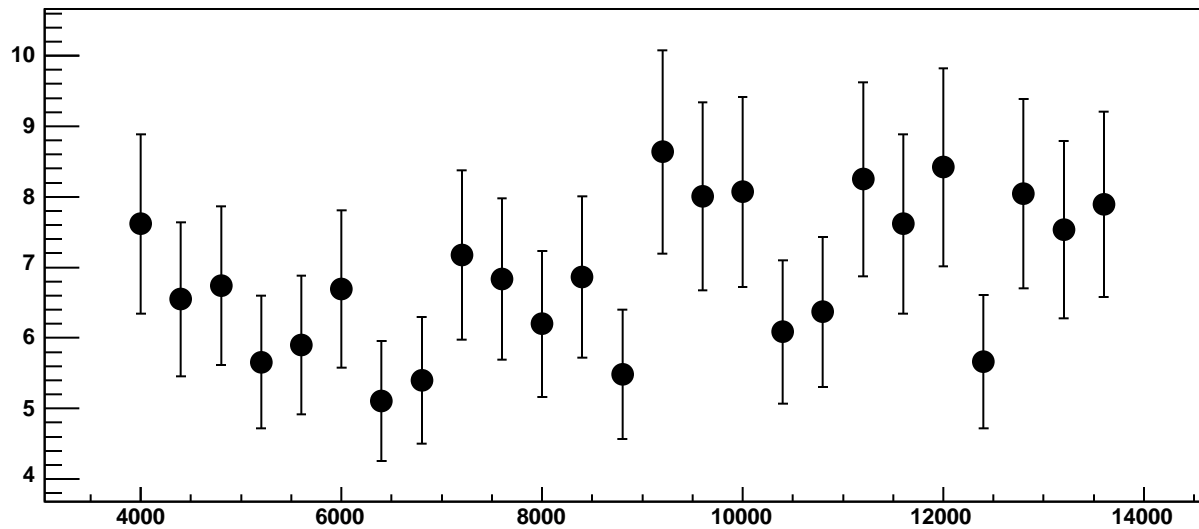
Chip 1, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



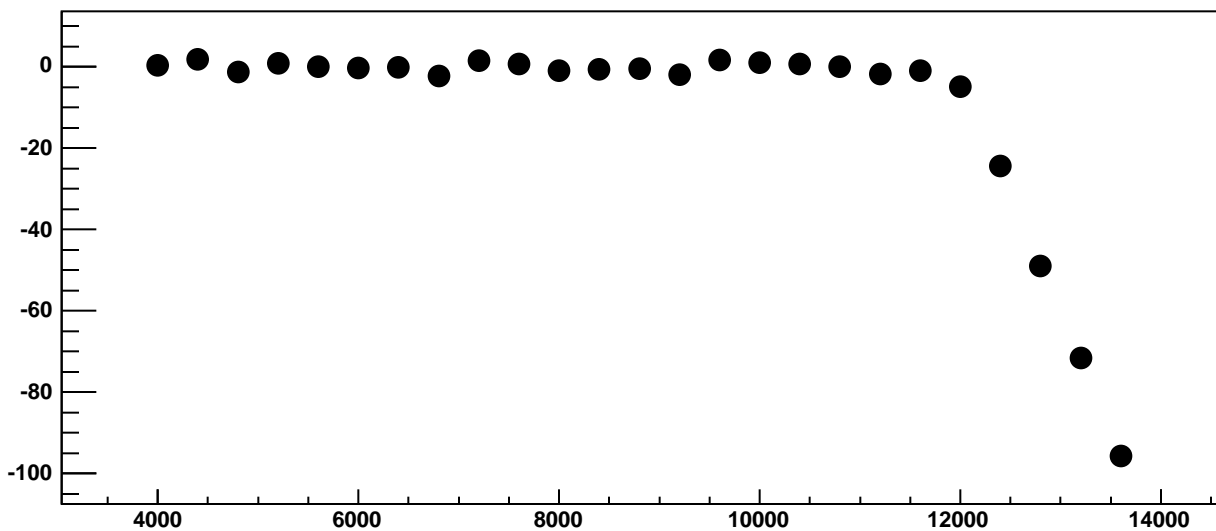
Chip 1, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



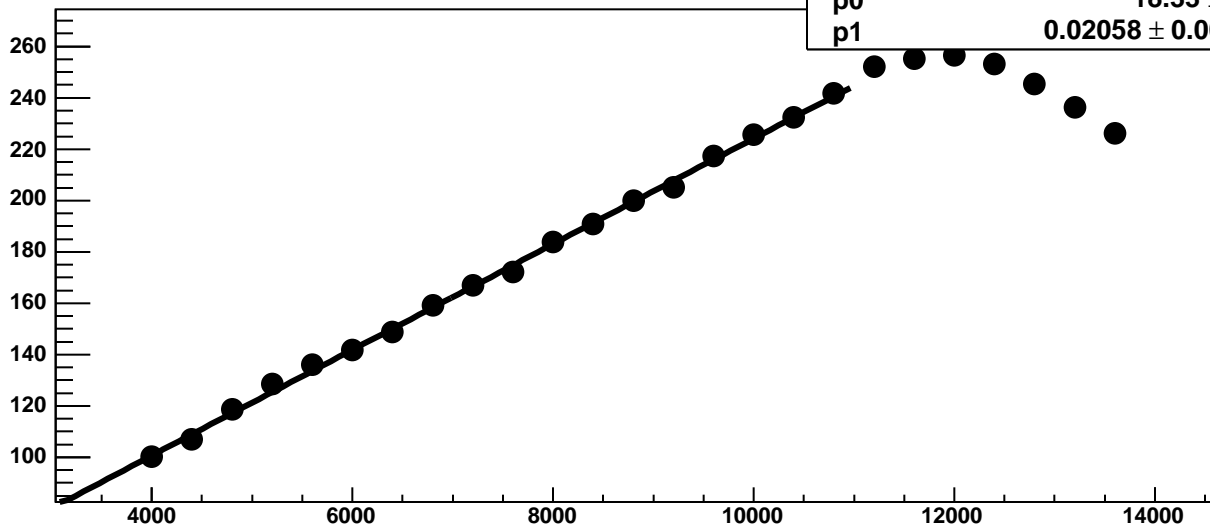
Chip 1, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



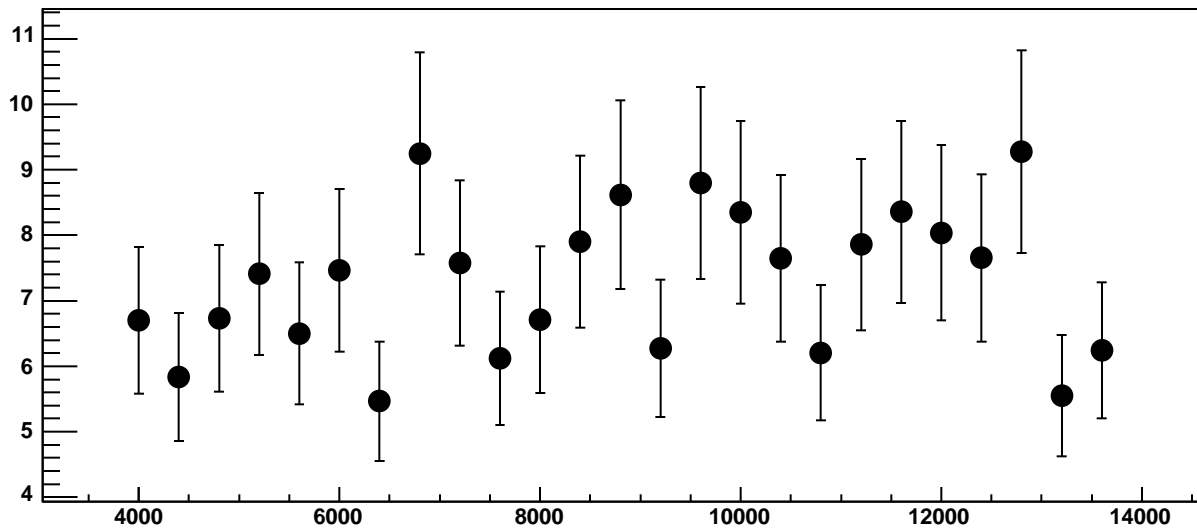
Chip 1, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



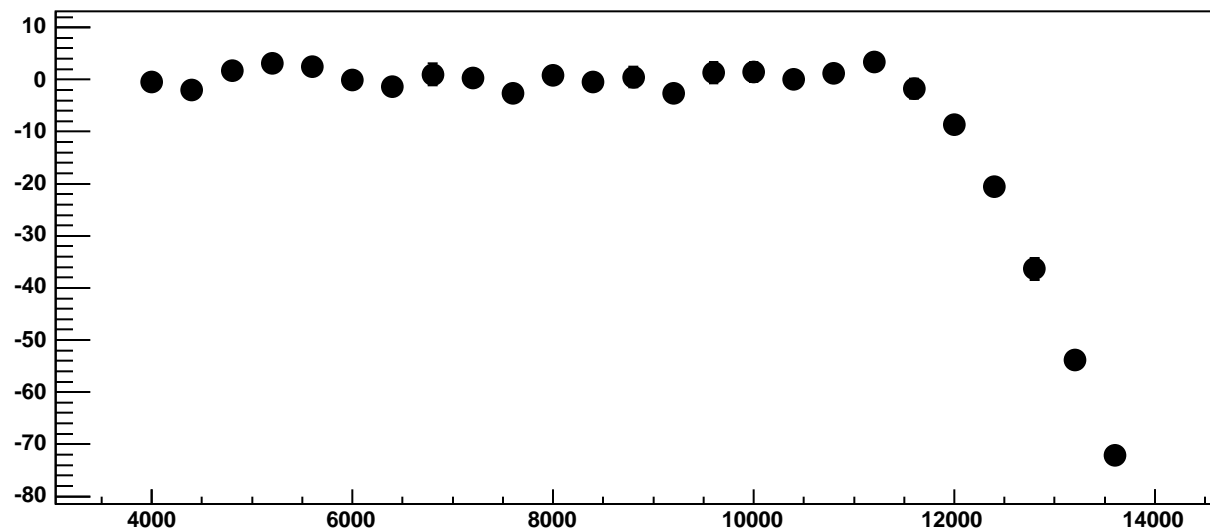
Chip 1, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC



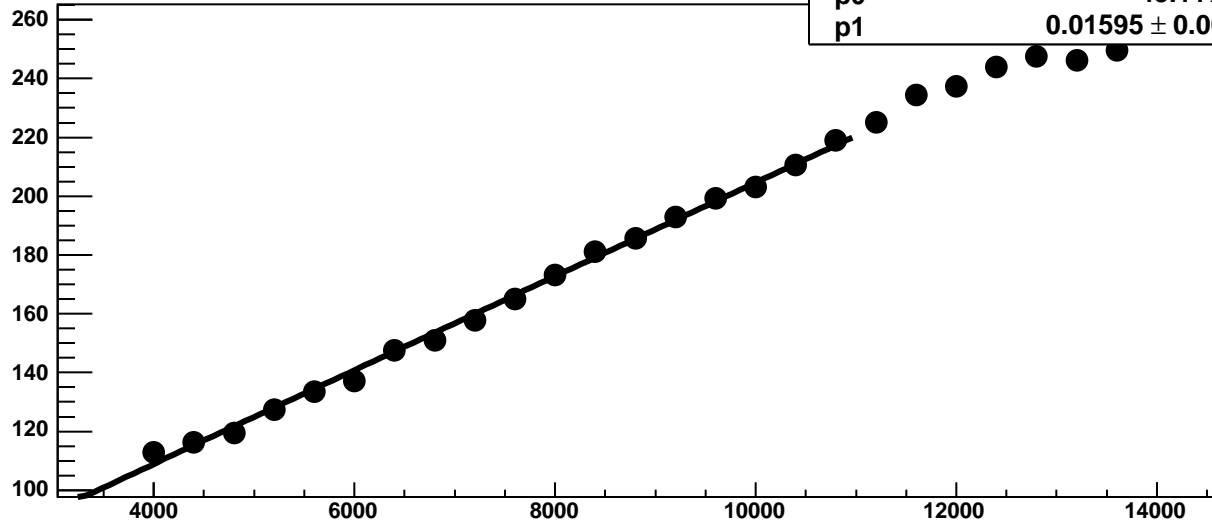
Chip 1, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC

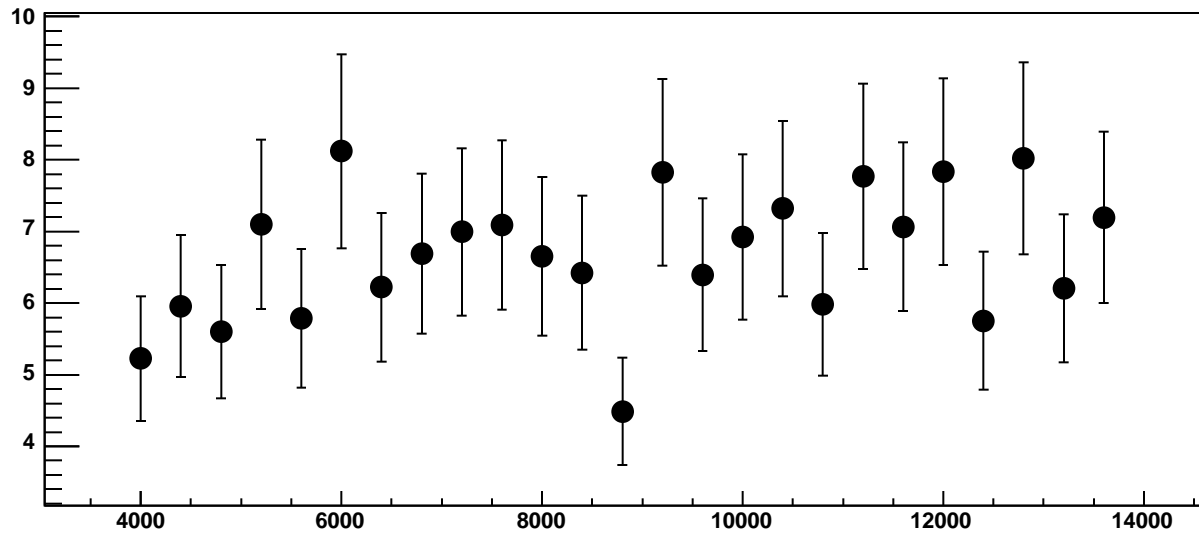


Chip 1, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

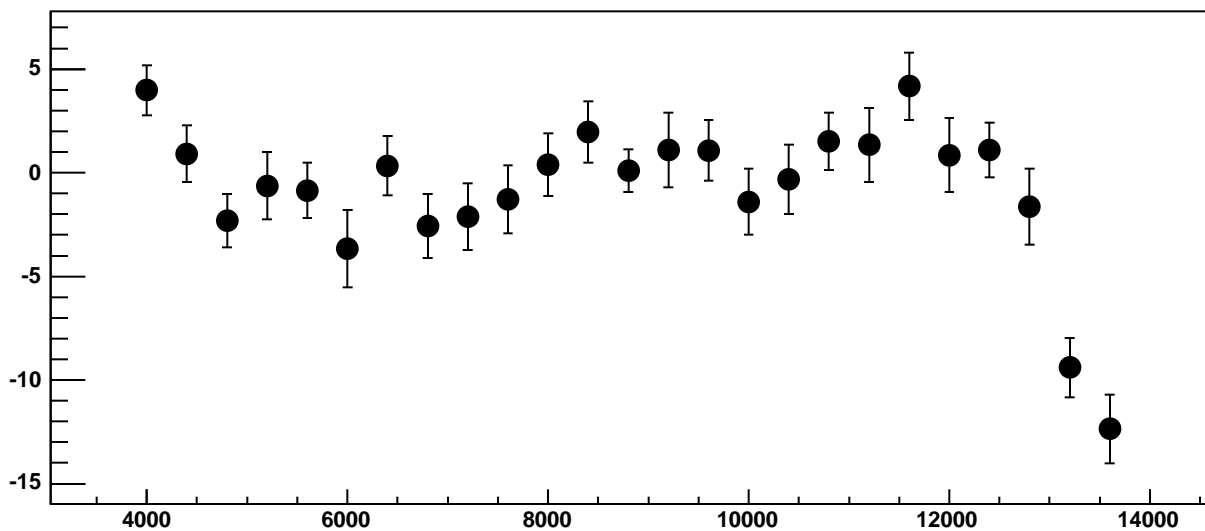


χ^2 / ndf 29.13 / 16
p0 45.11 ± 1.208
p1 0.01595 ± 0.0001592

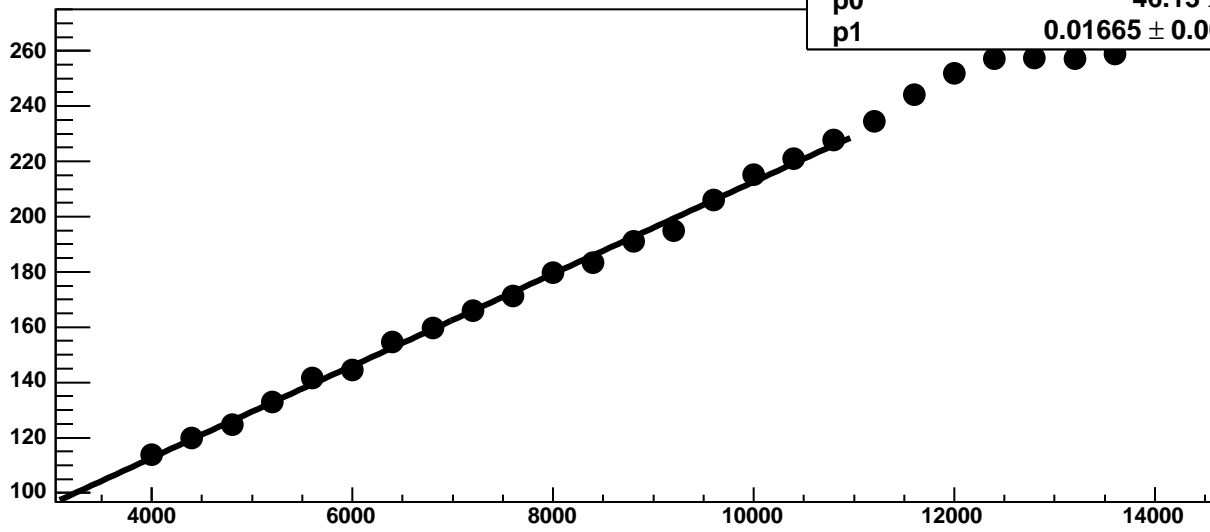
Chip 1, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



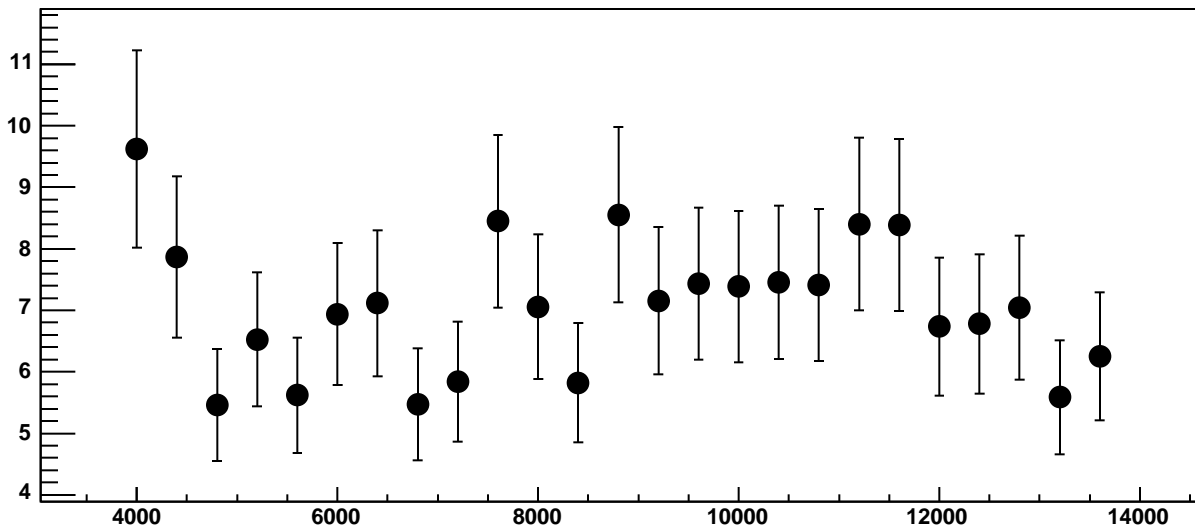
Chip 1, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



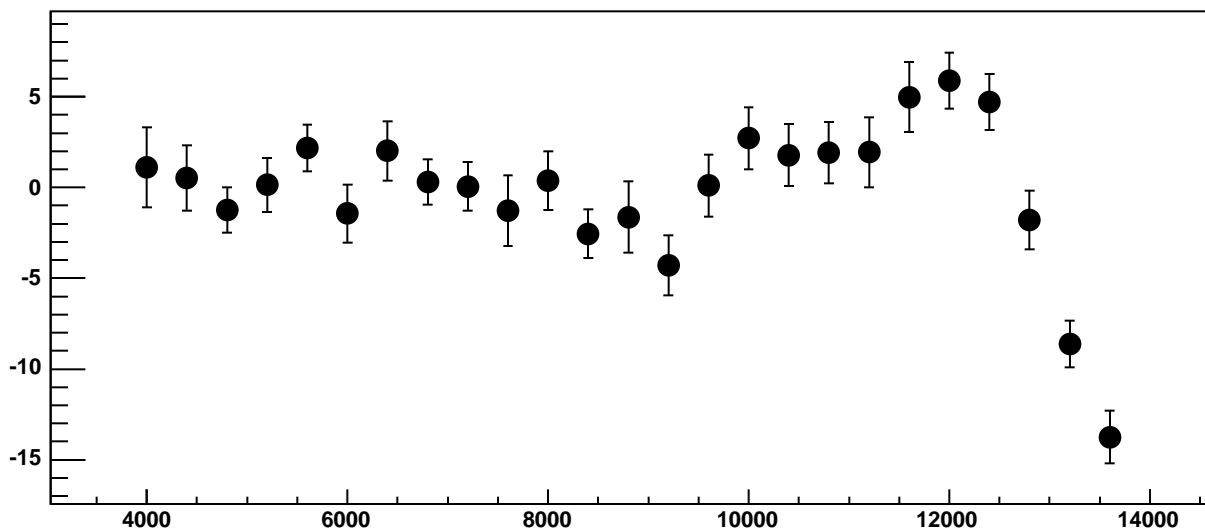
Chip 1, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC



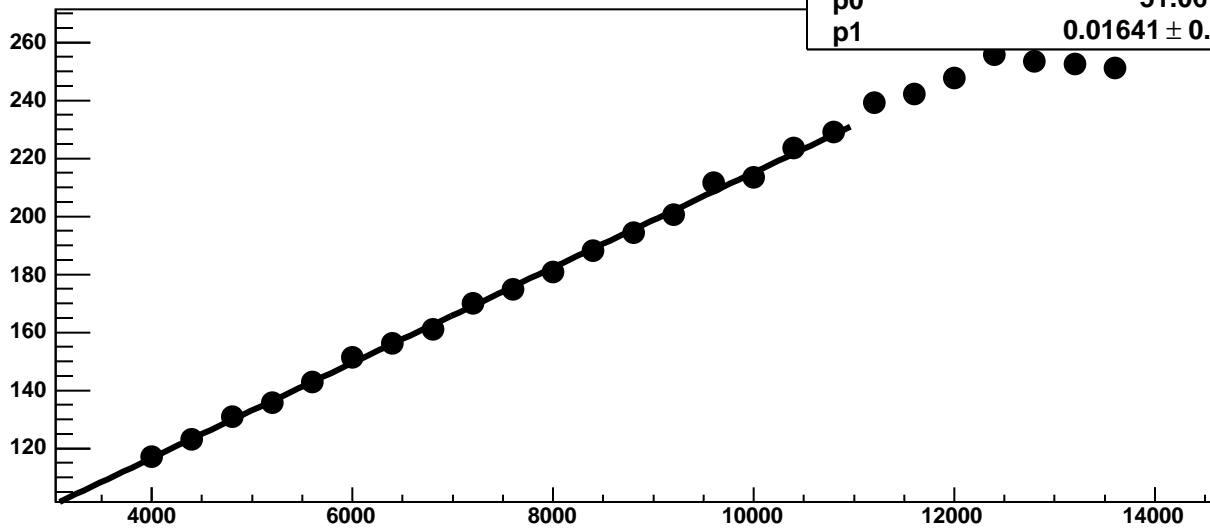
Chip 1, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



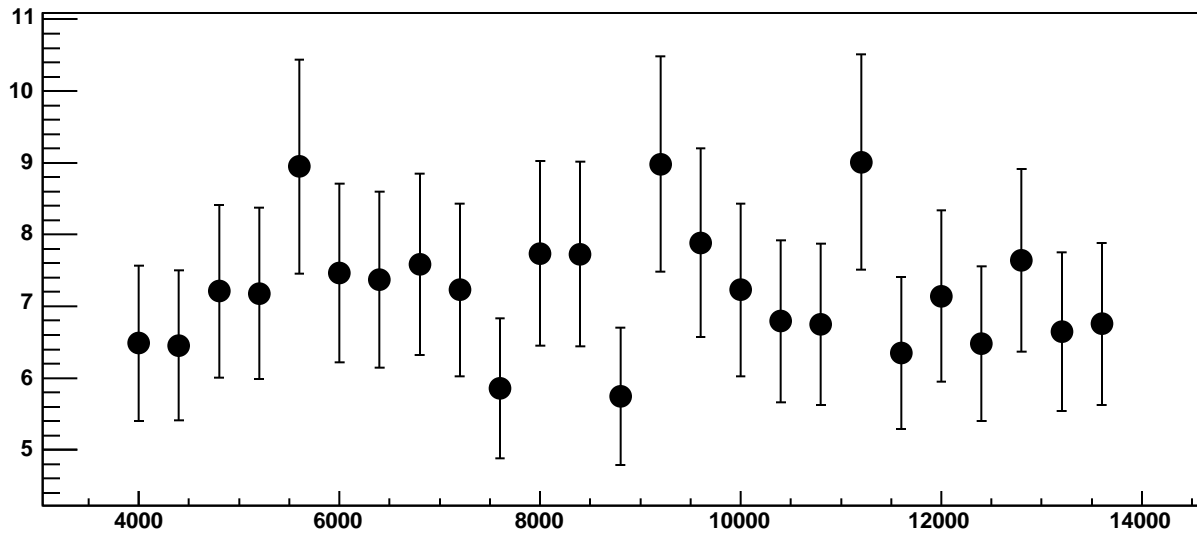
Chip 1, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC



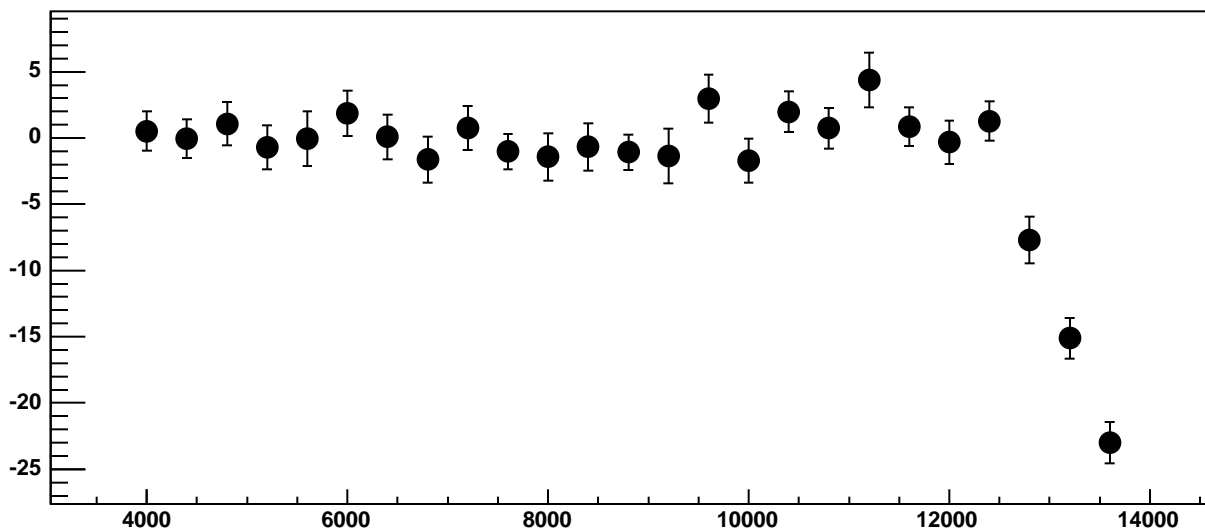
Chip 1, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



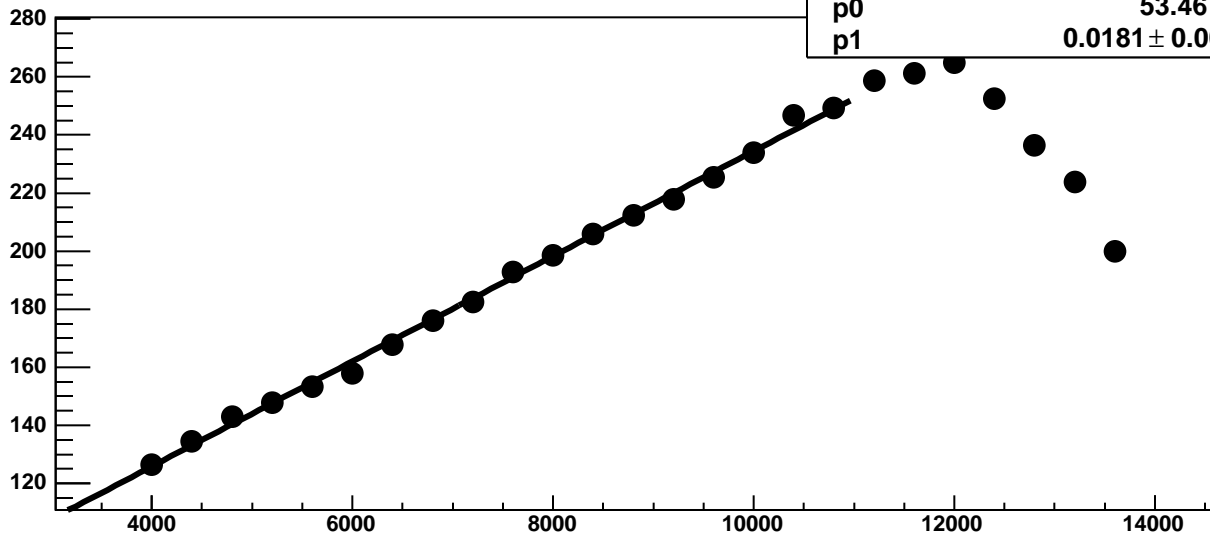
Chip 1, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



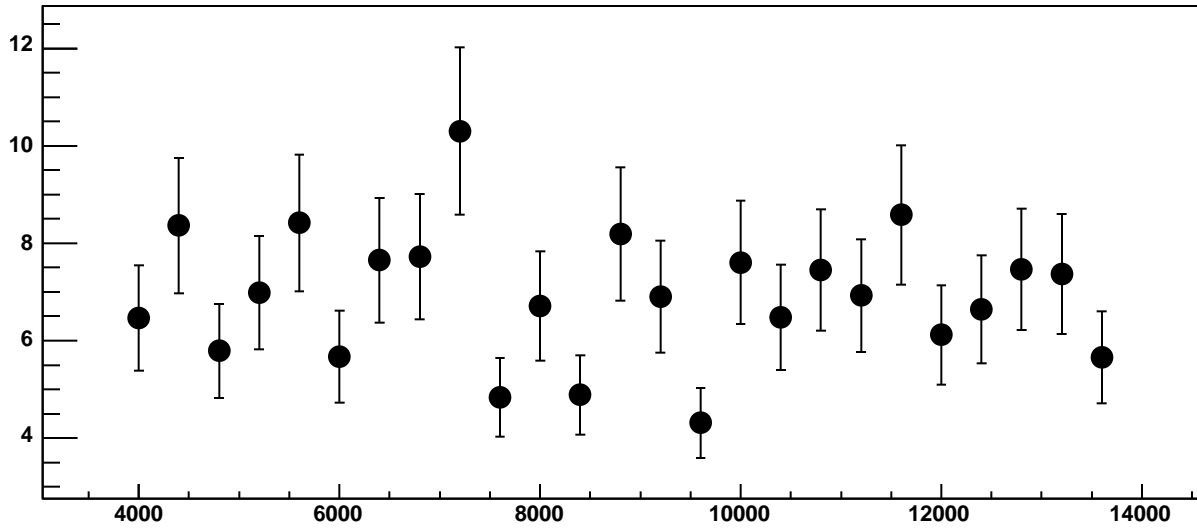
Chip 1, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



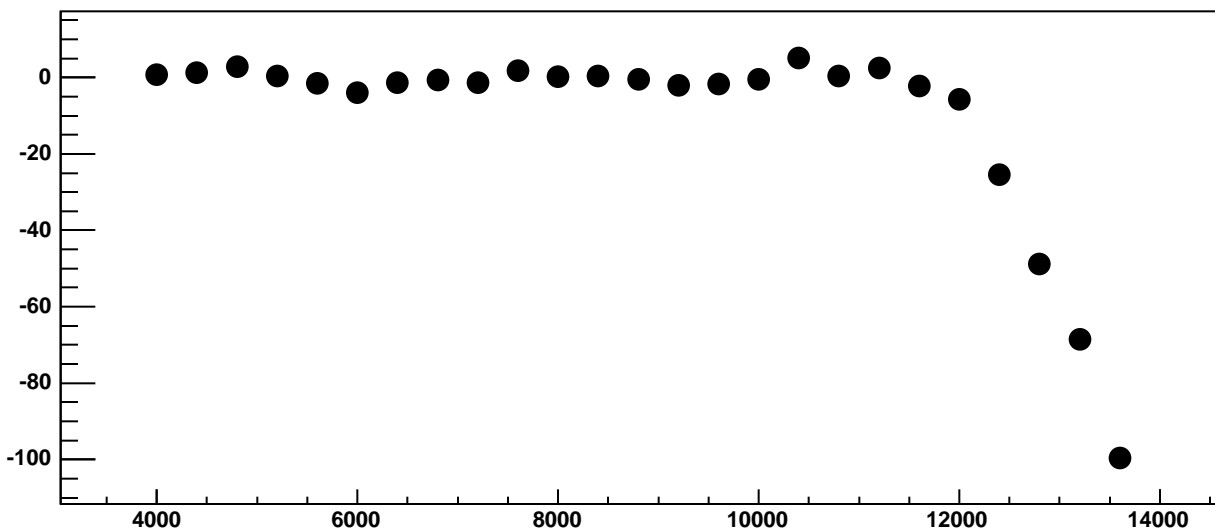
Chip 1, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



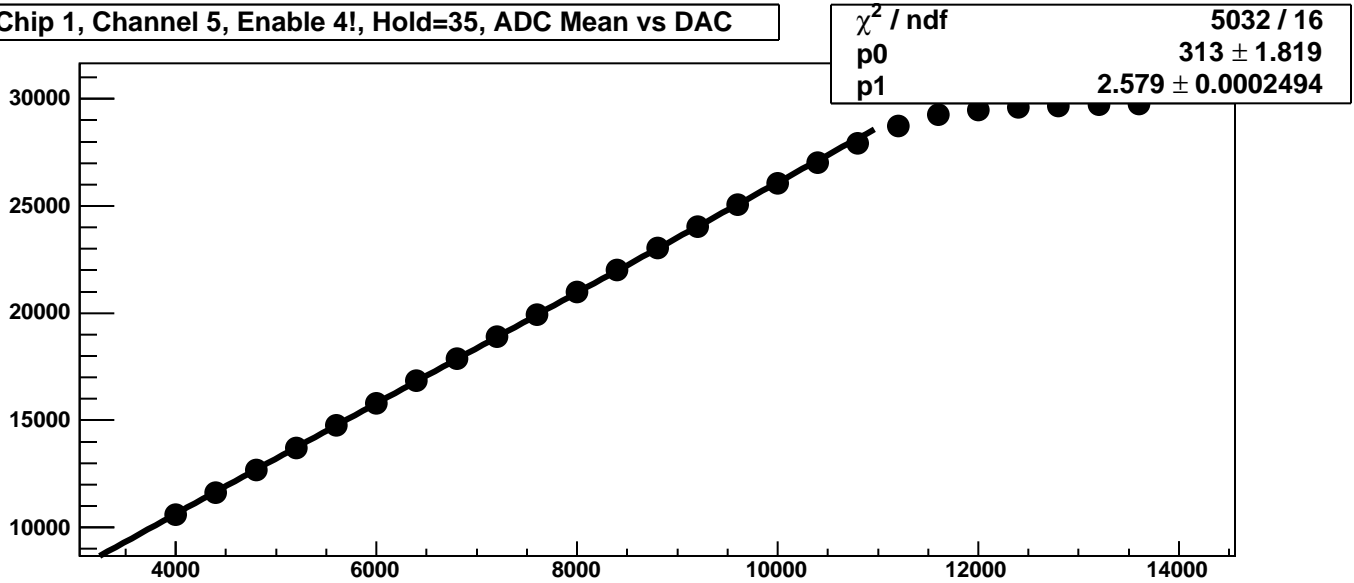
Chip 1, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



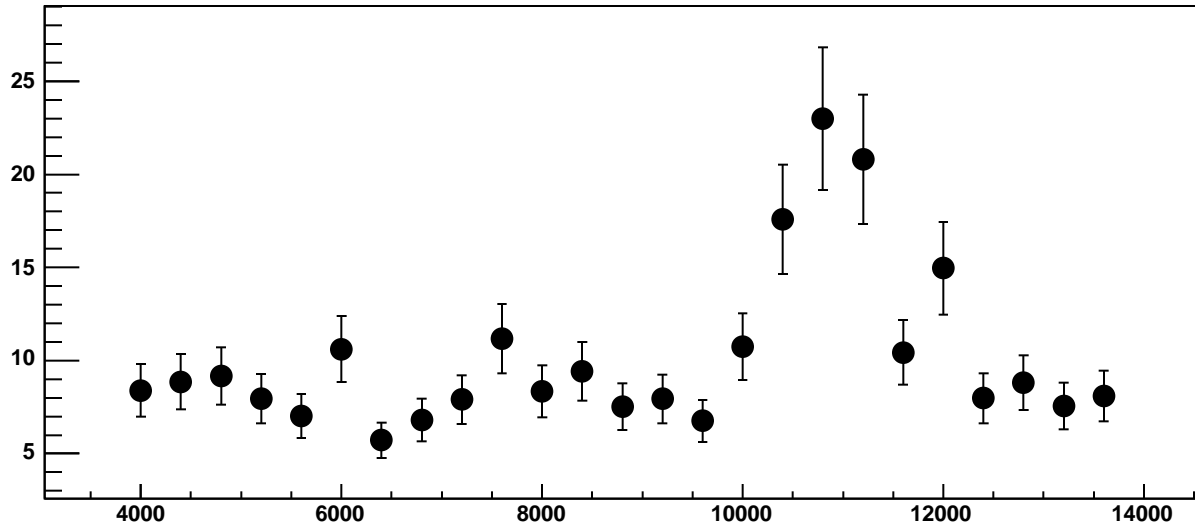
Chip 1, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



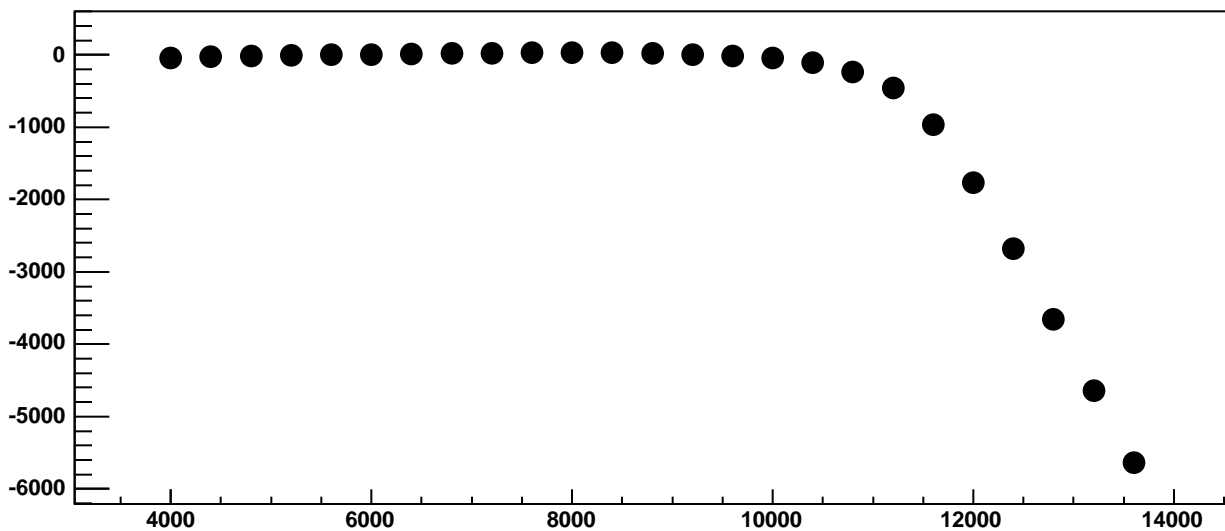
Chip 1, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC



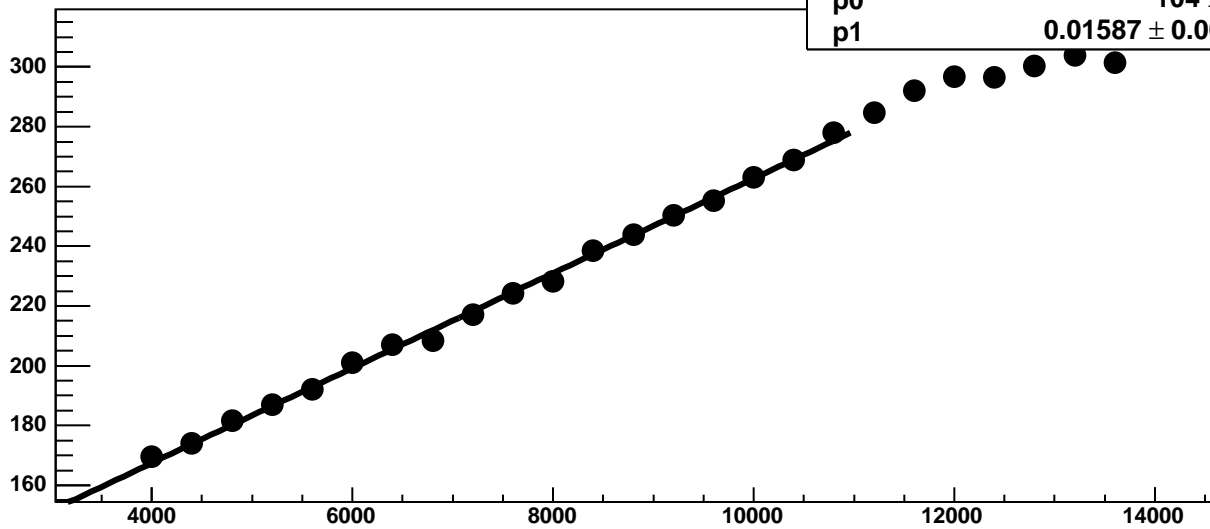
Chip 1, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

20 / 16

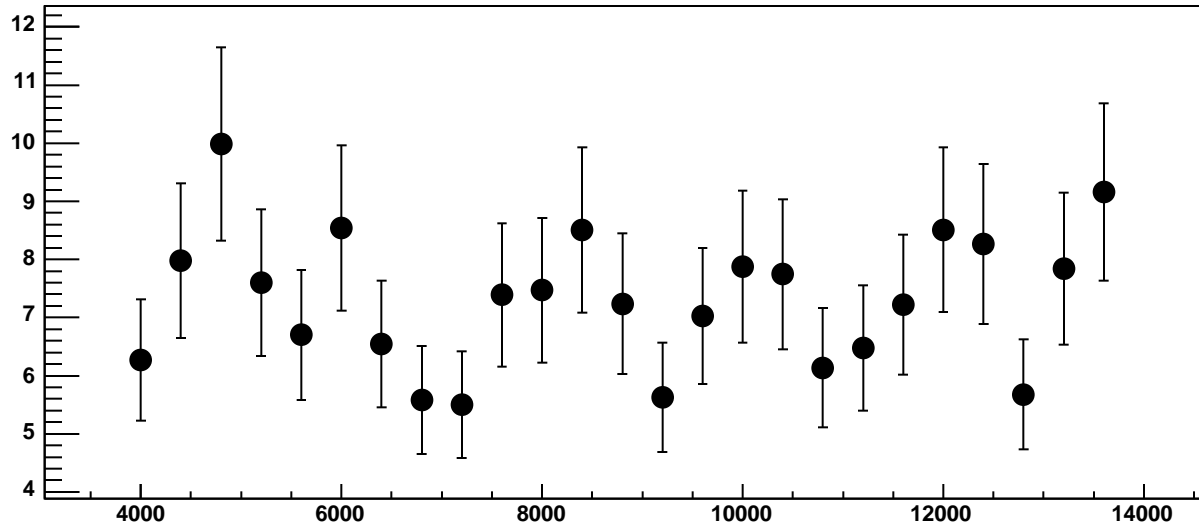
p0

104 ± 1.432

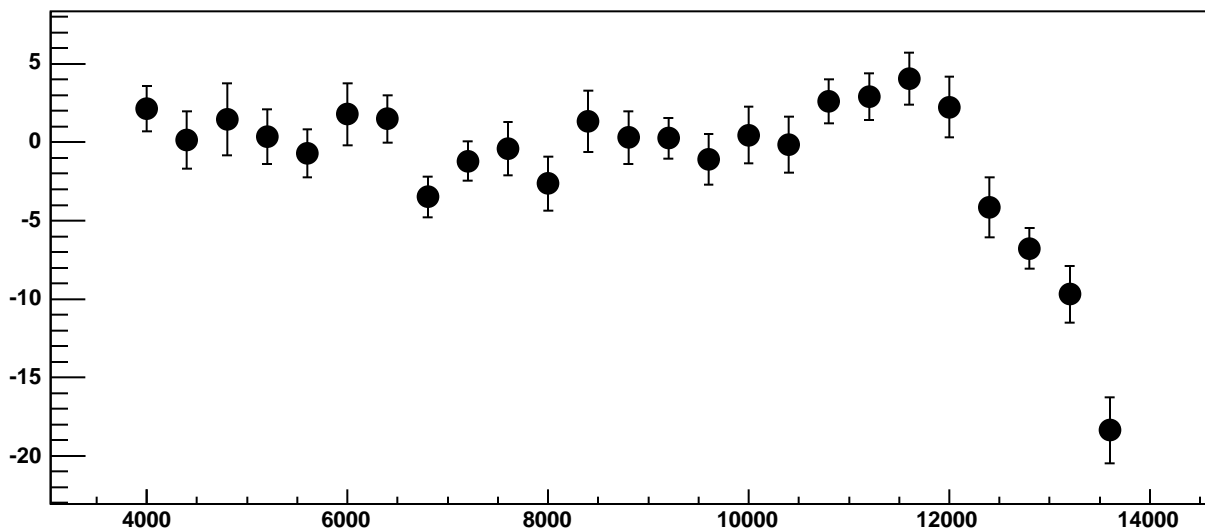
p1

0.01587 ± 0.0001846

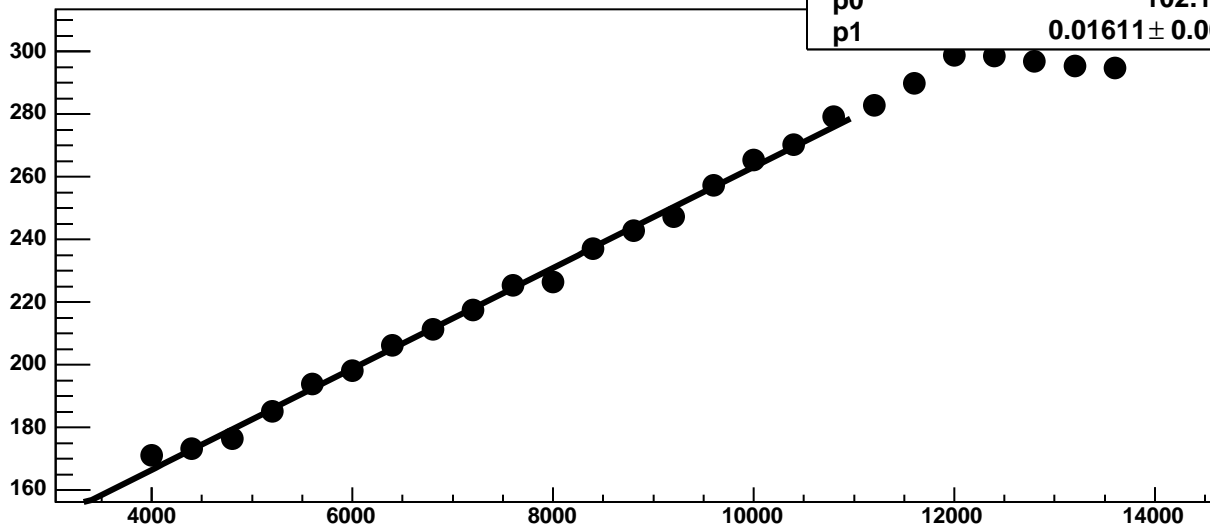
Chip 1, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

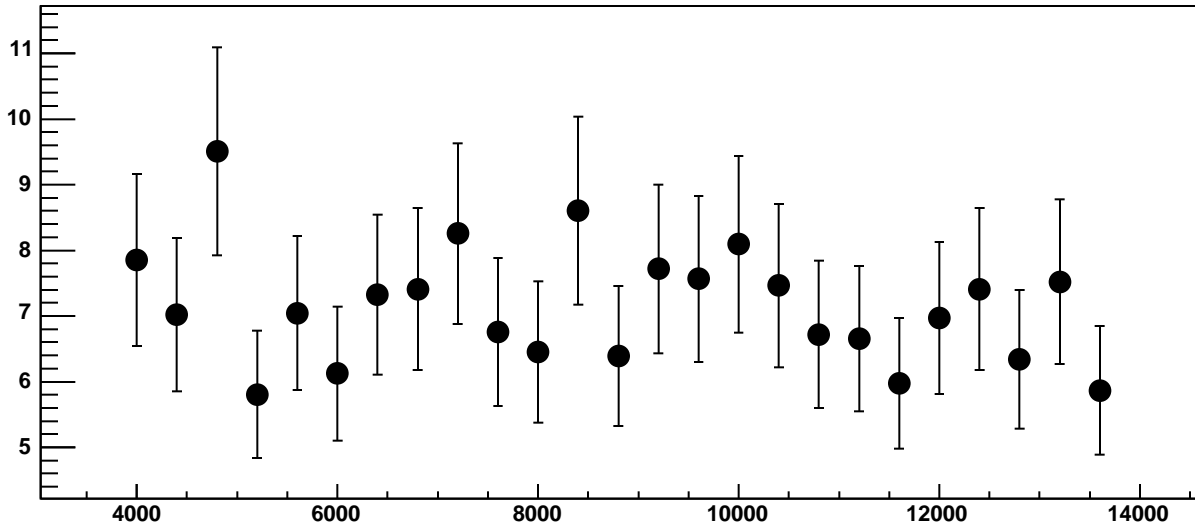


Chip 1, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

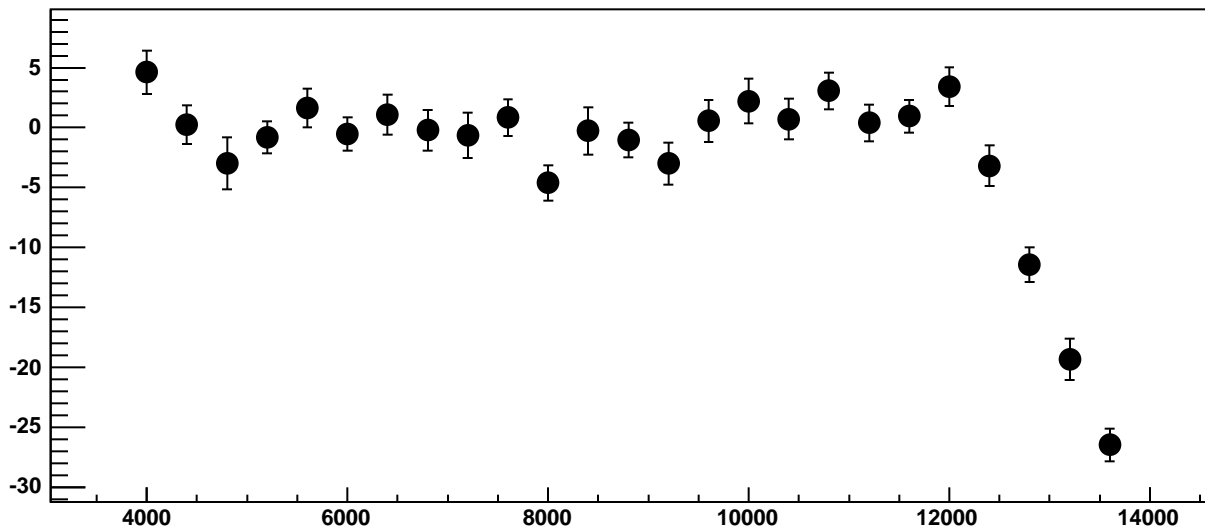


χ^2 / ndf 29.61 / 16
p0 102.1 ± 1.45
p1 0.01611 ± 0.0001897

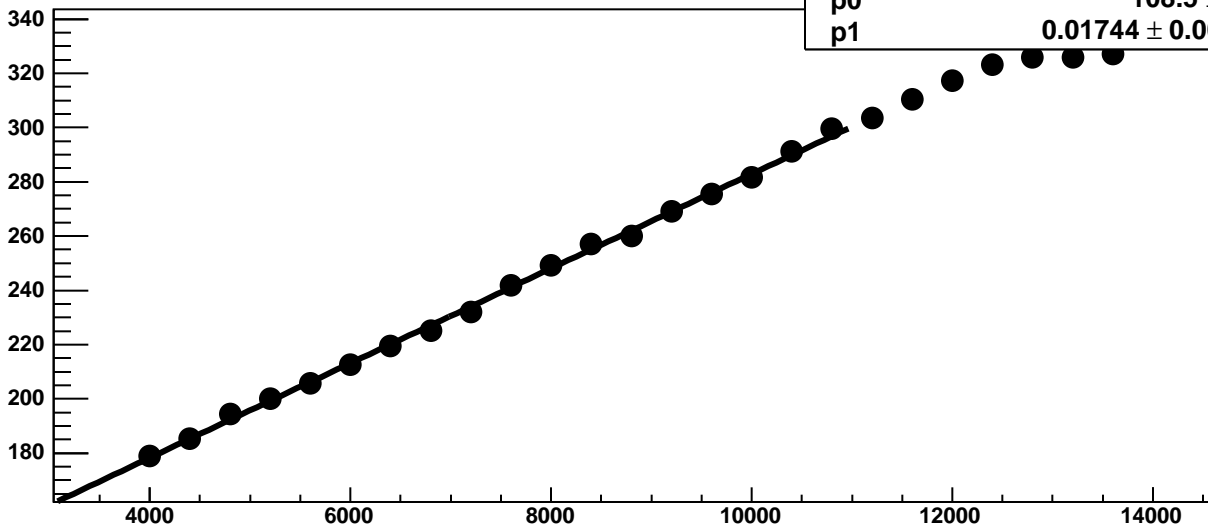
Chip 1, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

18.17 / 16

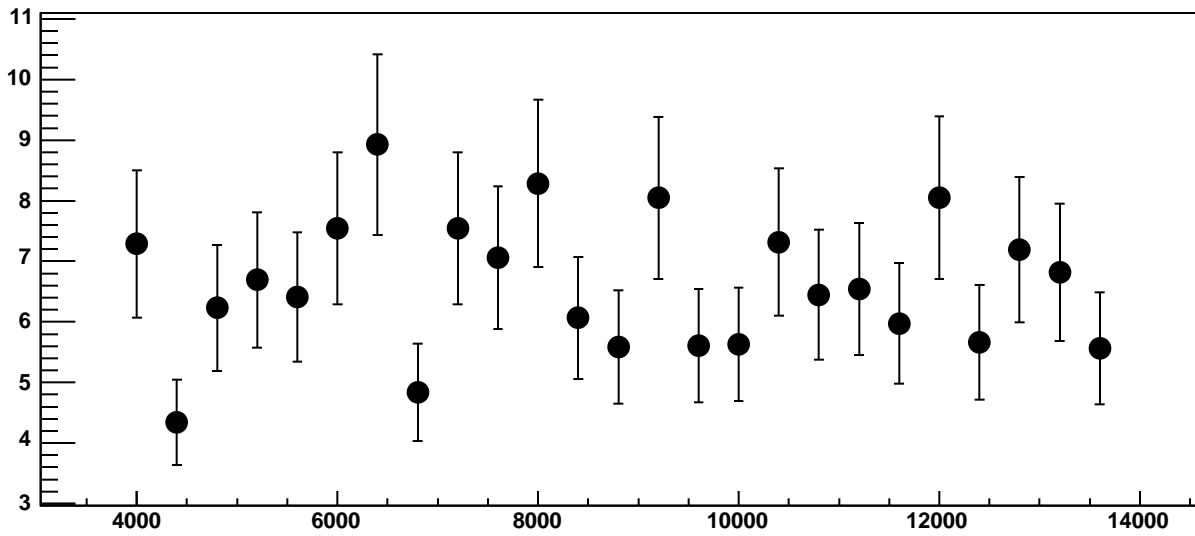
p0

108.5 ± 1.203

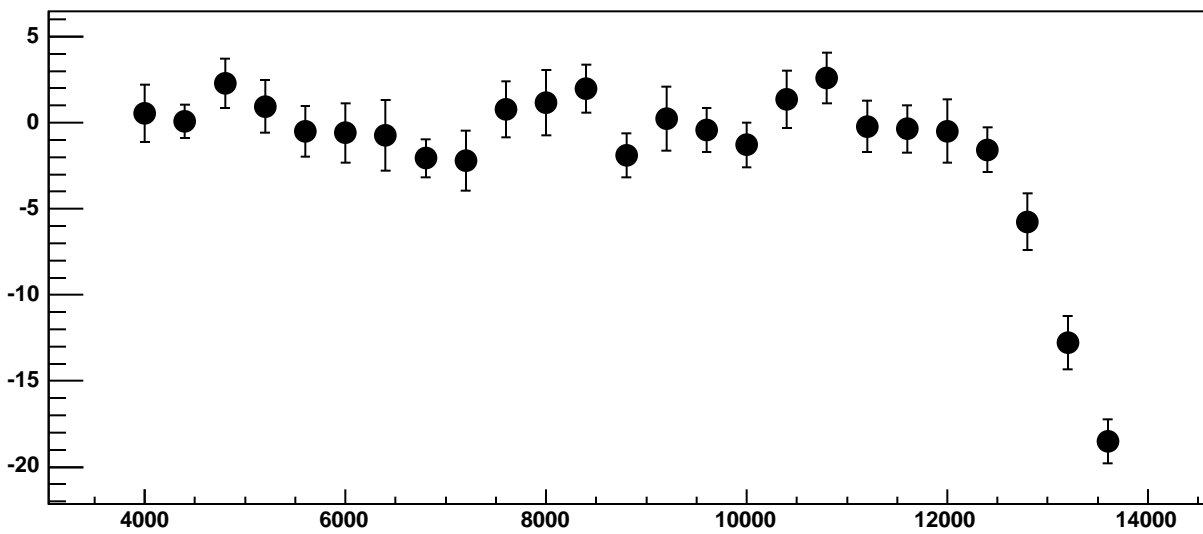
p1

0.01744 ± 0.0001585

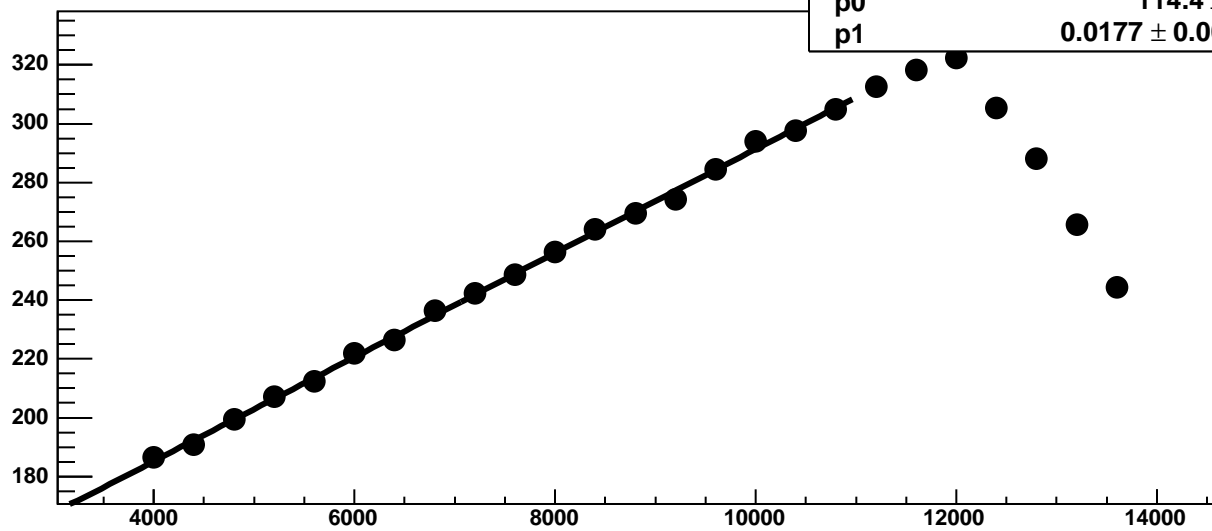
Chip 1, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



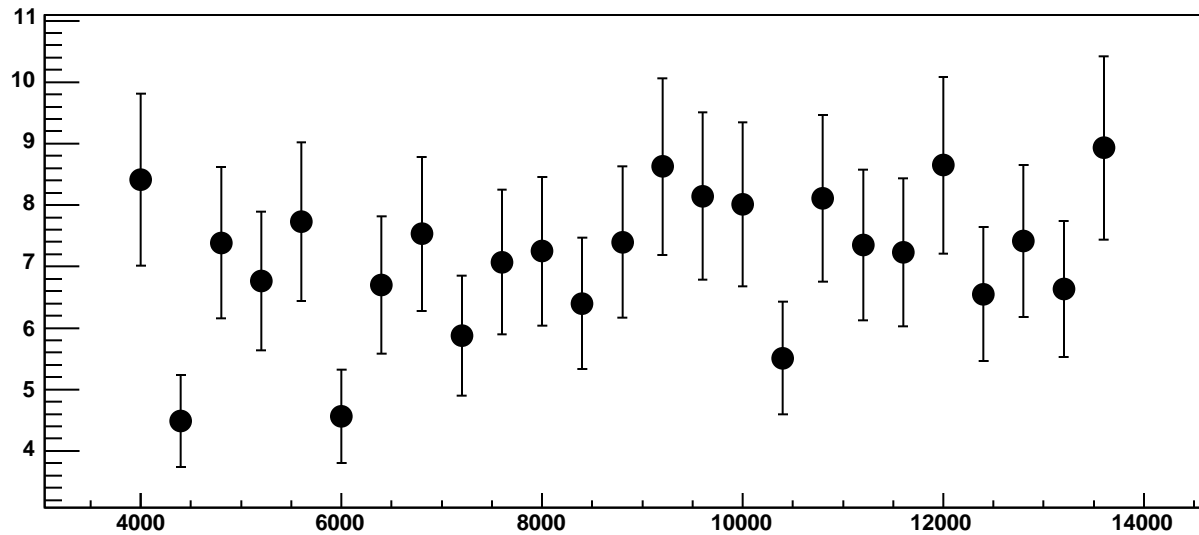
Chip 1, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC



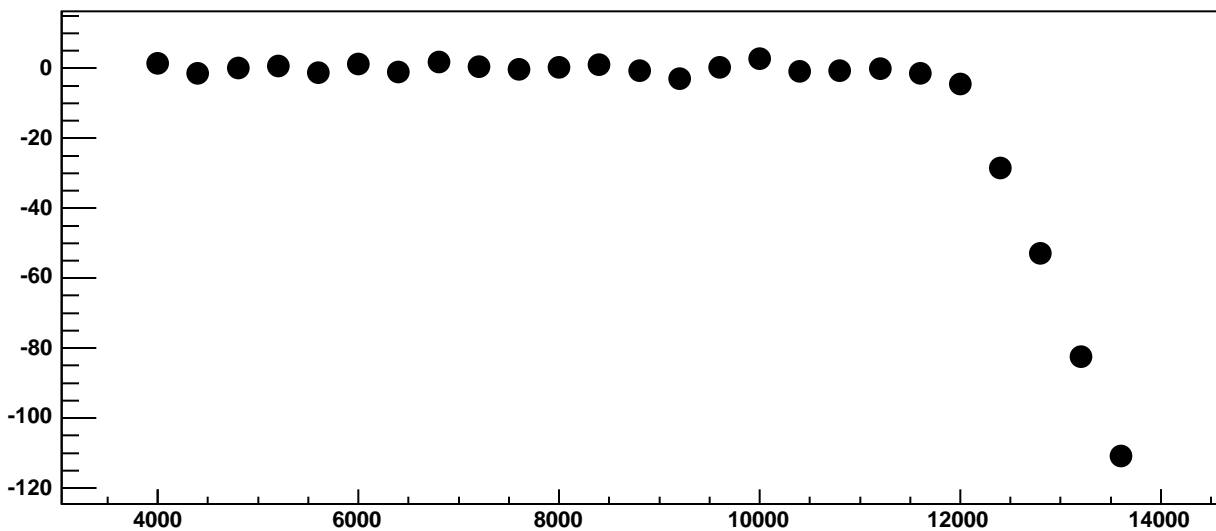
Chip 1, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



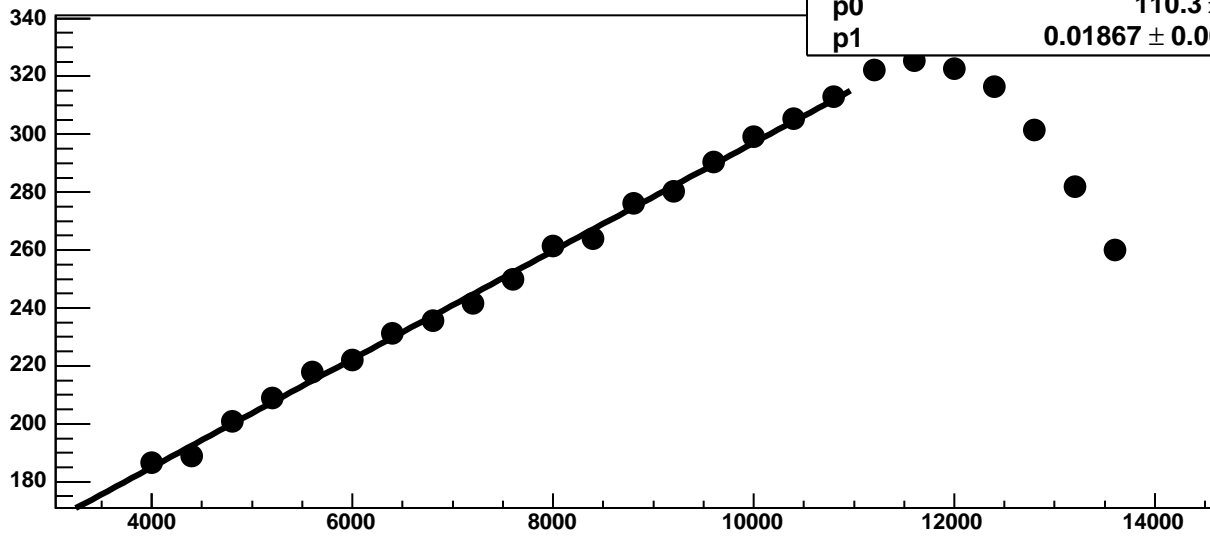
Chip 1, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



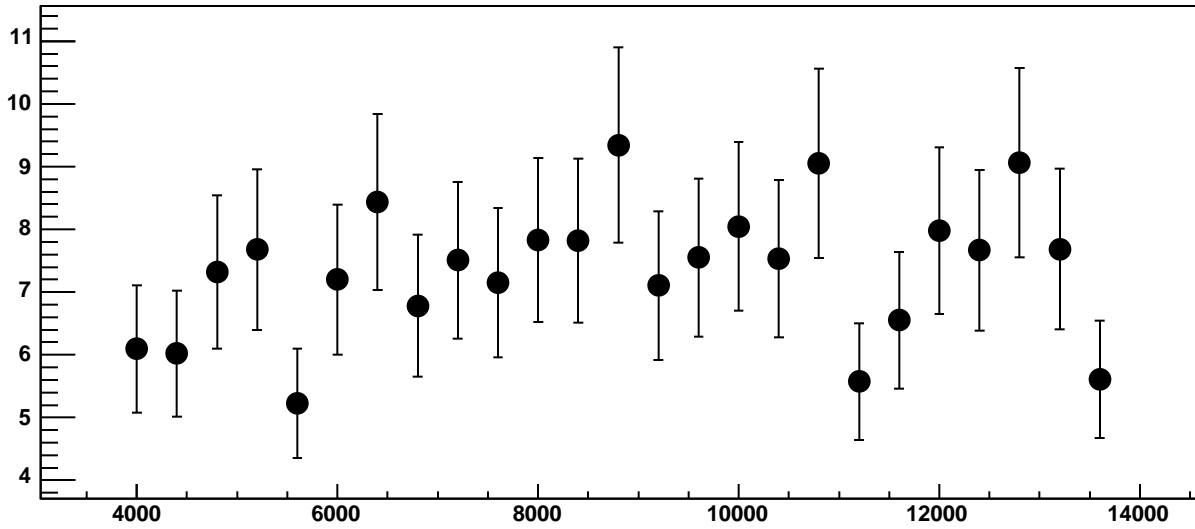
Chip 1, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



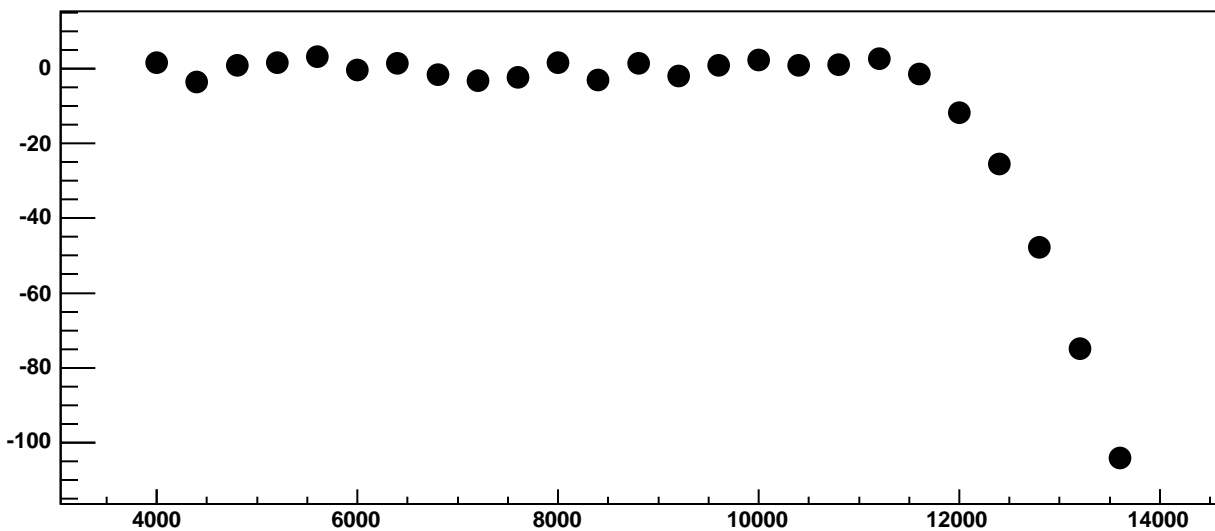
Chip 1, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



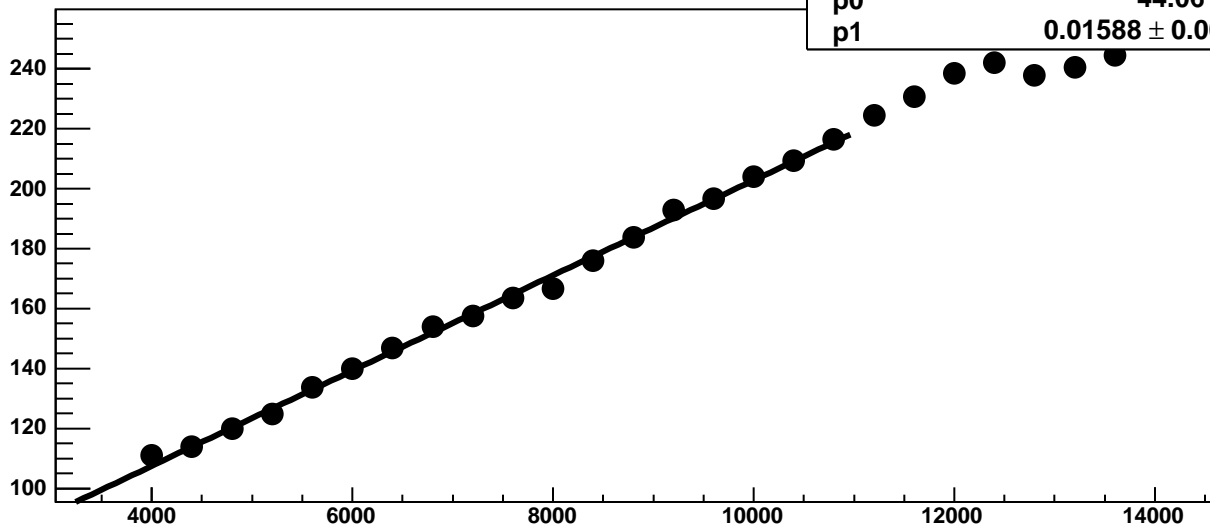
Chip 1, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



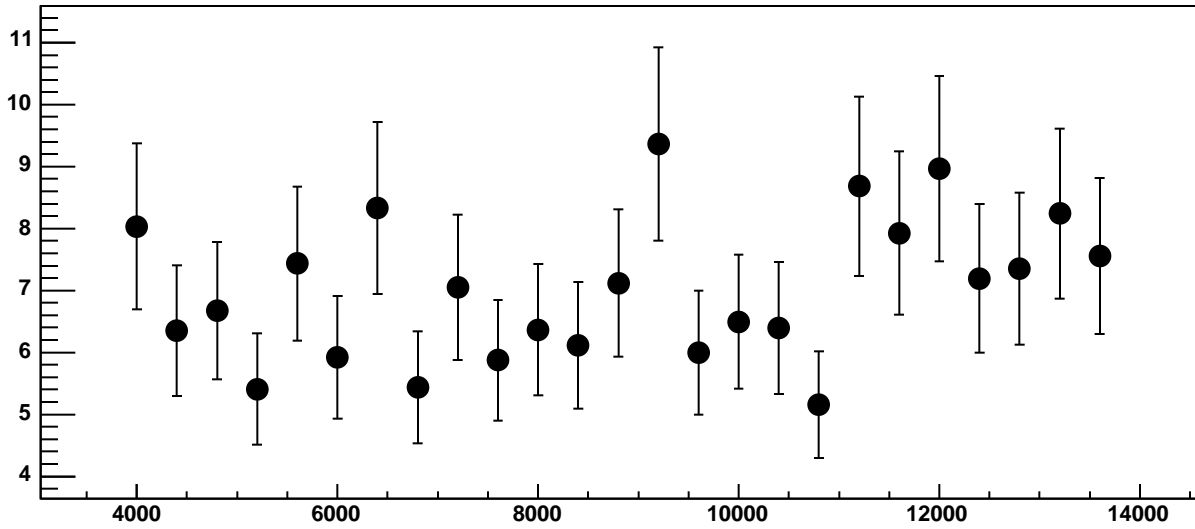
Chip 1, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



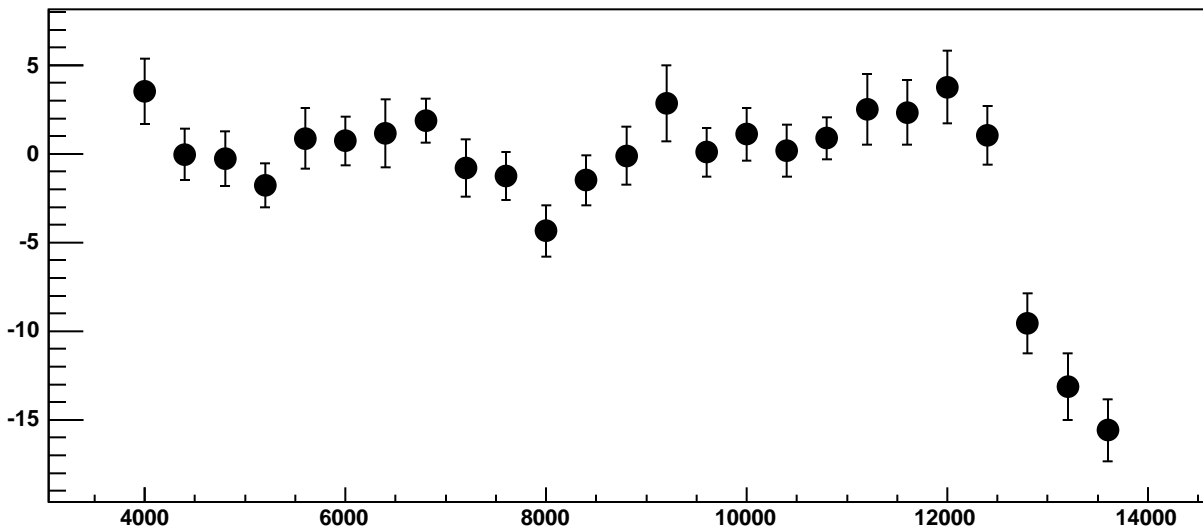
Chip 1, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



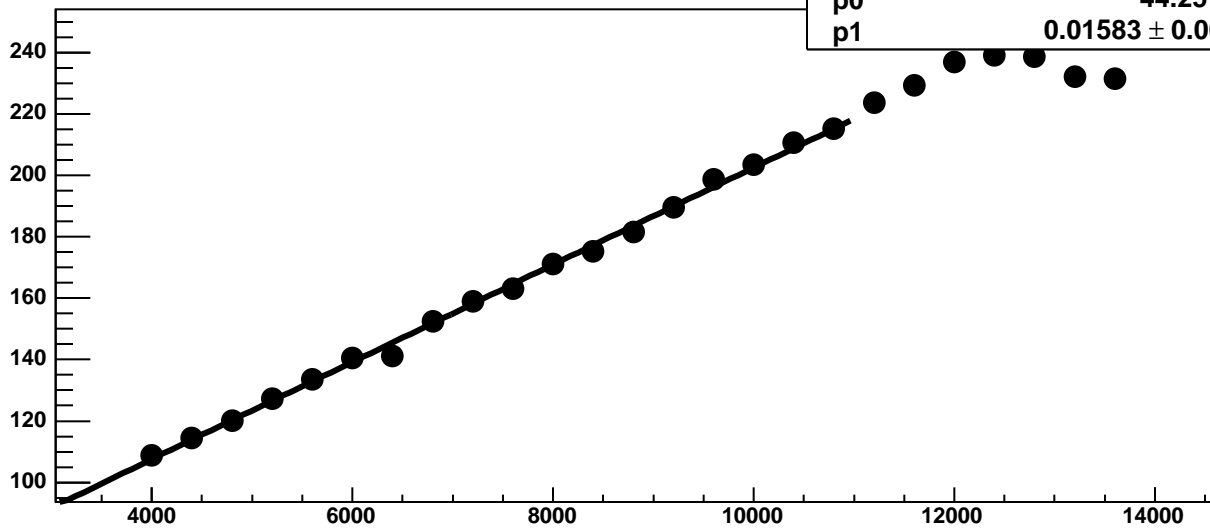
Chip 1, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



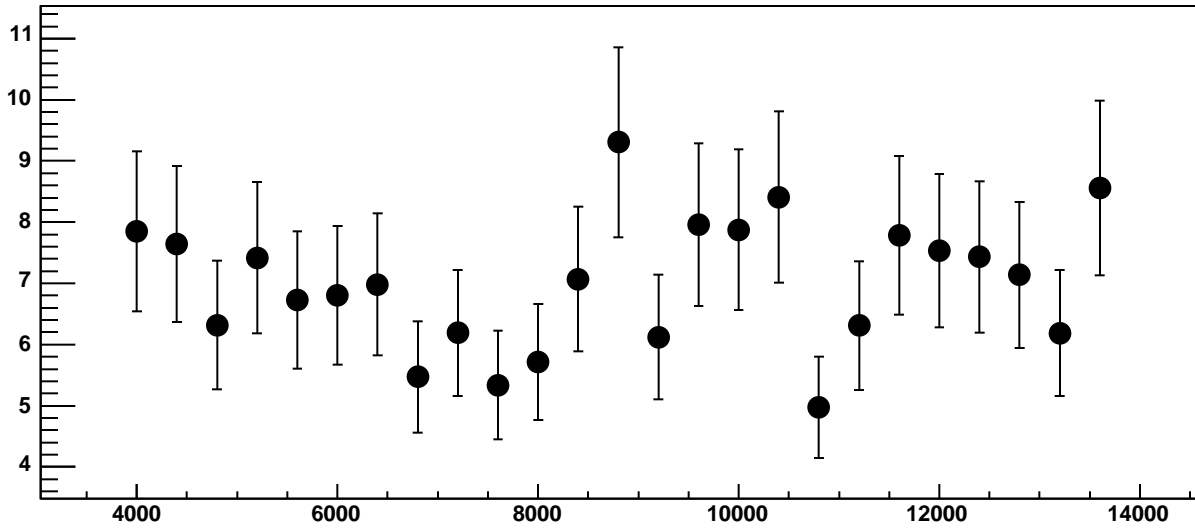
Chip 1, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



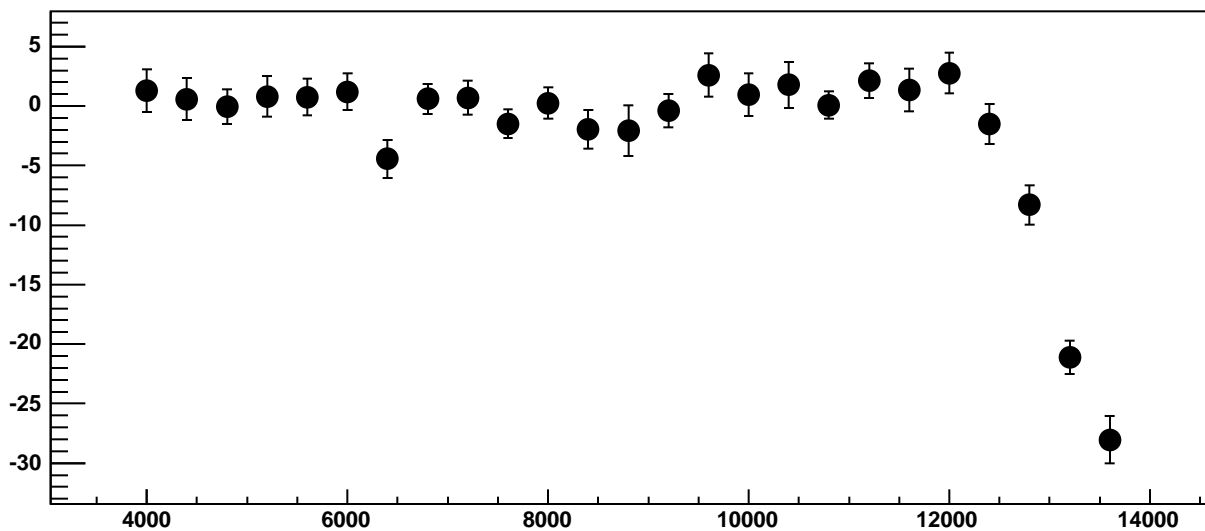
Chip 1, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



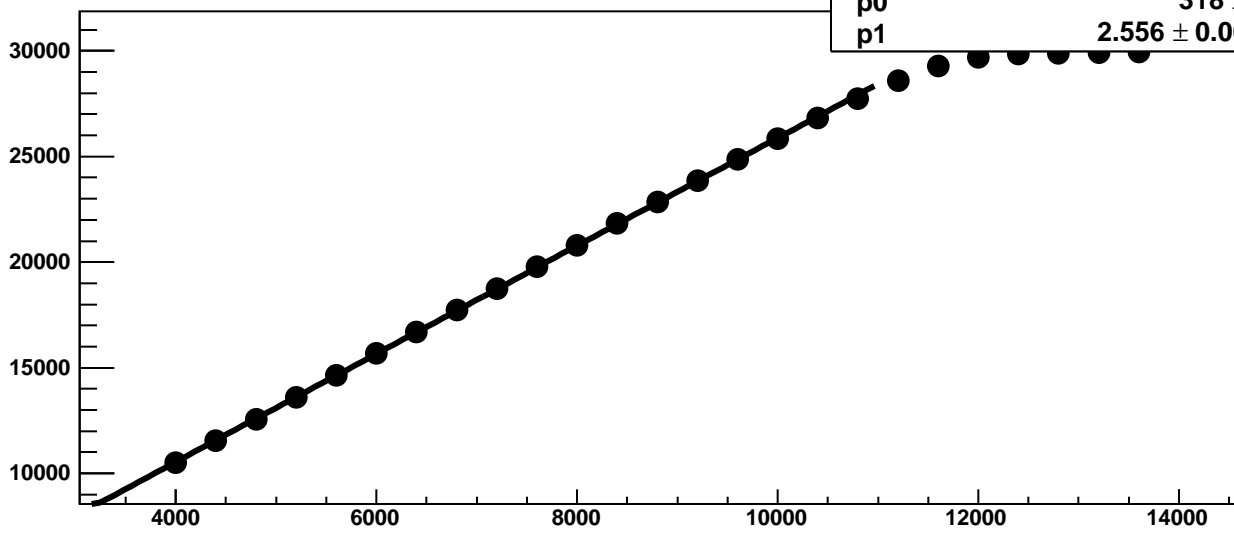
Chip 1, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

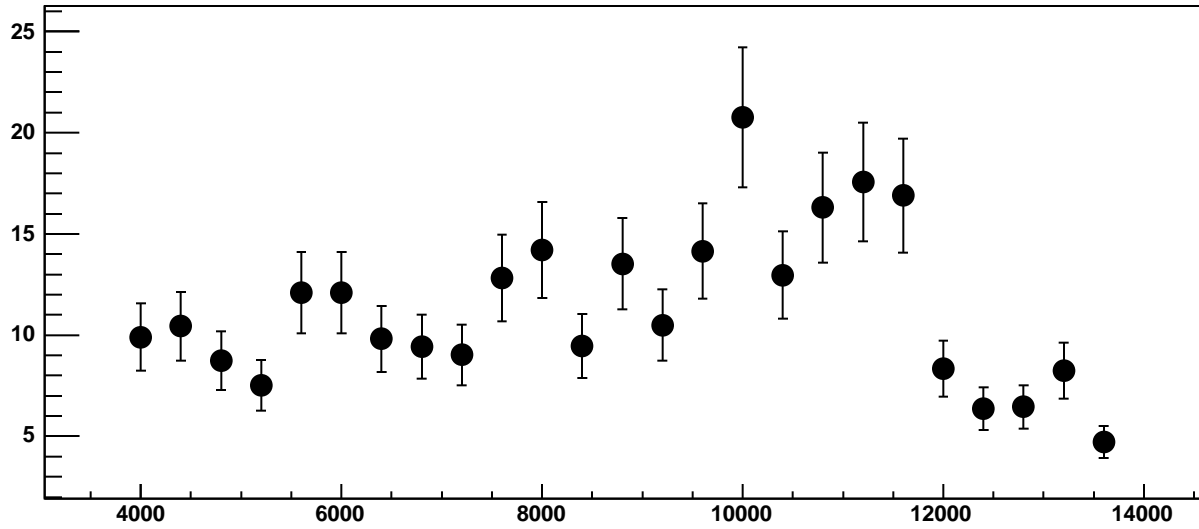


Chip 1, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC

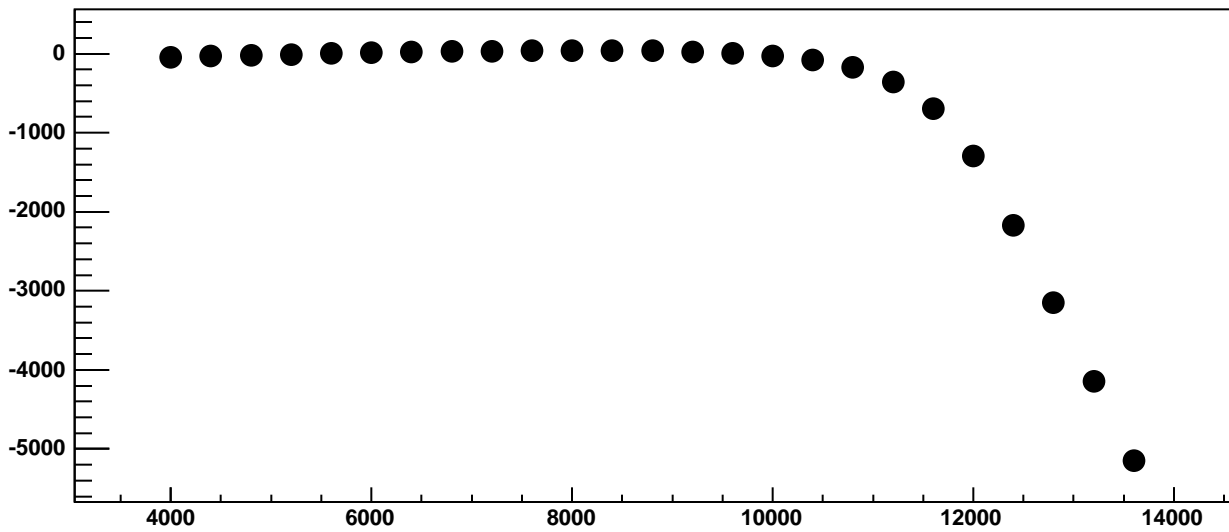


χ^2 / ndf 4799 / 16
p0 318 ± 2.158
p1 2.556 ± 0.0003052

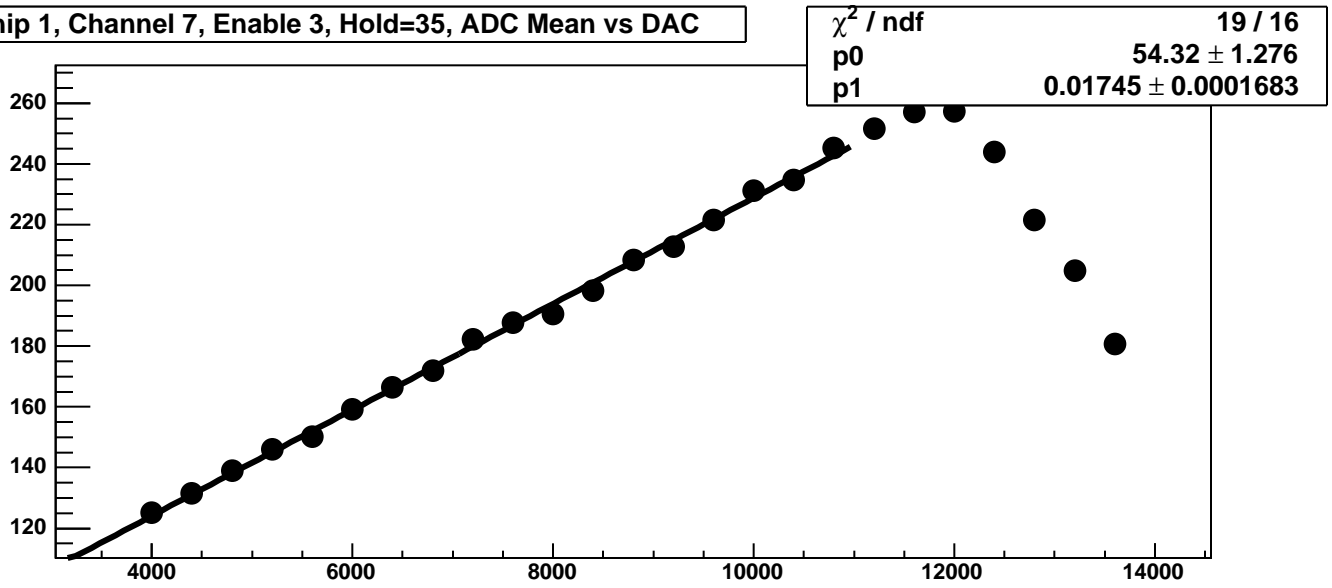
Chip 1, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



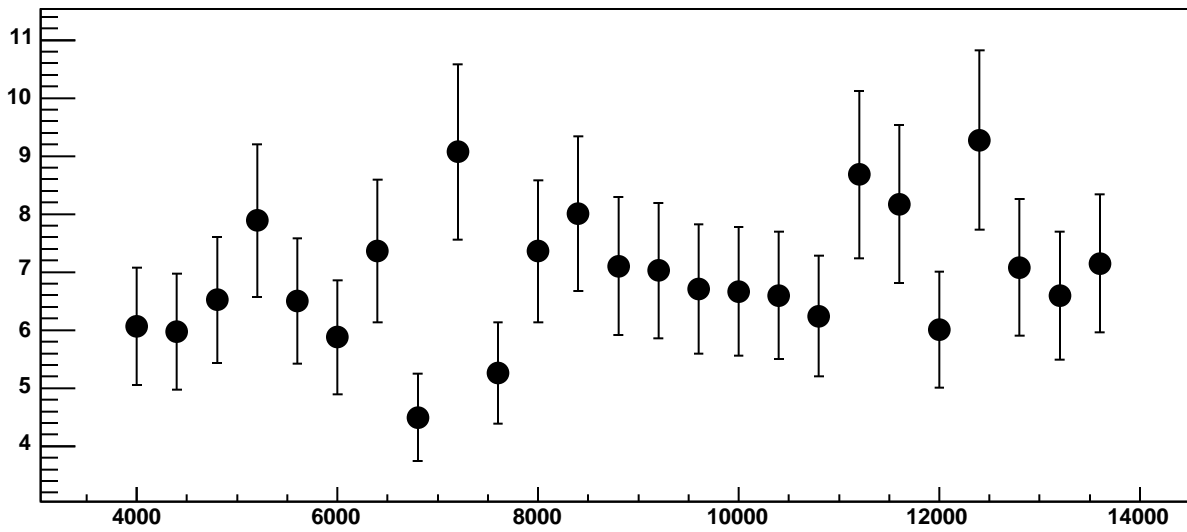
Chip 1, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC



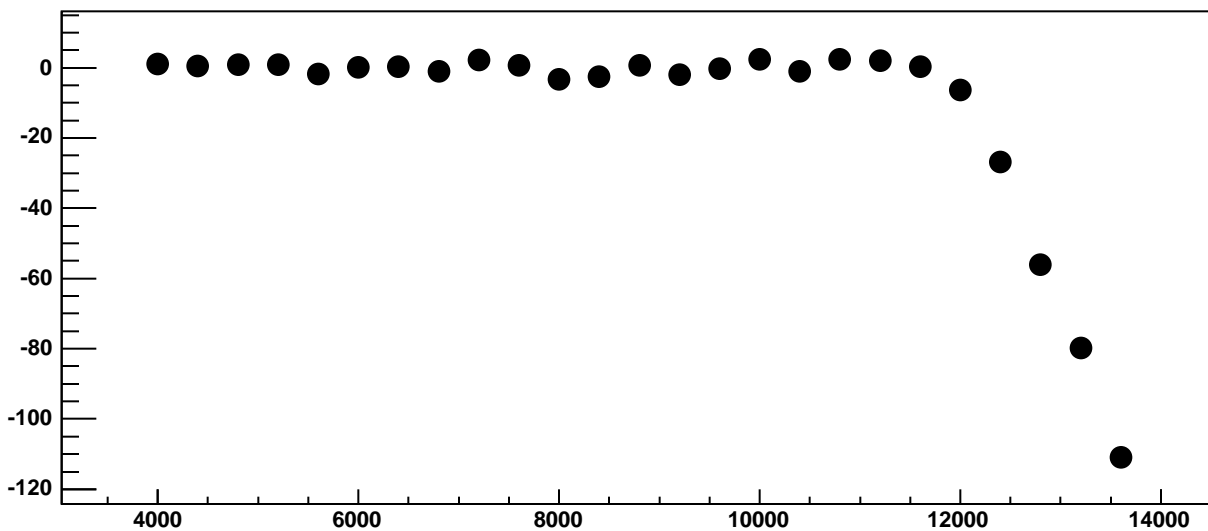
Chip 1, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



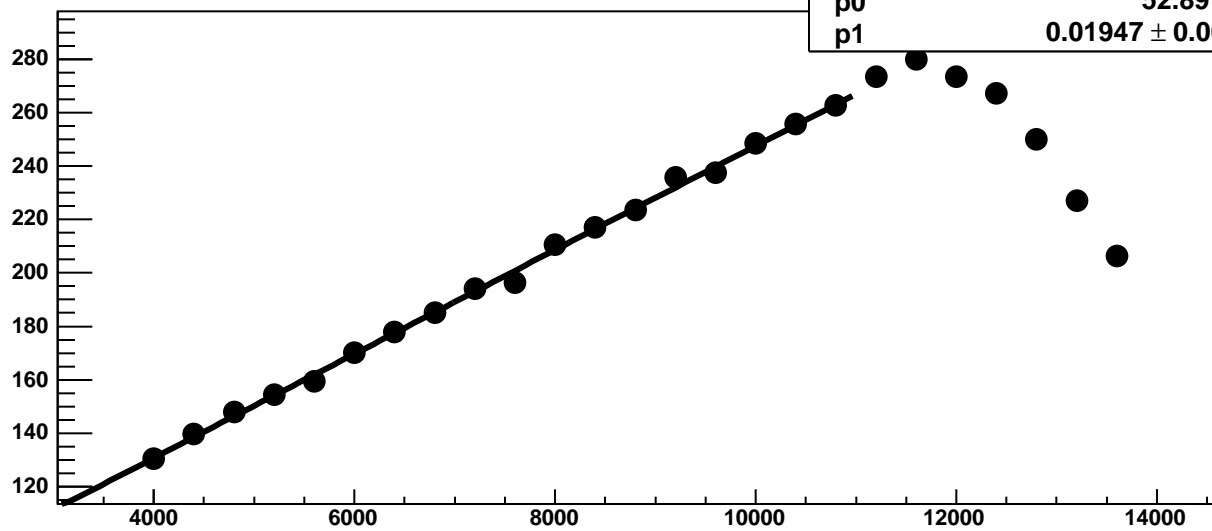
Chip 1, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



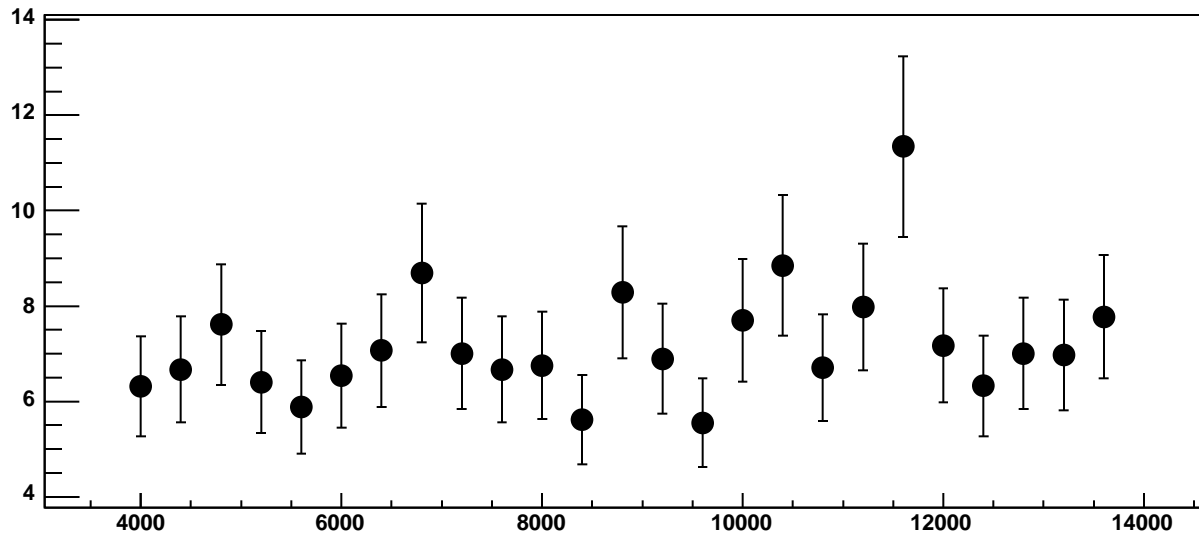
Chip 1, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



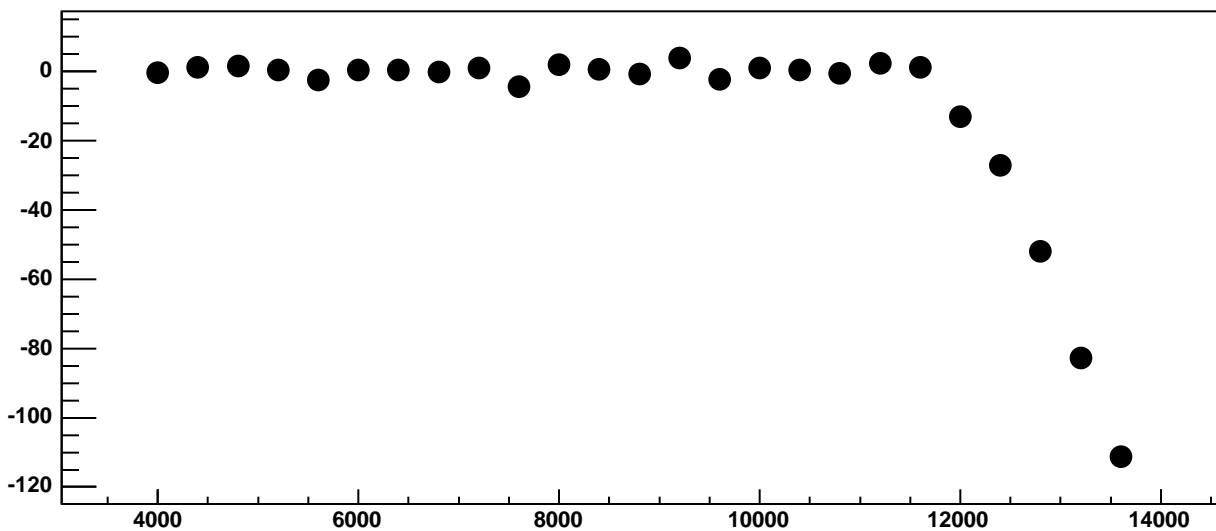
Chip 1, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



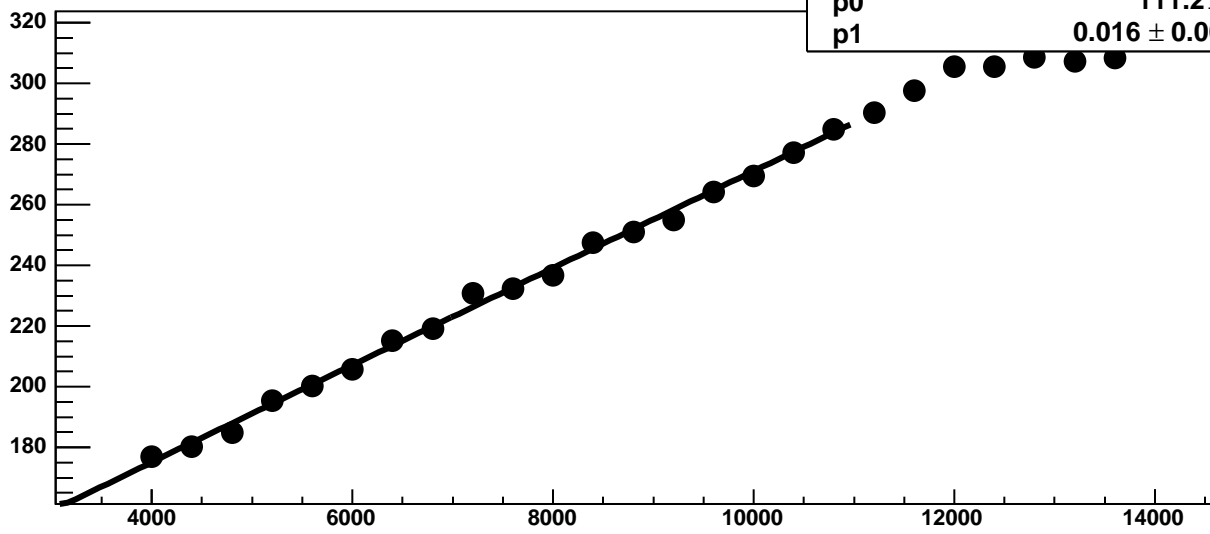
Chip 1, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

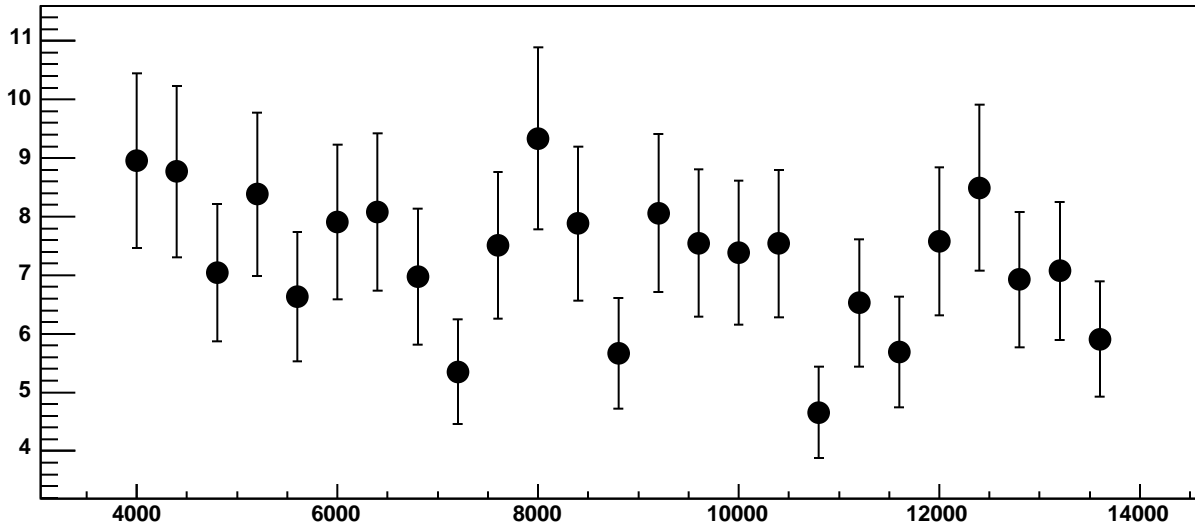


Chip 1, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

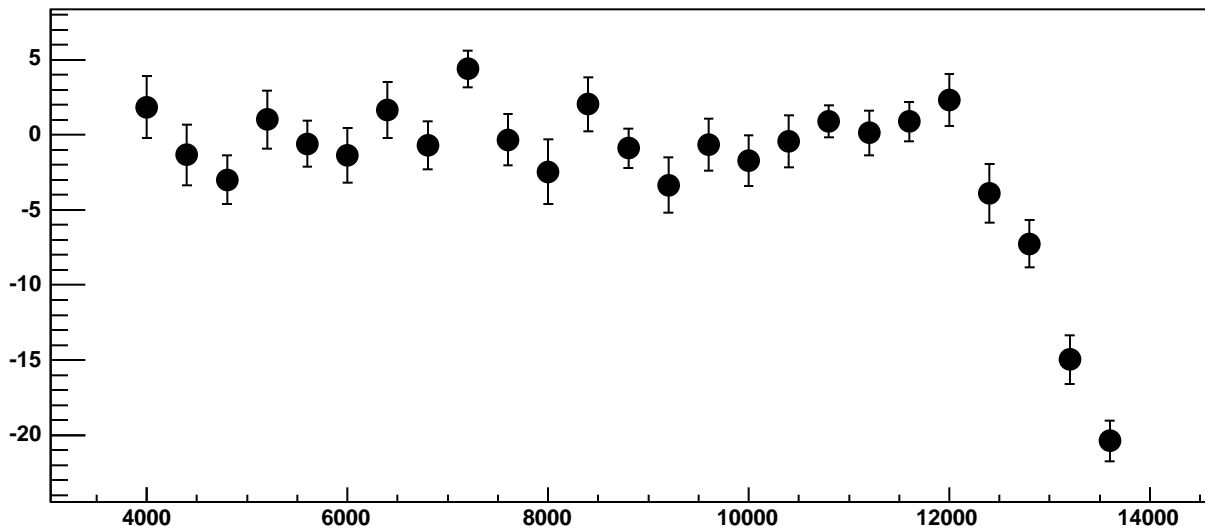


χ^2 / ndf 27.77 / 16
p0 111.2 ± 1.467
p1 0.016 ± 0.0001825

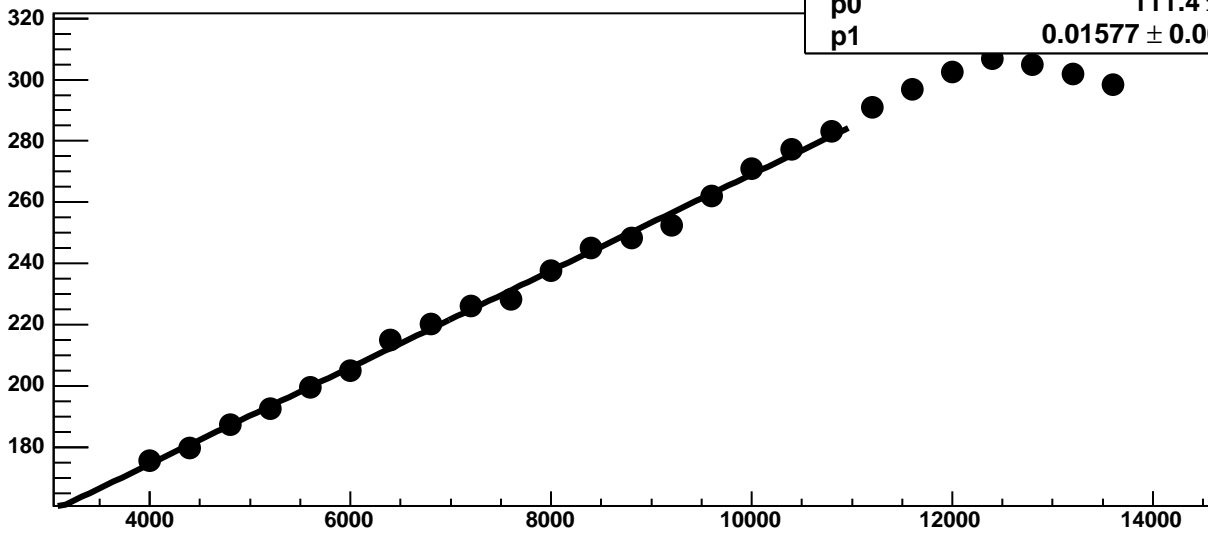
Chip 1, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



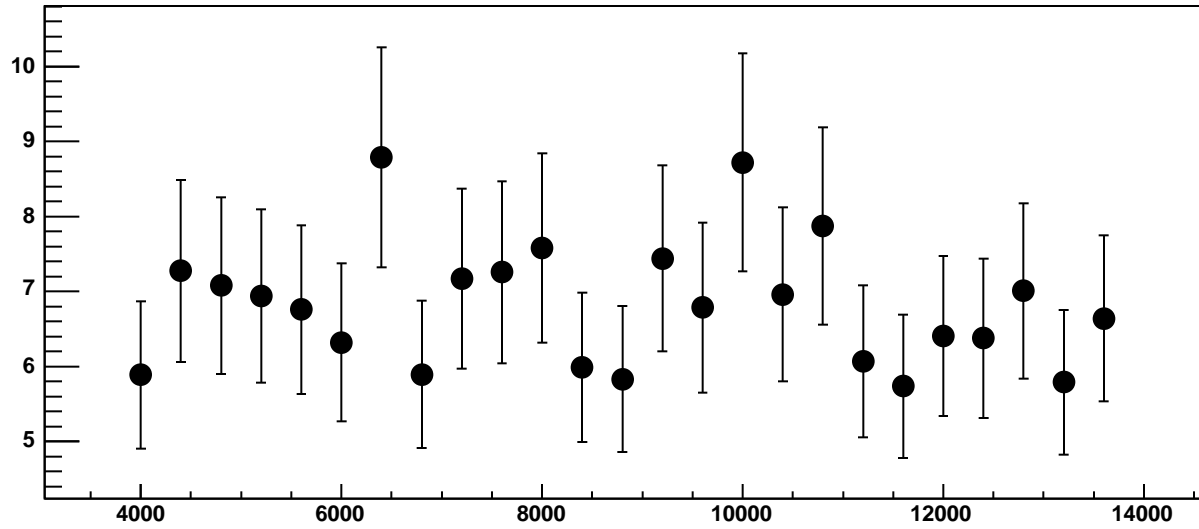
Chip 1, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



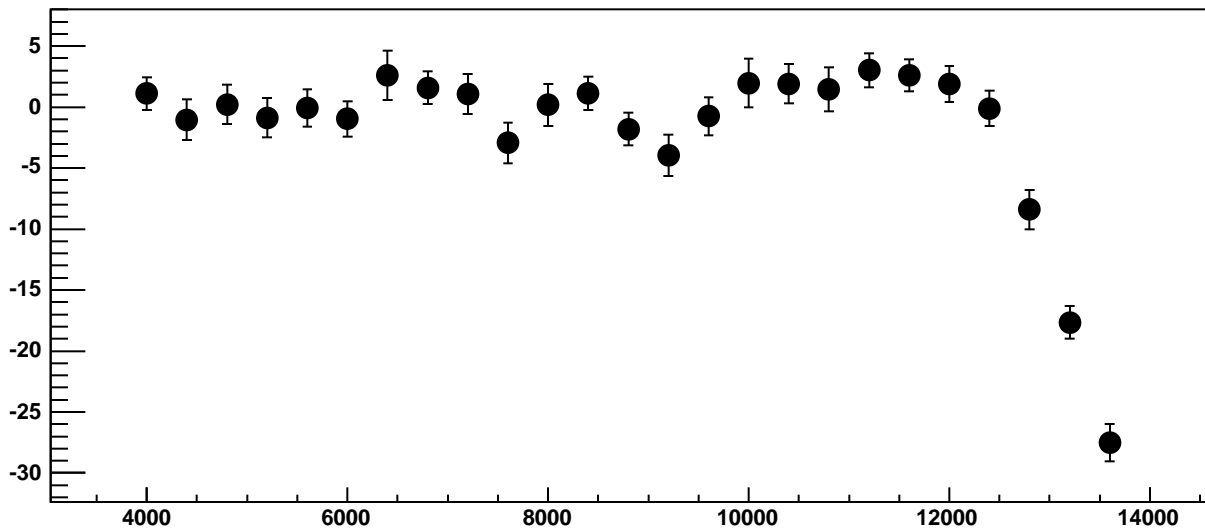
Chip 1, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



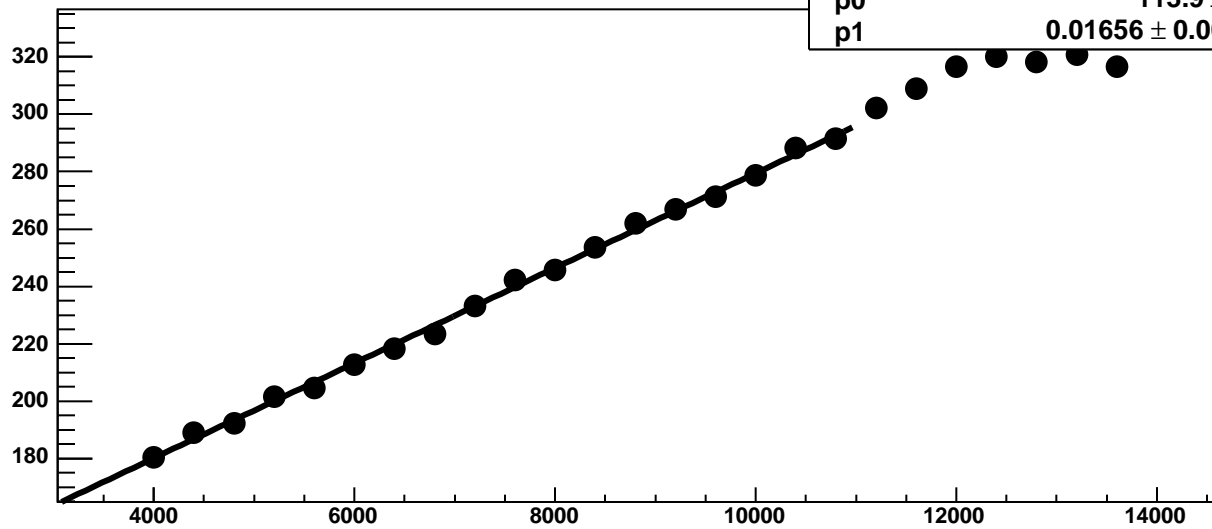
Chip 1, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



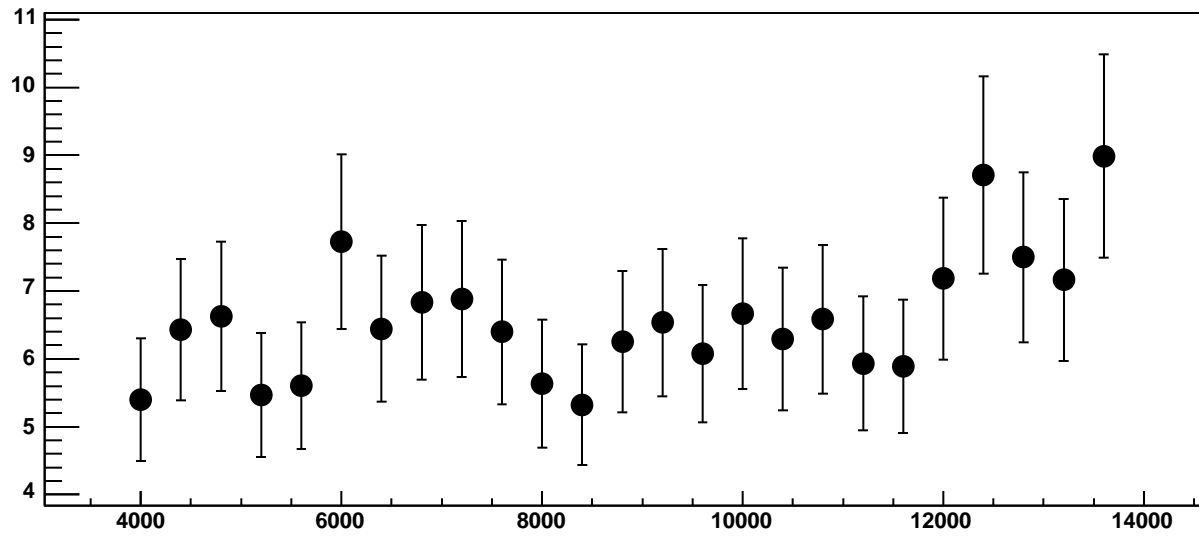
Chip 1, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



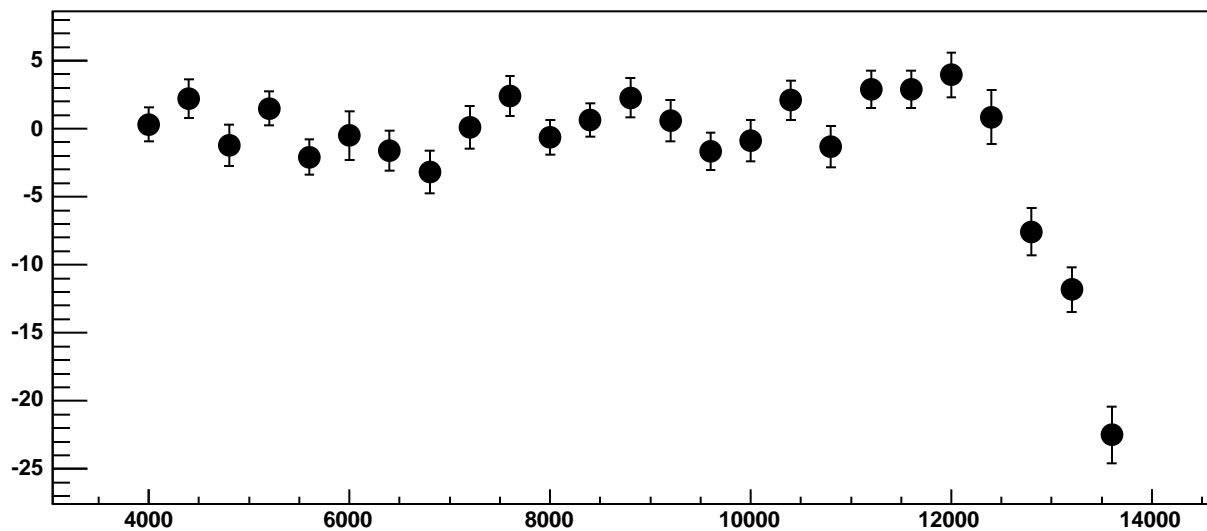
Chip 1, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC



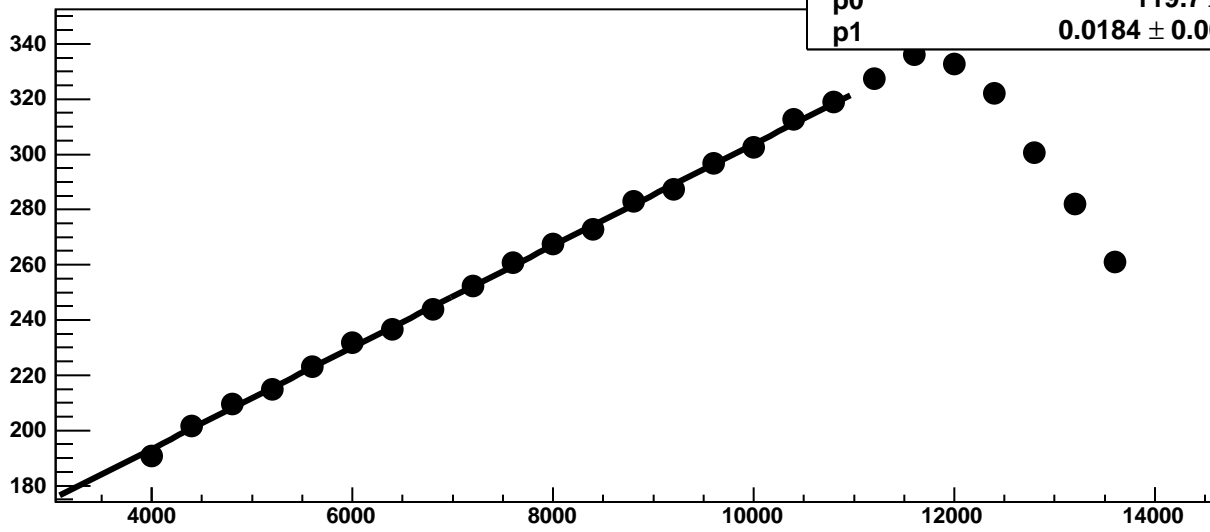
Chip 1, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



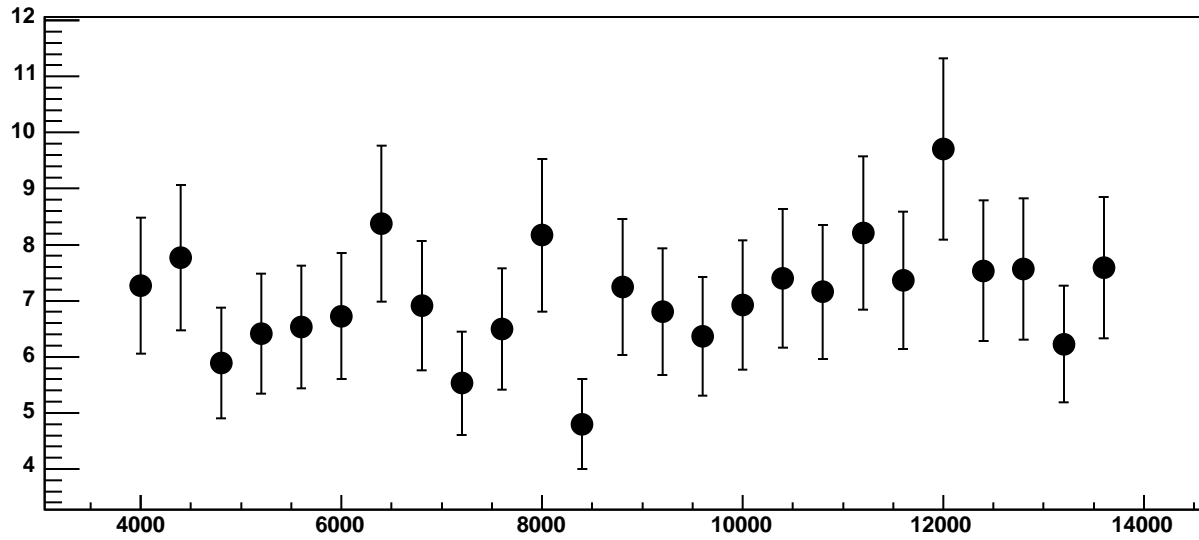
Chip 1, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC



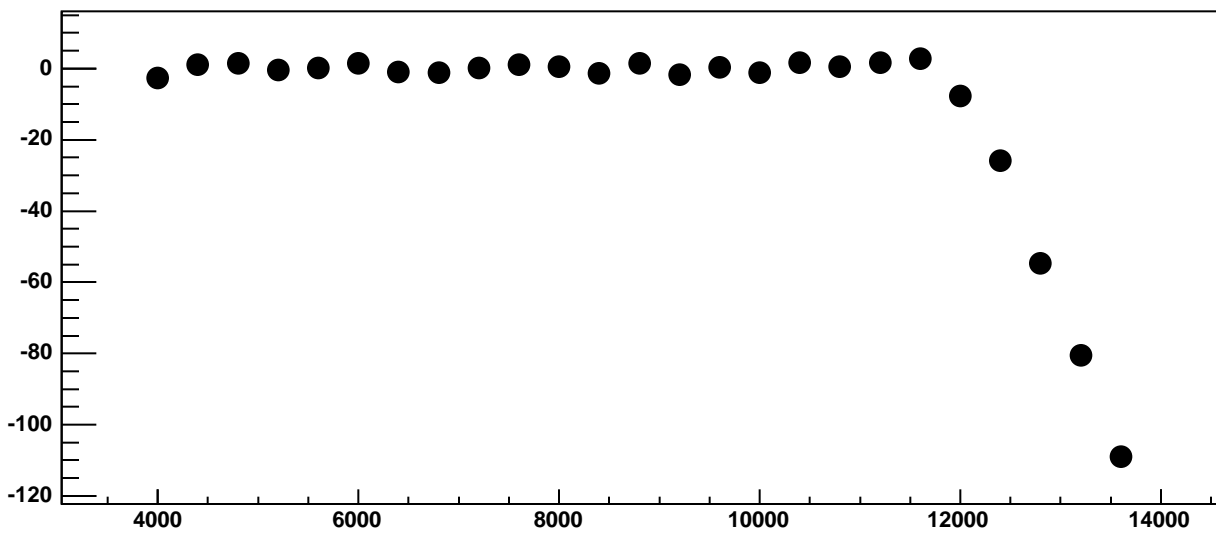
Chip 1, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



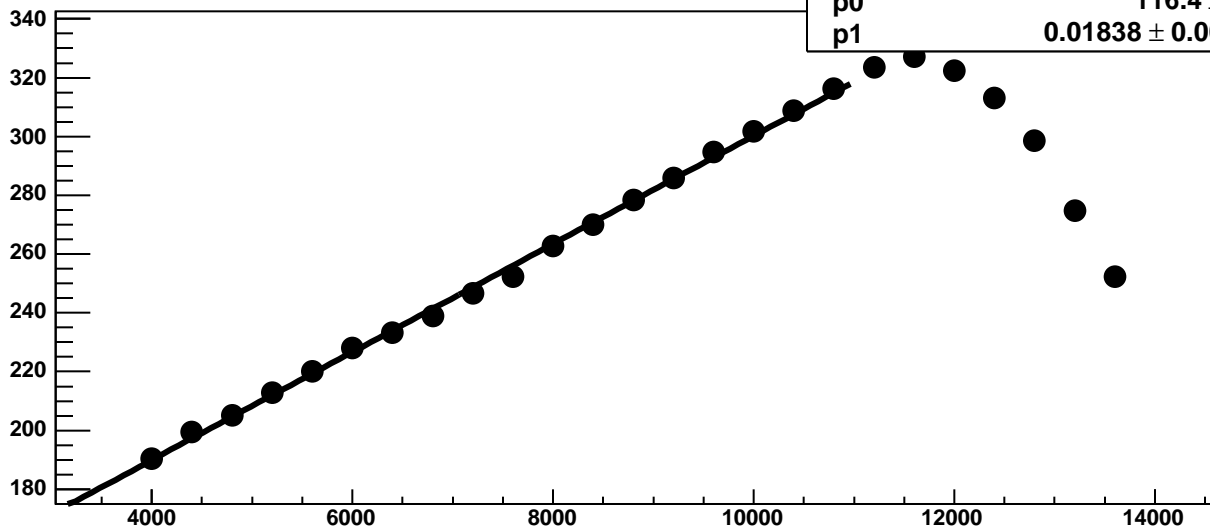
Chip 1, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



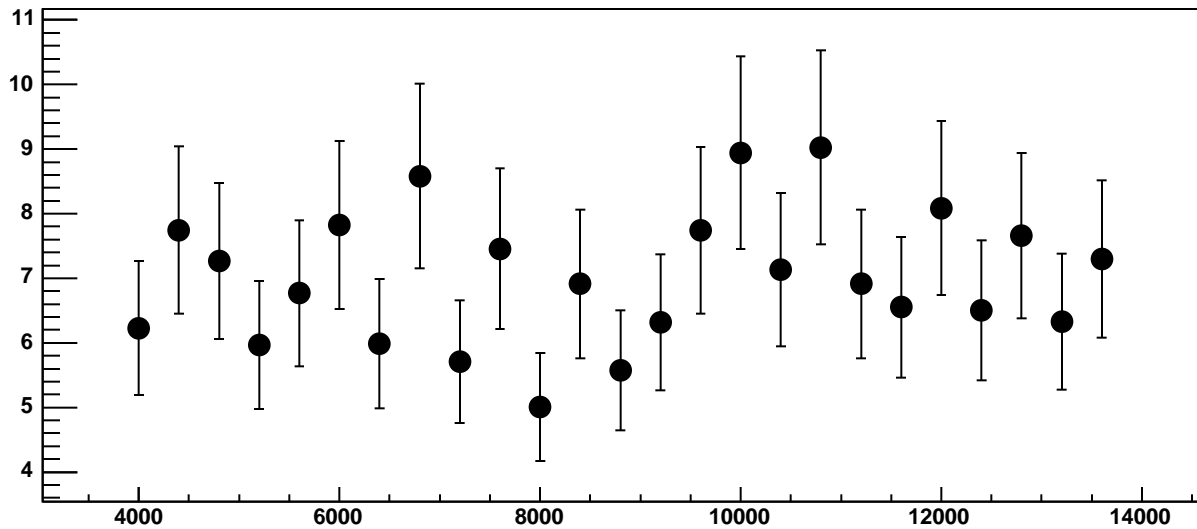
Chip 1, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



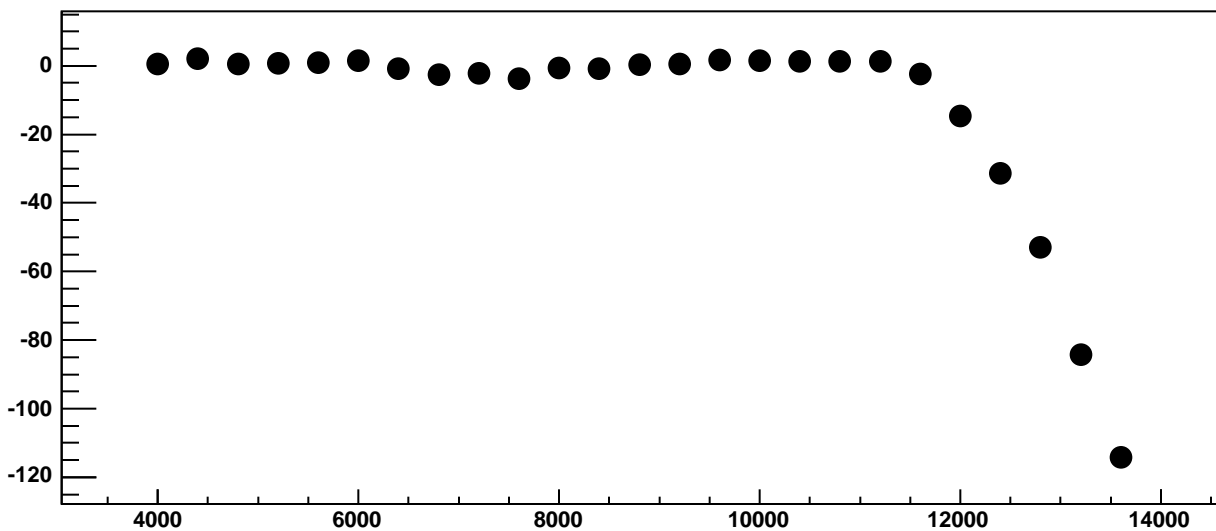
Chip 1, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



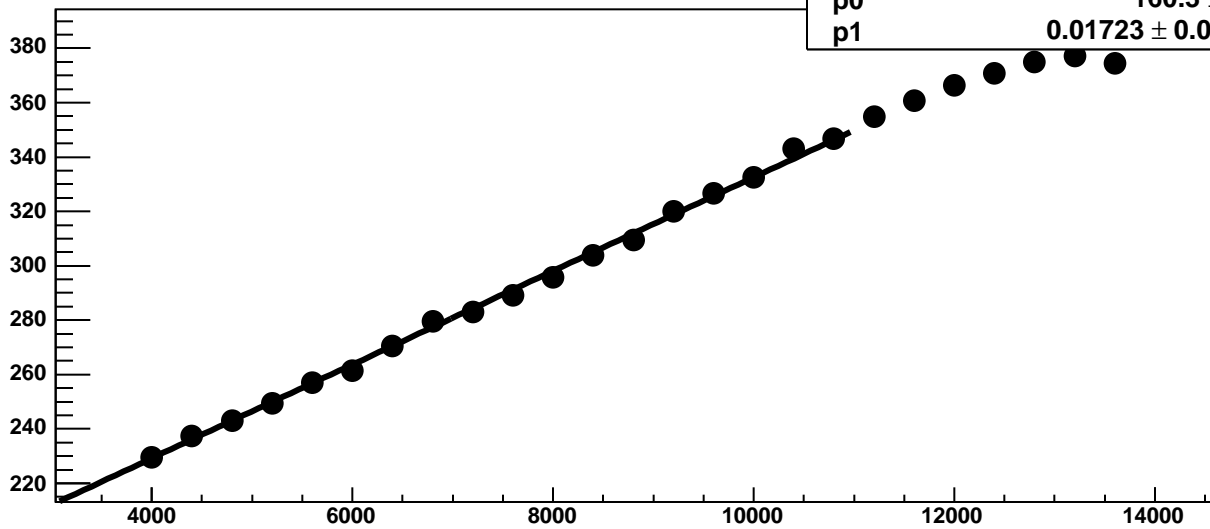
Chip 1, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

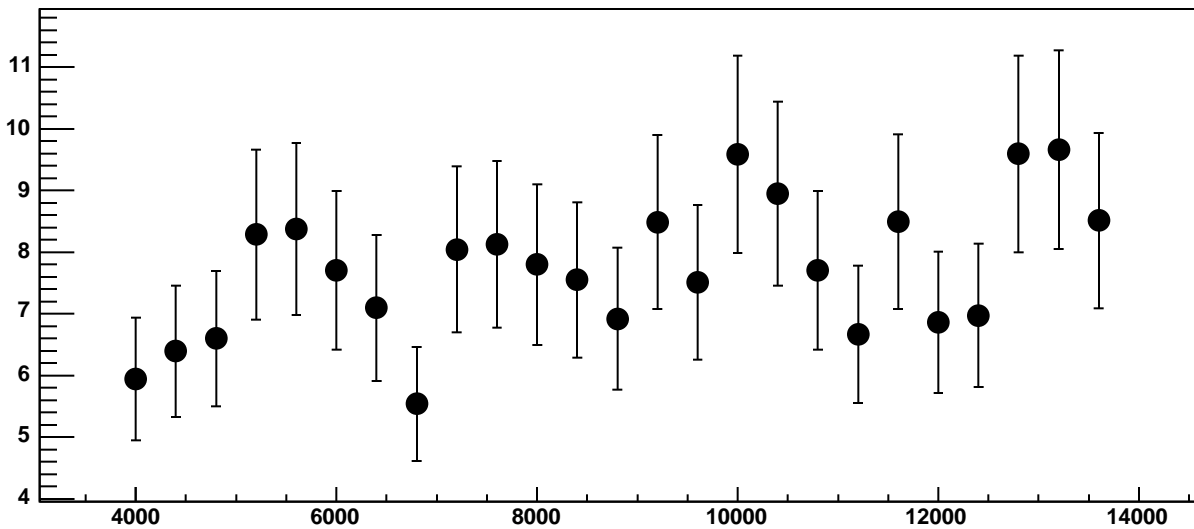


Chip 1, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

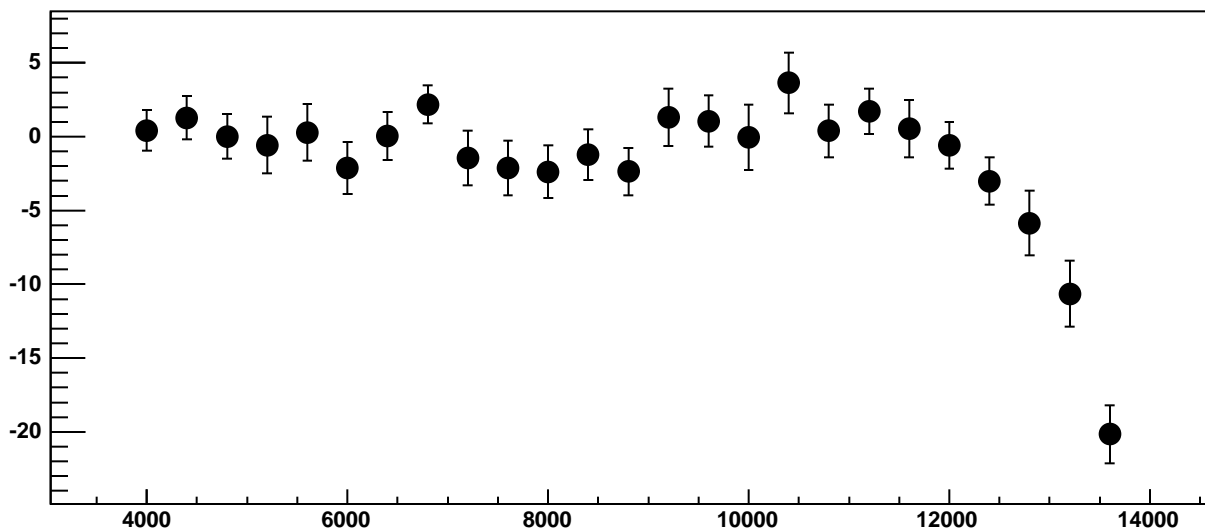


χ^2 / ndf 15.8 / 16
p0 160.3 ± 1.417
p1 0.01723 ± 0.0001921

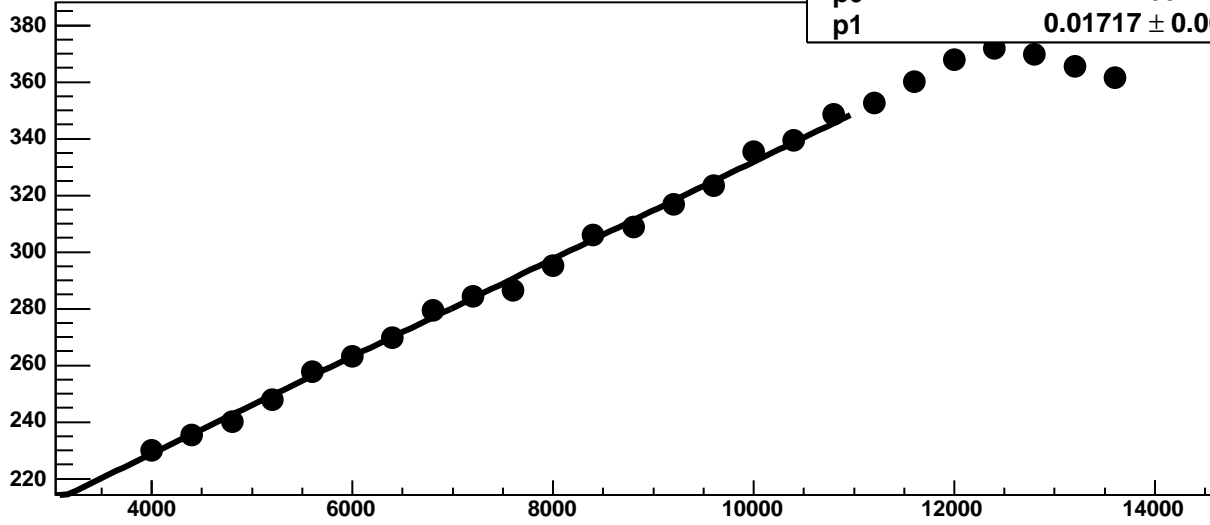
Chip 1, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



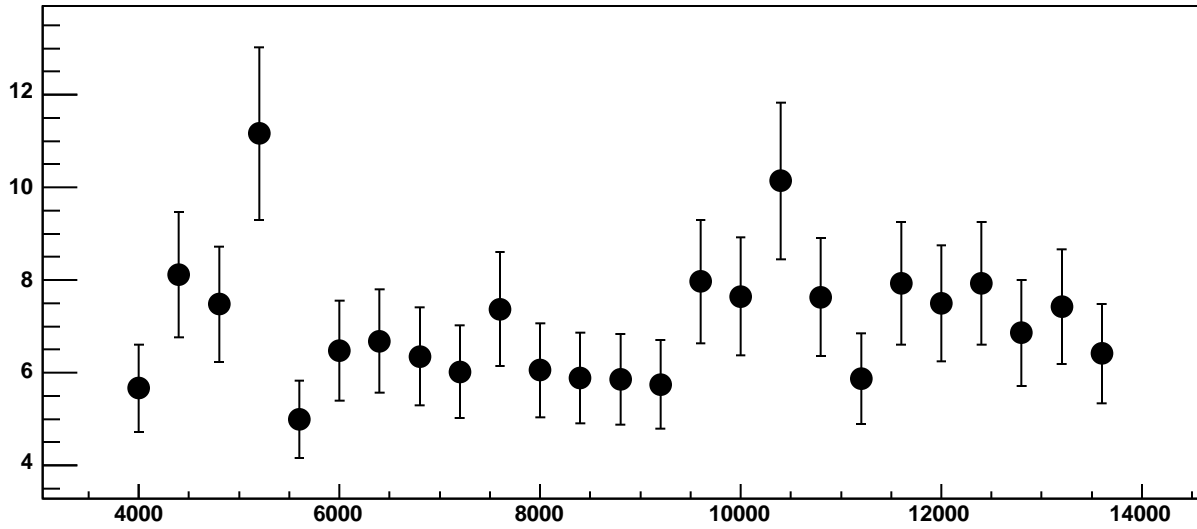
Chip 1, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC



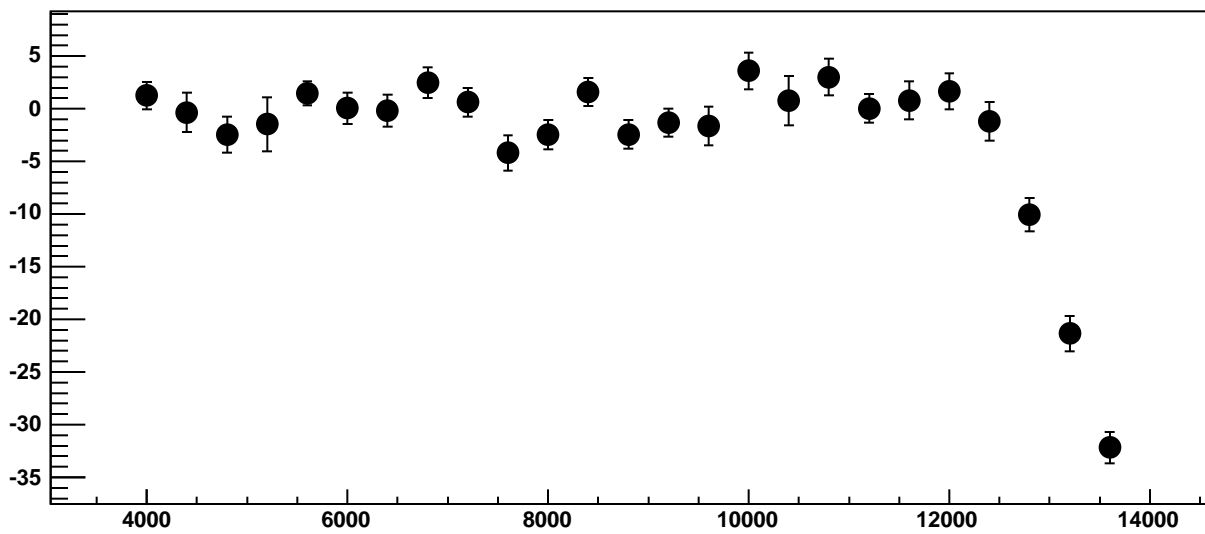
Chip 1, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



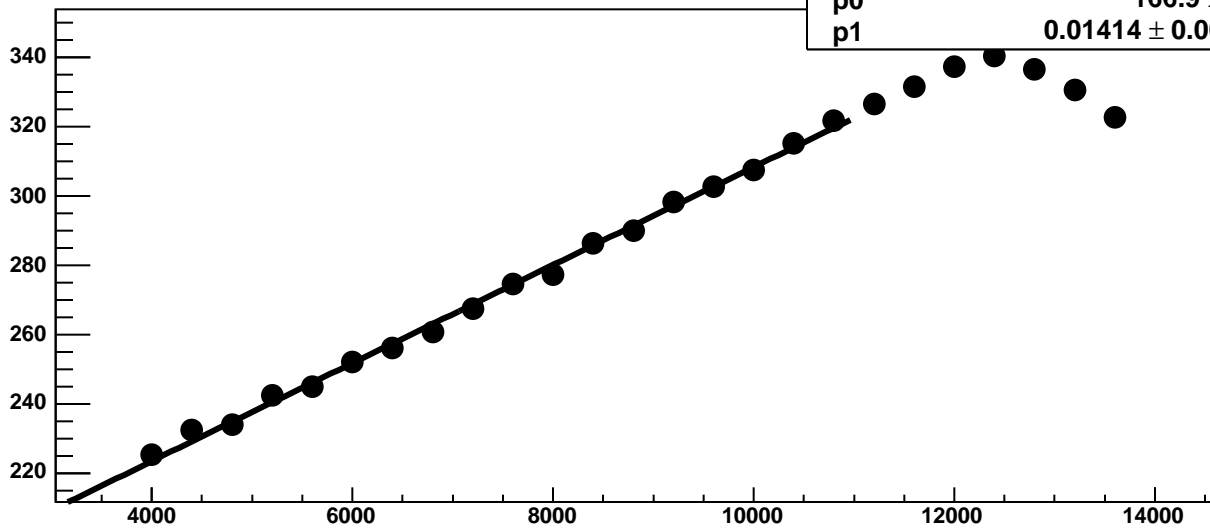
Chip 1, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



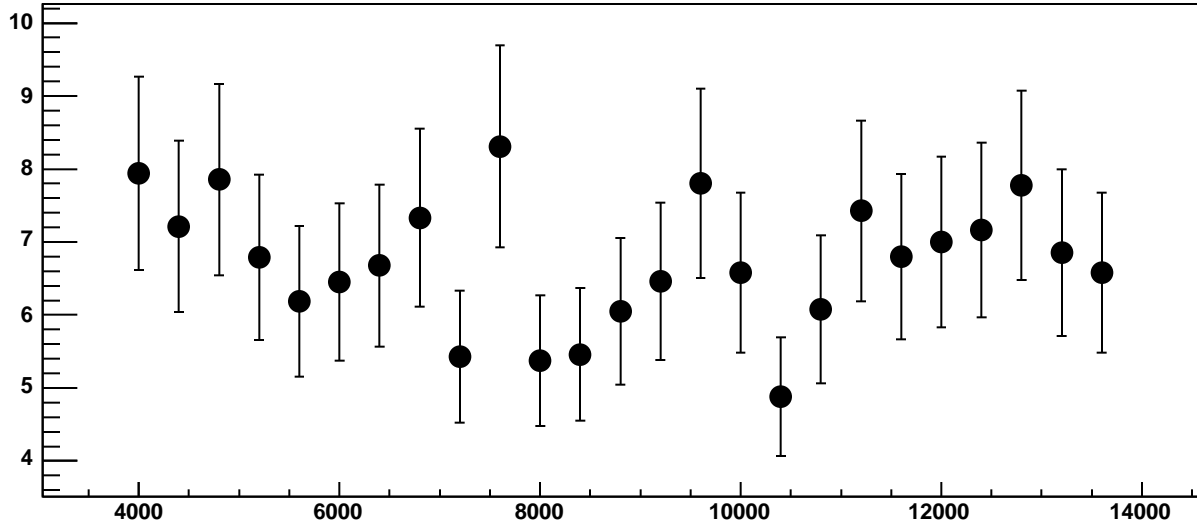
Chip 1, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



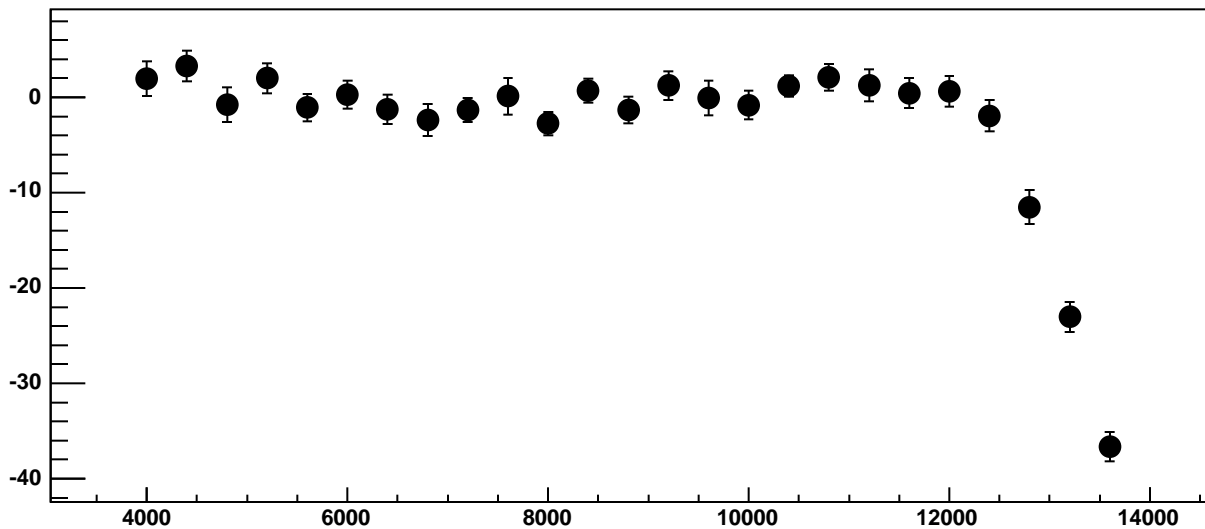
Chip 1, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



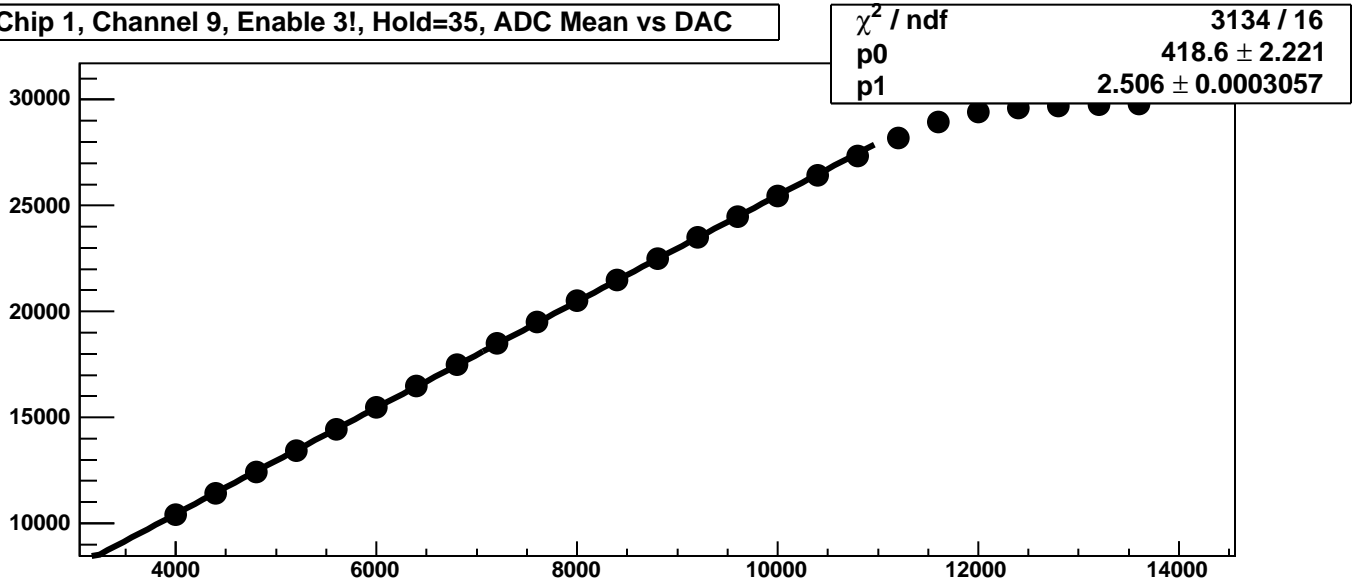
Chip 1, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



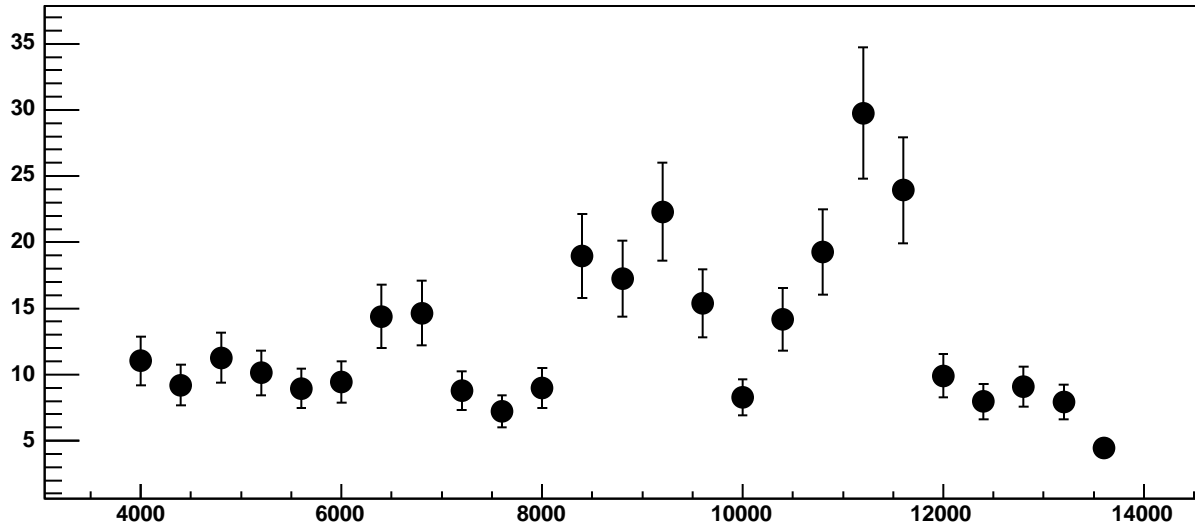
Chip 1, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



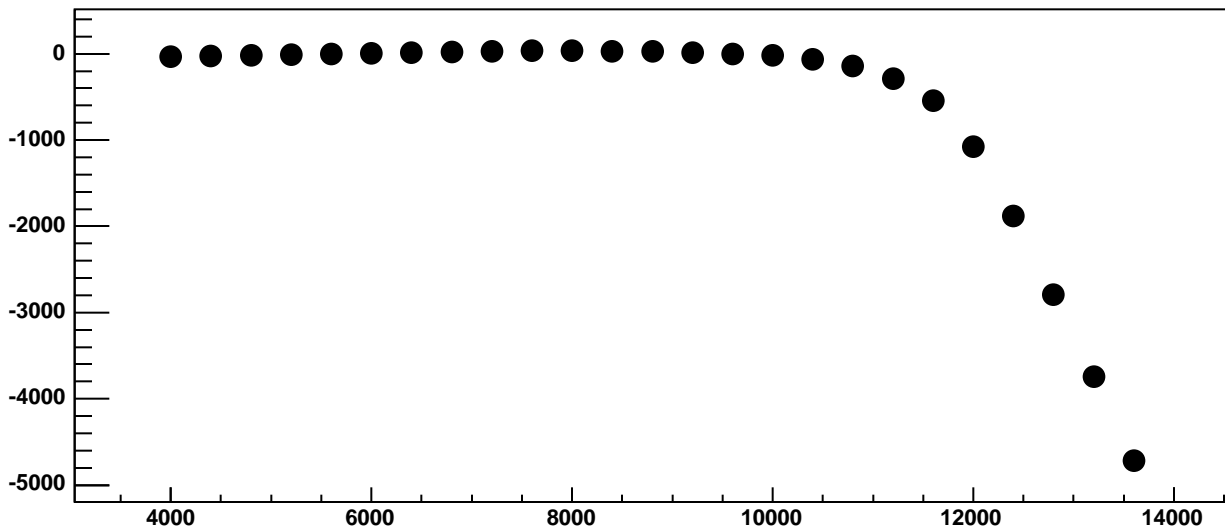
Chip 1, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC



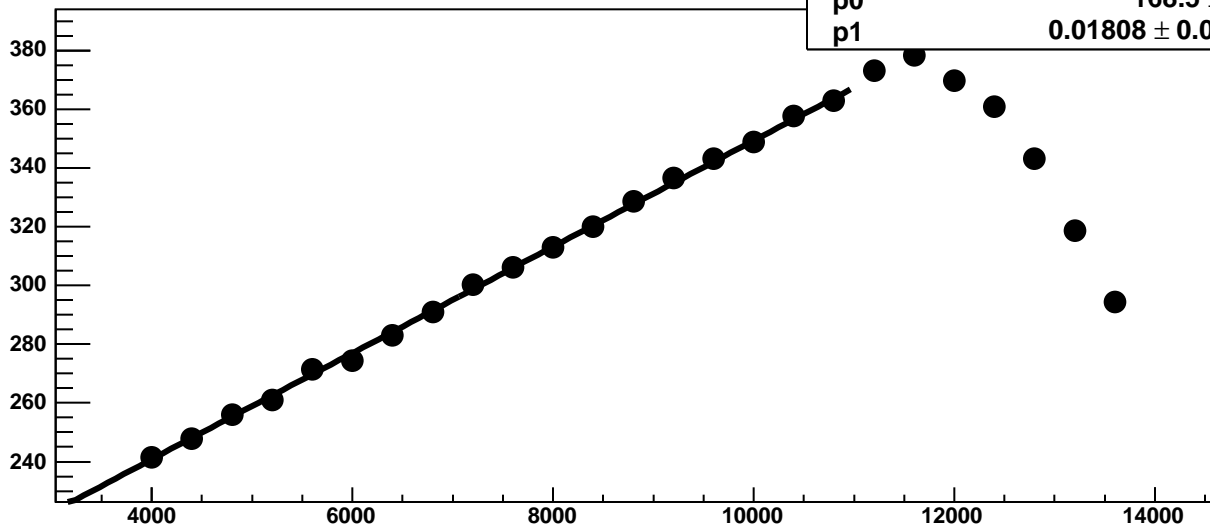
Chip 1, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



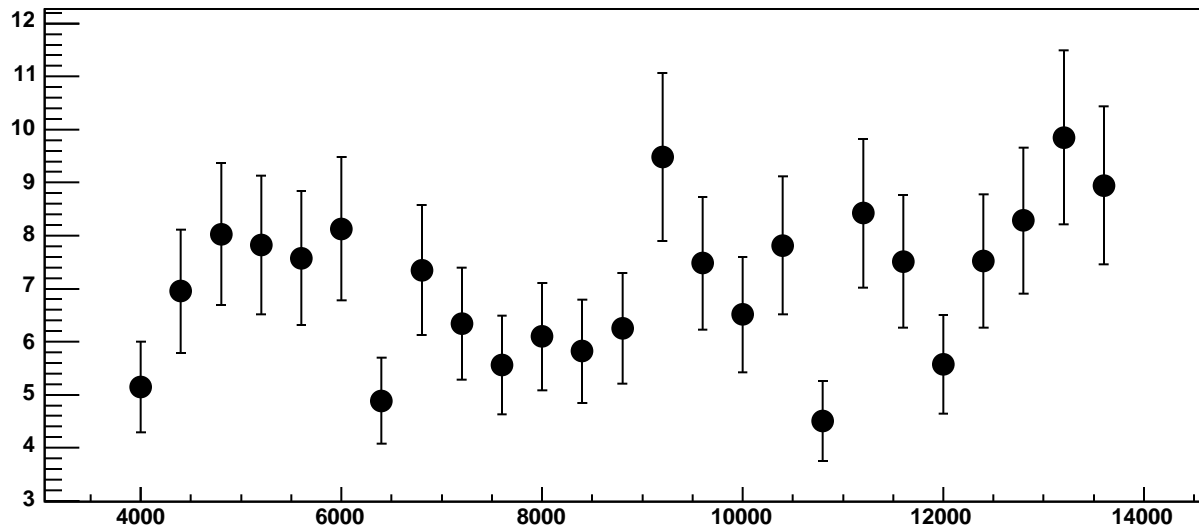
Chip 1, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC



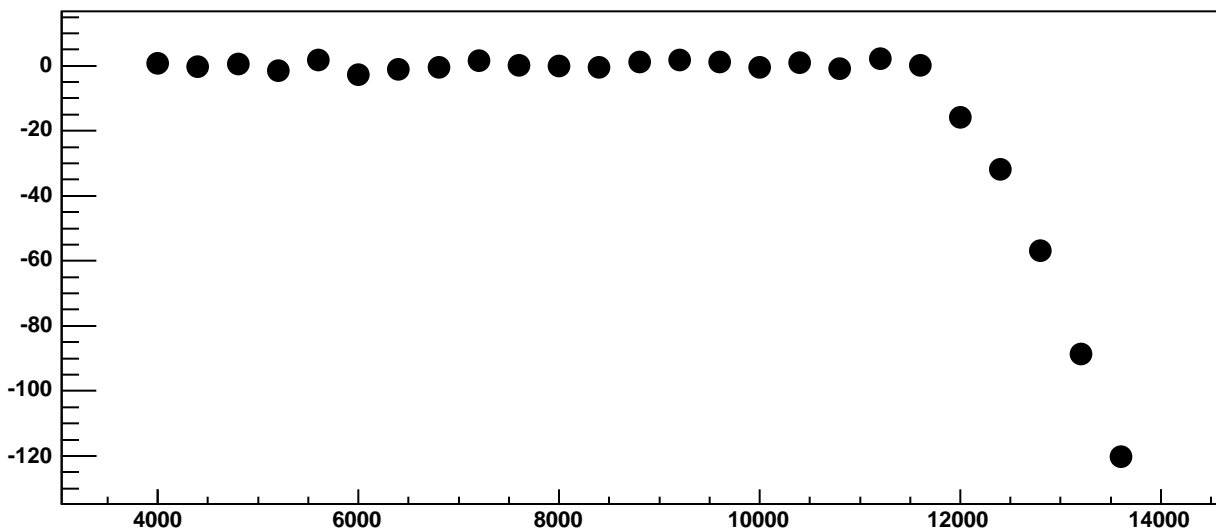
Chip 1, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



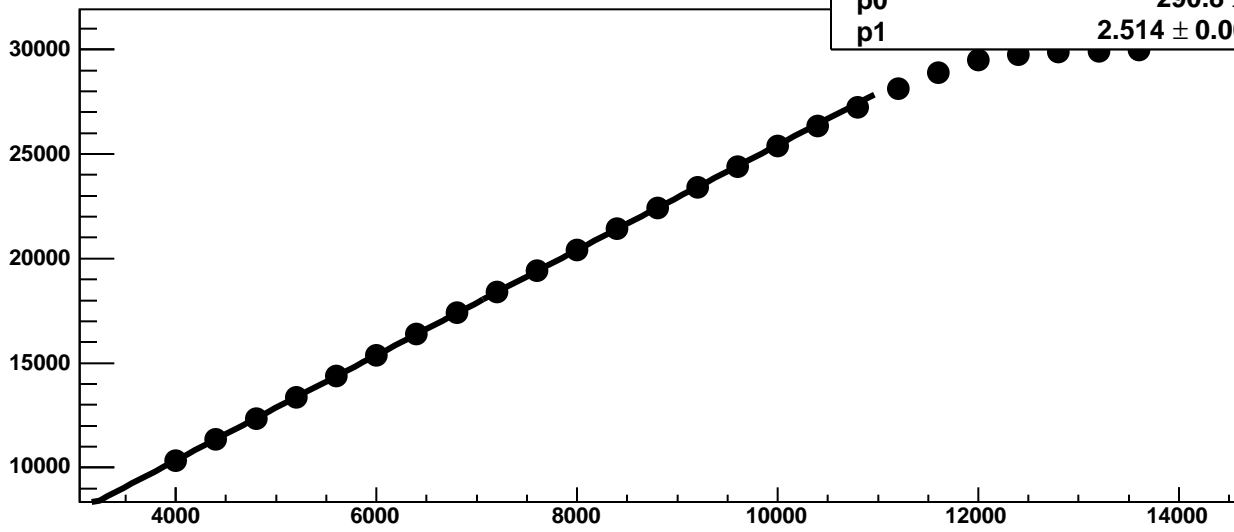
Chip 1, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



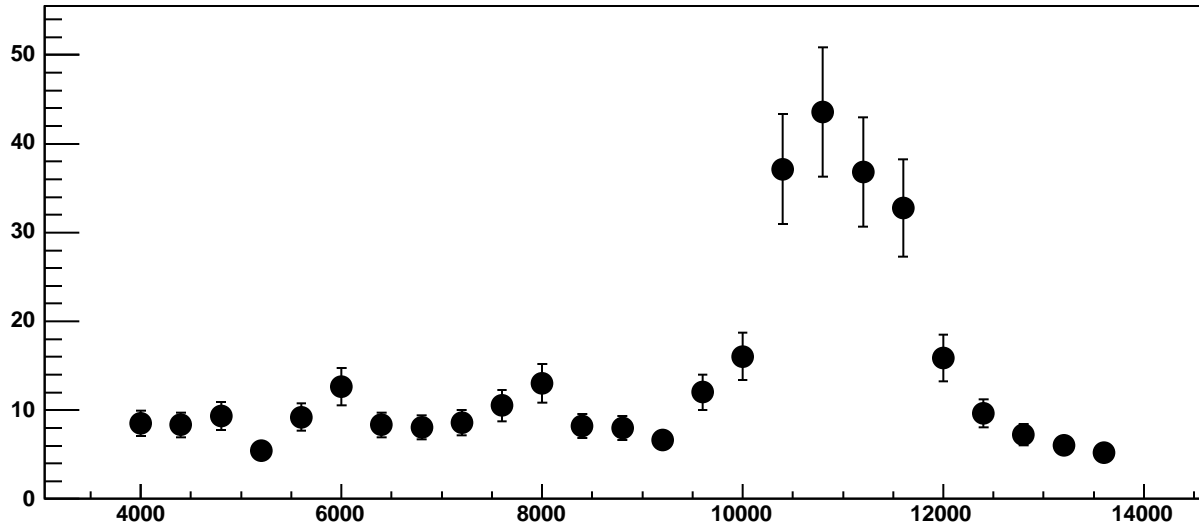
Chip 1, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



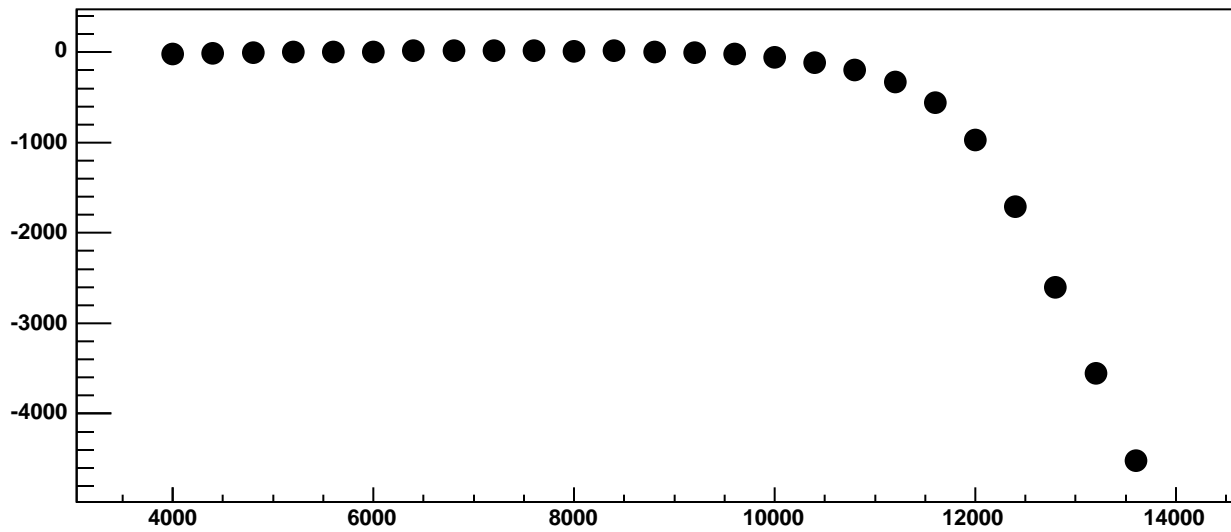
Chip 1, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC



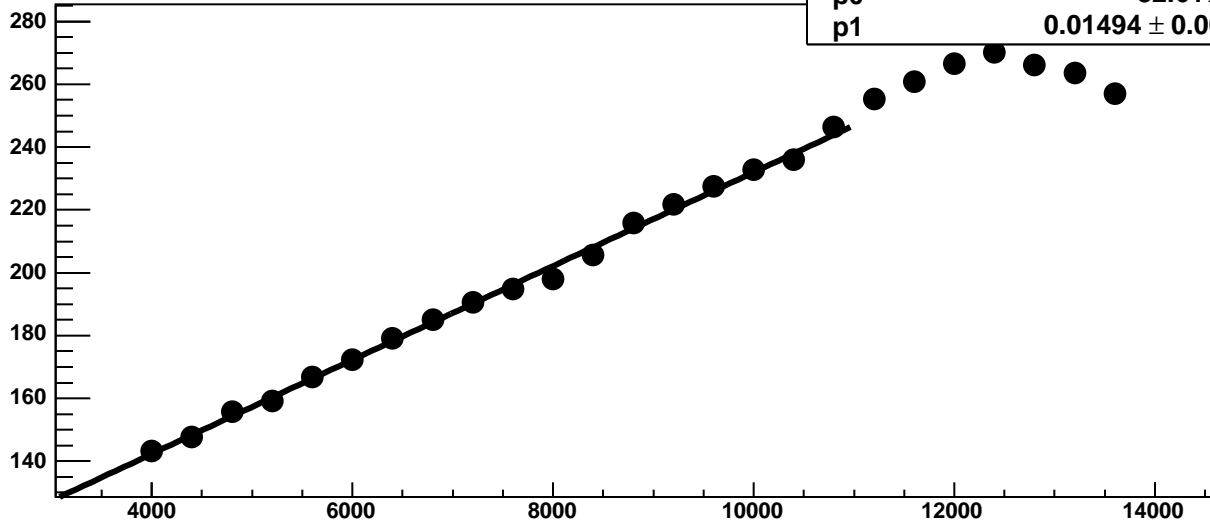
Chip 1, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



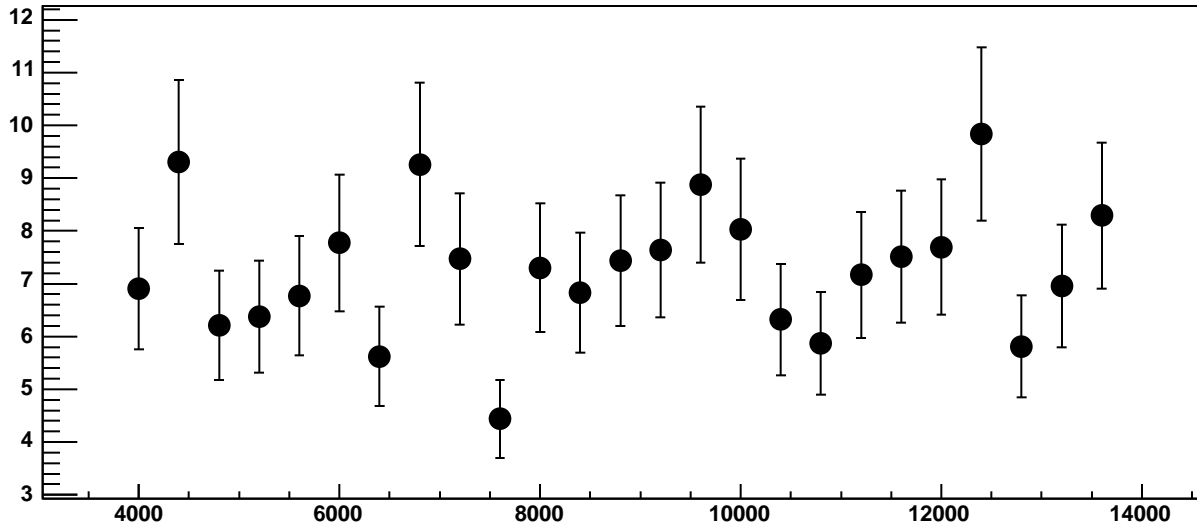
Chip 1, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC



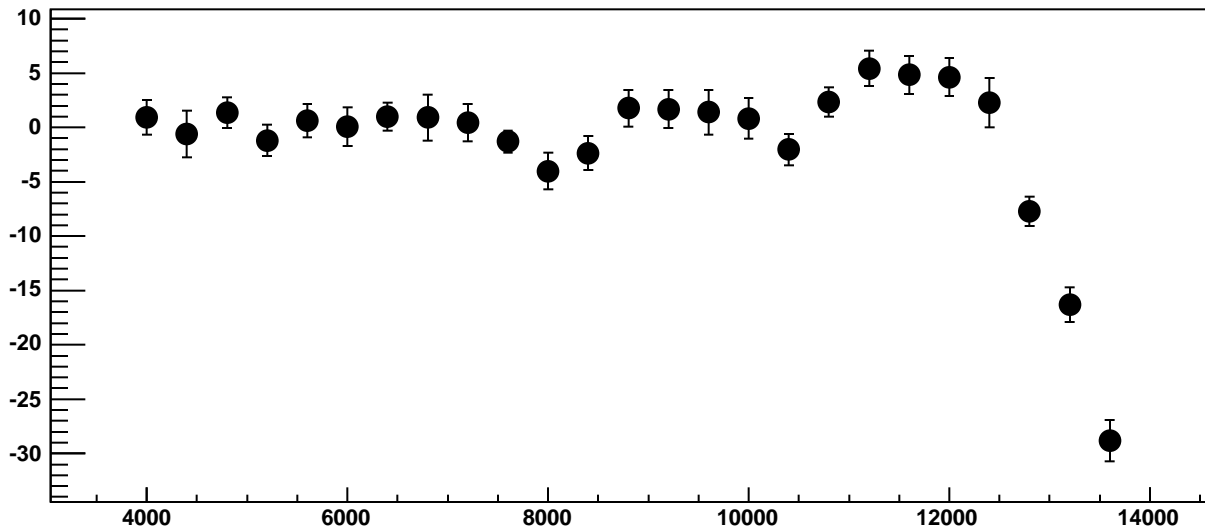
Chip 1, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



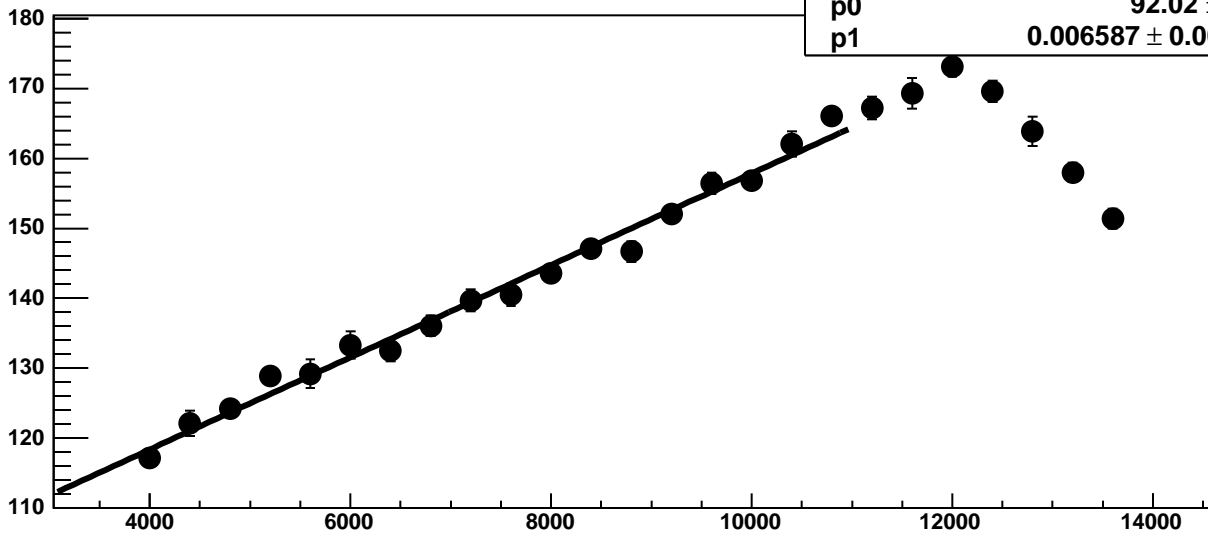
Chip 1, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



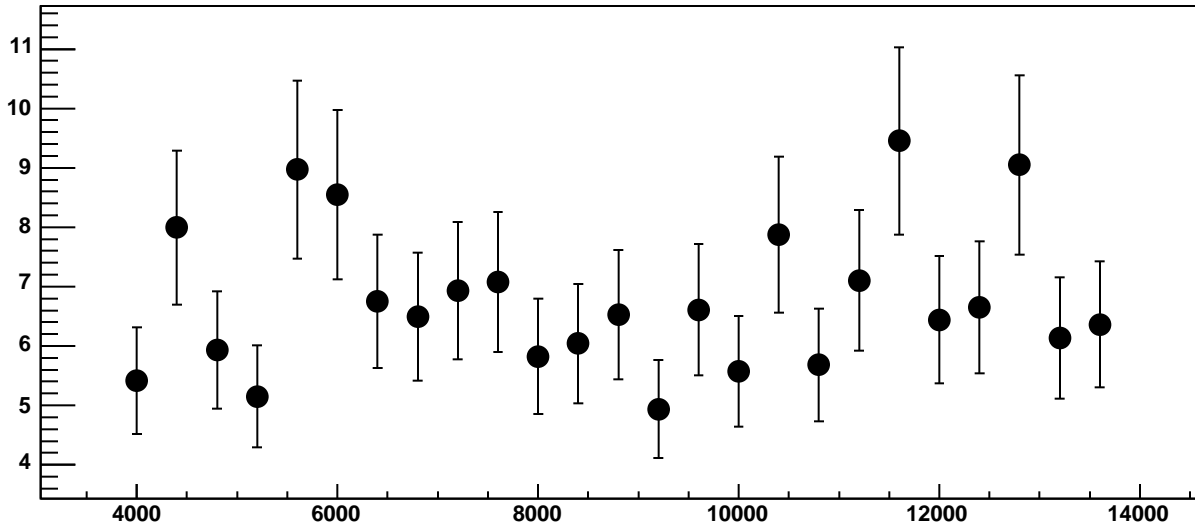
Chip 1, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



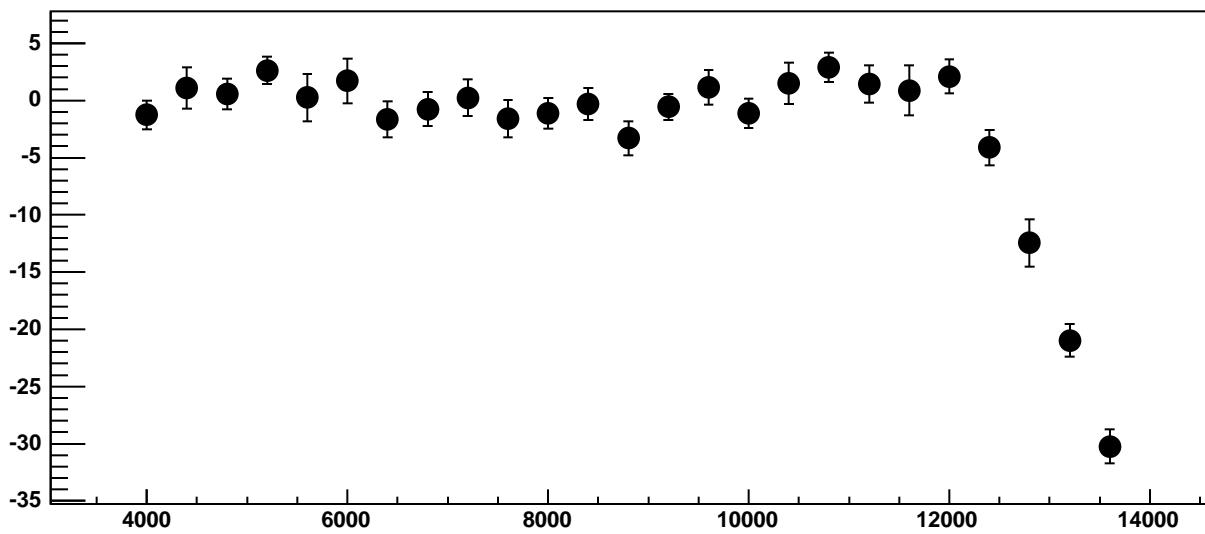
Chip 1, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



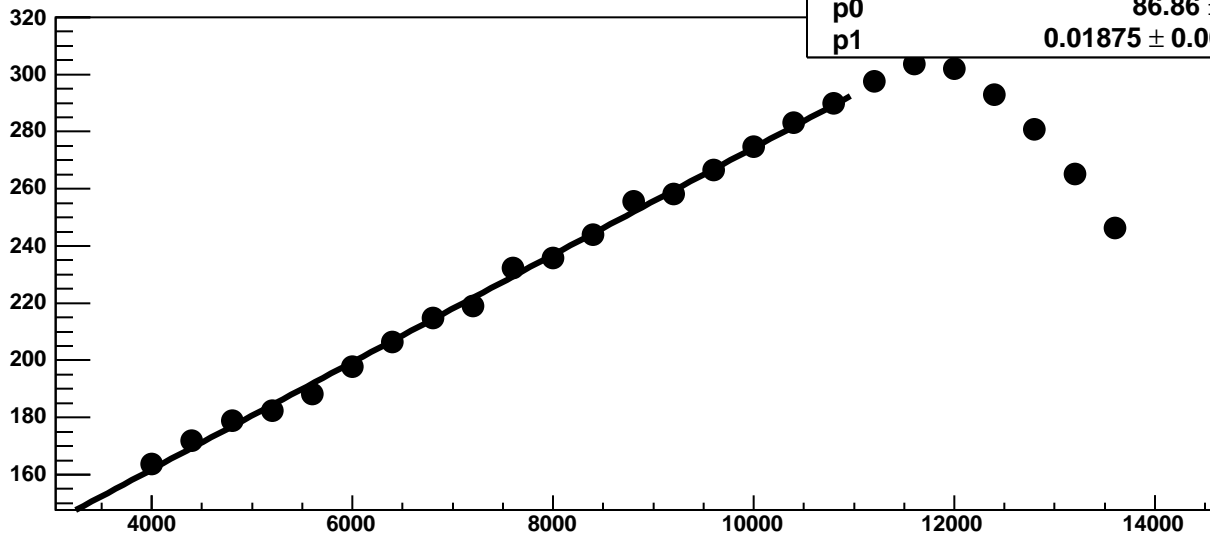
Chip 1, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



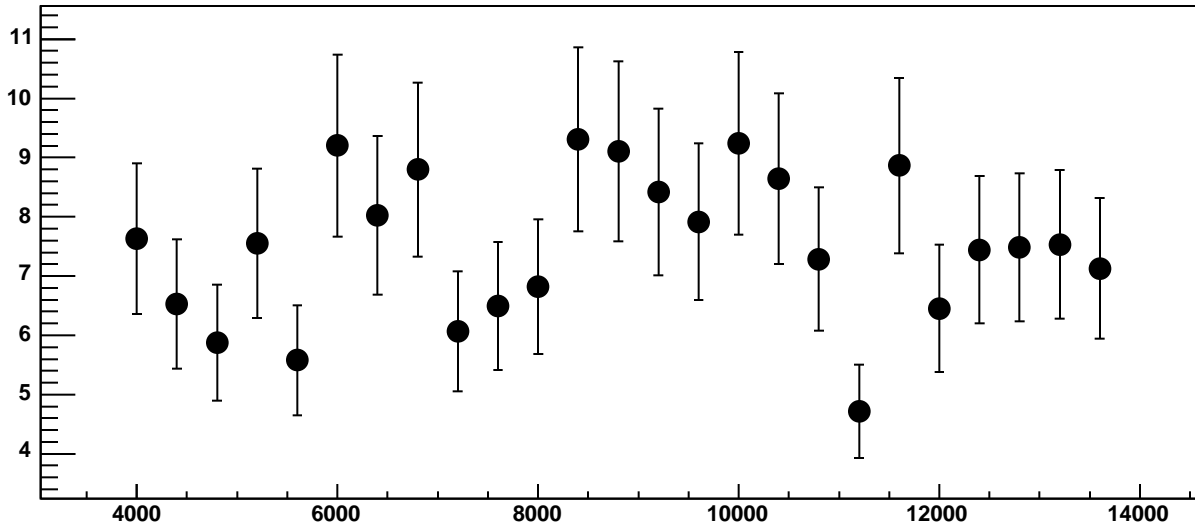
Chip 1, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



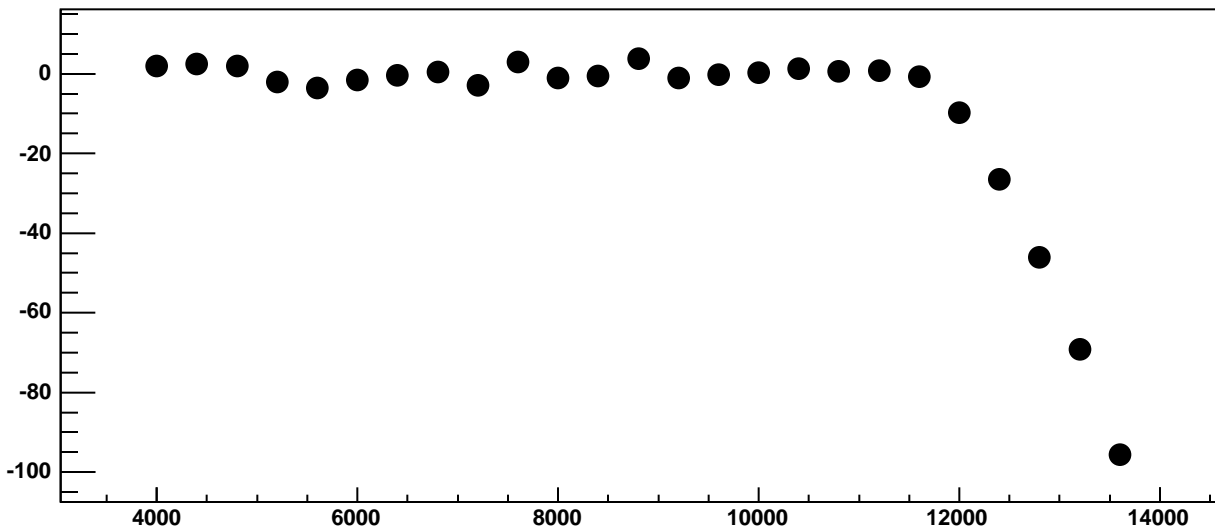
Chip 1, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC



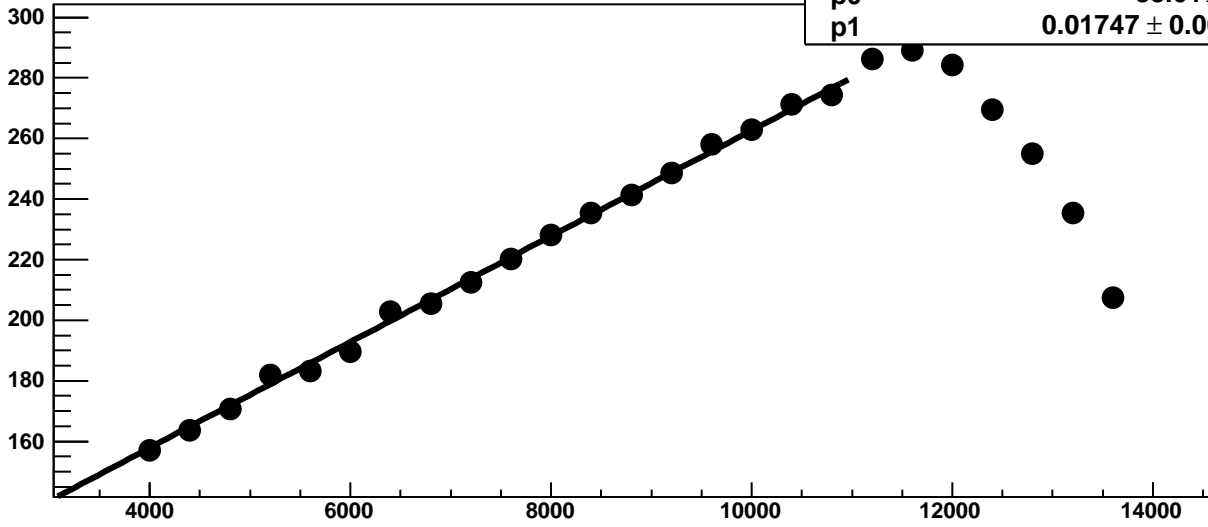
Chip 1, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

29.14 / 16

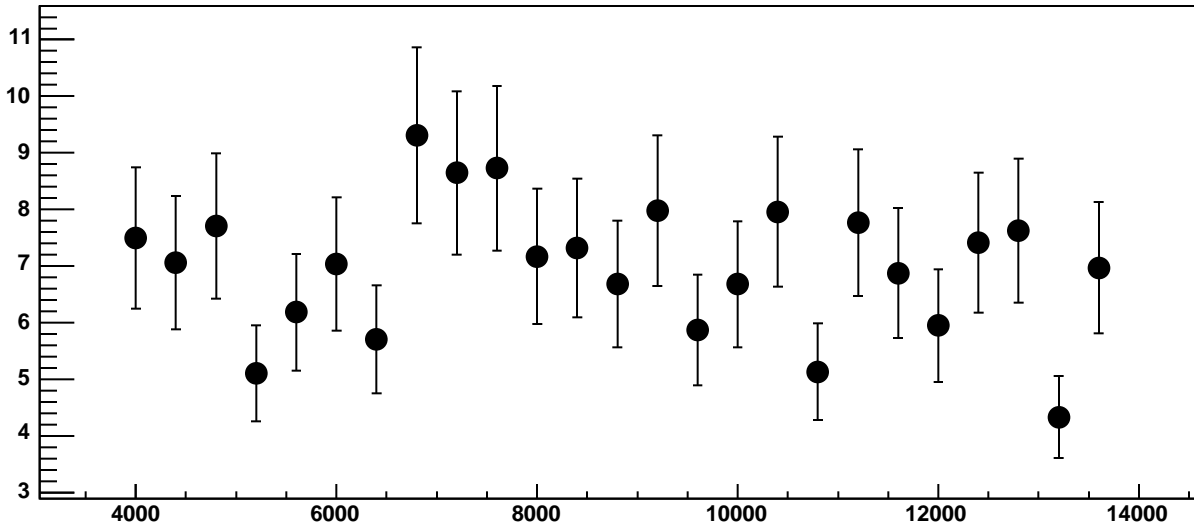
p0

88.01 ± 1.317

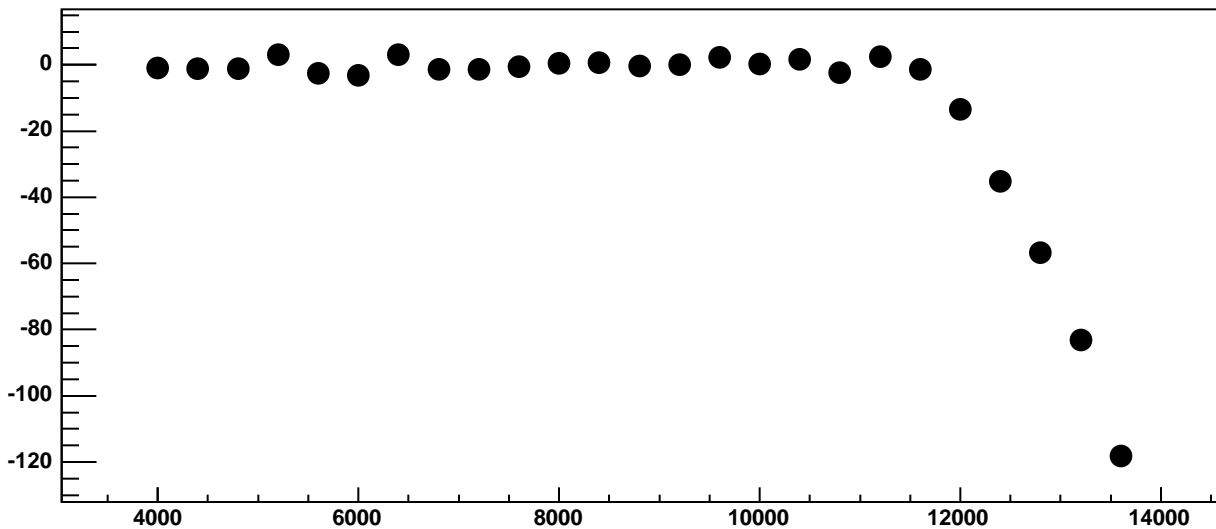
p1

0.01747 ± 0.0001693

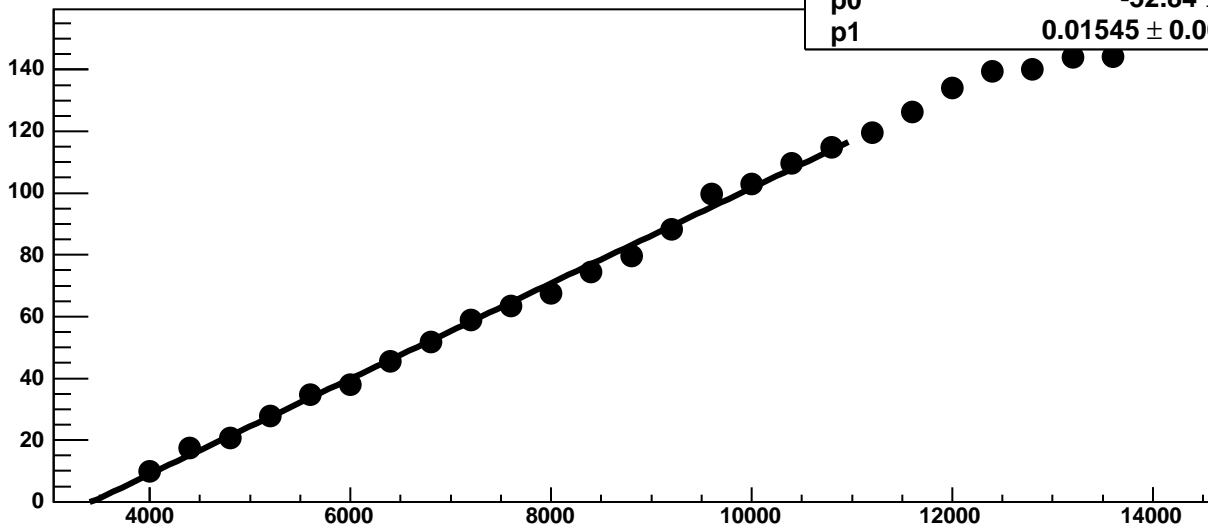
Chip 1, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

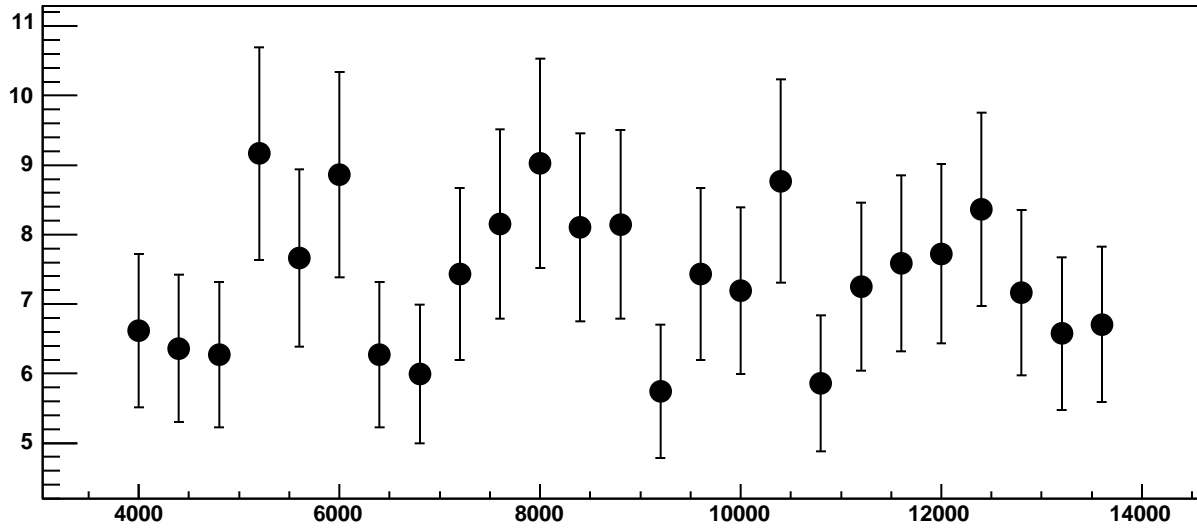


Chip 1, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC

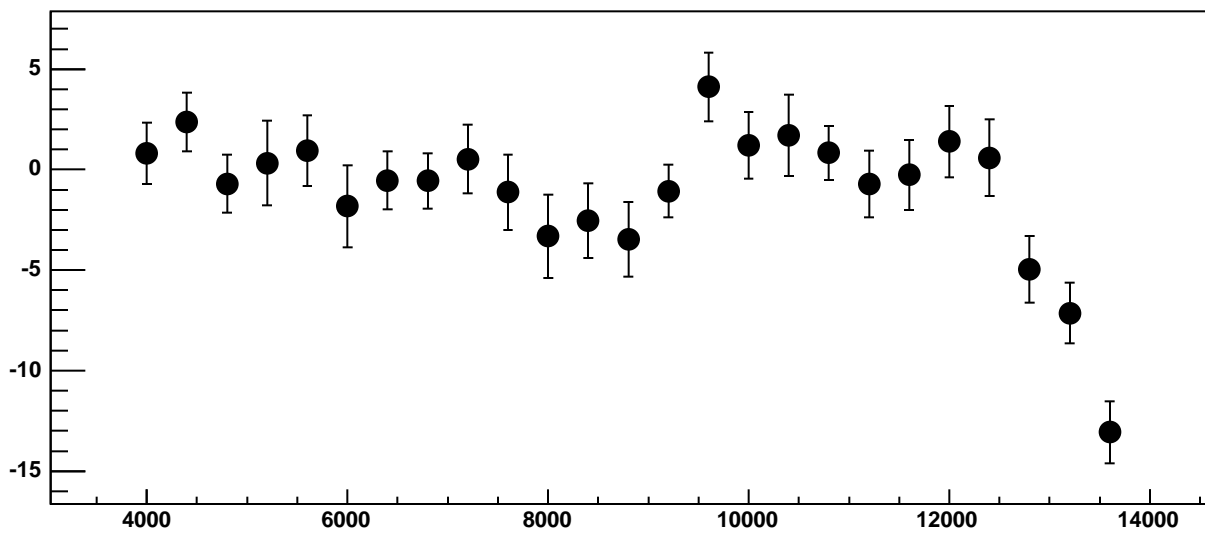


χ^2 / ndf 21.05 / 16
p0 -52.84 ± 1.372
p1 0.01545 ± 0.0001785

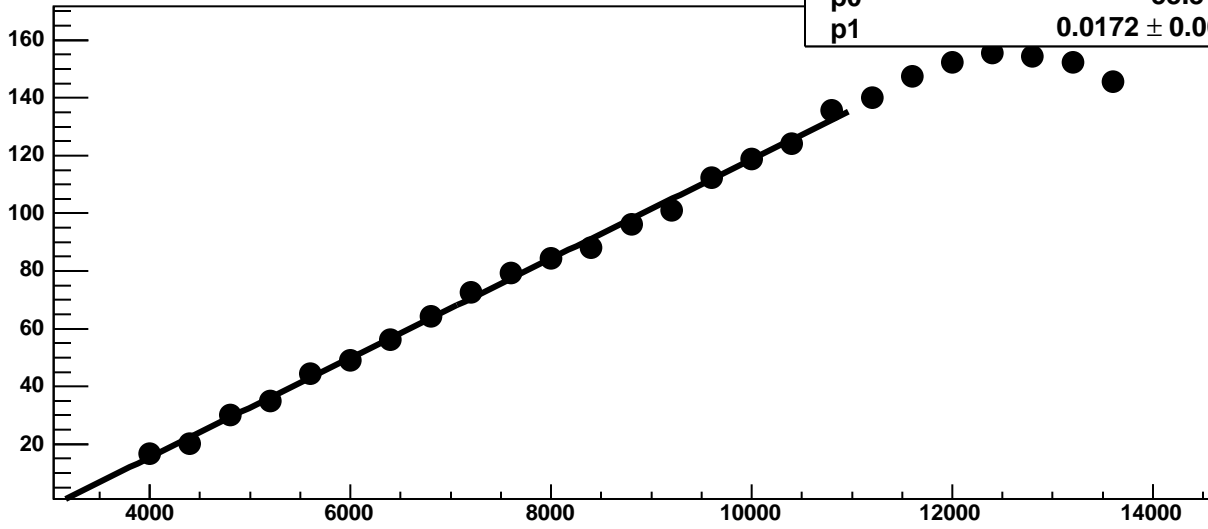
Chip 1, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



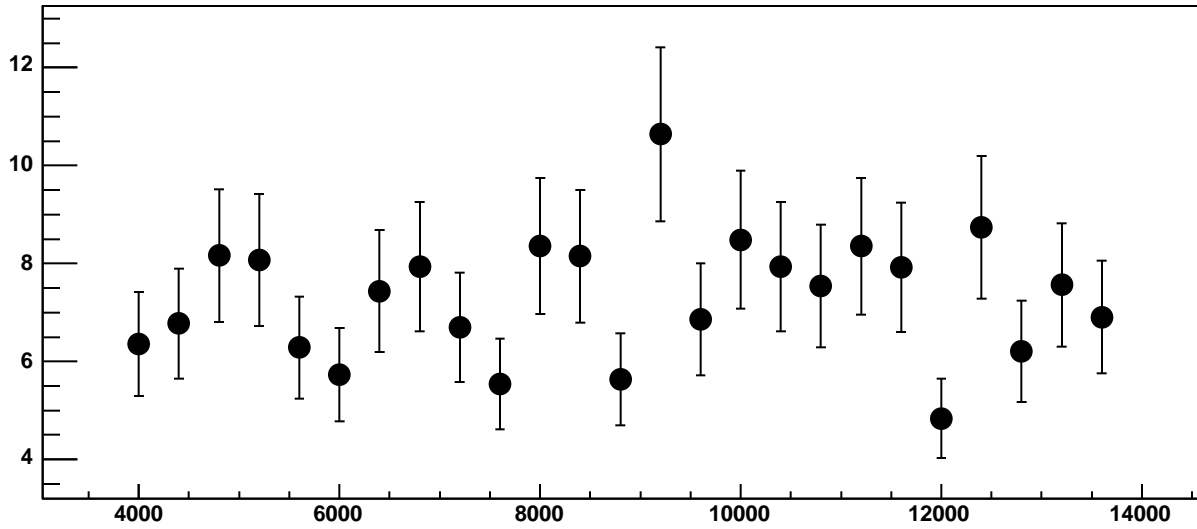
Chip 1, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC



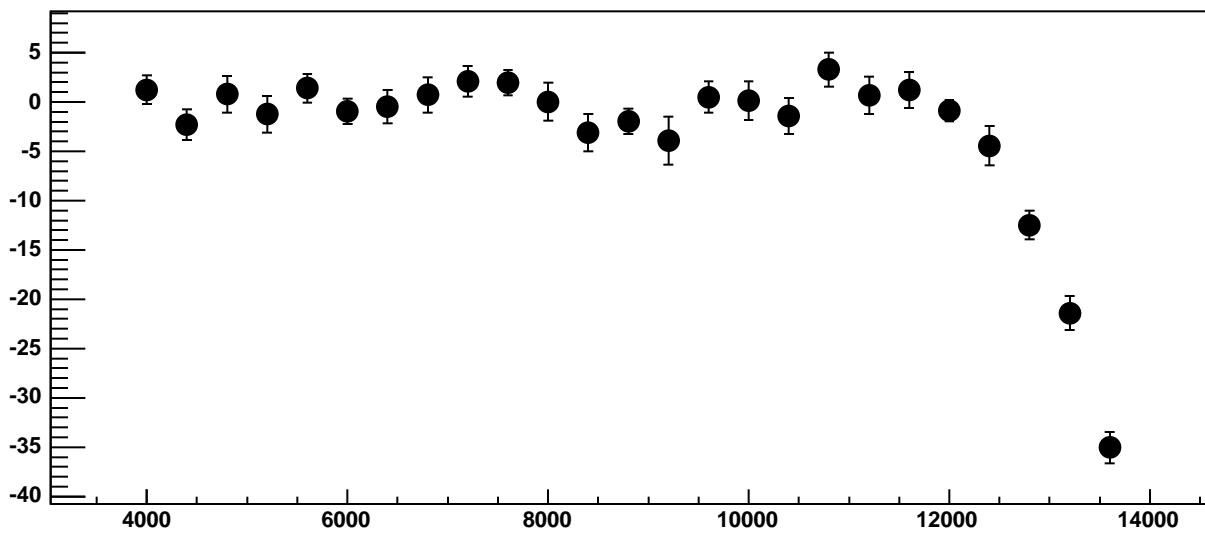
Chip 1, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



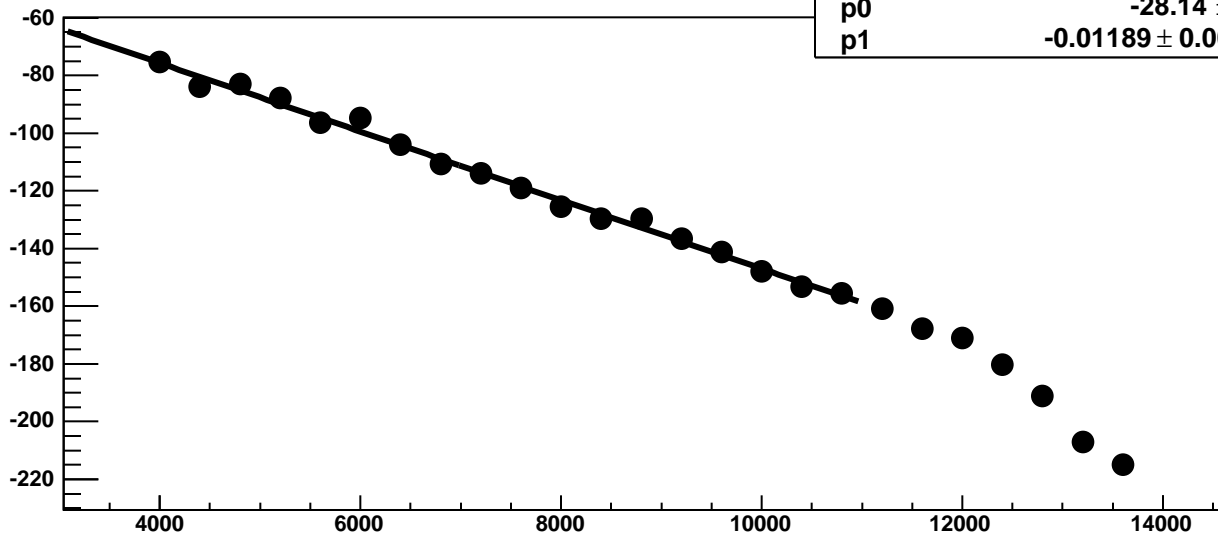
Chip 1, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



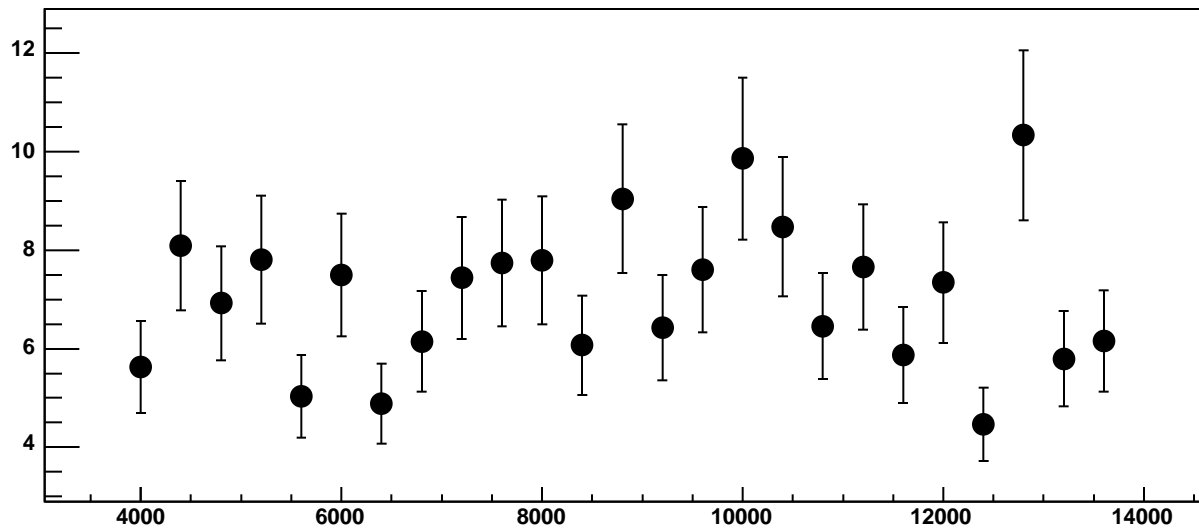
Chip 1, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



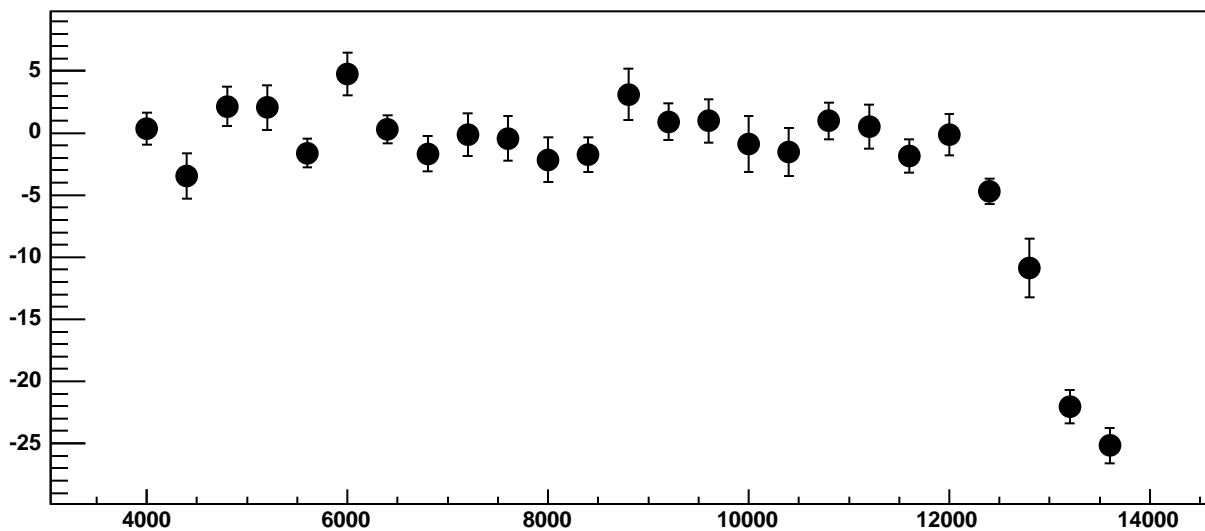
Chip 1, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



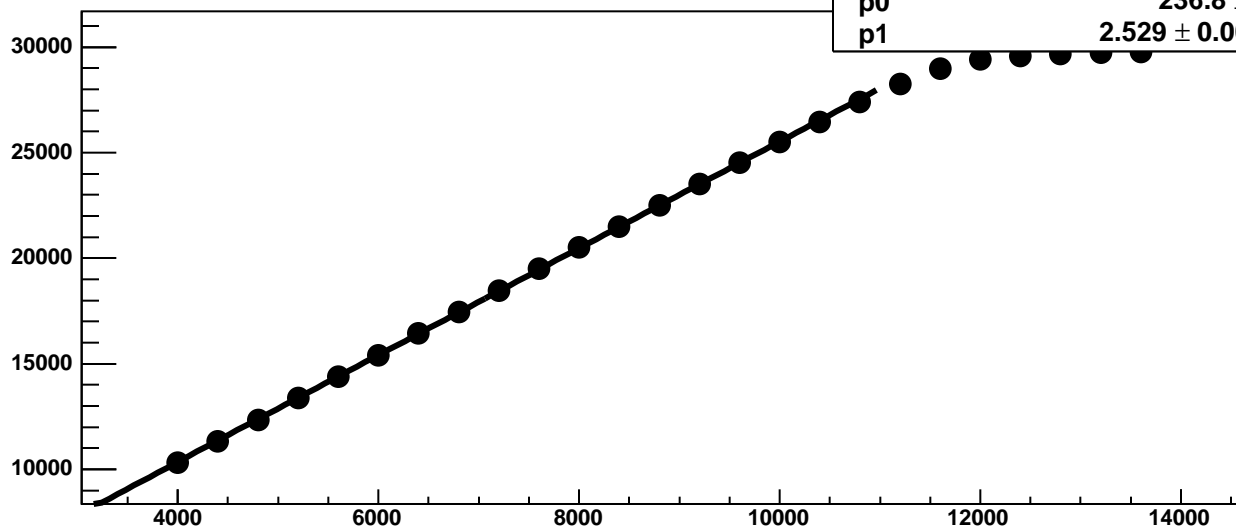
Chip 1, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

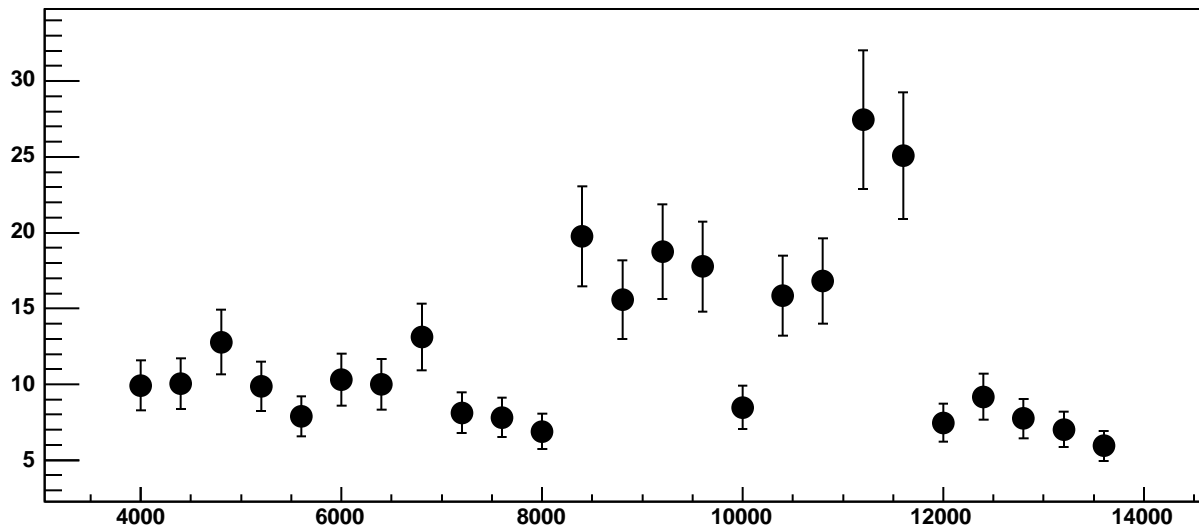


Chip 1, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC

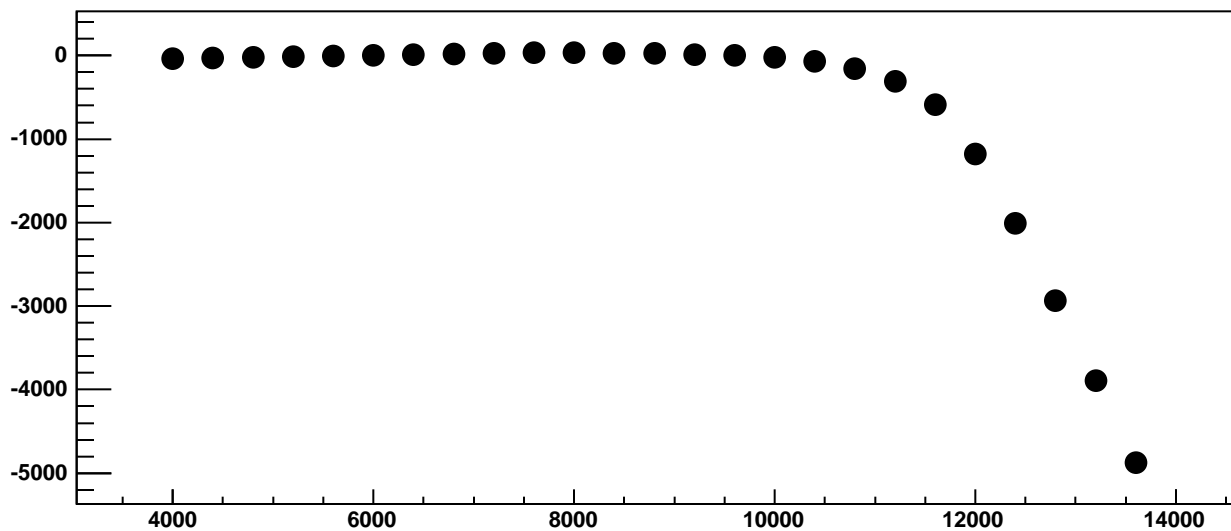


χ^2 / ndf 3869 / 16
p0 236.8 ± 2.203
p1 2.529 ± 0.0003039

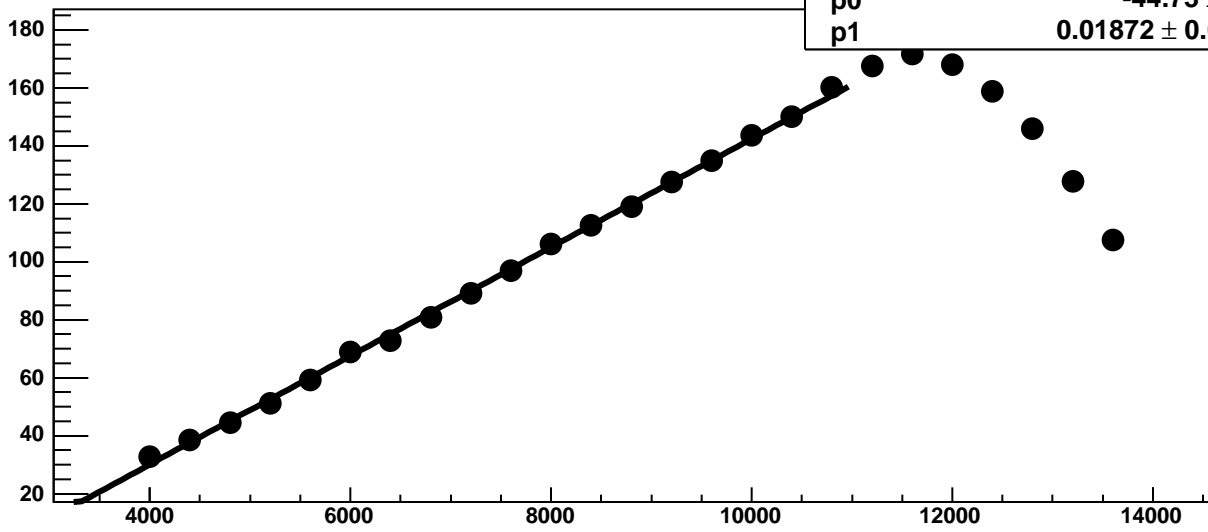
Chip 1, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



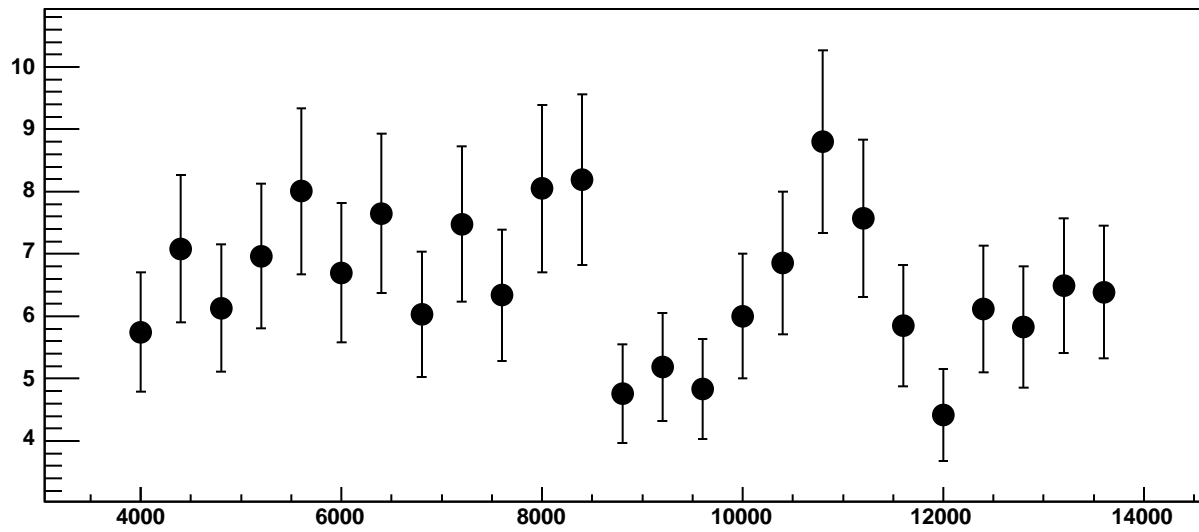
Chip 1, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC



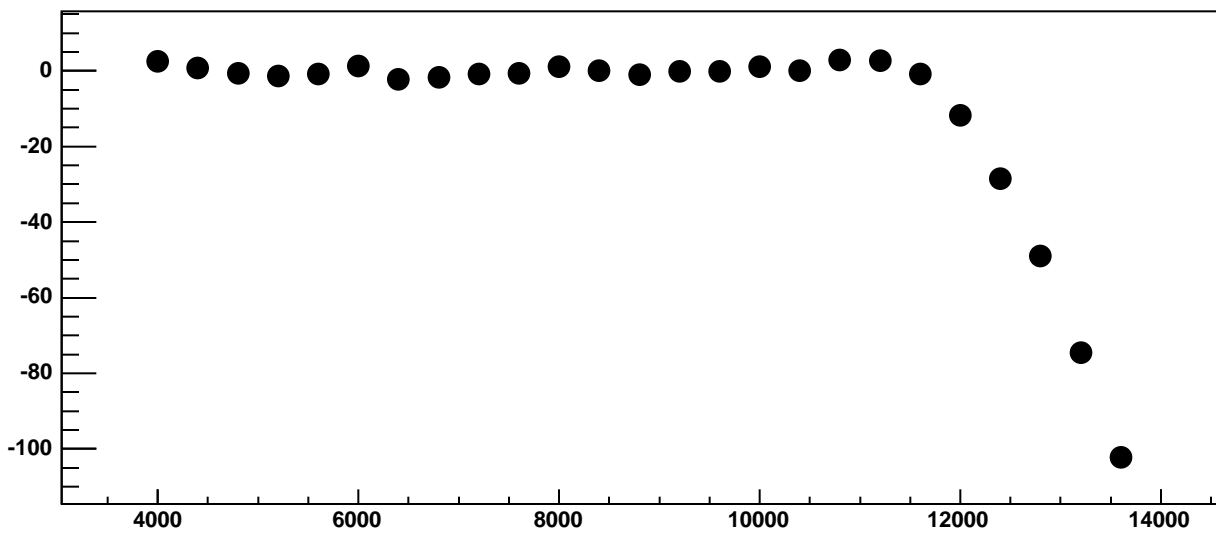
Chip 1, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



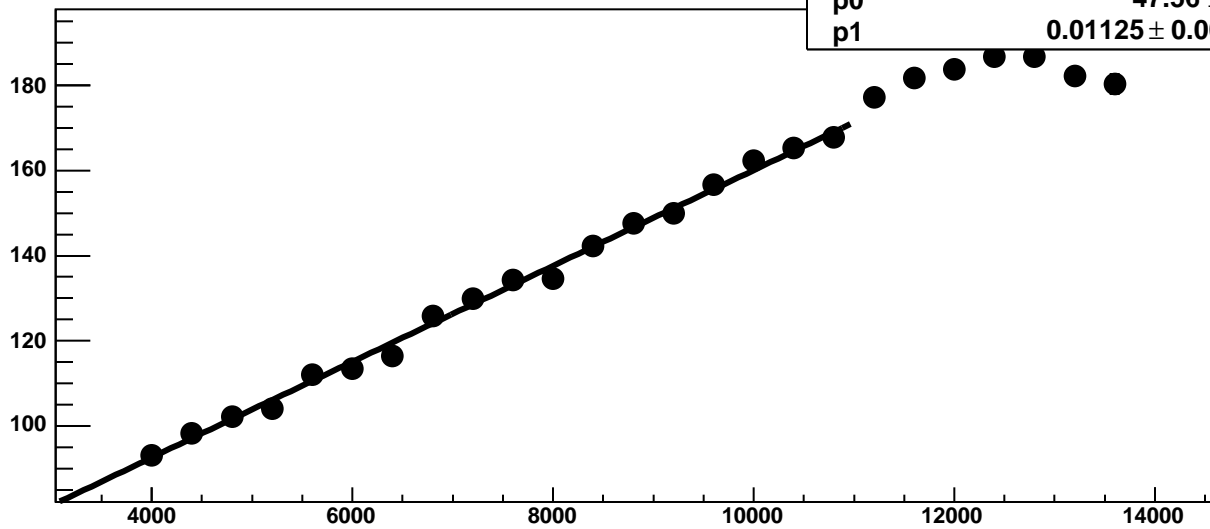
Chip 1, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



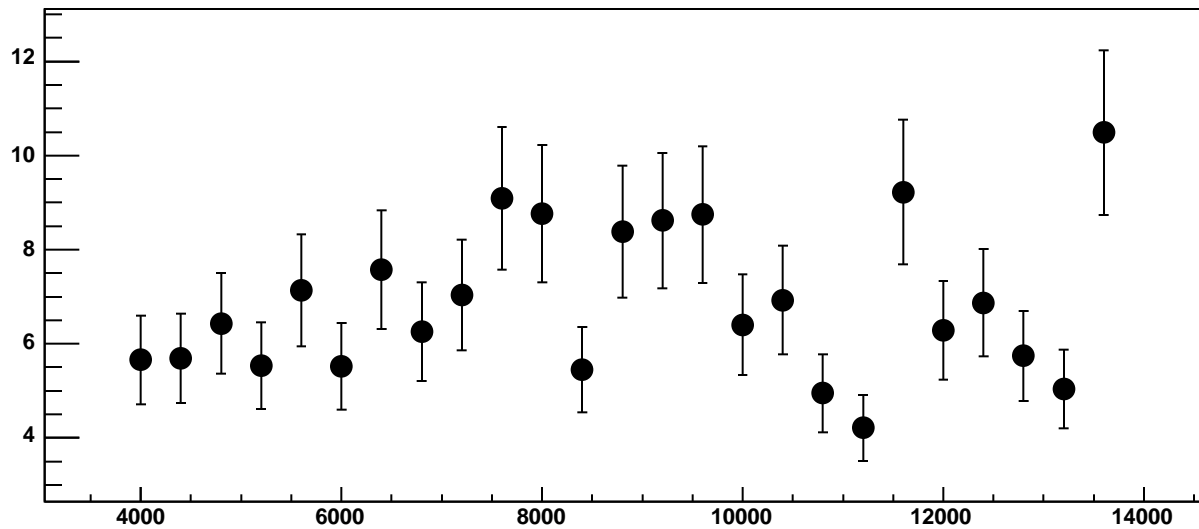
Chip 1, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



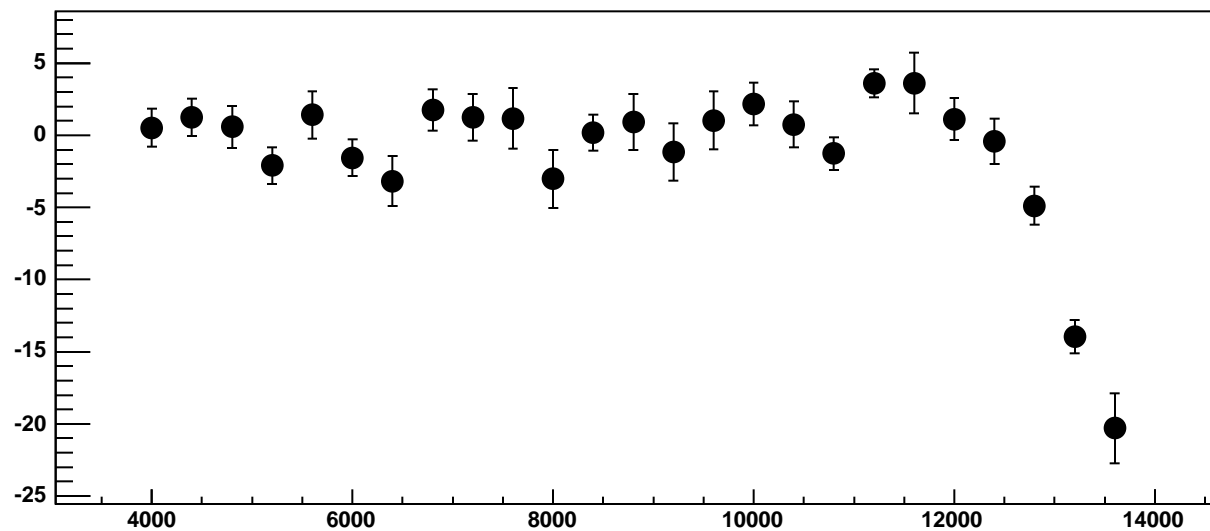
Chip 1, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



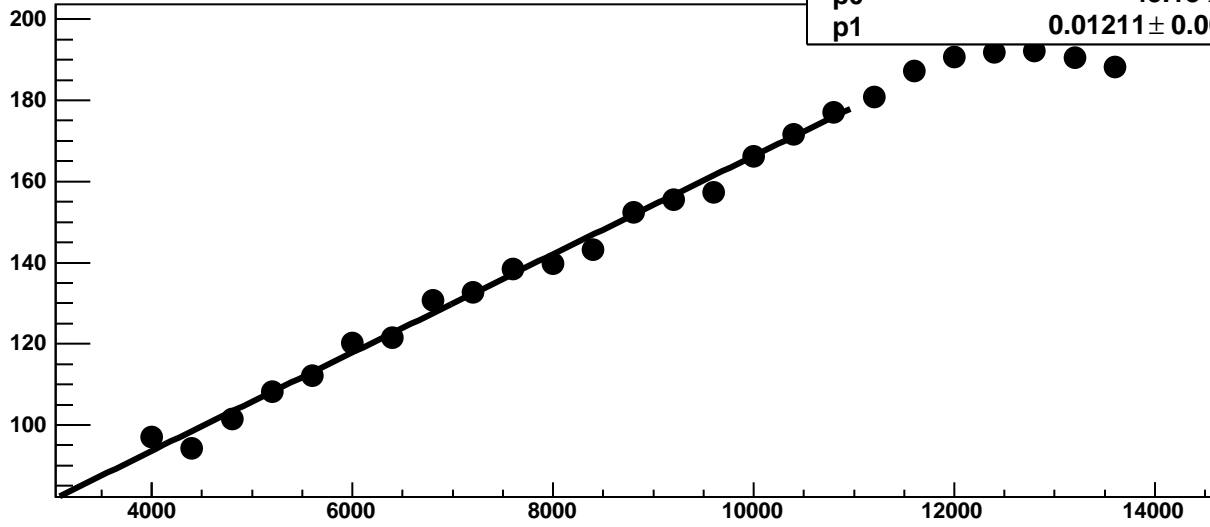
Chip 1, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



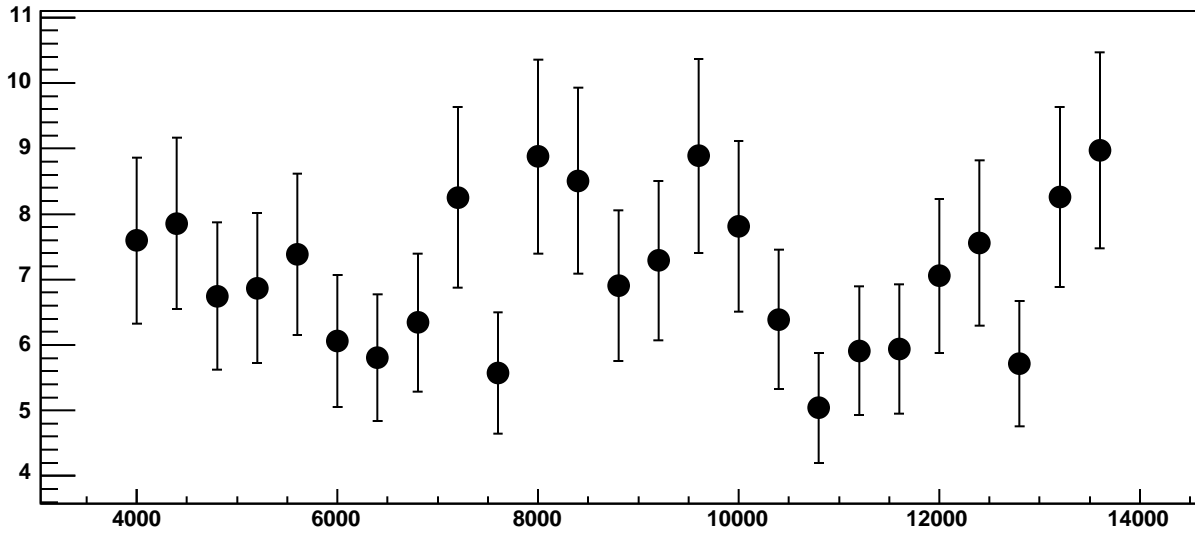
Chip 1, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



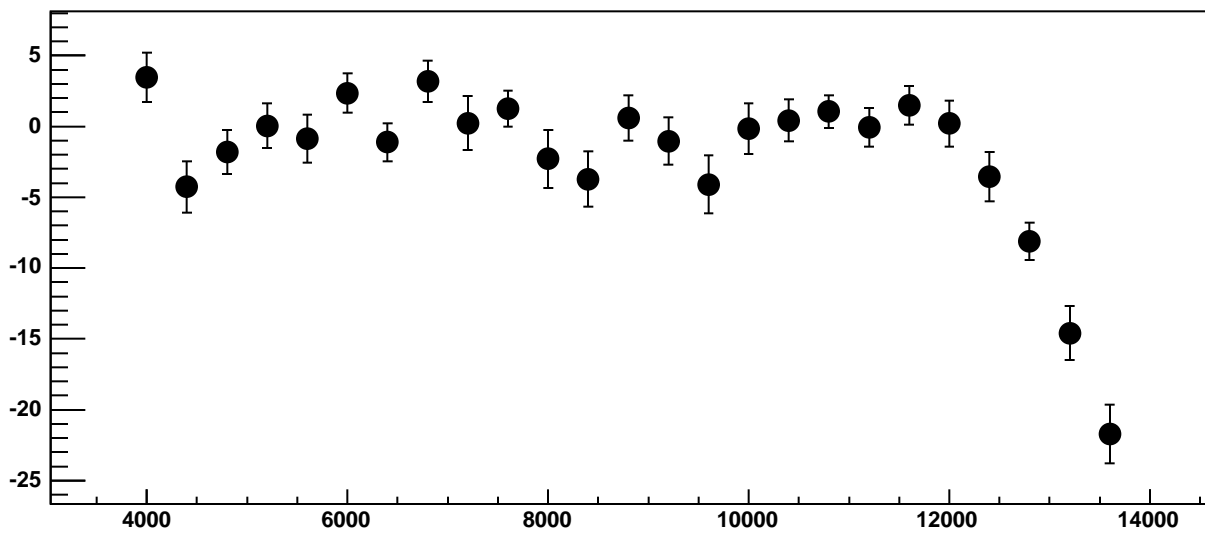
Chip 1, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC



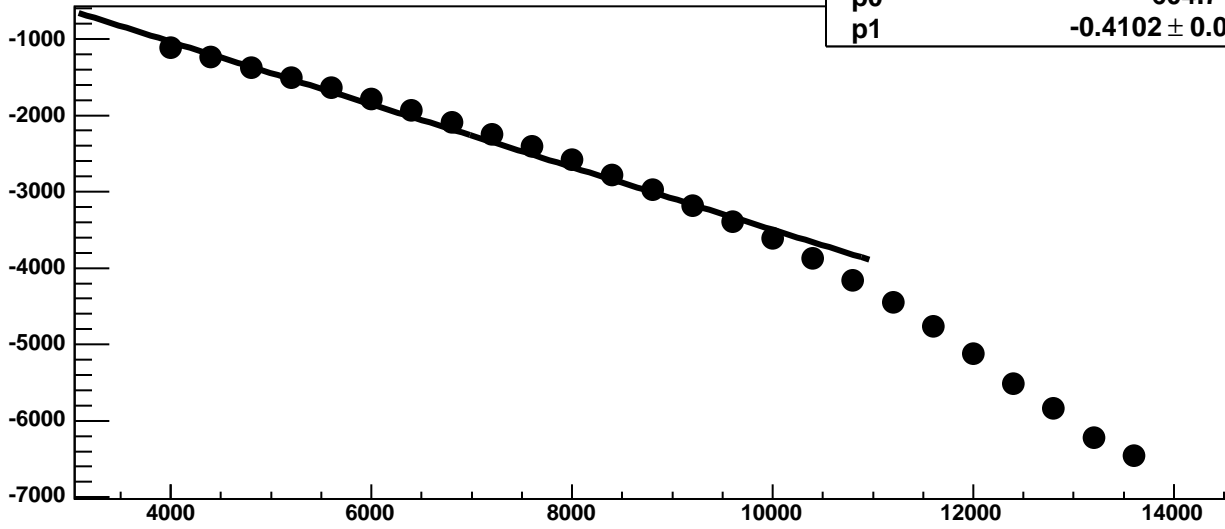
Chip 1, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



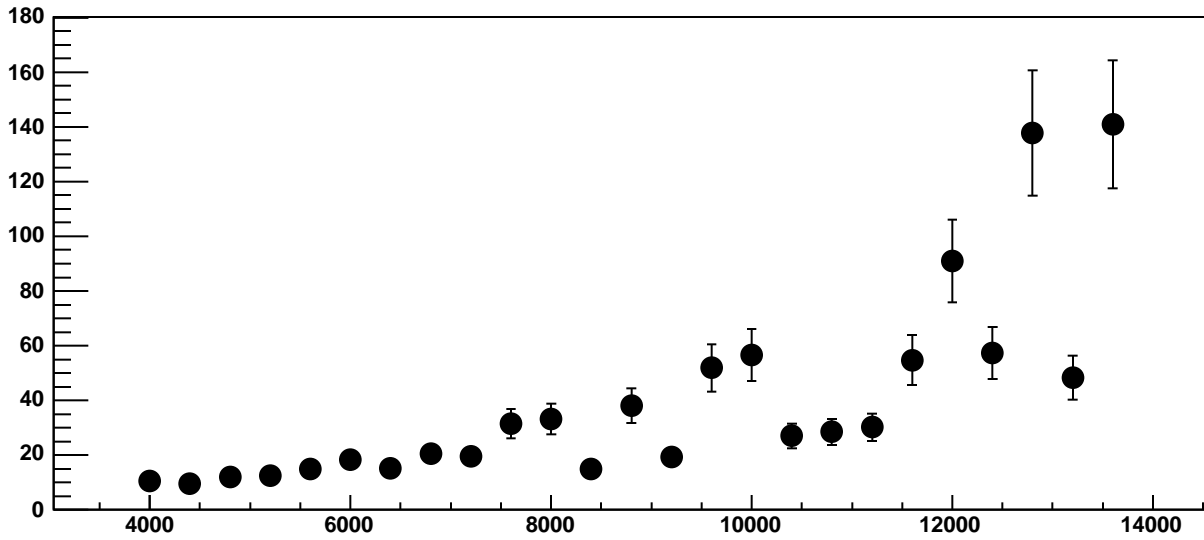
Chip 1, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC



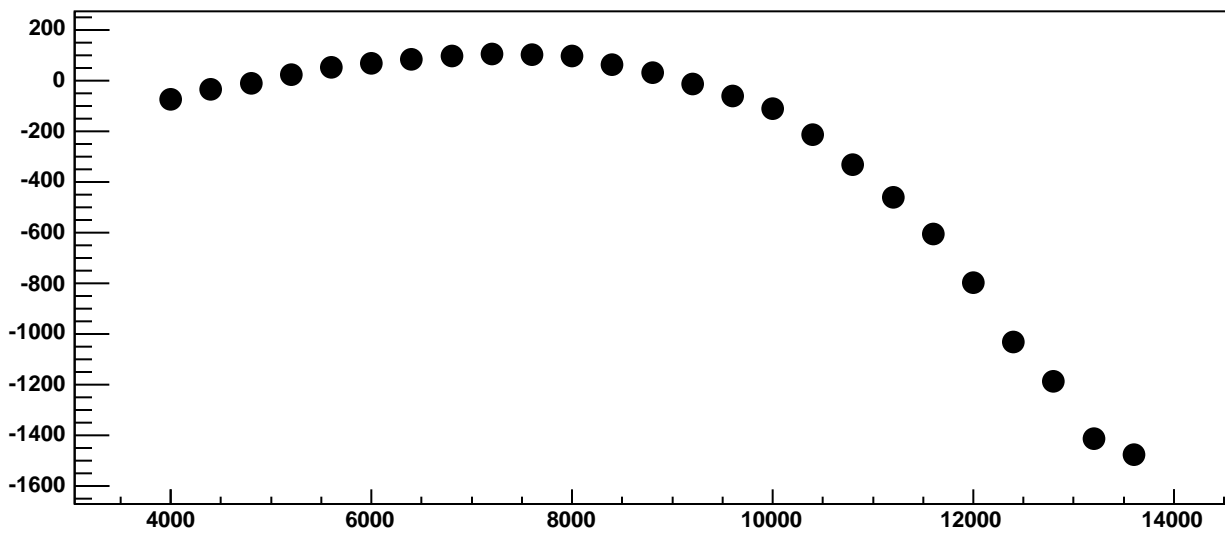
Chip 1, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



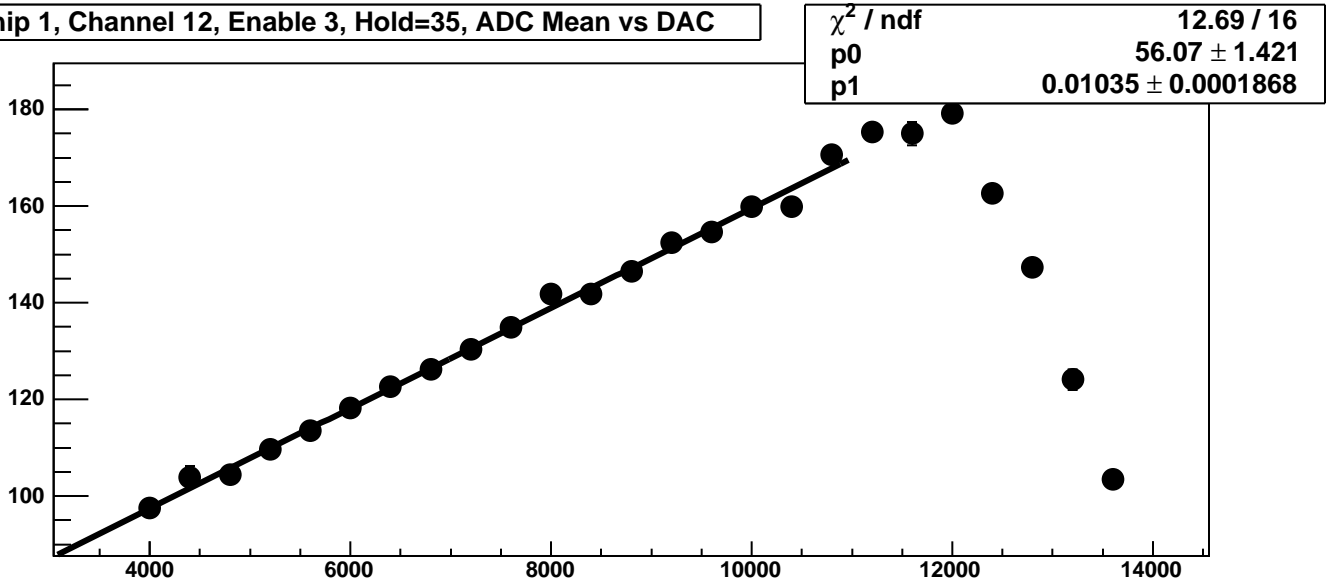
Chip 1, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



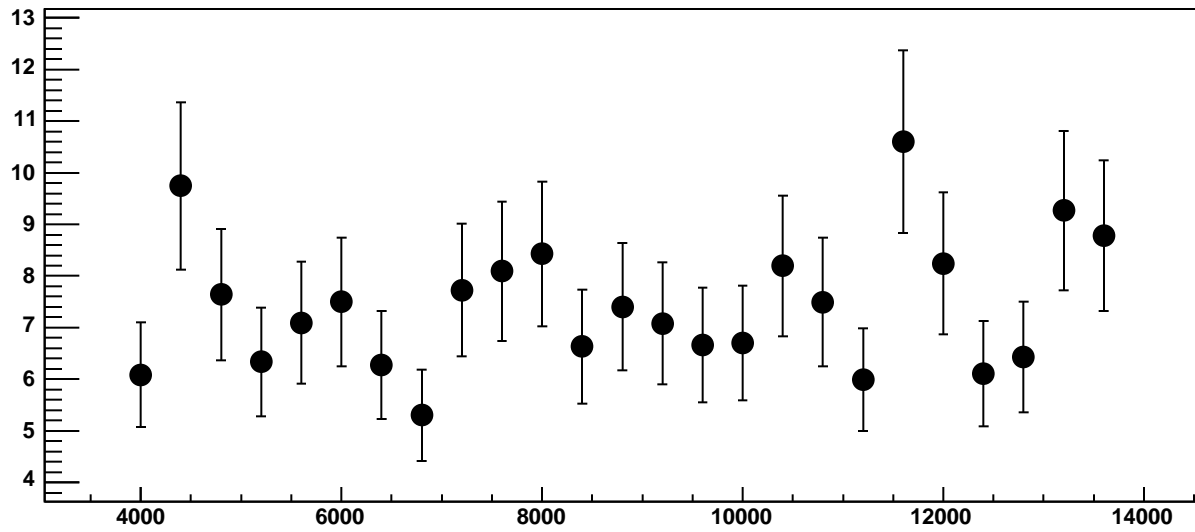
Chip 1, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



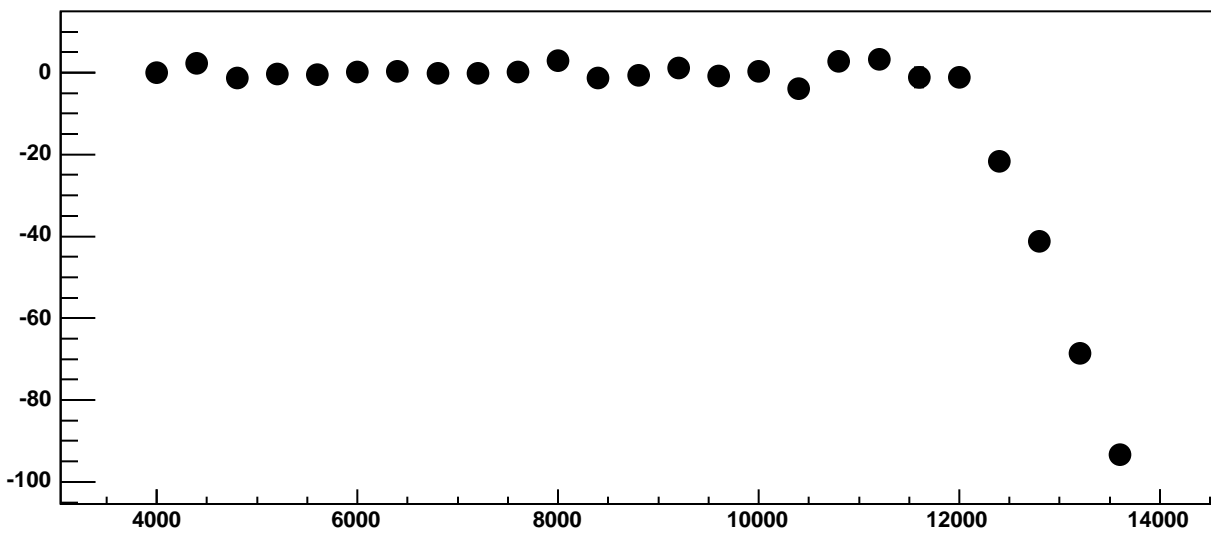
Chip 1, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



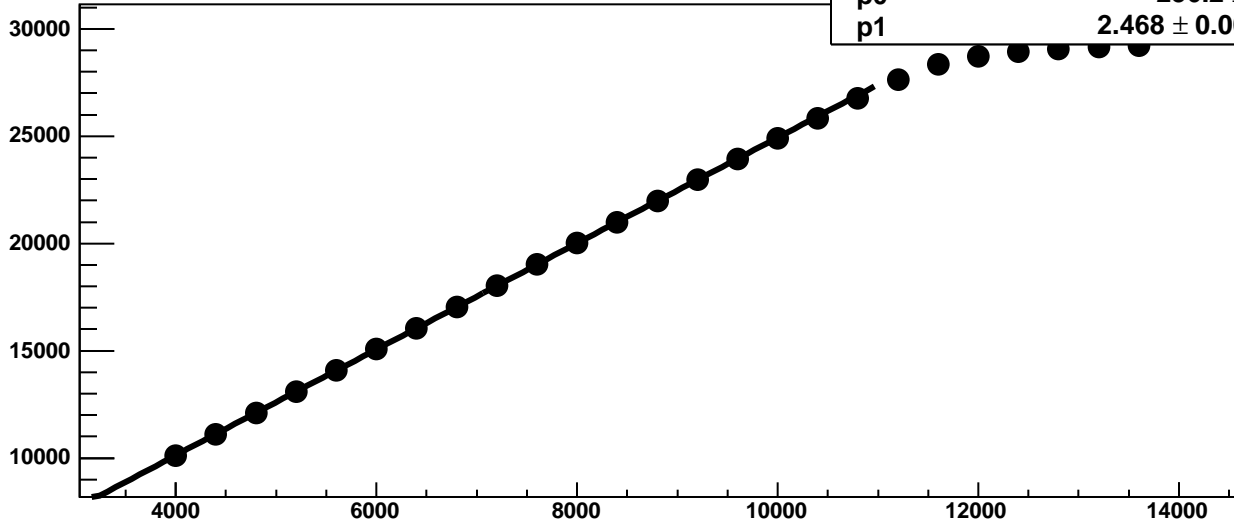
Chip 1, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

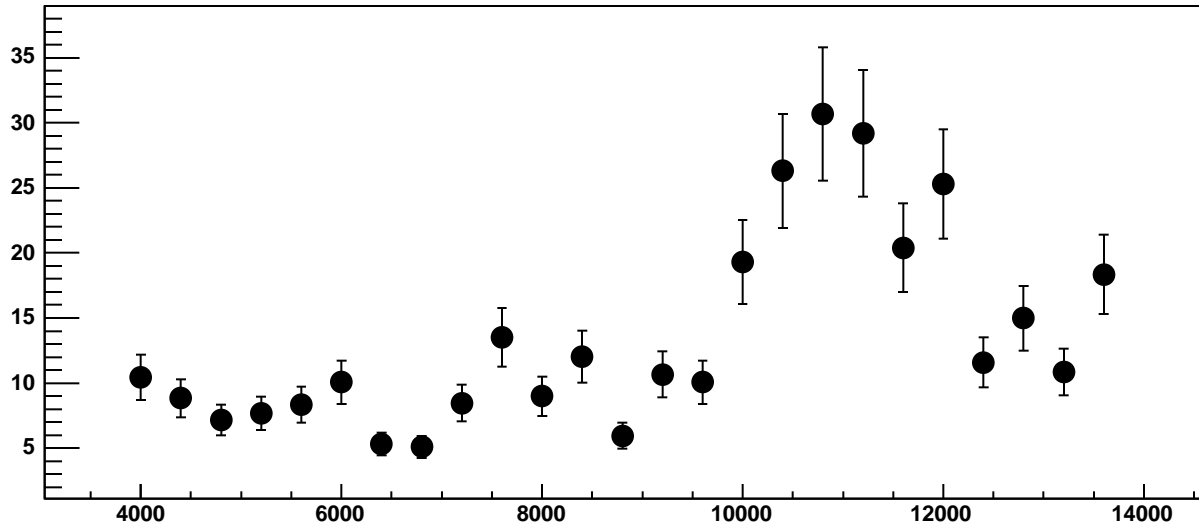


Chip 1, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC

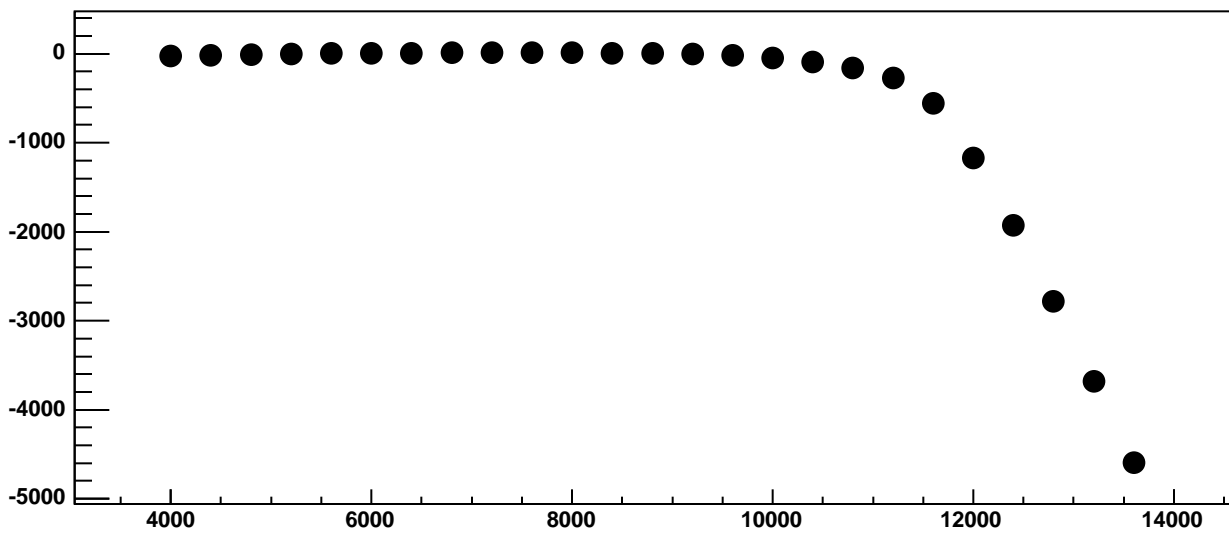


χ^2 / ndf 1373 / 16
p0 256.2 ± 1.992
p1 2.468 ± 0.0002854

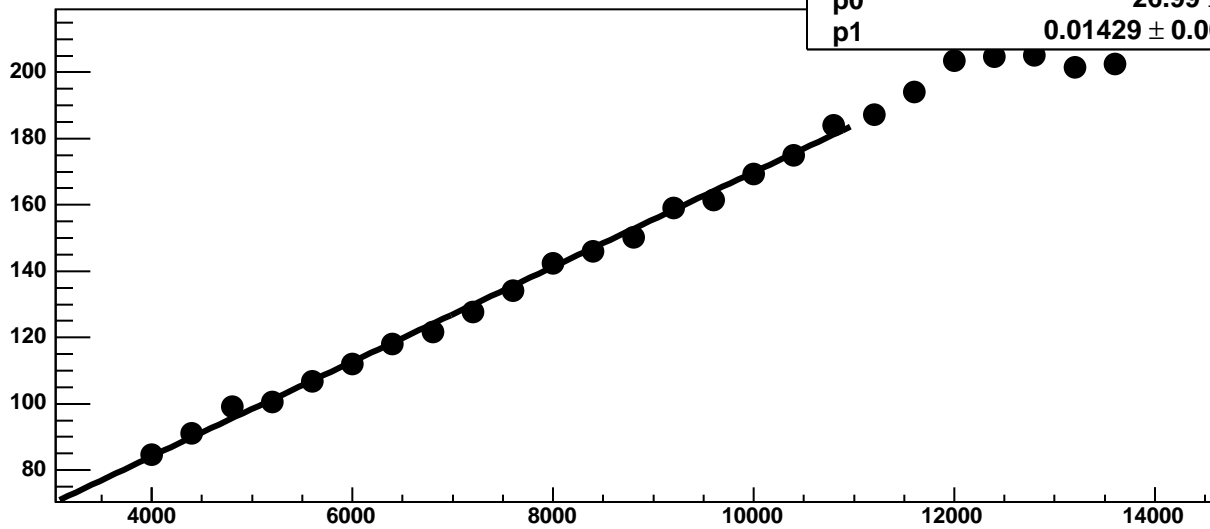
Chip 1, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC

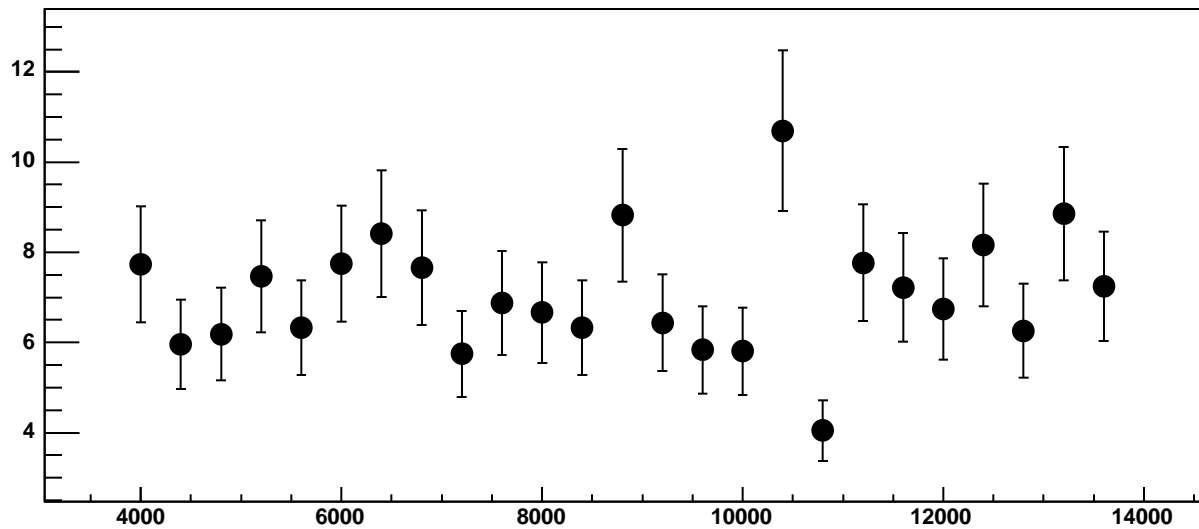


Chip 1, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

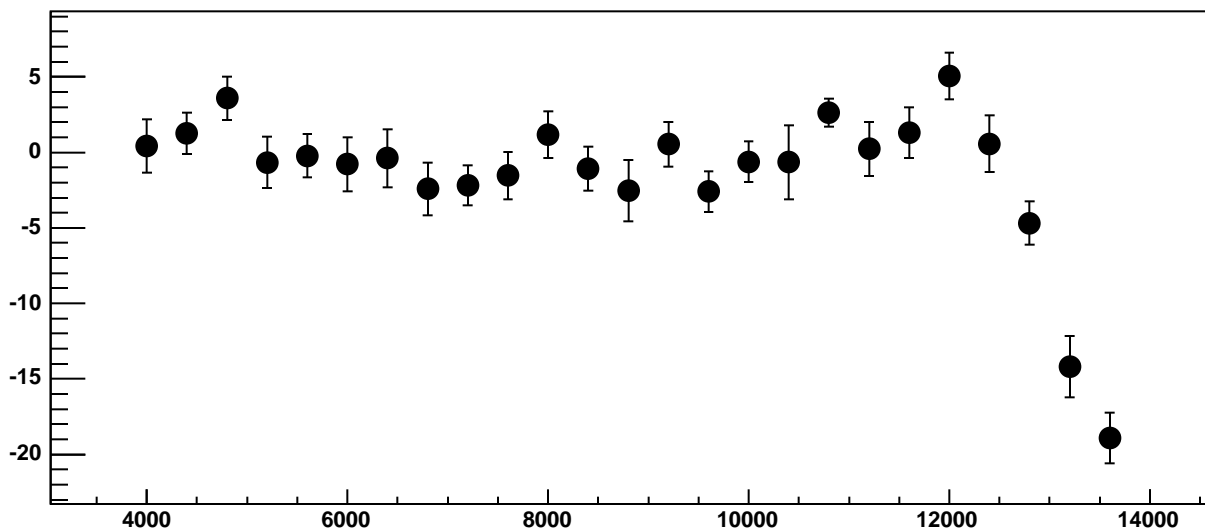


χ^2 / ndf 28.15 / 16
p0 26.99 ± 1.279
p1 0.01429 ± 0.0001595

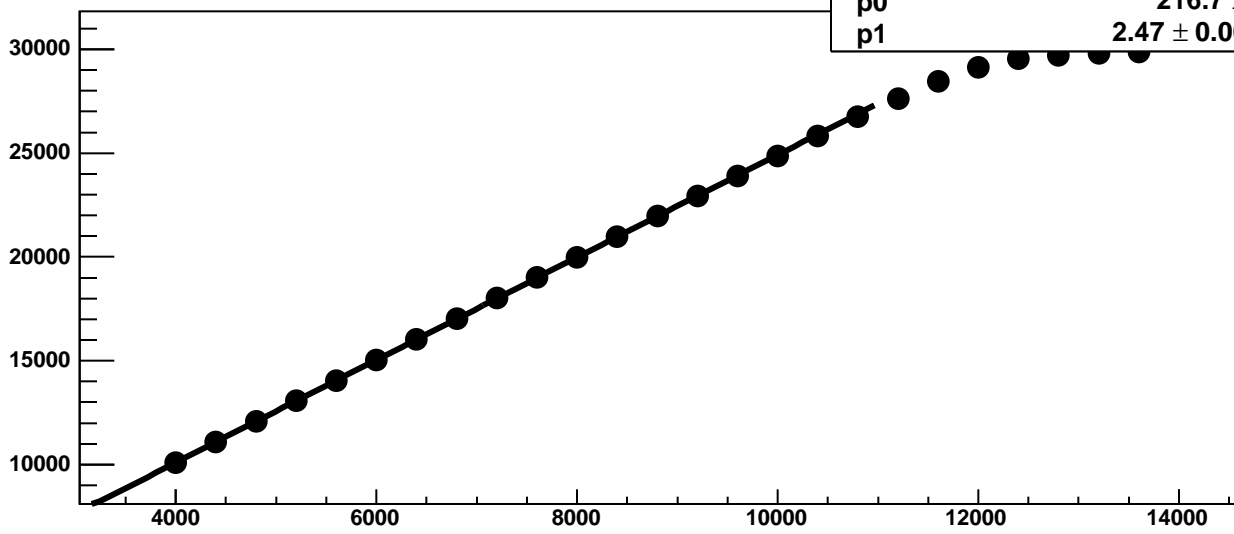
Chip 1, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



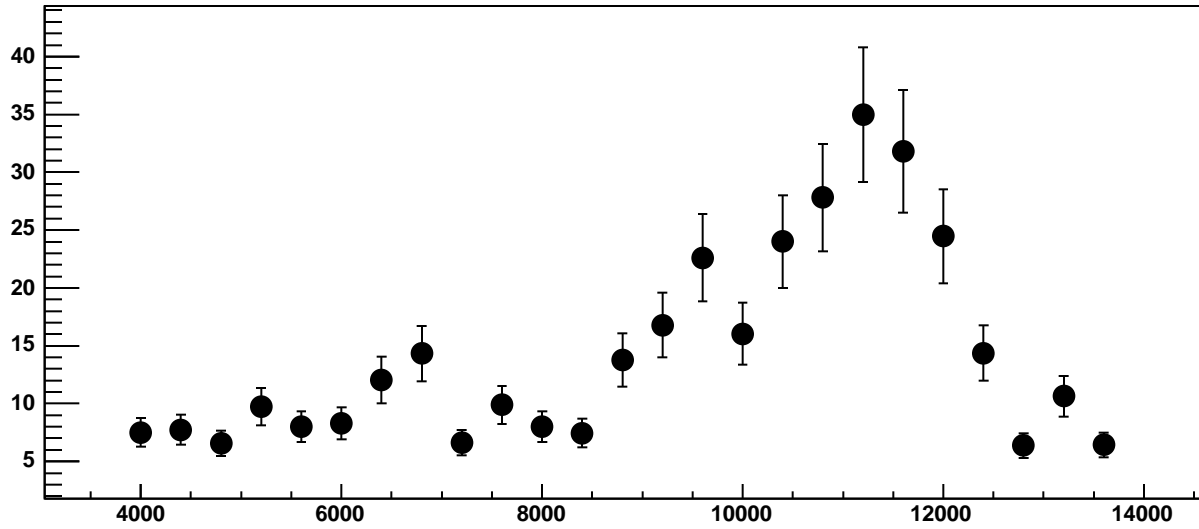
Chip 1, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



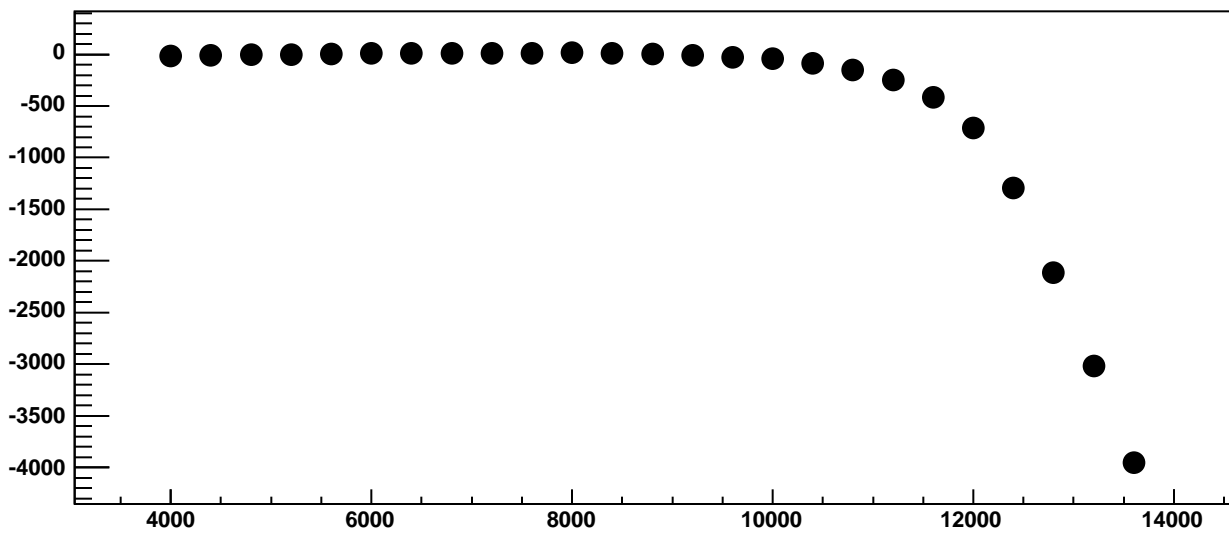
Chip 1, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC



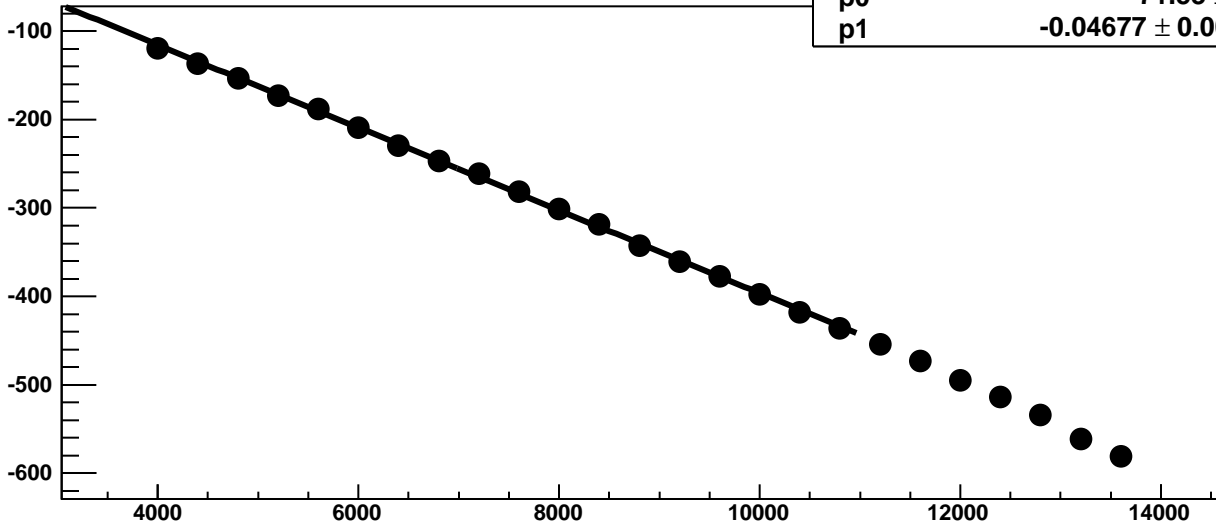
Chip 1, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

39.26 / 16

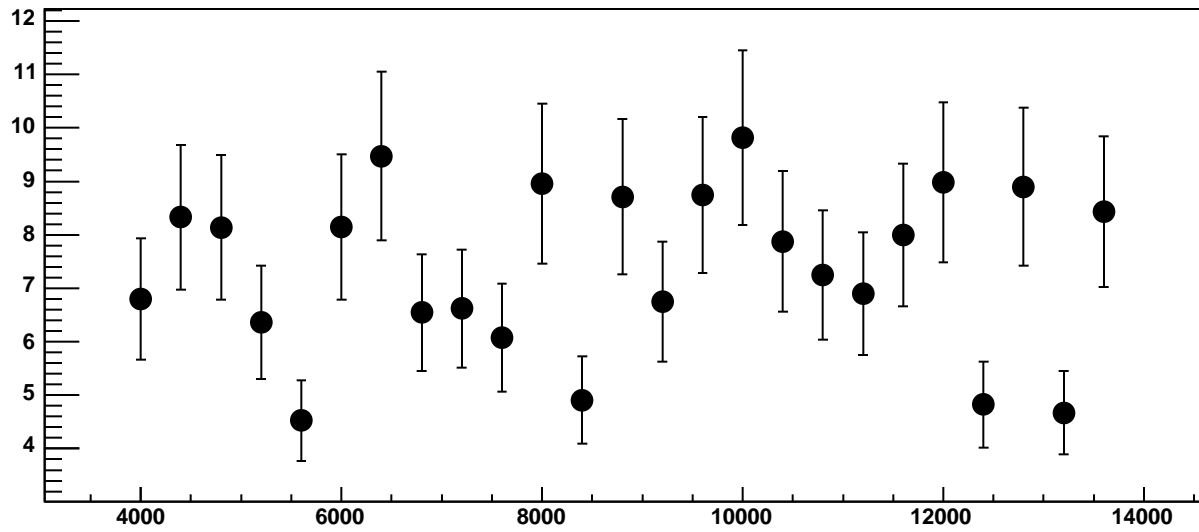
p0

71.53 ± 1.427

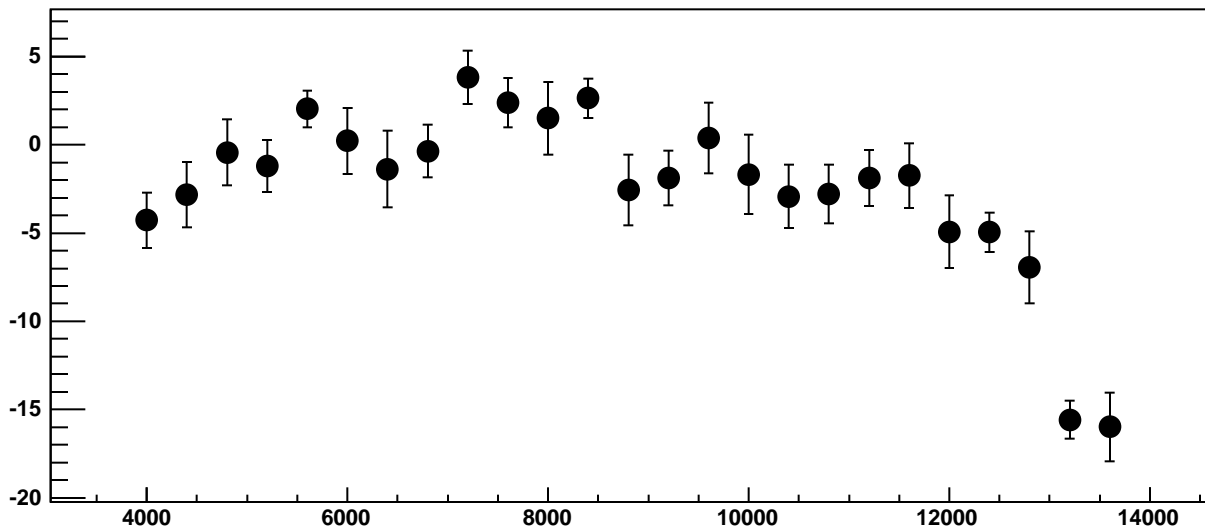
p1

-0.04677 ± 0.0001908

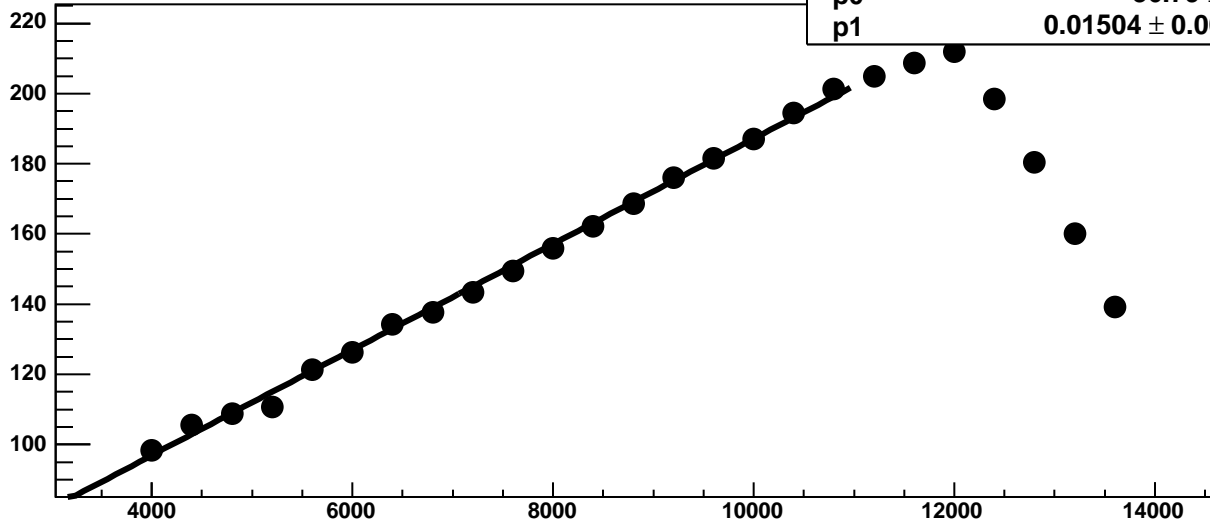
Chip 1, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



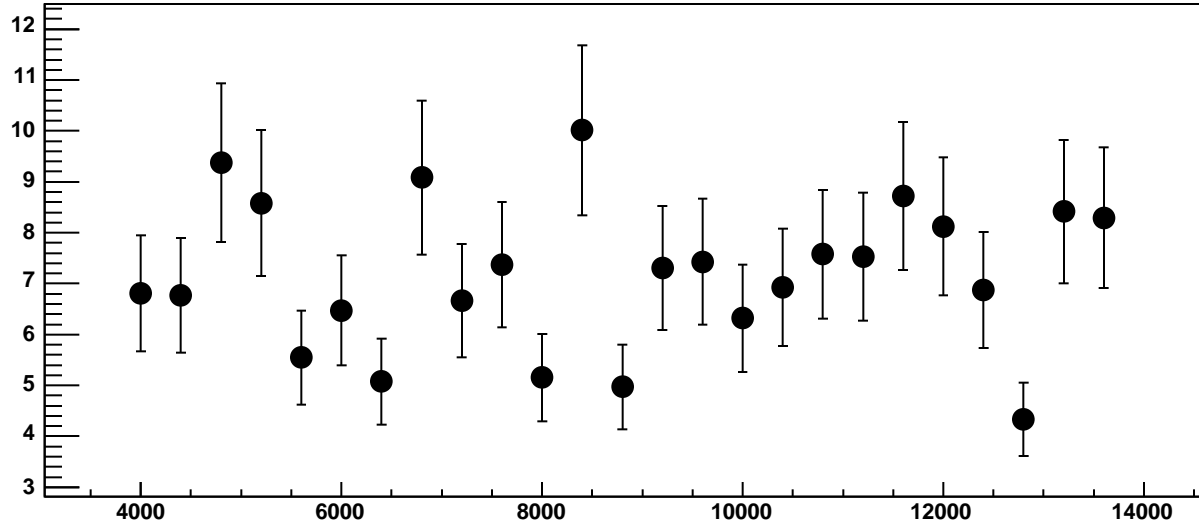
Chip 1, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



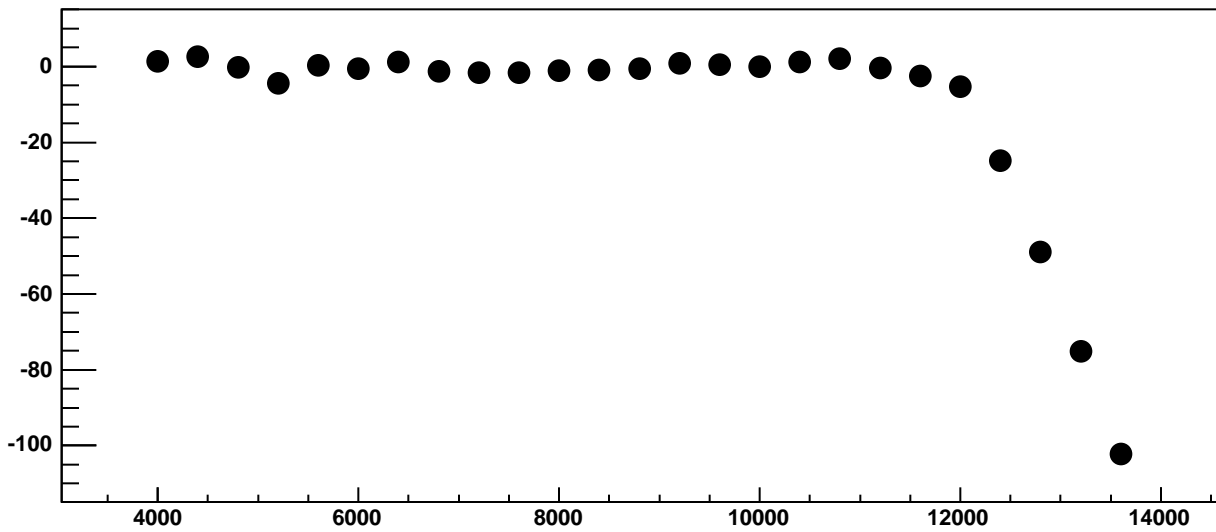
Chip 1, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC



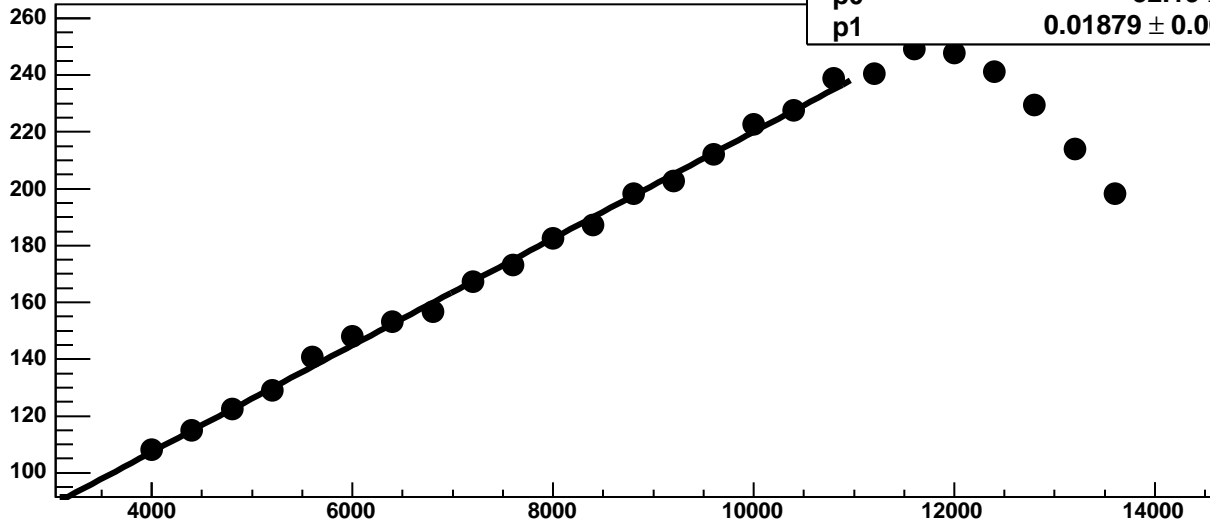
Chip 1, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



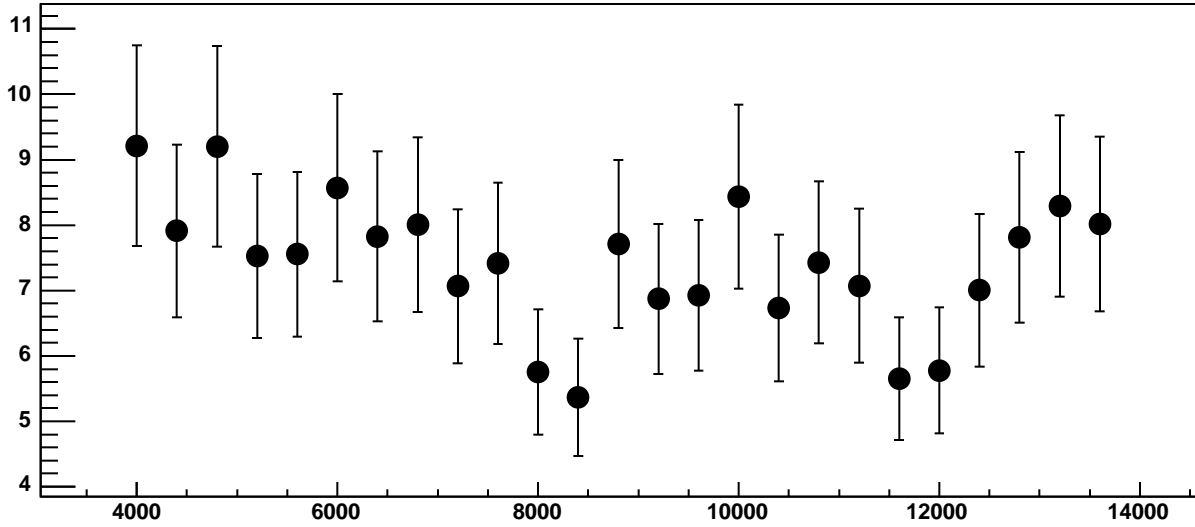
Chip 1, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



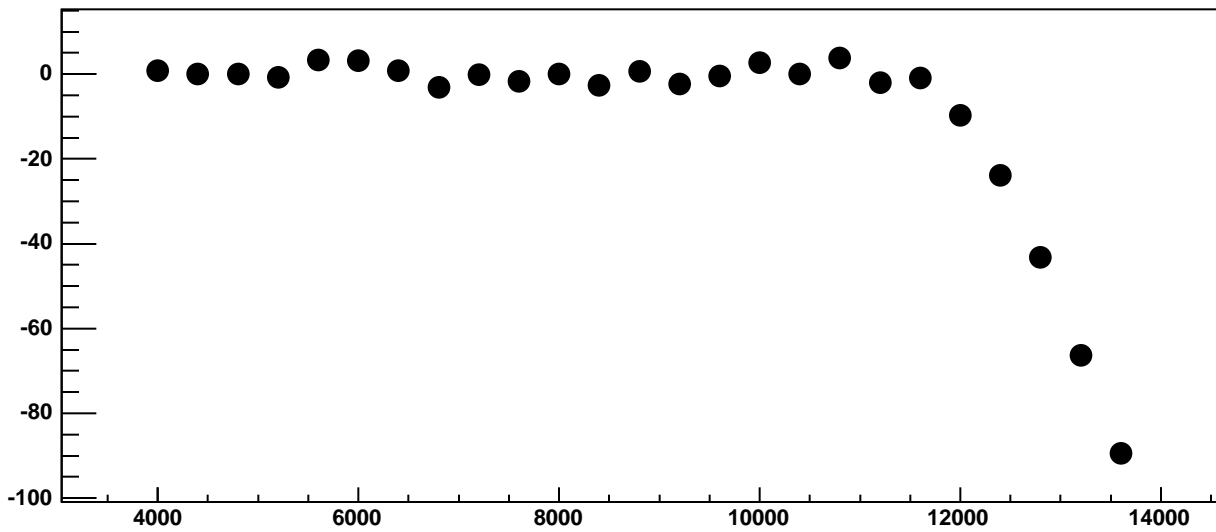
Chip 1, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC



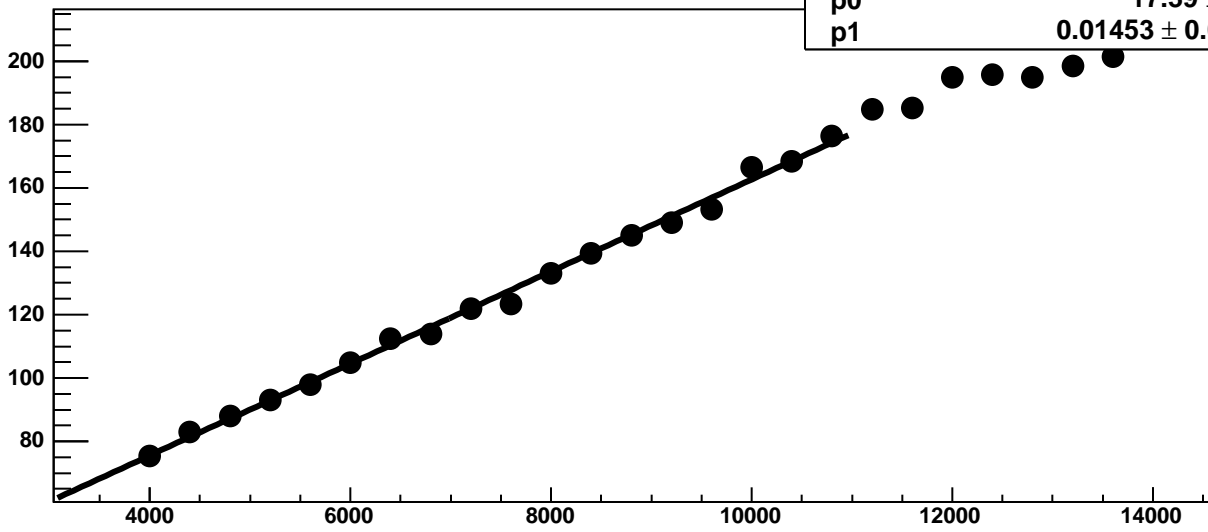
Chip 1, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC

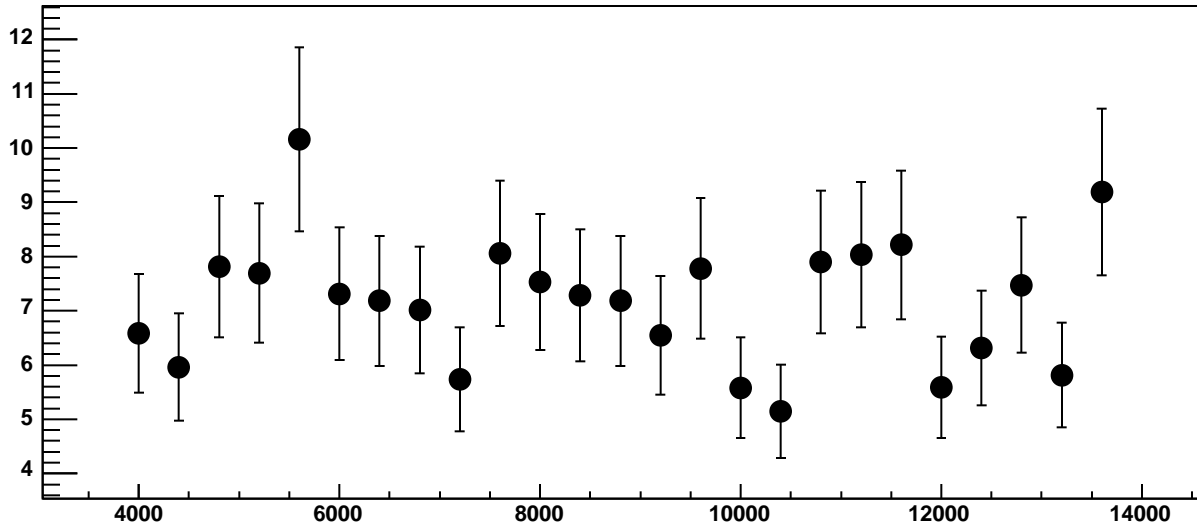


Chip 1, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

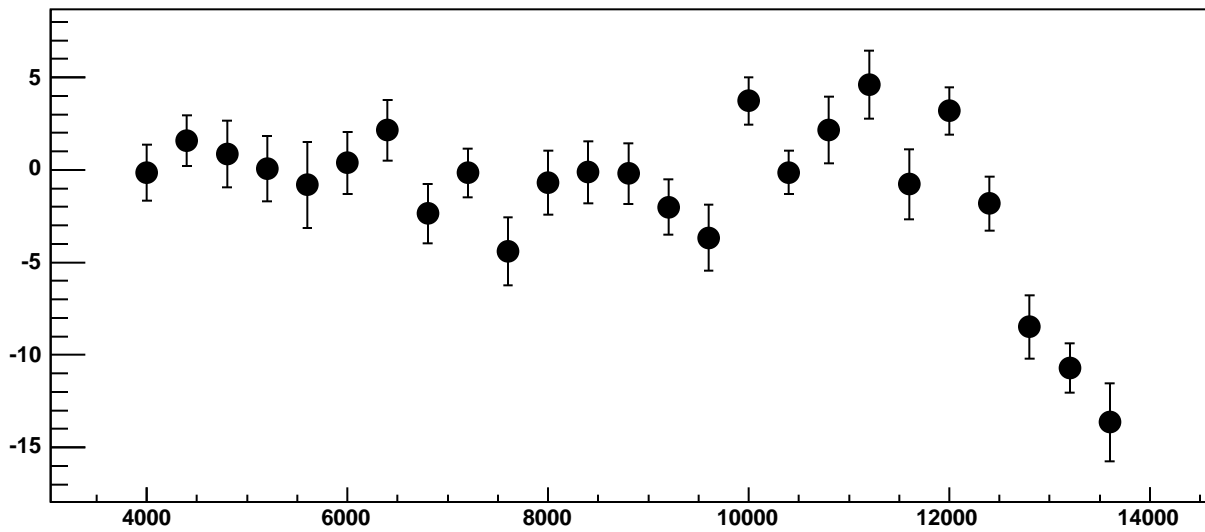


χ^2 / ndf 27.37 / 16
p0 17.39 ± 1.362
p1 0.01453 ± 0.000173

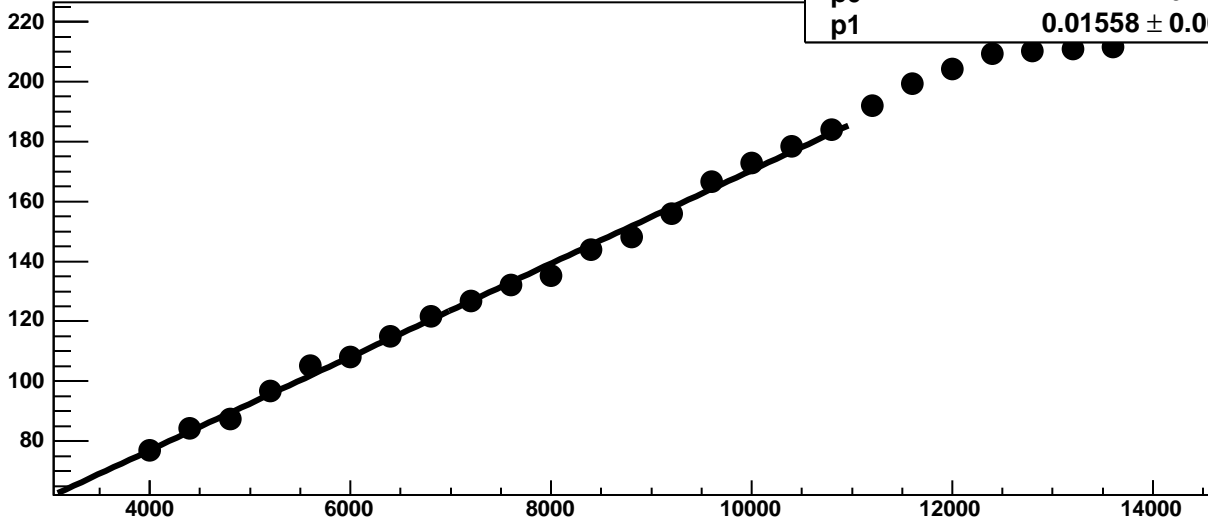
Chip 1, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

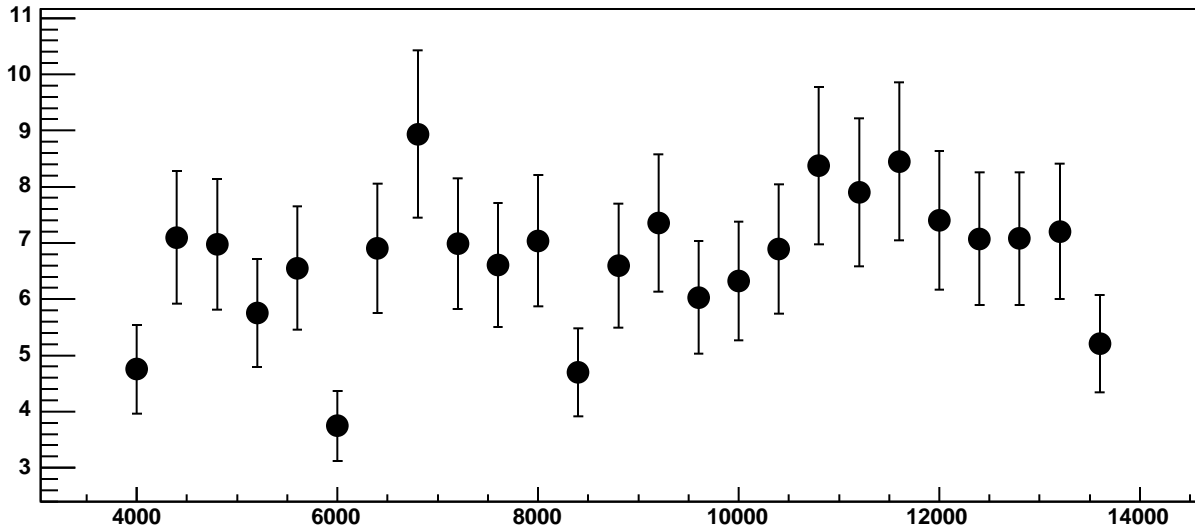


Chip 1, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC

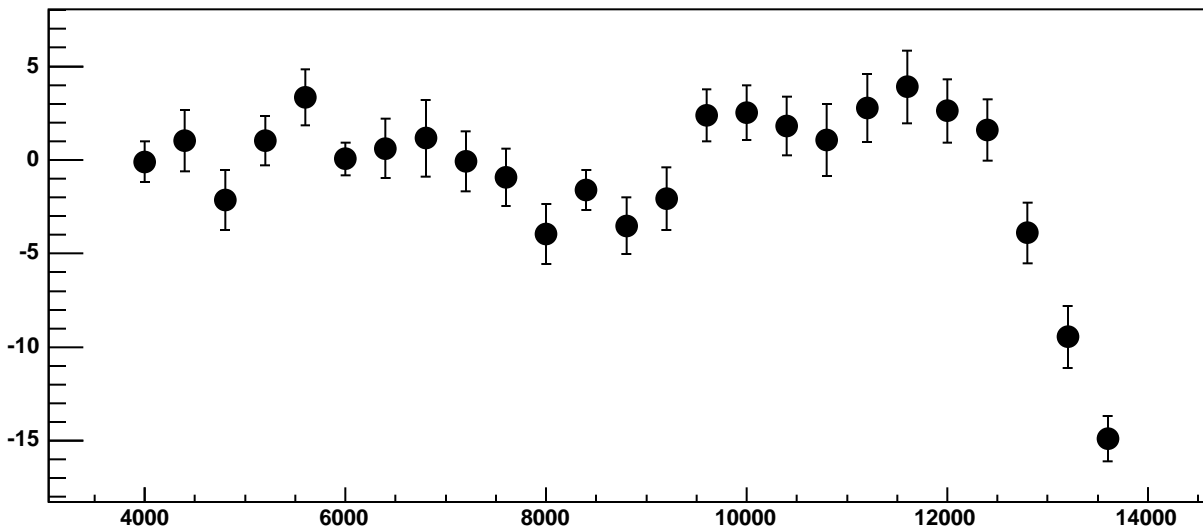


χ^2 / ndf 31.33 / 16
p0 14.62 ± 1.202
p1 0.01558 ± 0.0001626

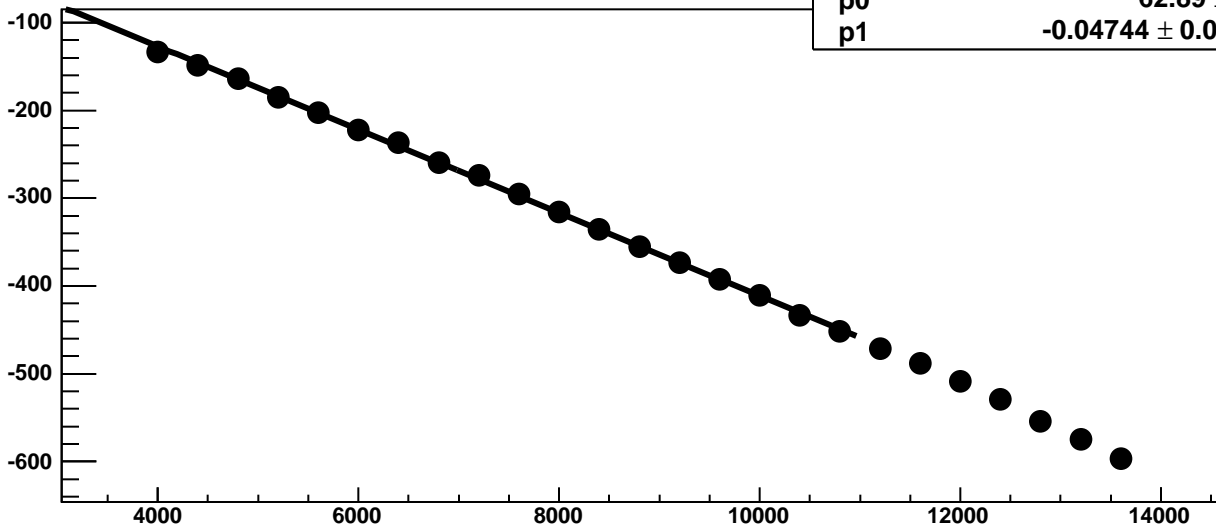
Chip 1, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

36.06 / 16

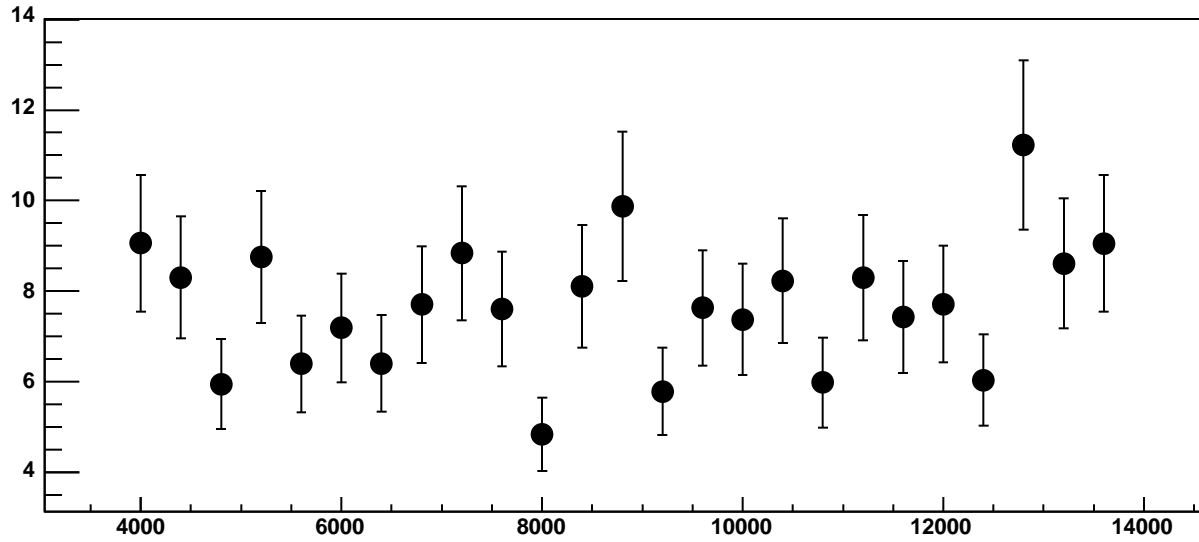
p0

62.89 ± 1.464

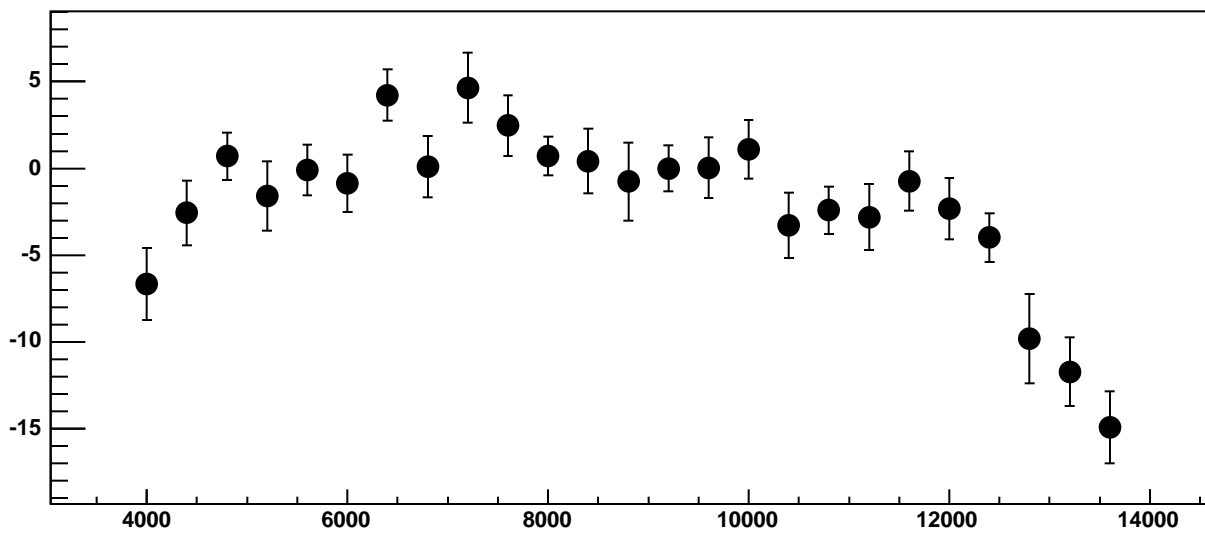
p1

-0.04744 ± 0.0001881

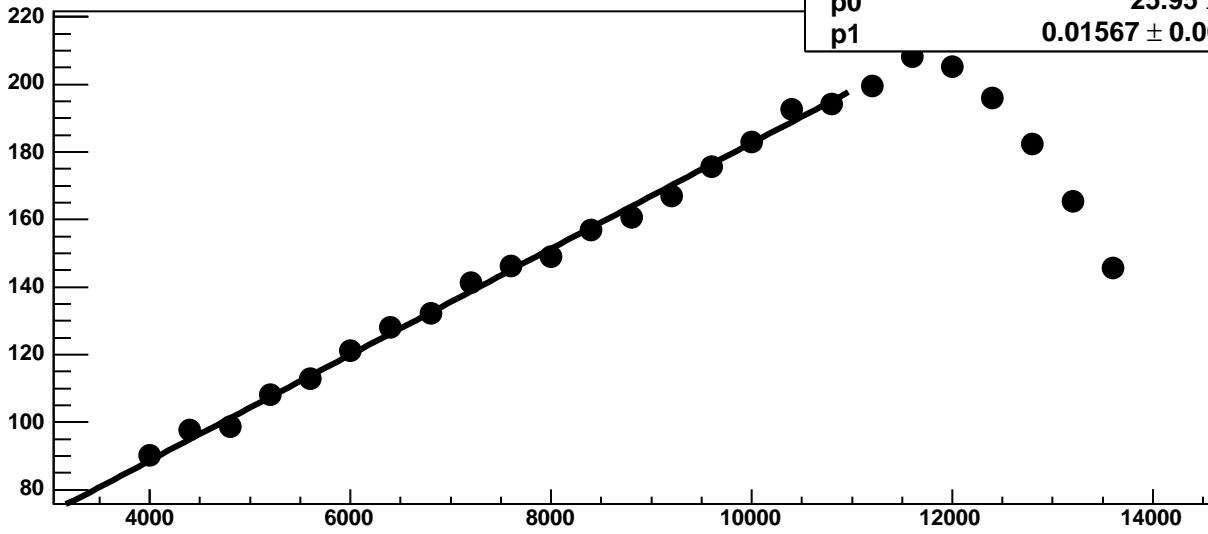
Chip 1, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



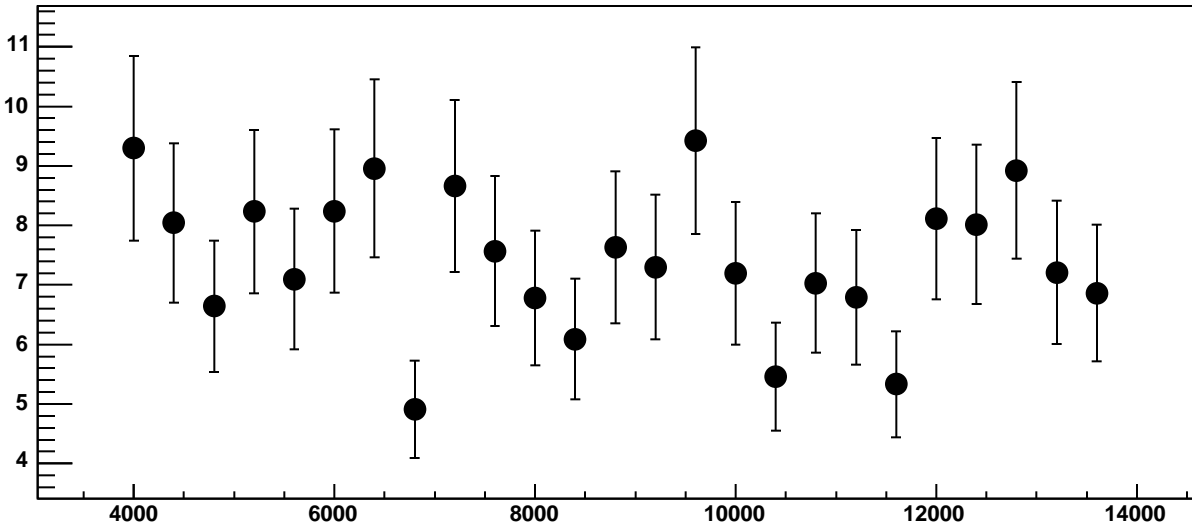
Chip 1, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



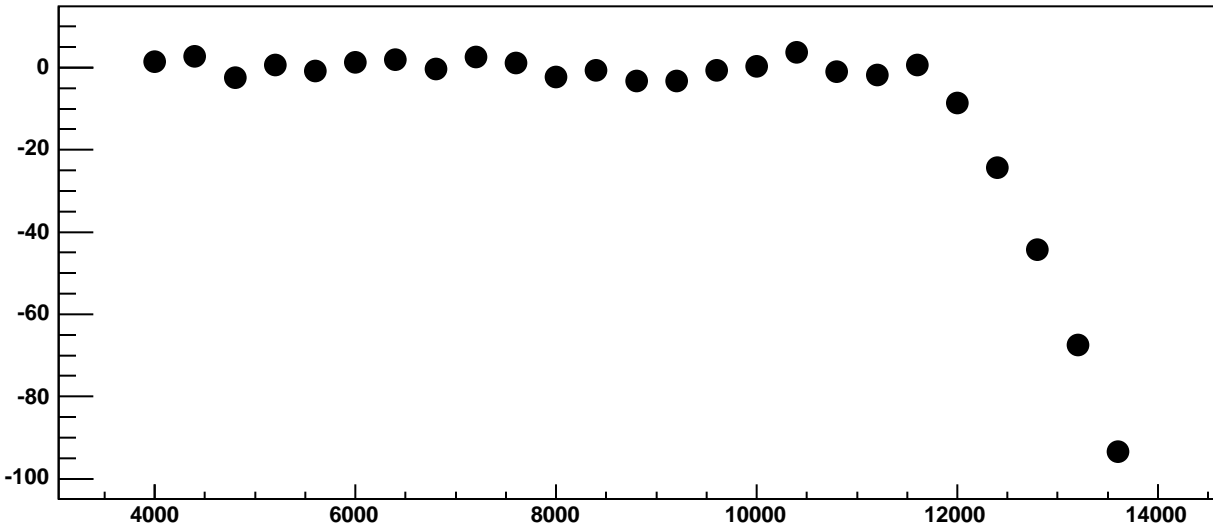
Chip 1, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



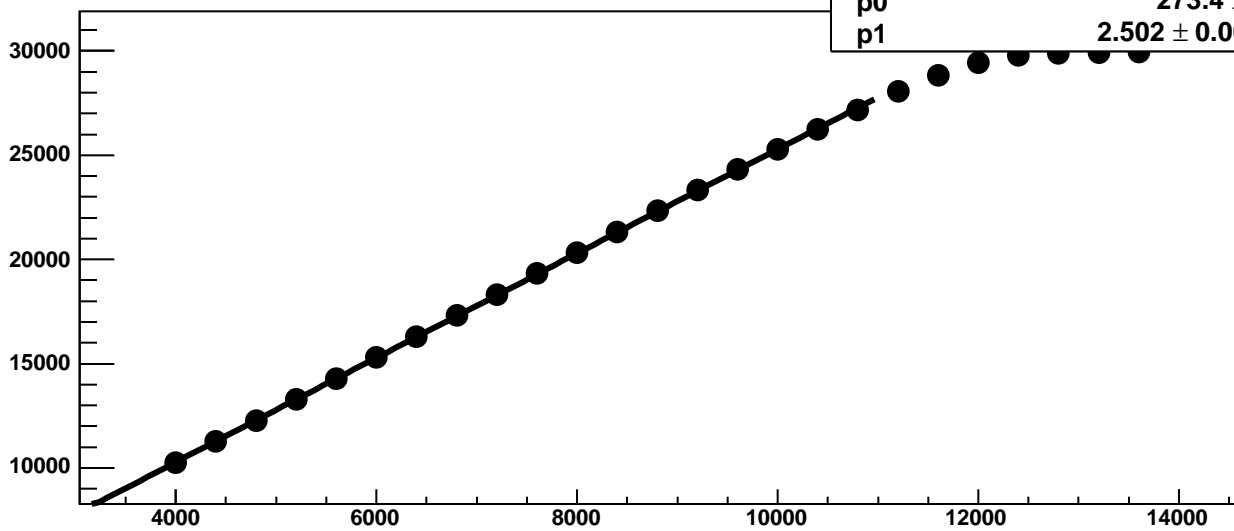
Chip 1, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

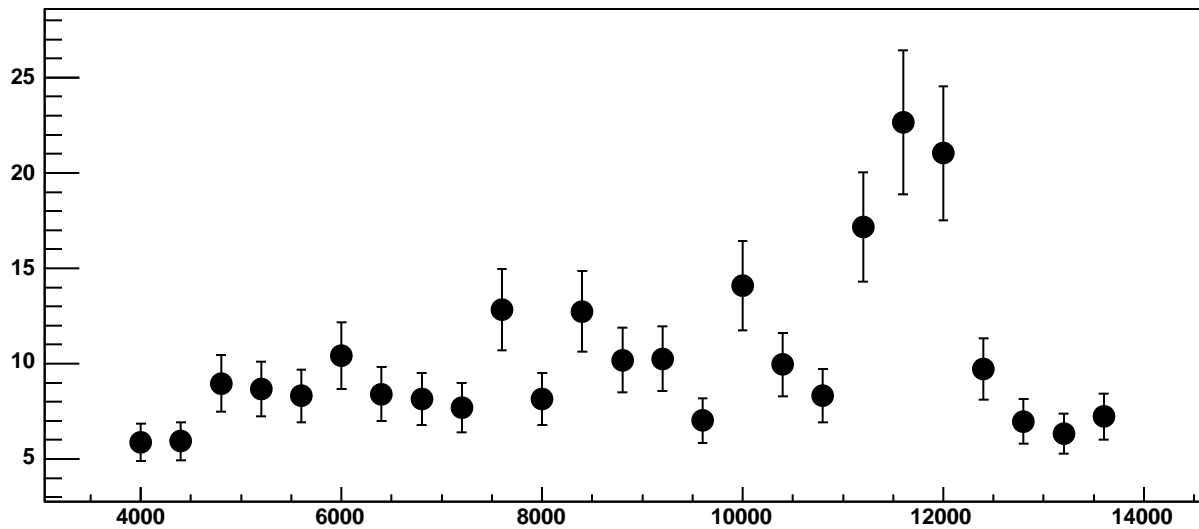


Chip 1, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC

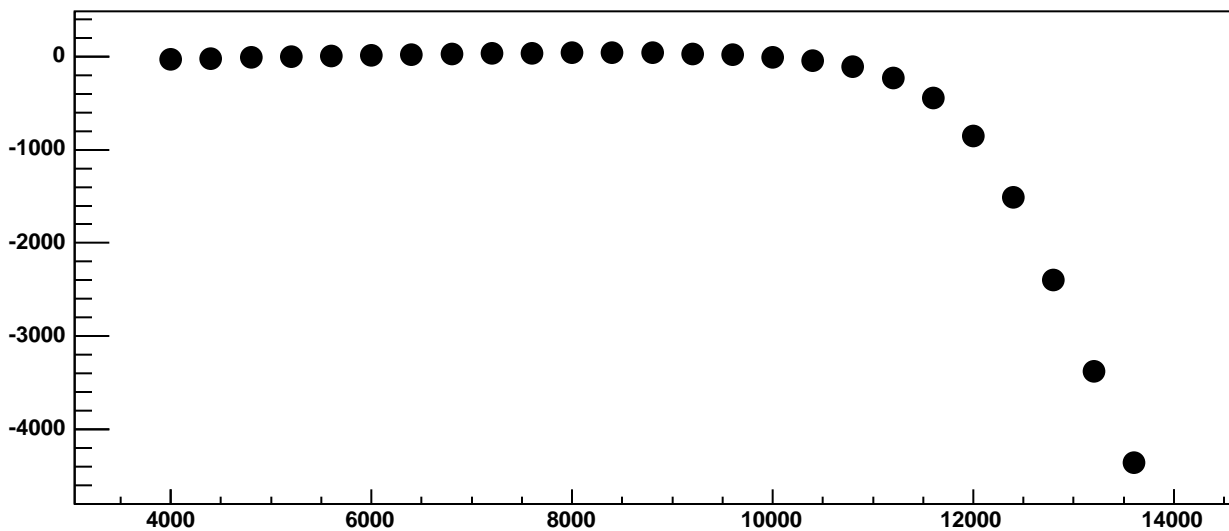


χ^2 / ndf 6481 / 16
p0 273.4 ± 1.509
p1 2.502 ± 0.0002085

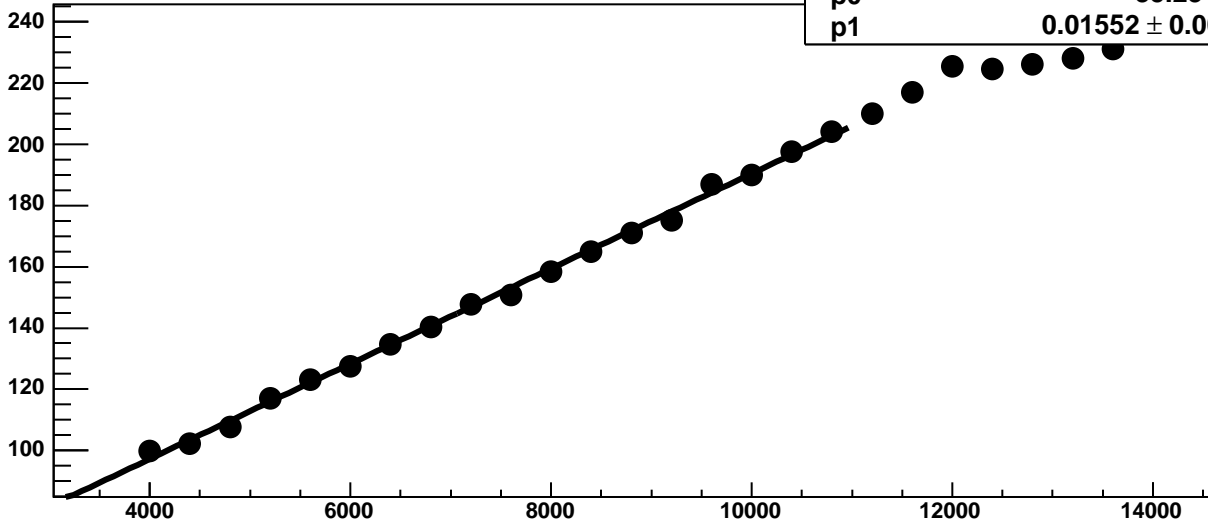
Chip 1, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC

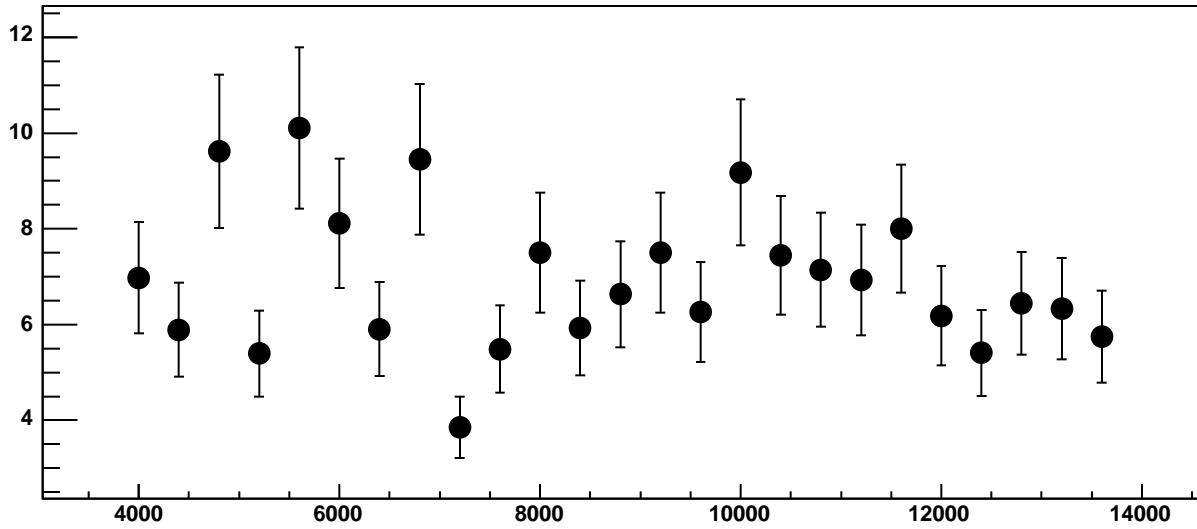


Chip 1, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

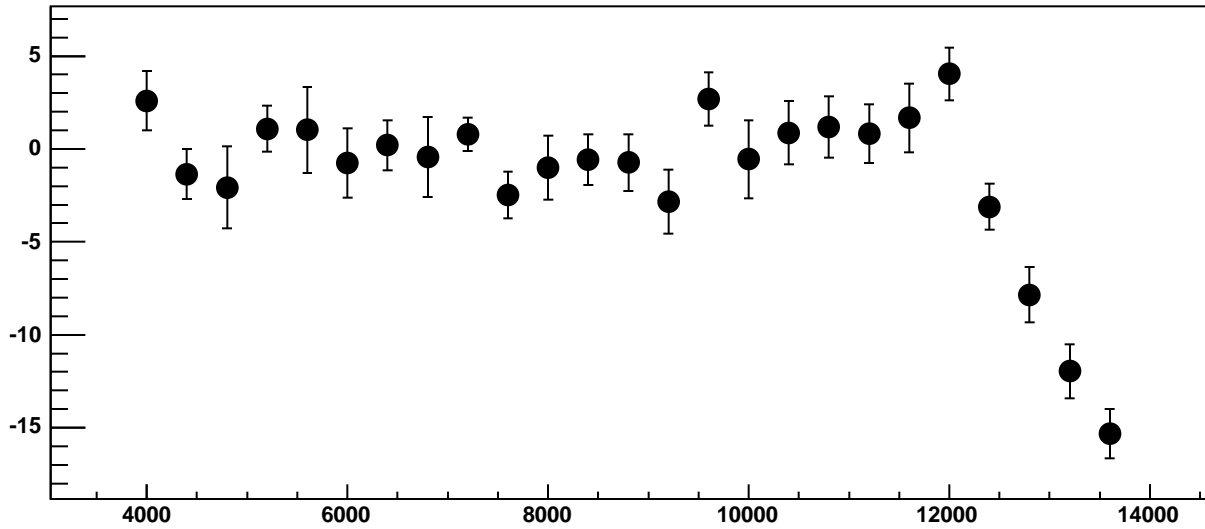


χ^2 / ndf 18.18 / 16
p0 35.23 ± 1.391
p1 0.01552 ± 0.0001836

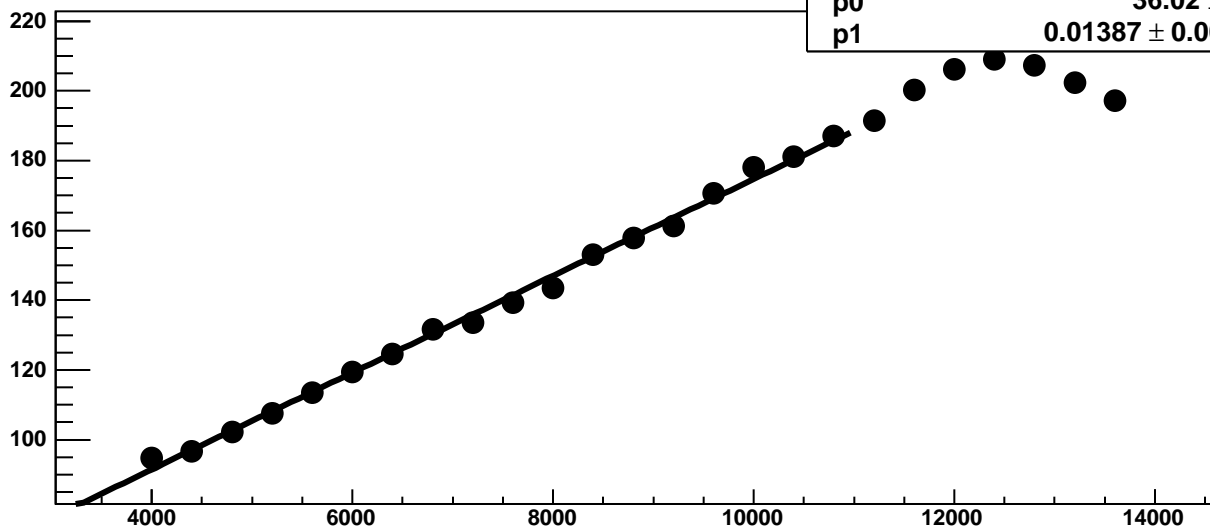
Chip 1, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



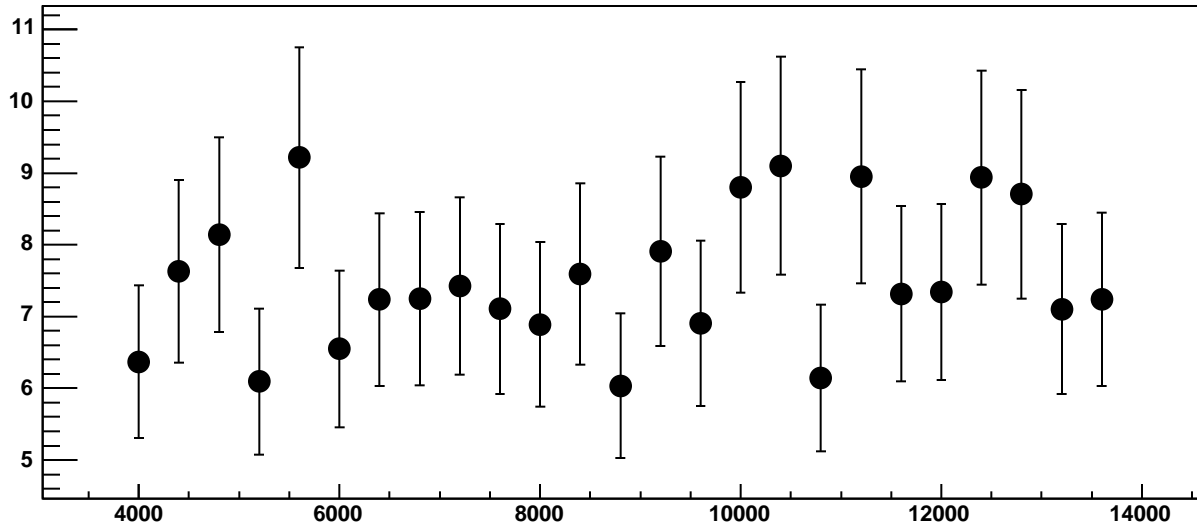
Chip 1, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



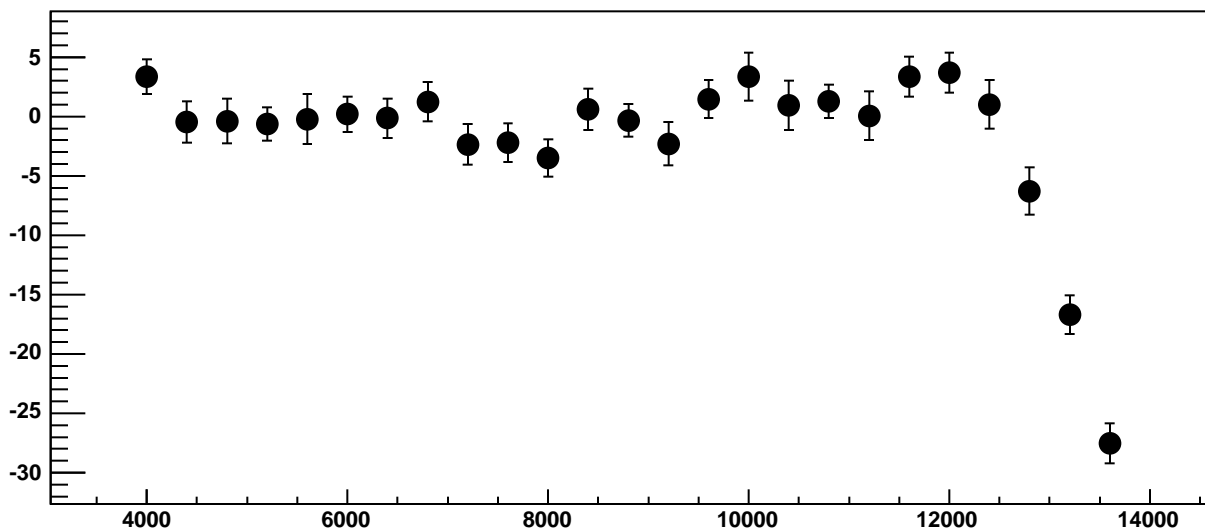
Chip 1, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



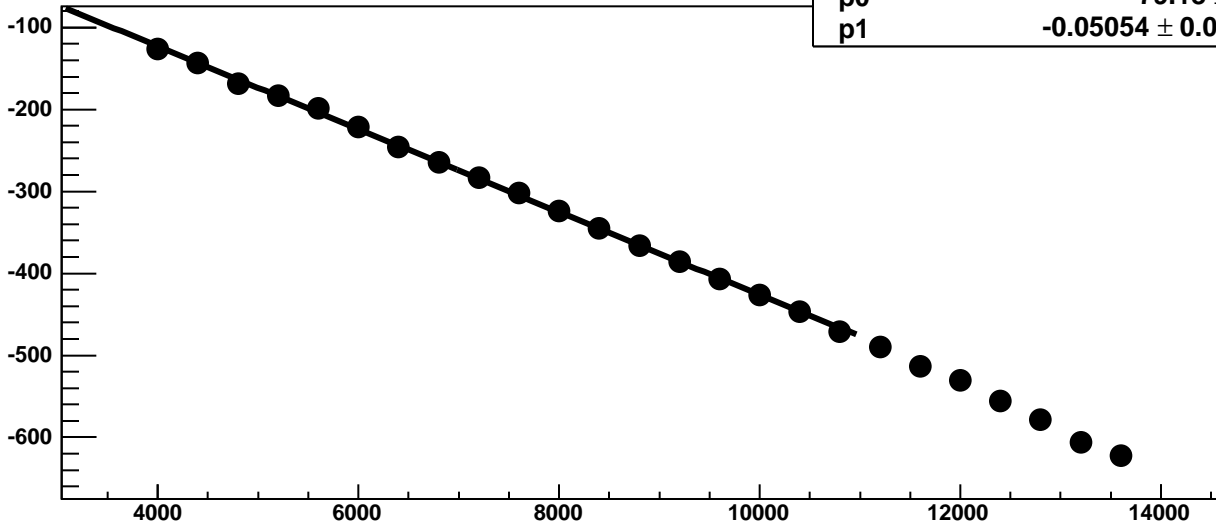
Chip 1, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

24.79 / 16

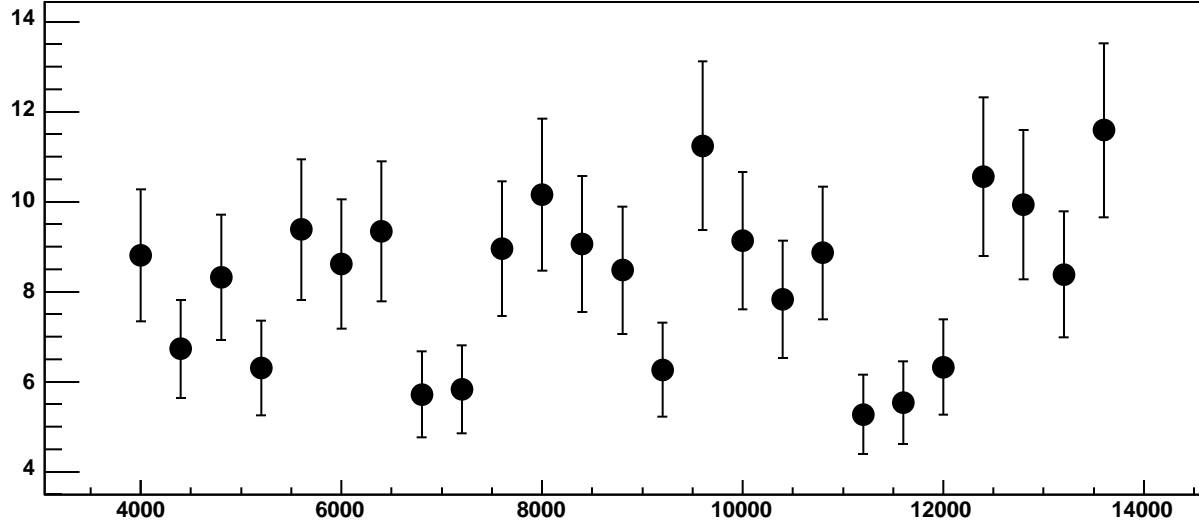
p0

79.18 ± 1.573

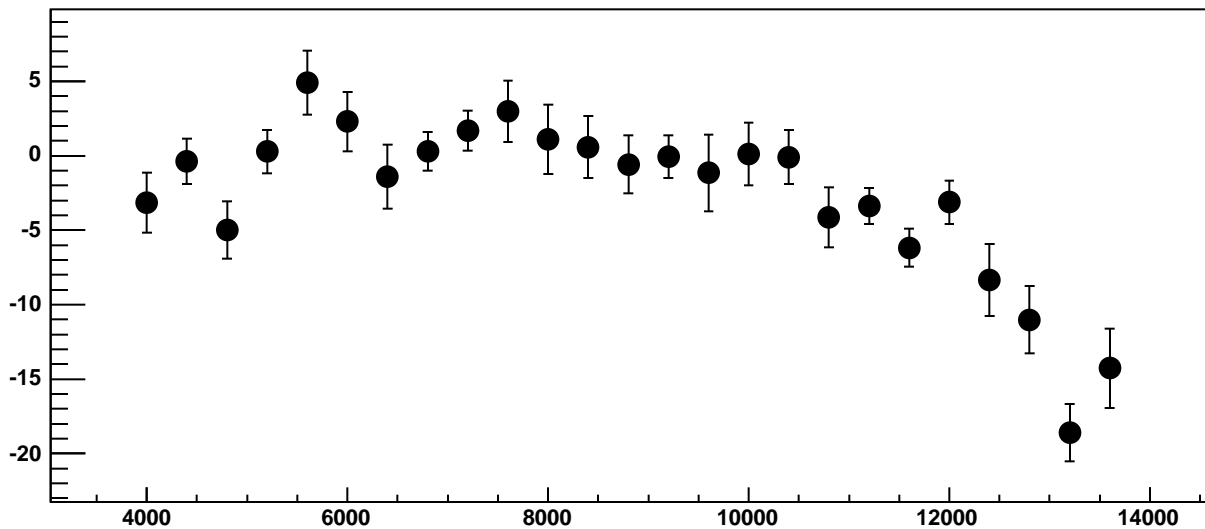
p1

-0.05054 ± 0.0002101

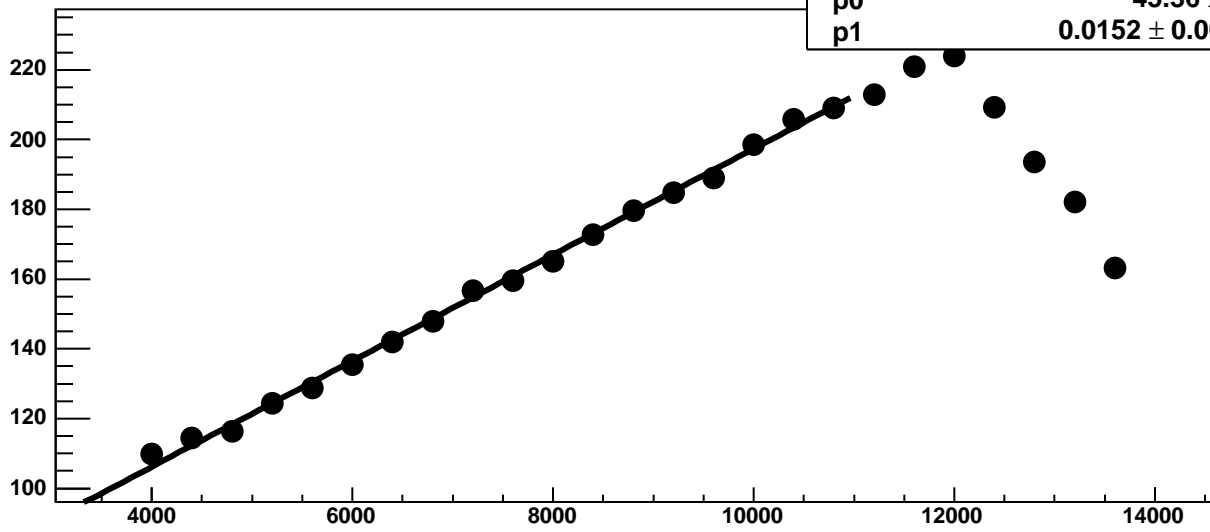
Chip 1, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



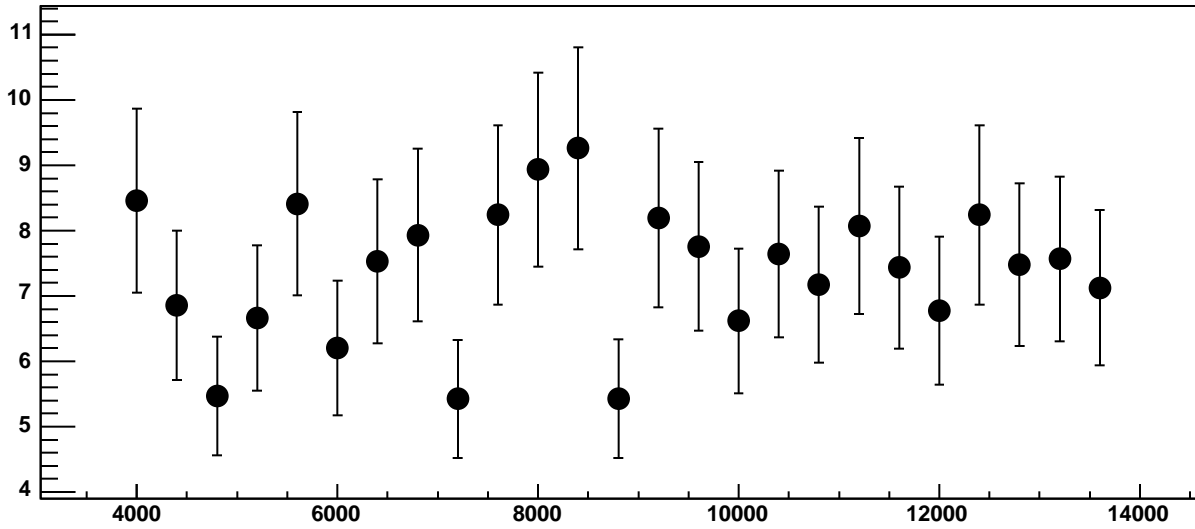
Chip 1, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC



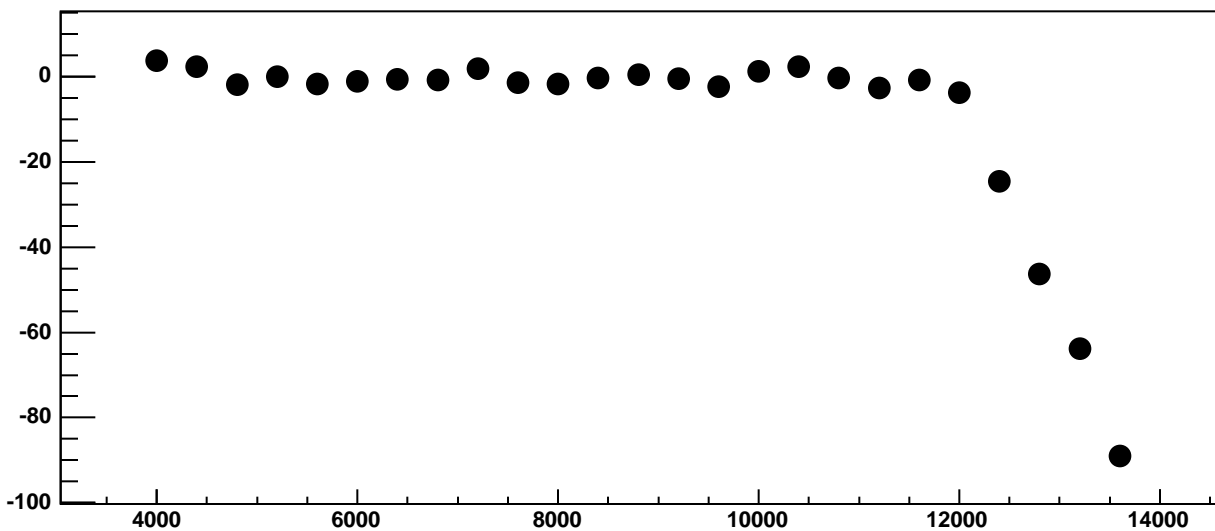
Chip 1, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



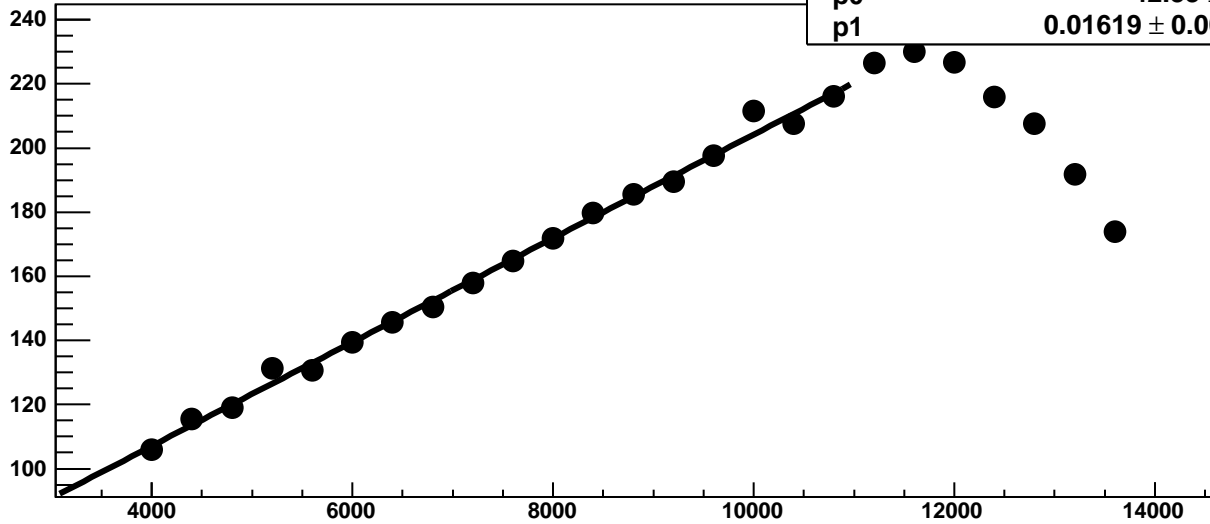
Chip 1, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



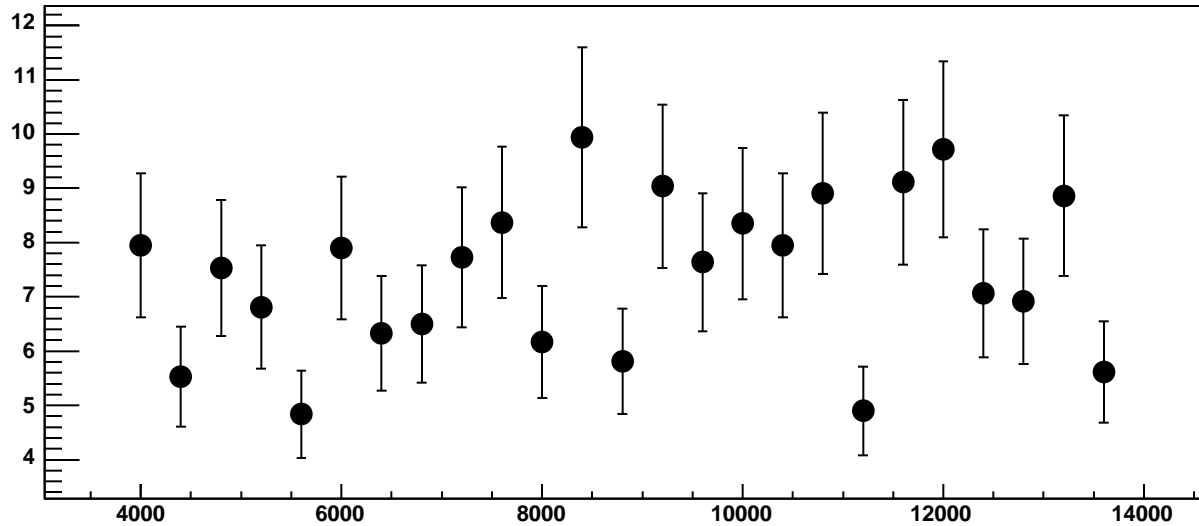
Chip 1, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



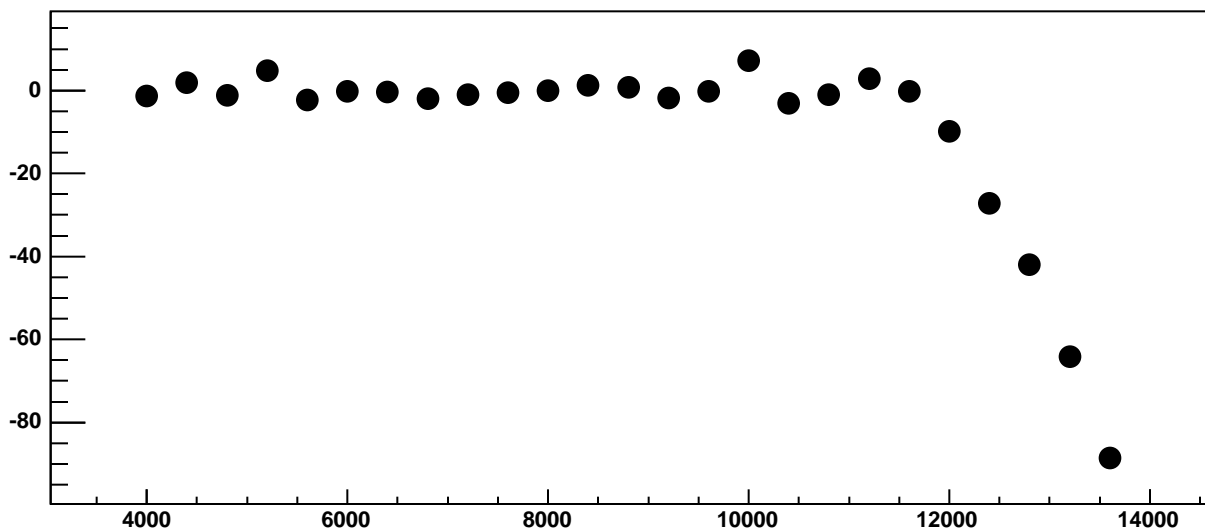
Chip 1, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



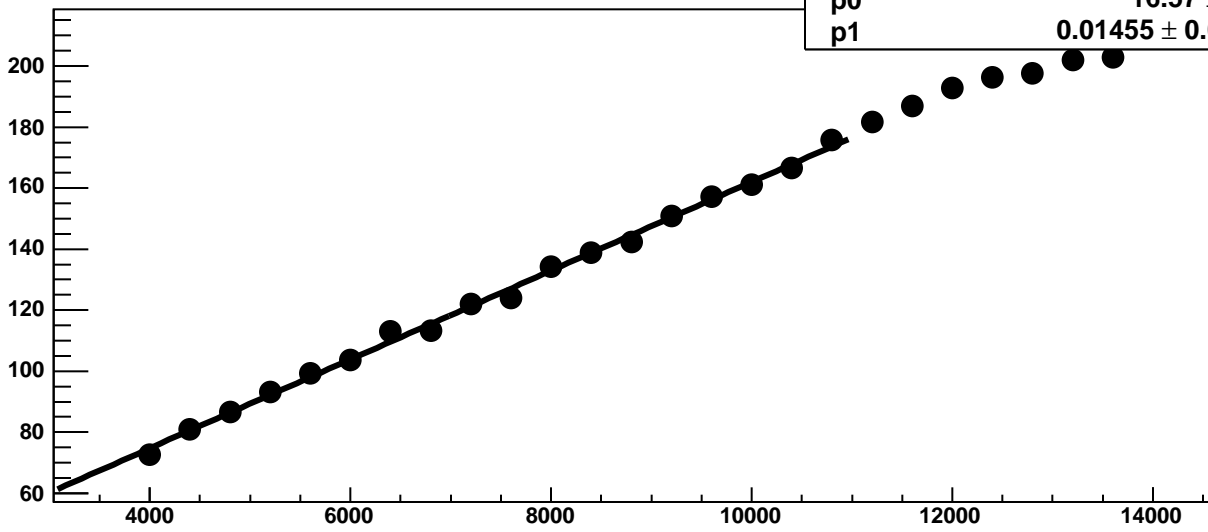
Chip 1, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



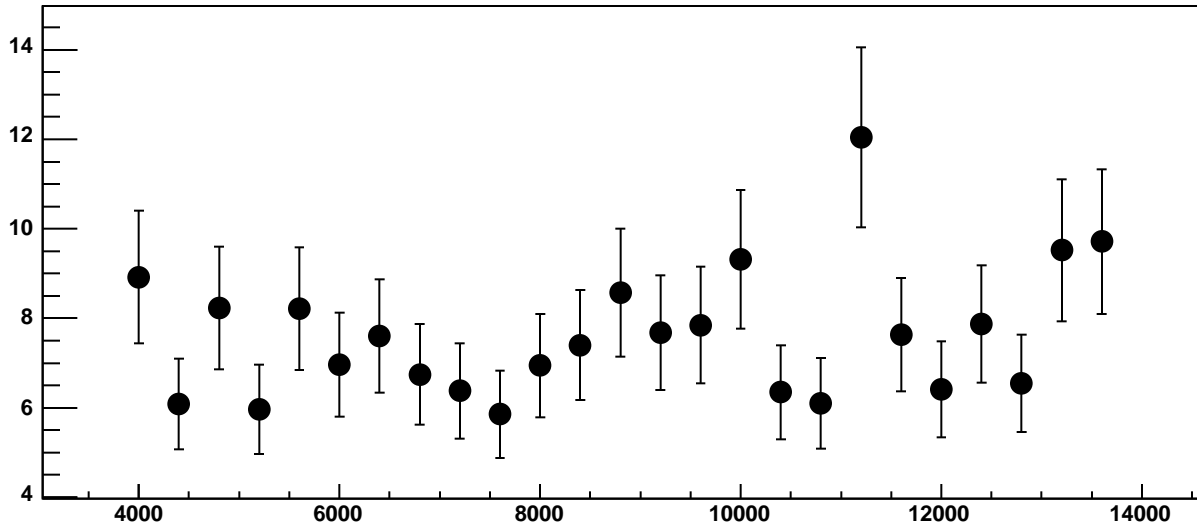
Chip 1, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



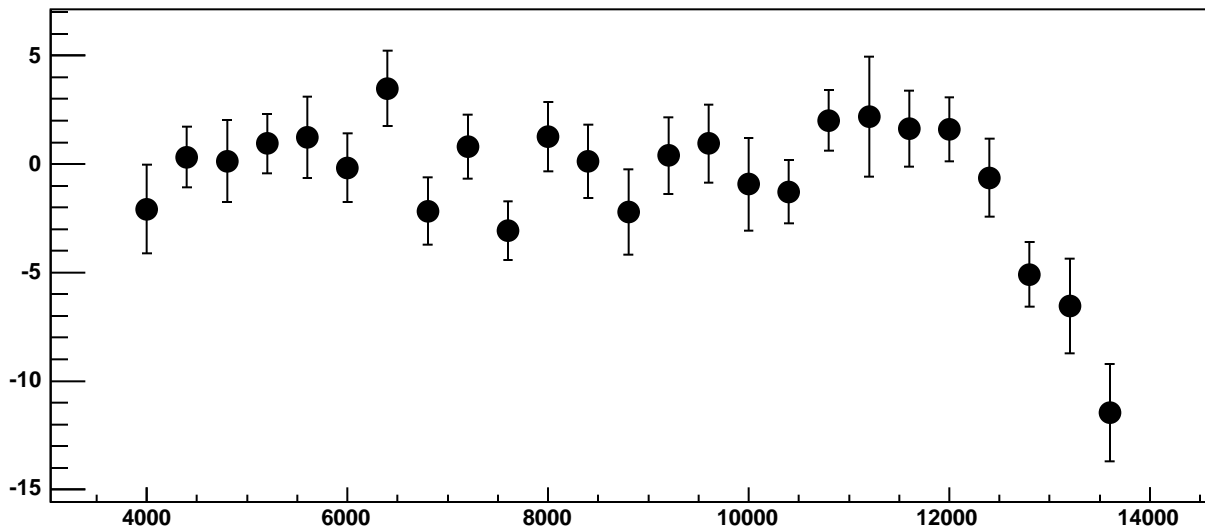
Chip 1, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



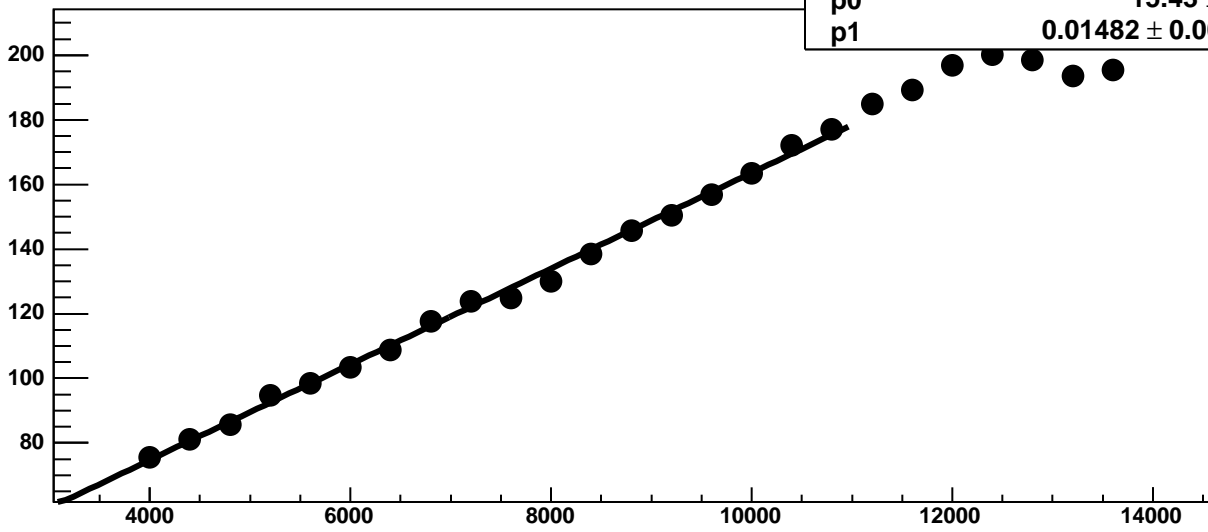
Chip 1, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

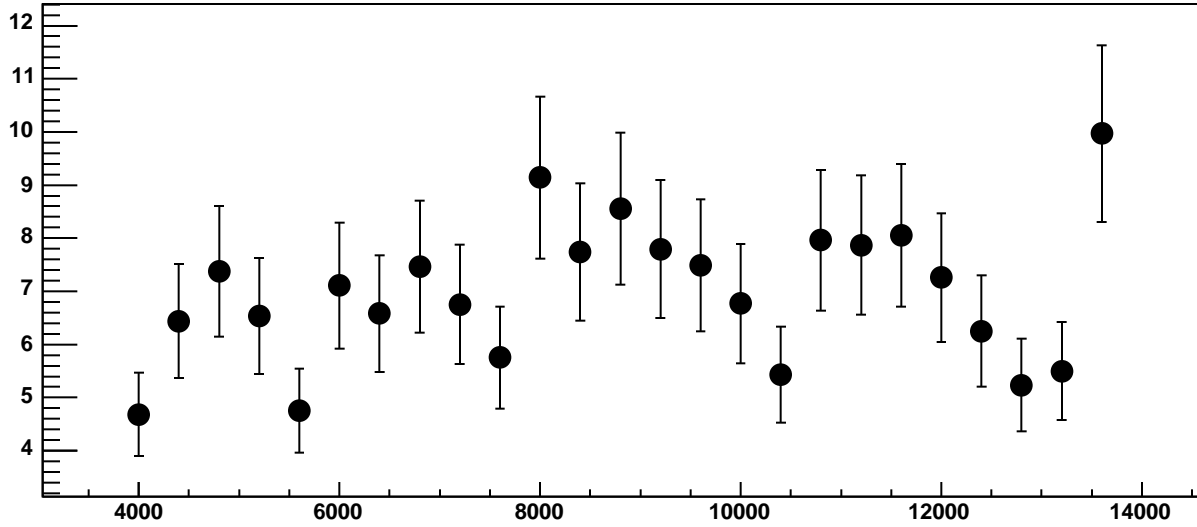


Chip 1, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

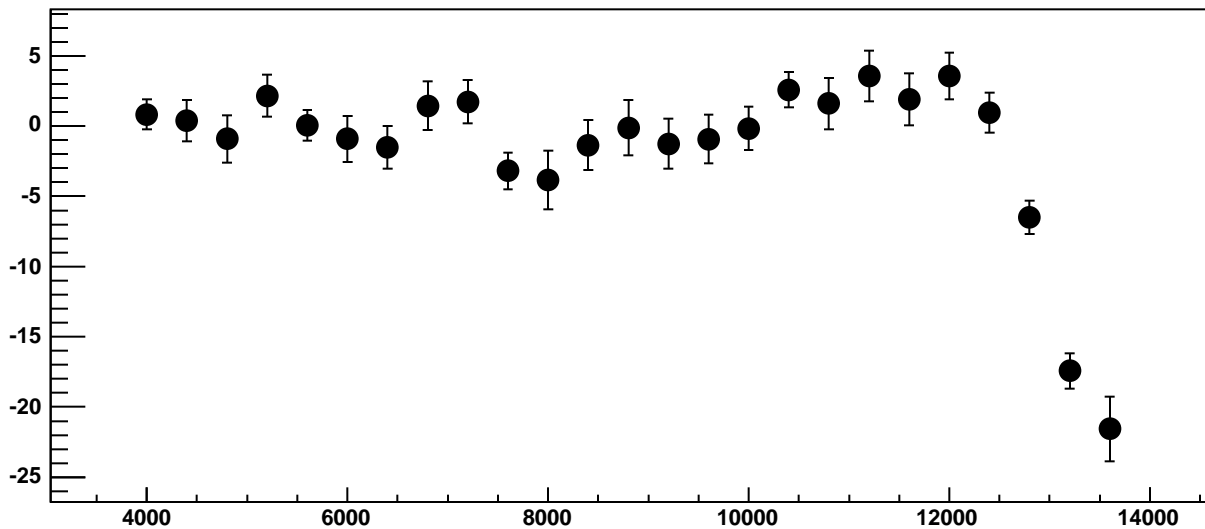


χ^2 / ndf 22.02 / 16
p0 15.43 ± 1.207
p1 0.01482 ± 0.0001635

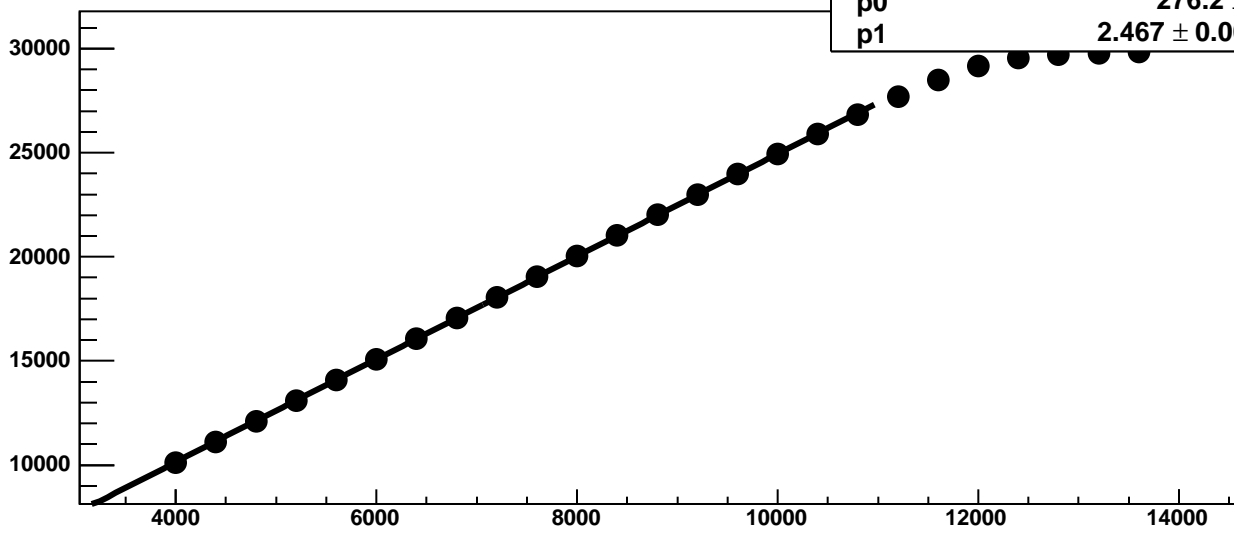
Chip 1, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



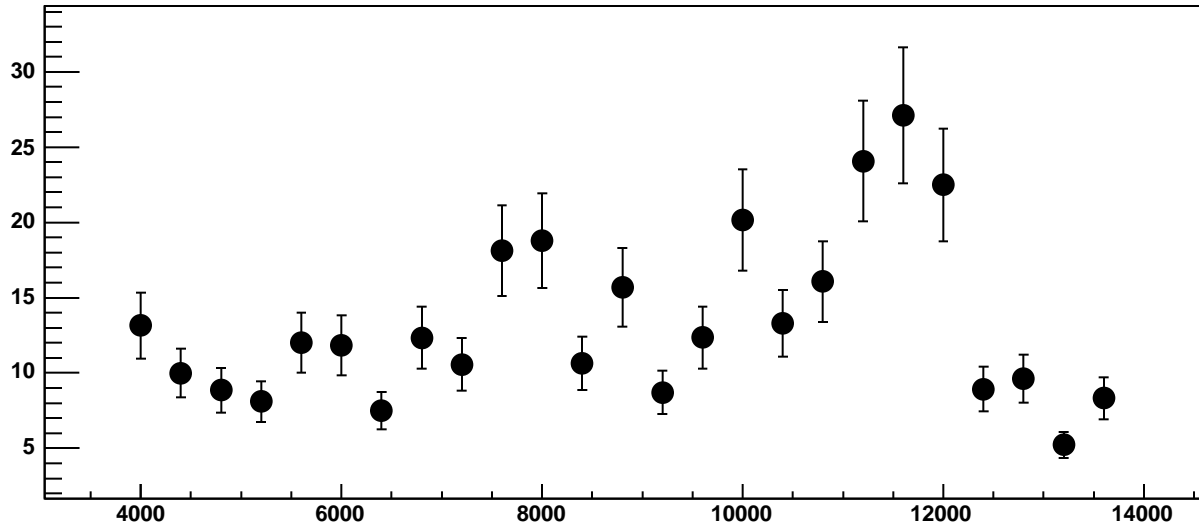
Chip 1, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



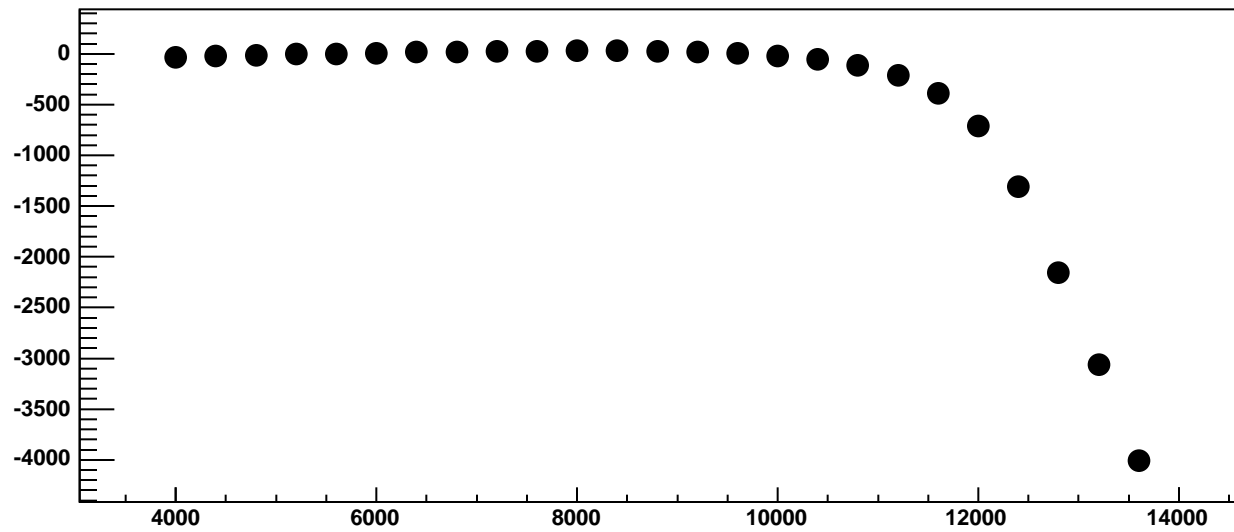
Chip 1, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC



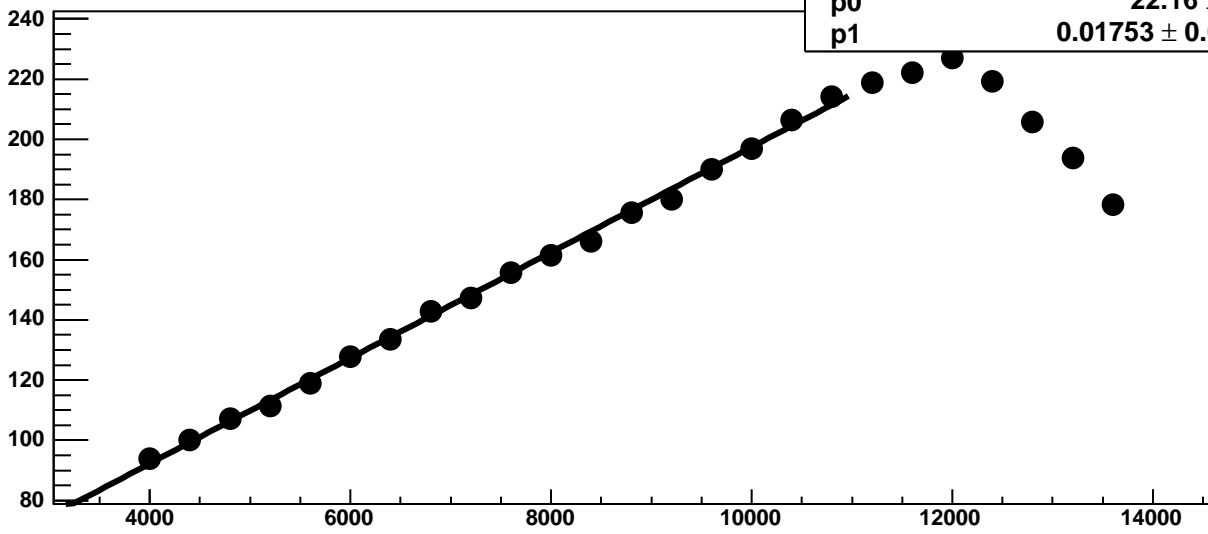
Chip 1, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC



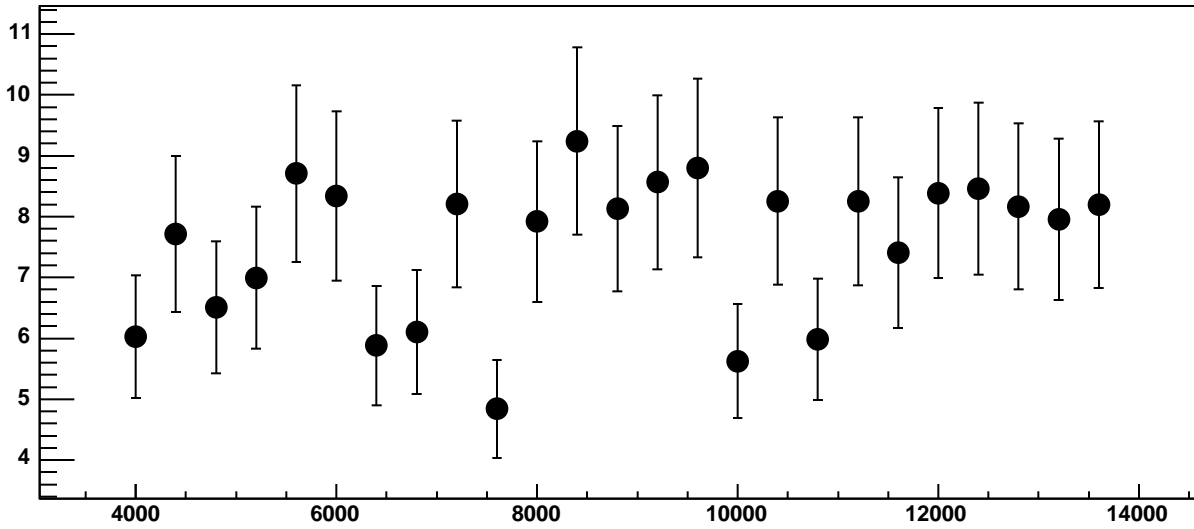
Chip 1, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC



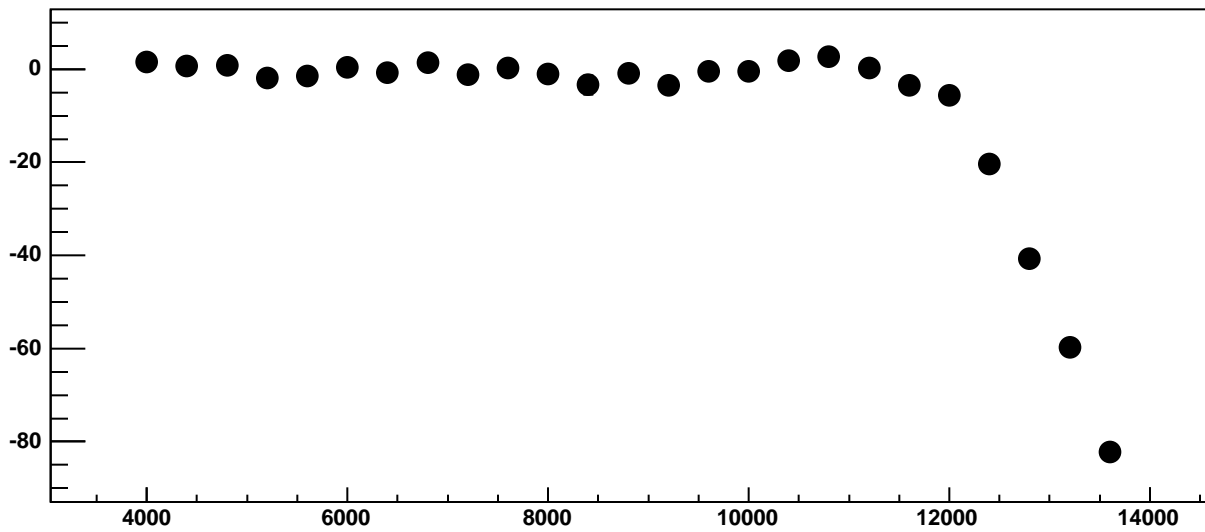
Chip 1, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



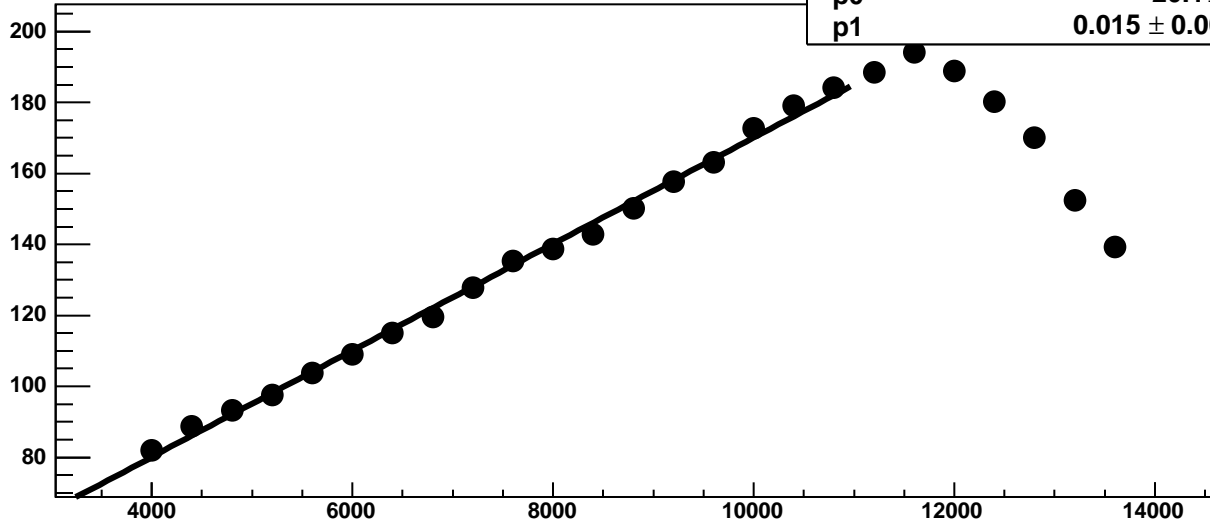
Chip 1, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



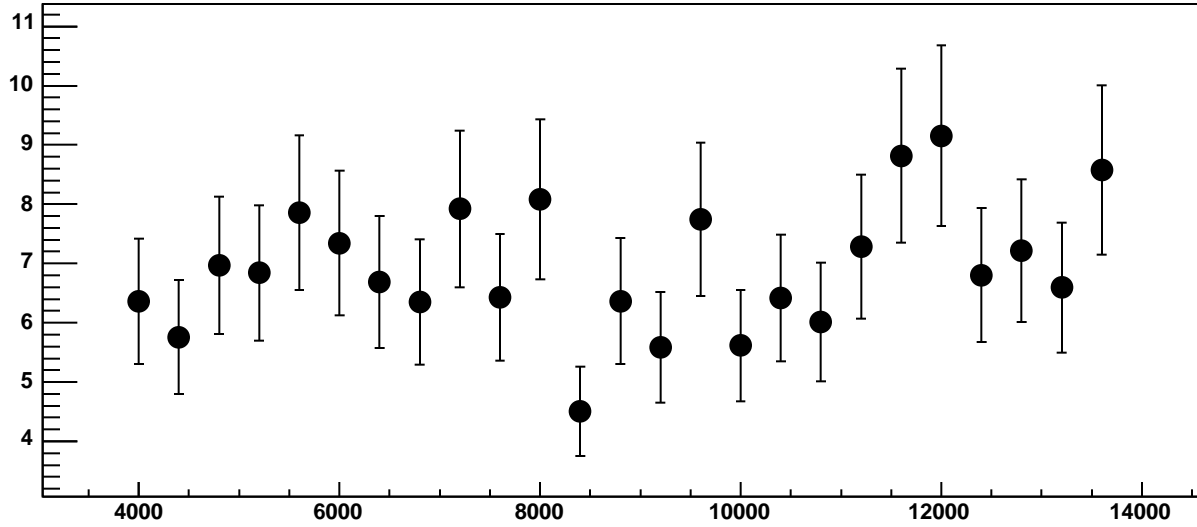
Chip 1, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



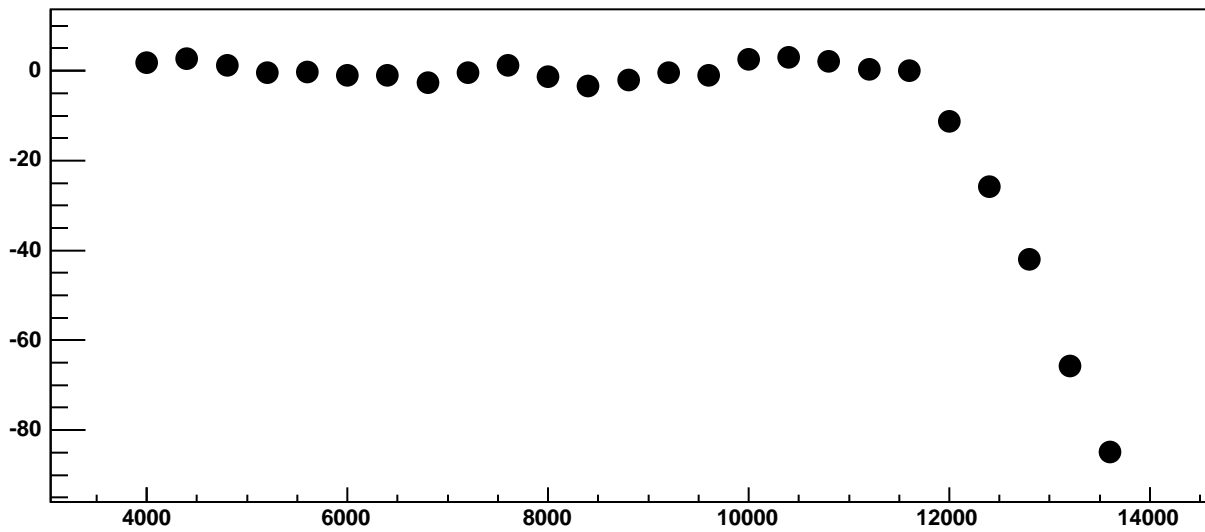
Chip 1, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



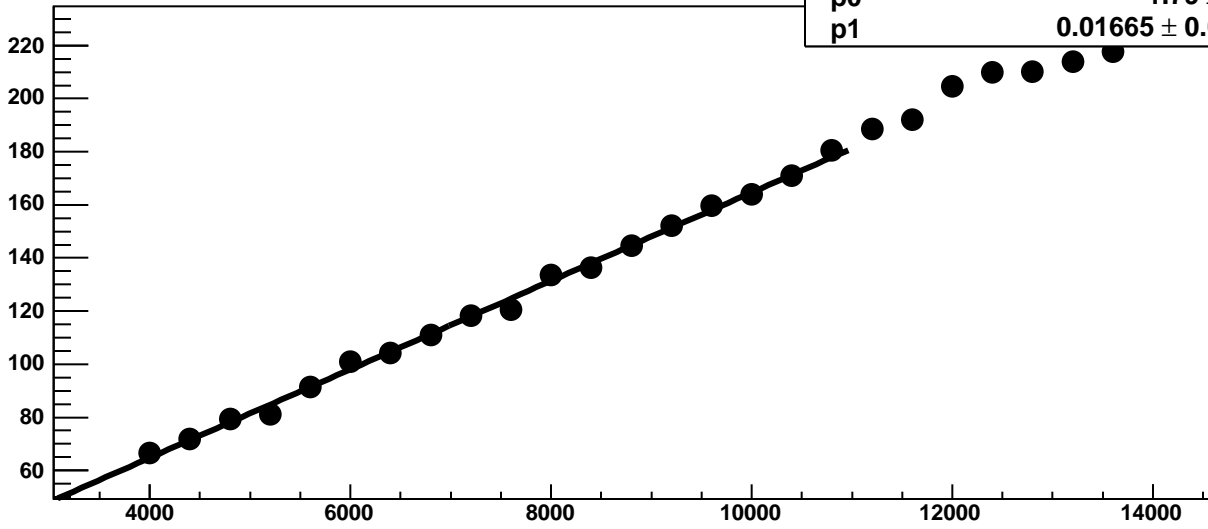
Chip 1, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



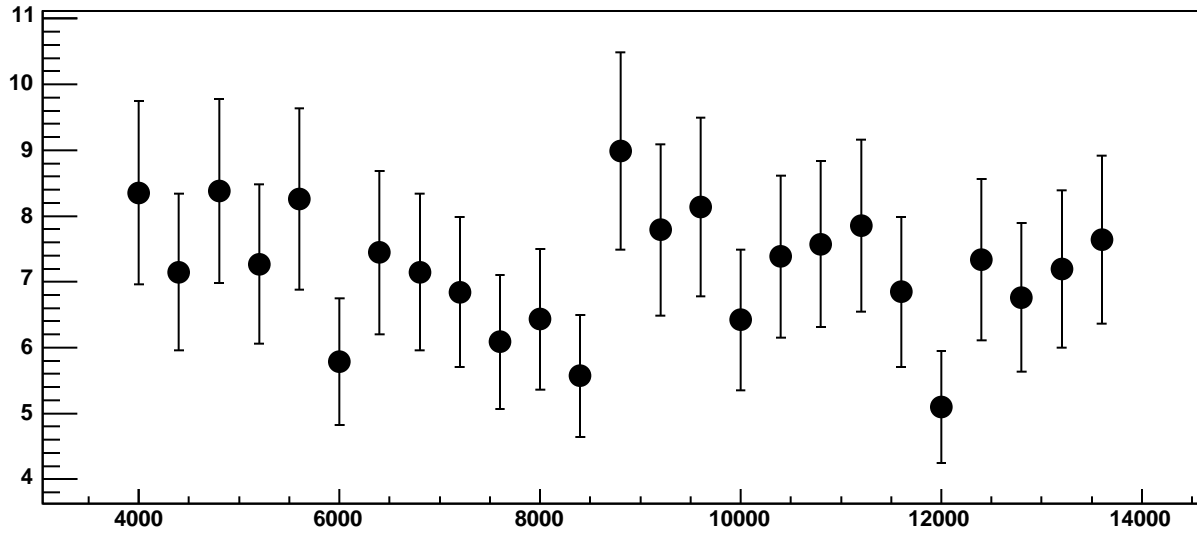
Chip 1, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



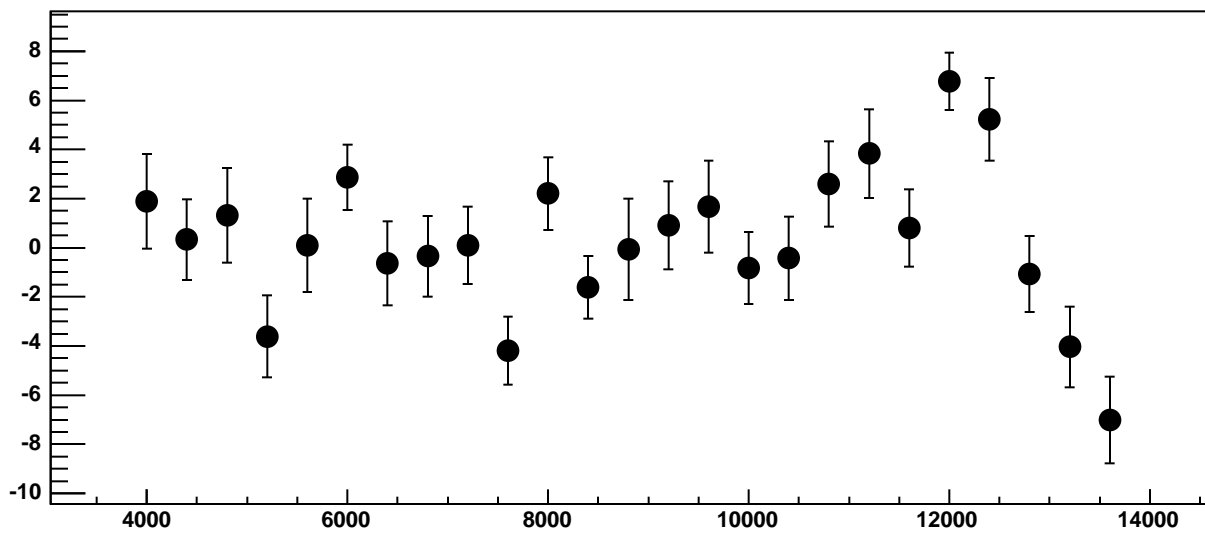
Chip 1, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



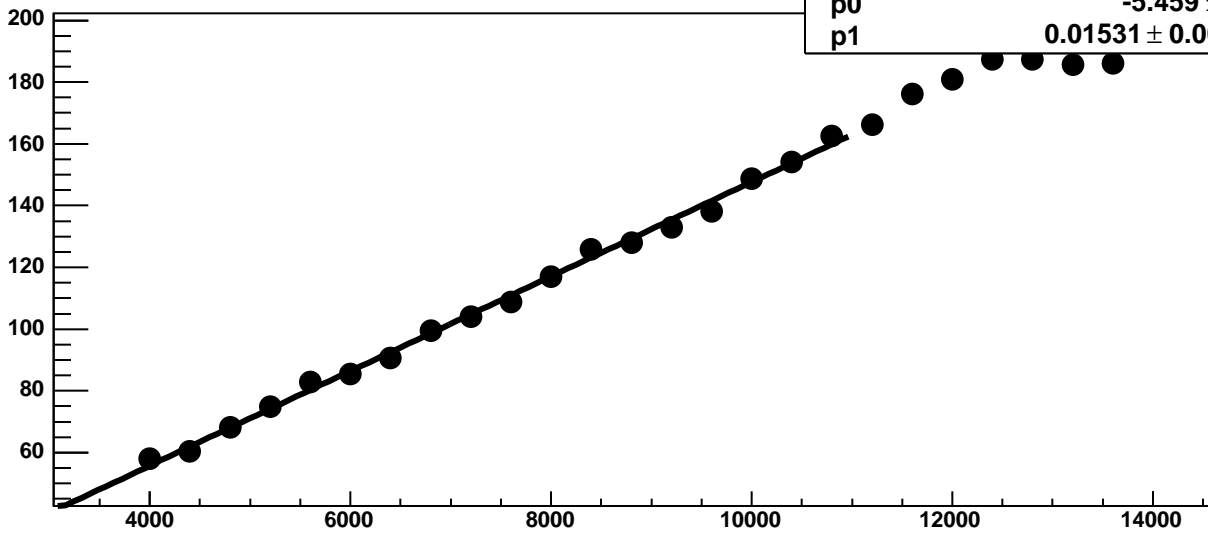
Chip 1, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



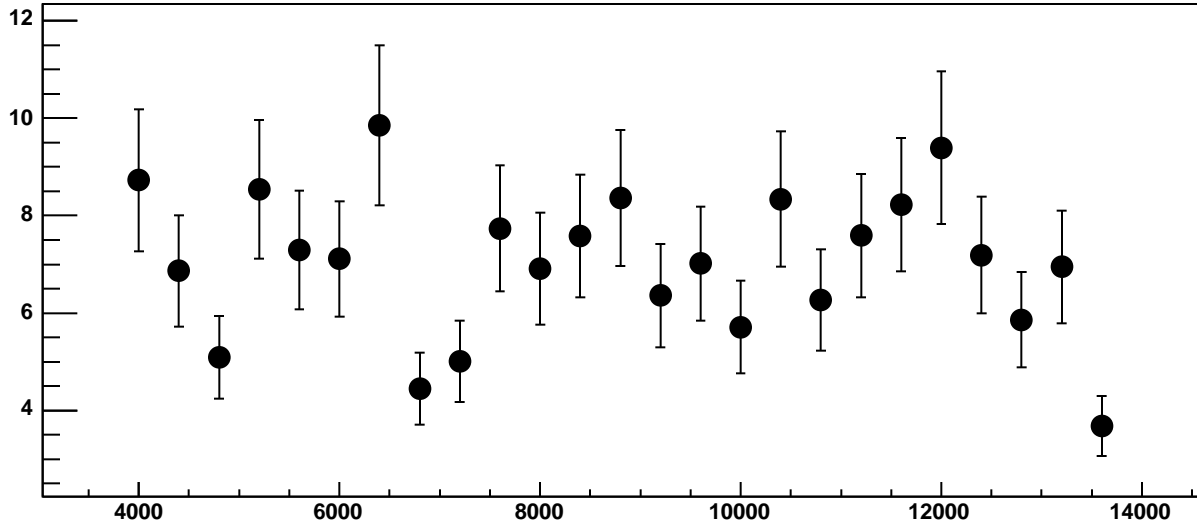
Chip 1, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



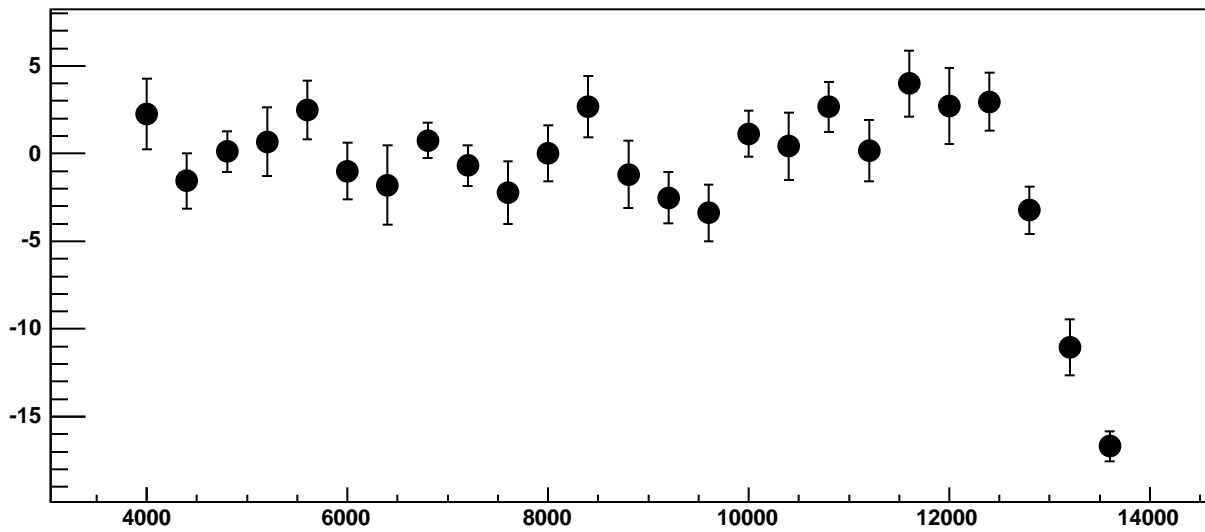
Chip 1, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



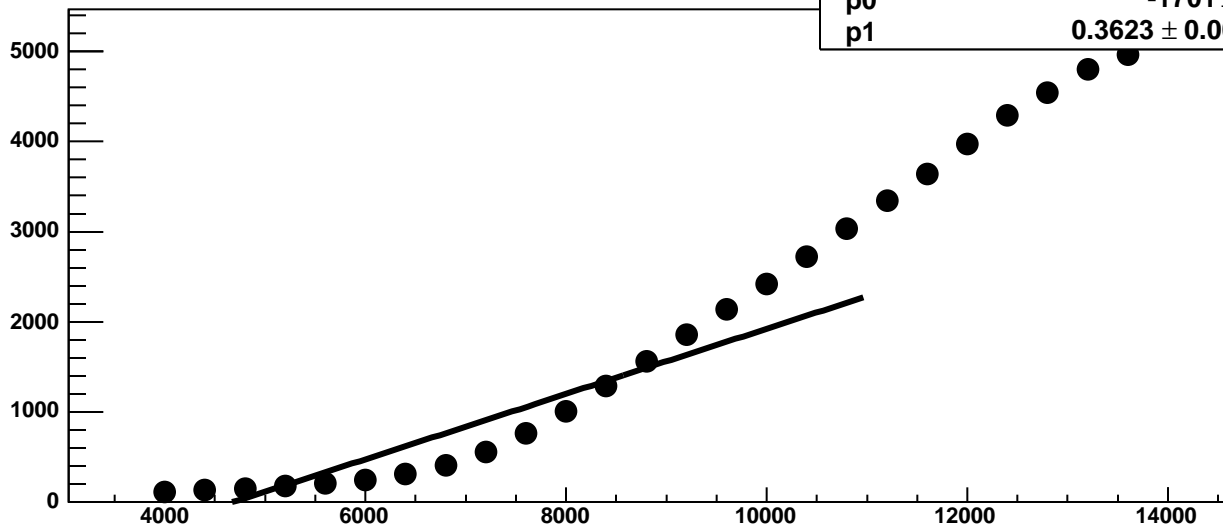
Chip 1, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

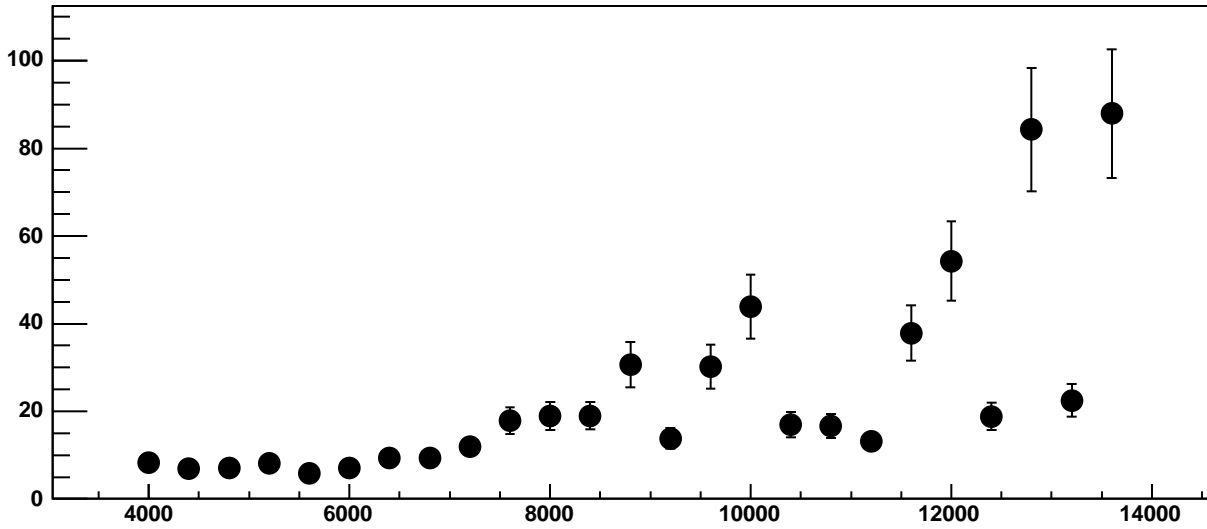


Chip 1, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC

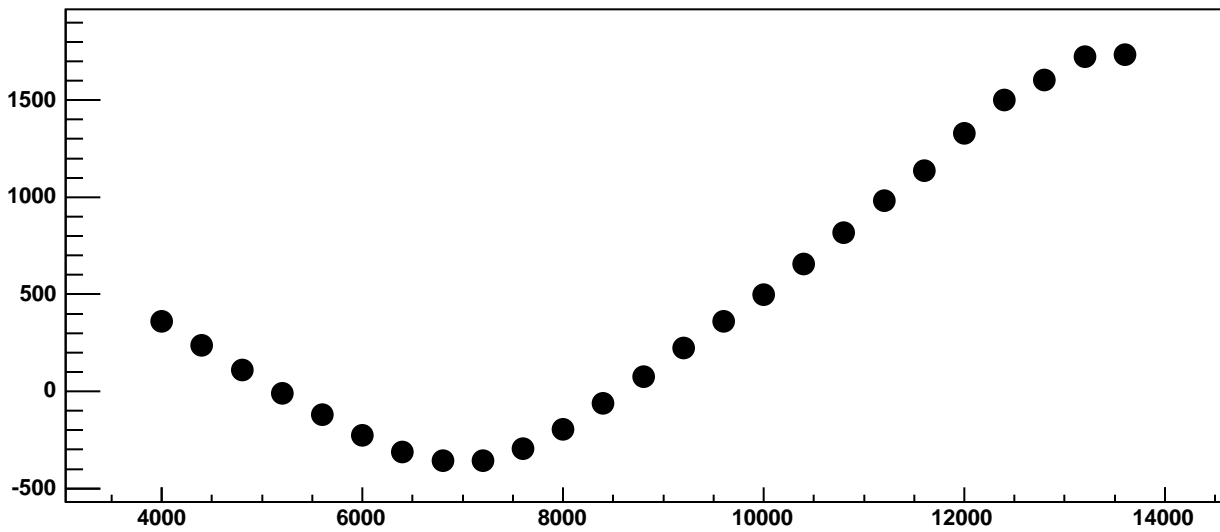


χ^2 / ndf 2.484e+05 / 16
p0 -1701 ± 2.106
p1 0.3623 ± 0.0003437

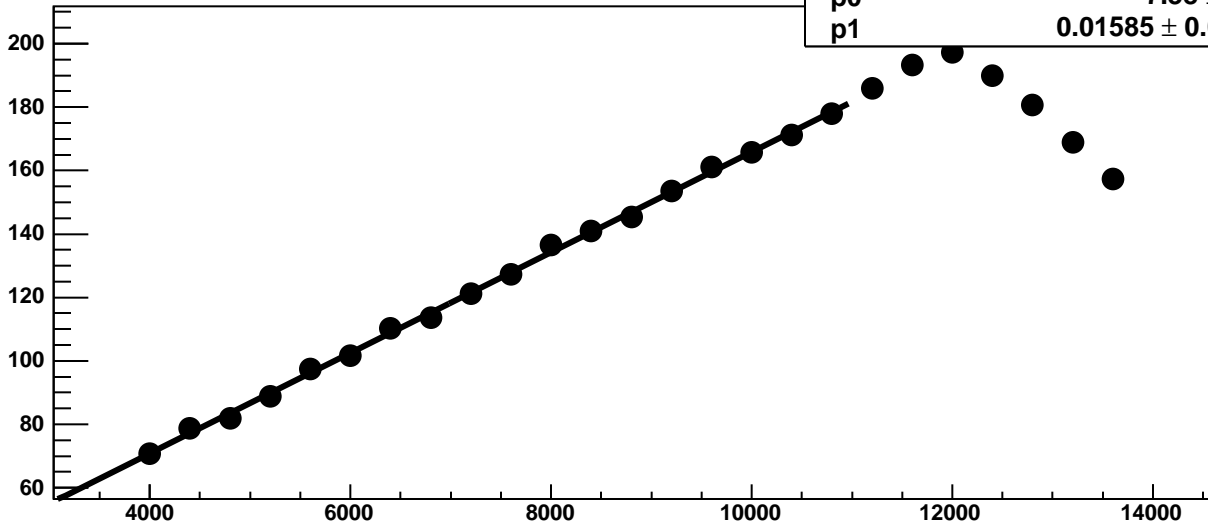
Chip 1, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



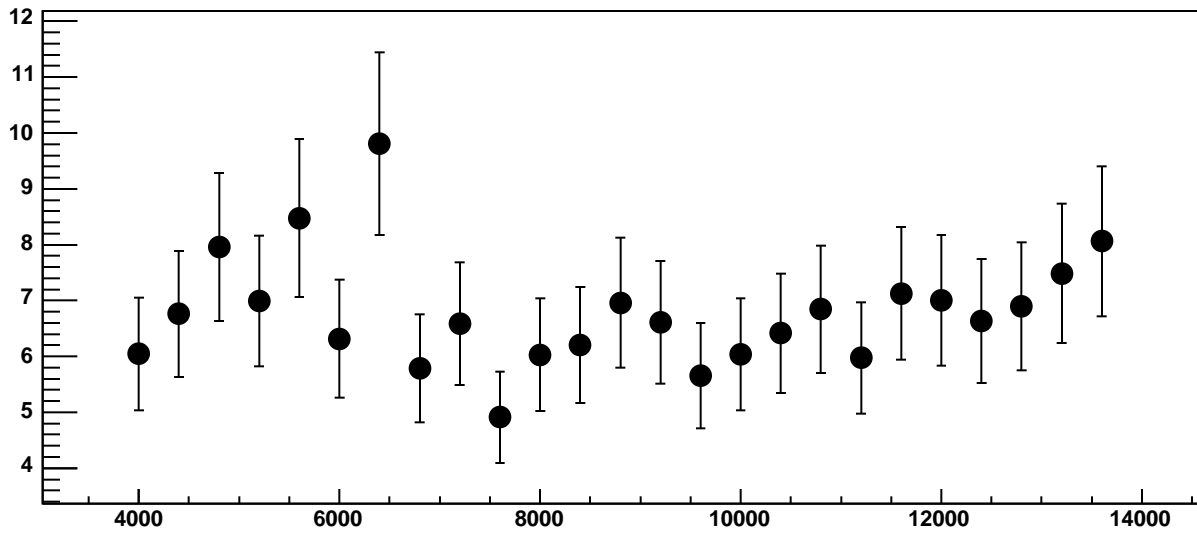
Chip 1, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC



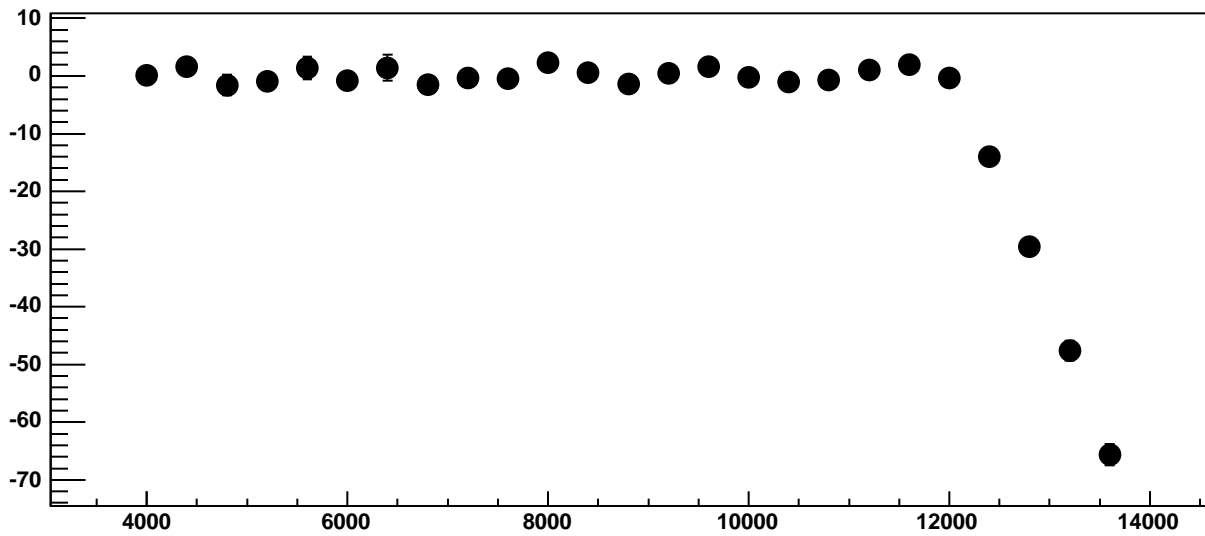
Chip 1, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



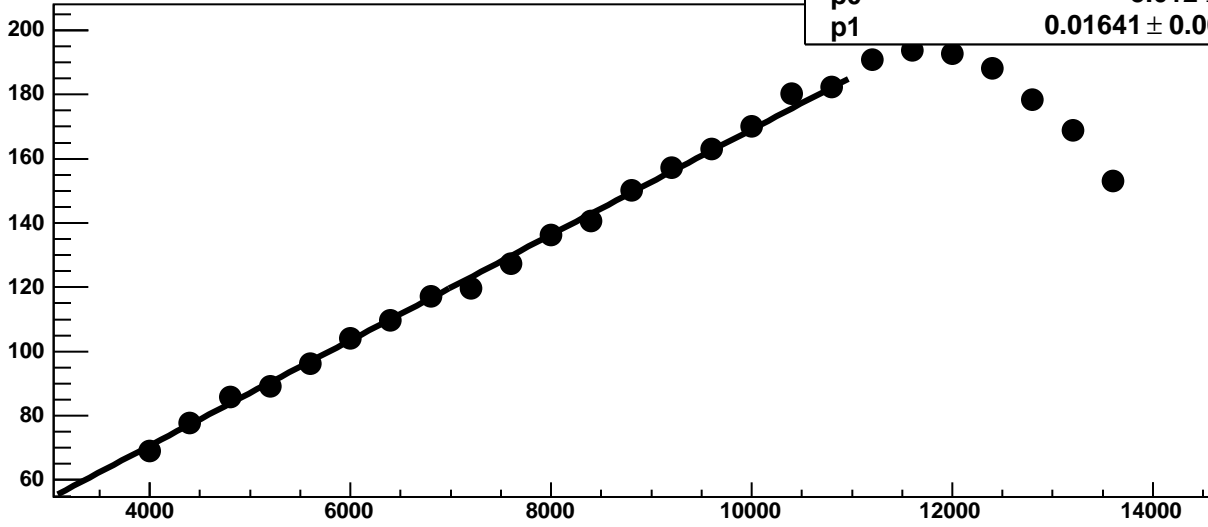
Chip 1, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



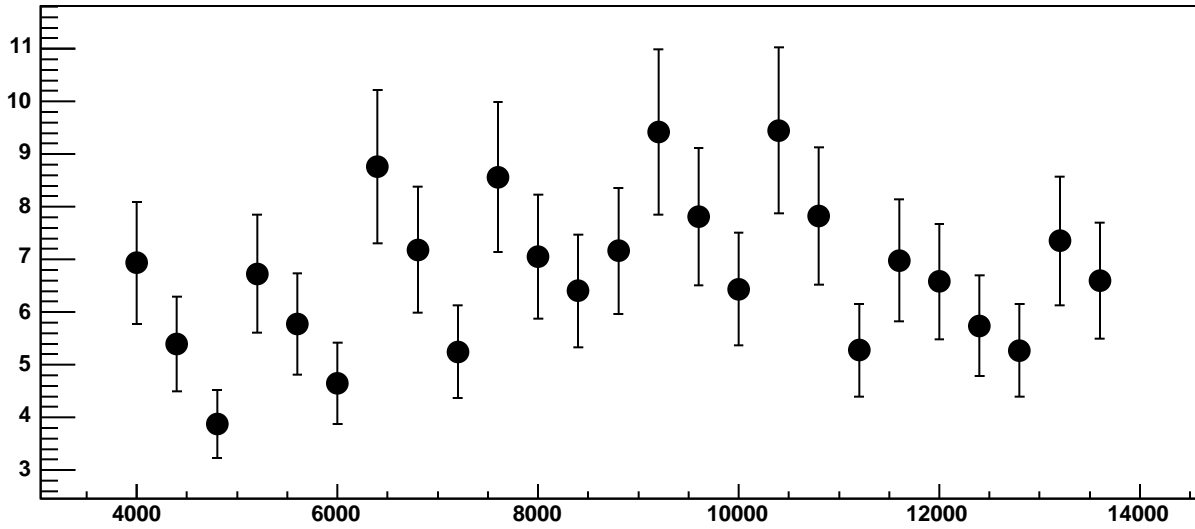
Chip 1, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



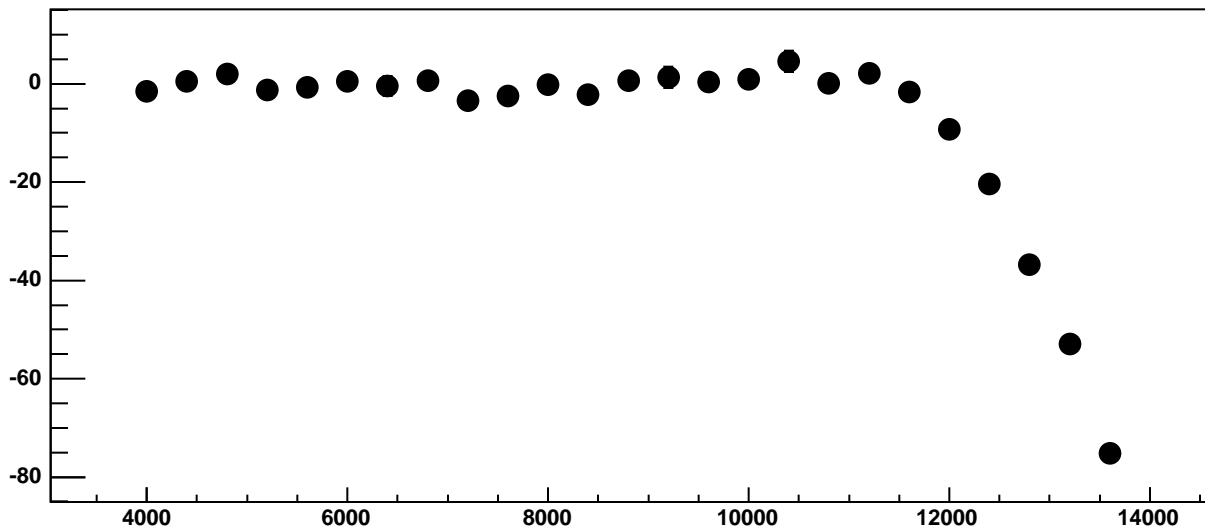
Chip 1, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



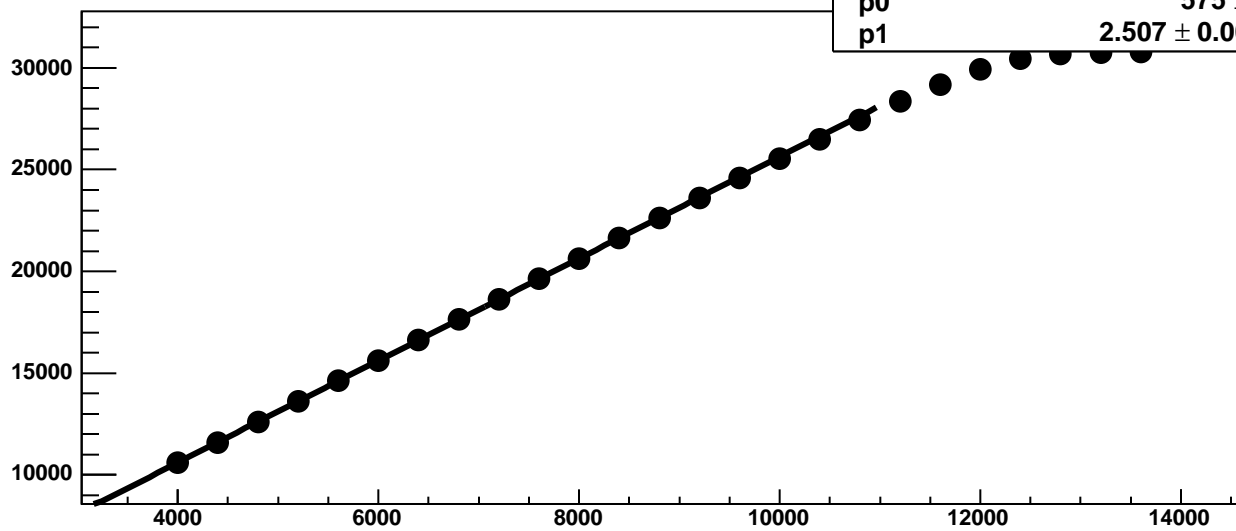
Chip 1, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



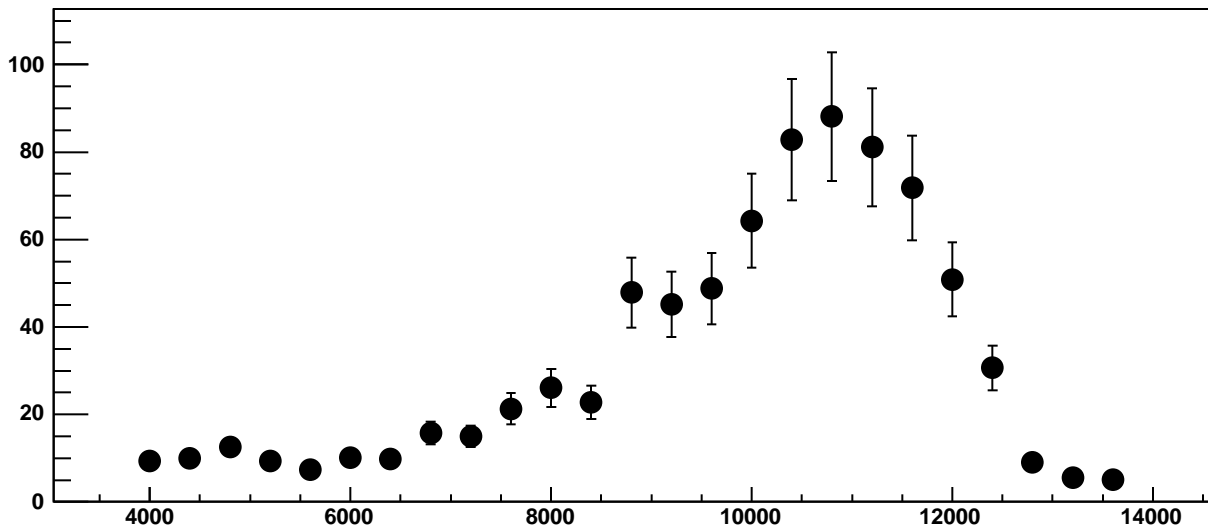
Chip 1, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



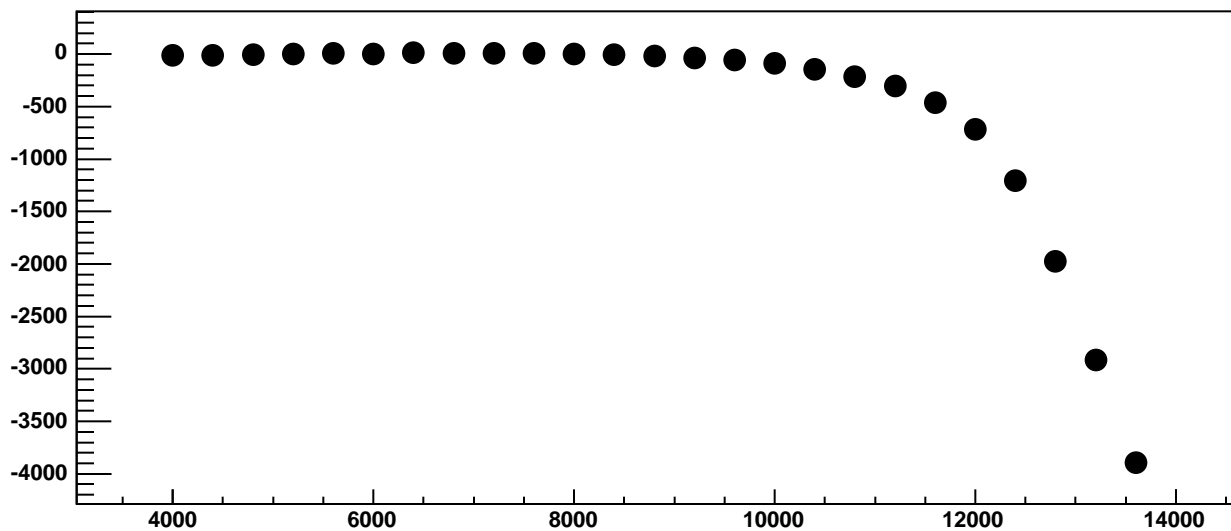
Chip 2, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



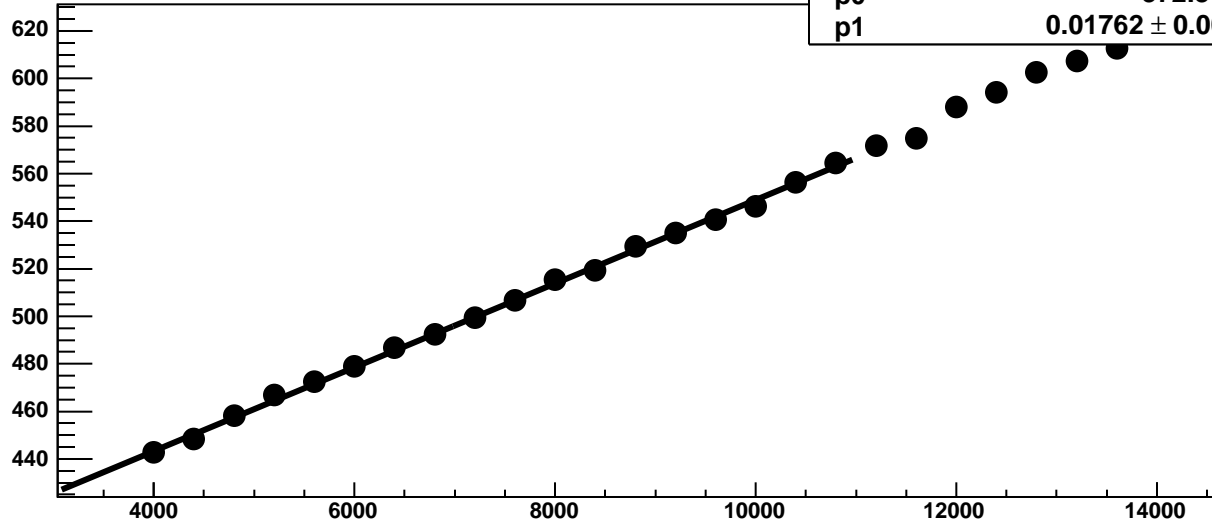
Chip 2, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

15.28 / 16

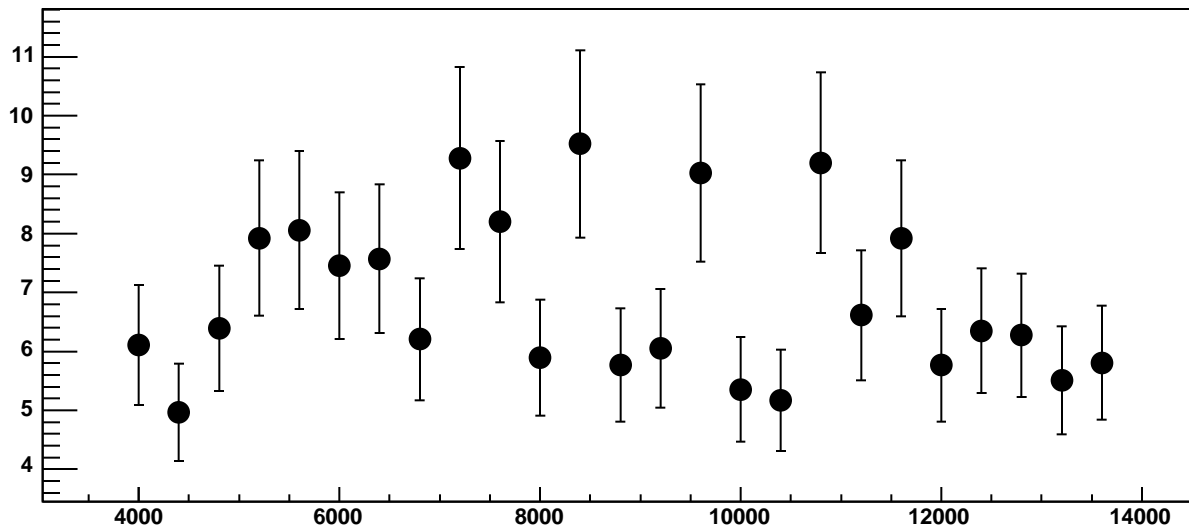
p0

372.8 ± 1.26

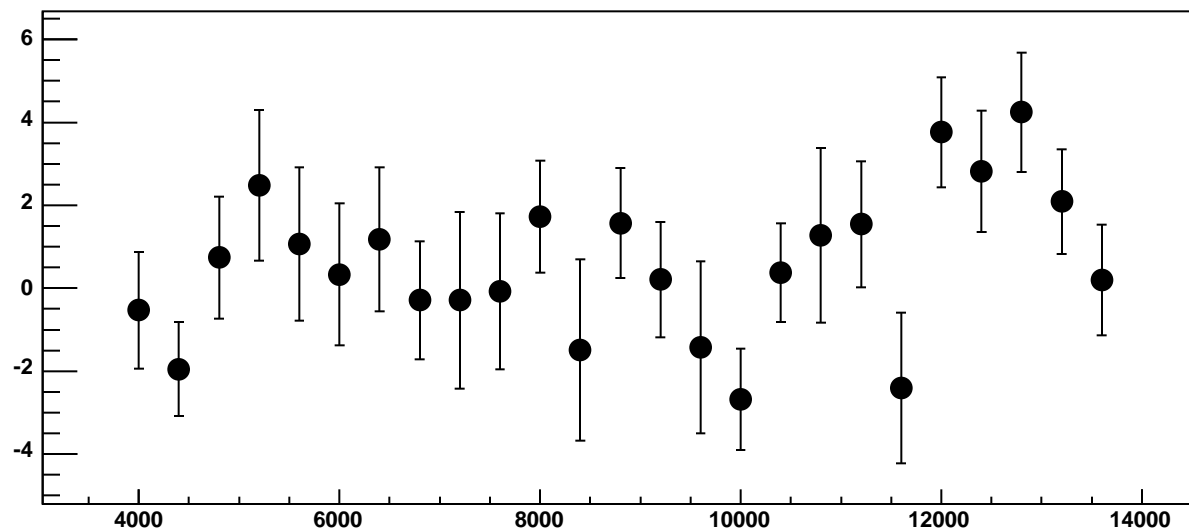
p1

0.01762 ± 0.0001636

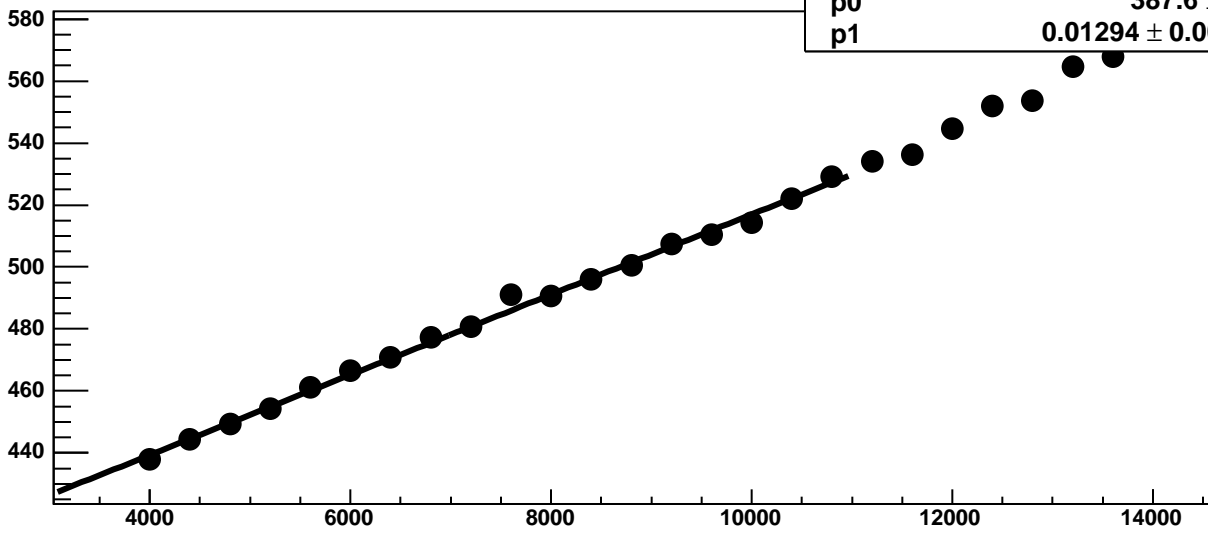
Chip 2, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



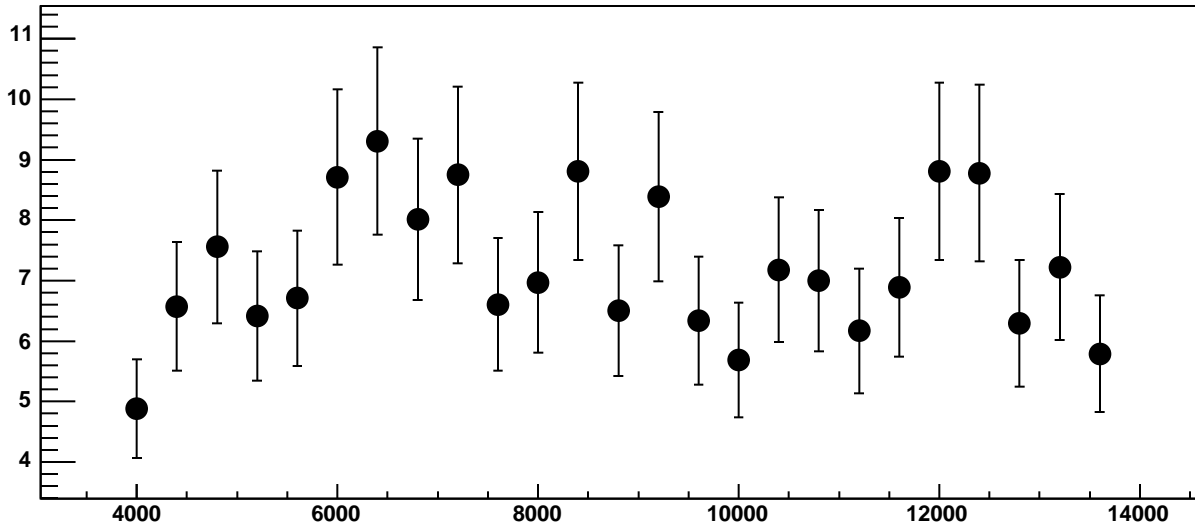
Chip 2, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



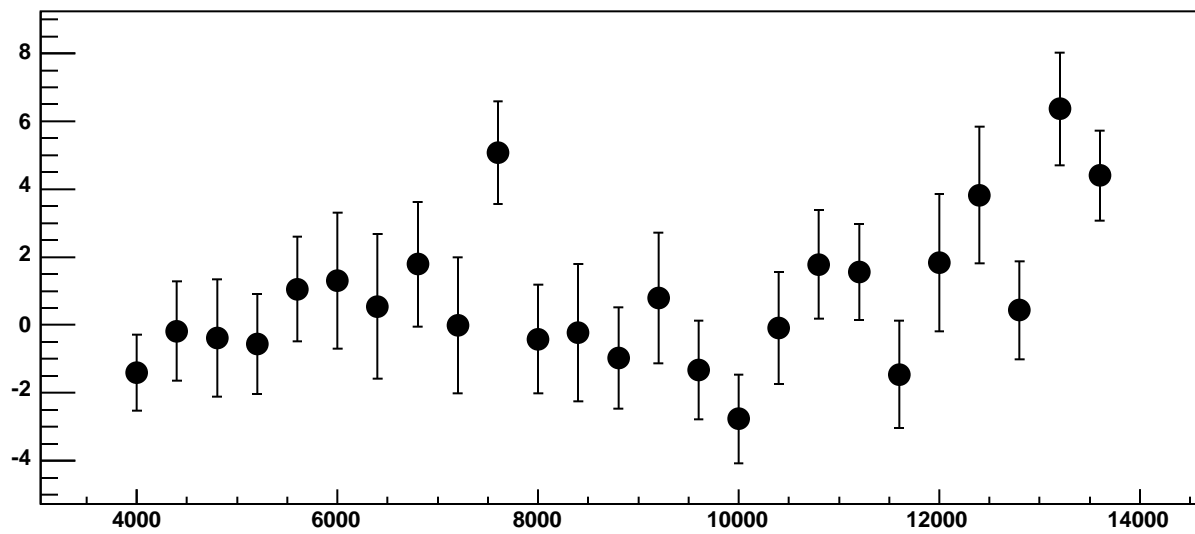
Chip 2, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



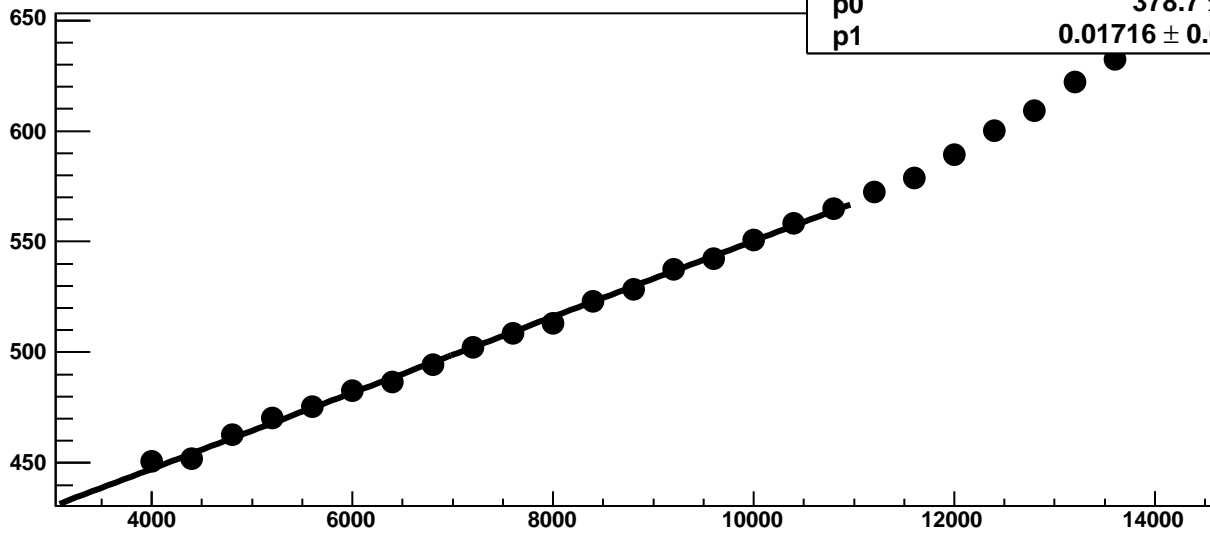
Chip 2, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

20.9 / 16

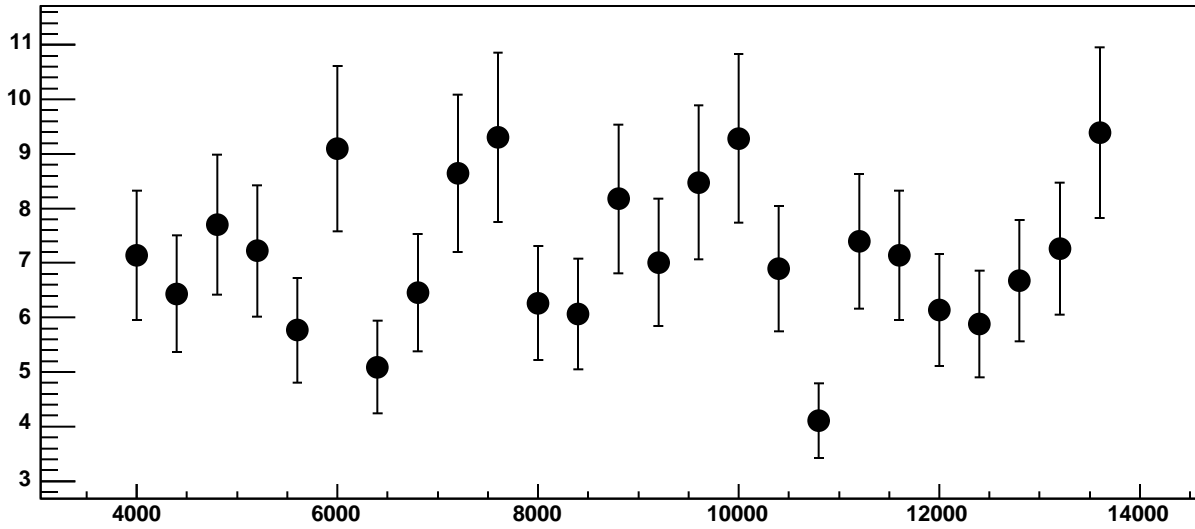
p0

378.7 ± 1.296

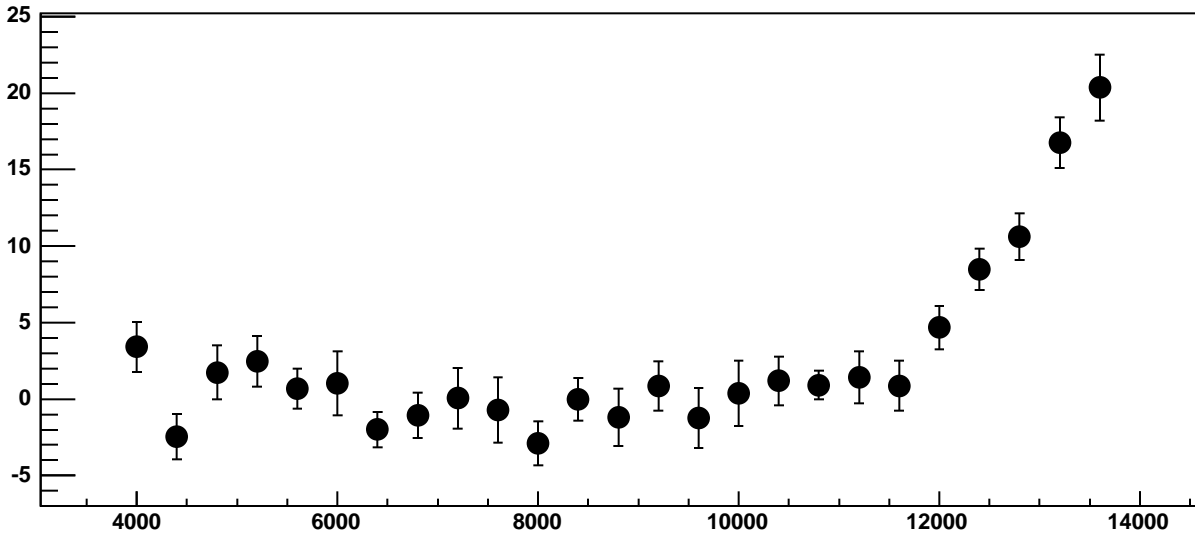
p1

0.01716 ± 0.000164

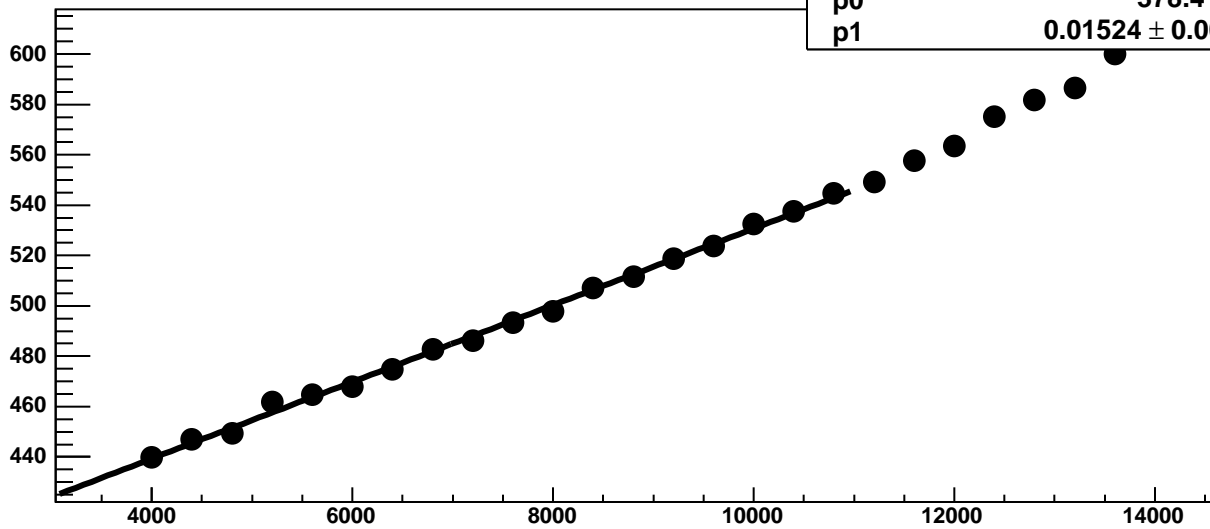
Chip 2, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

16.04 / 16

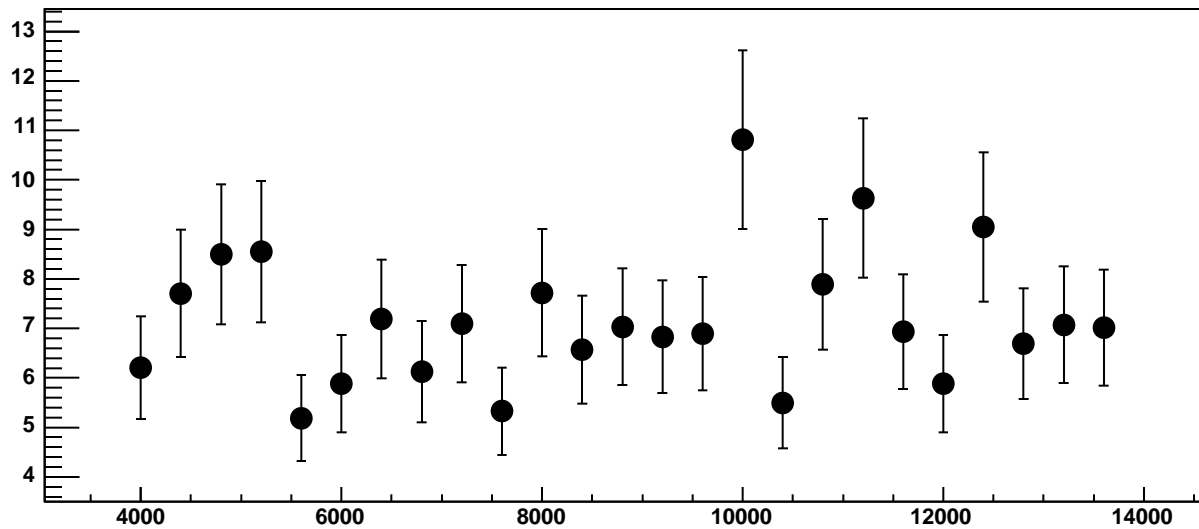
p0

378.4 ± 1.381

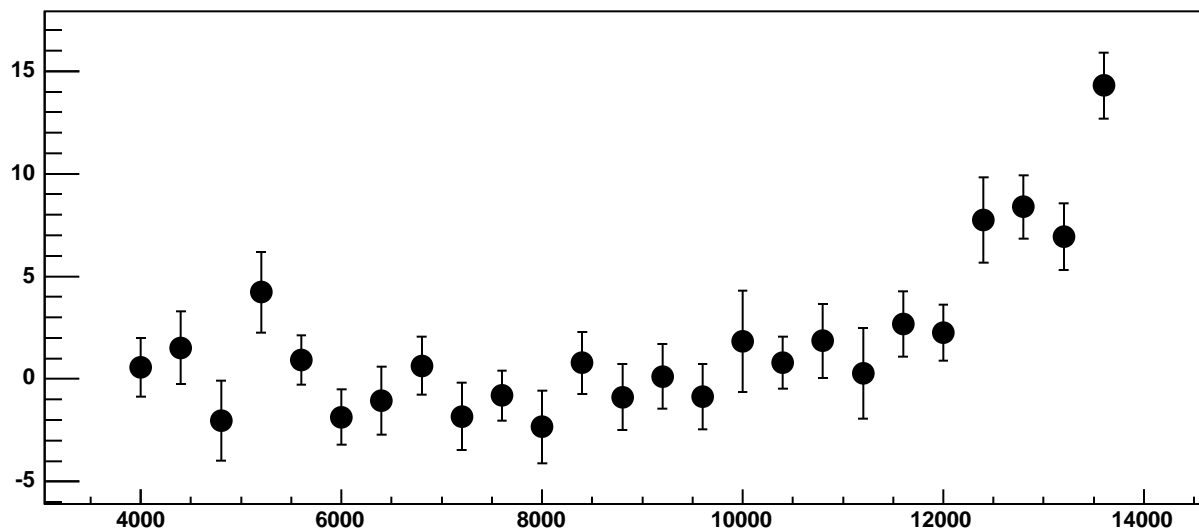
p1

0.01524 ± 0.0001814

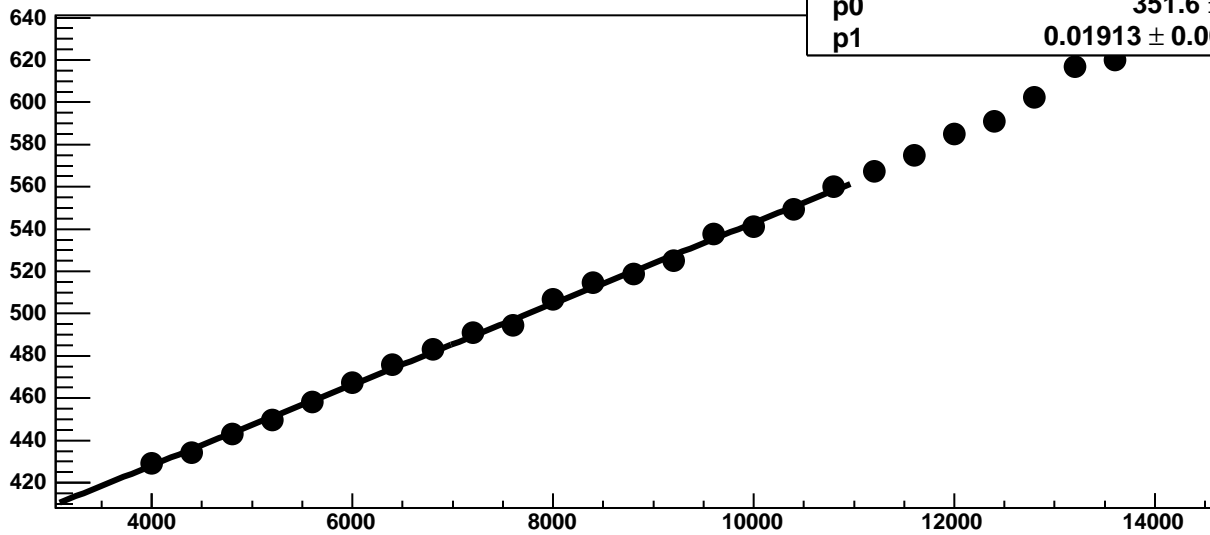
Chip 2, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

20.49 / 16

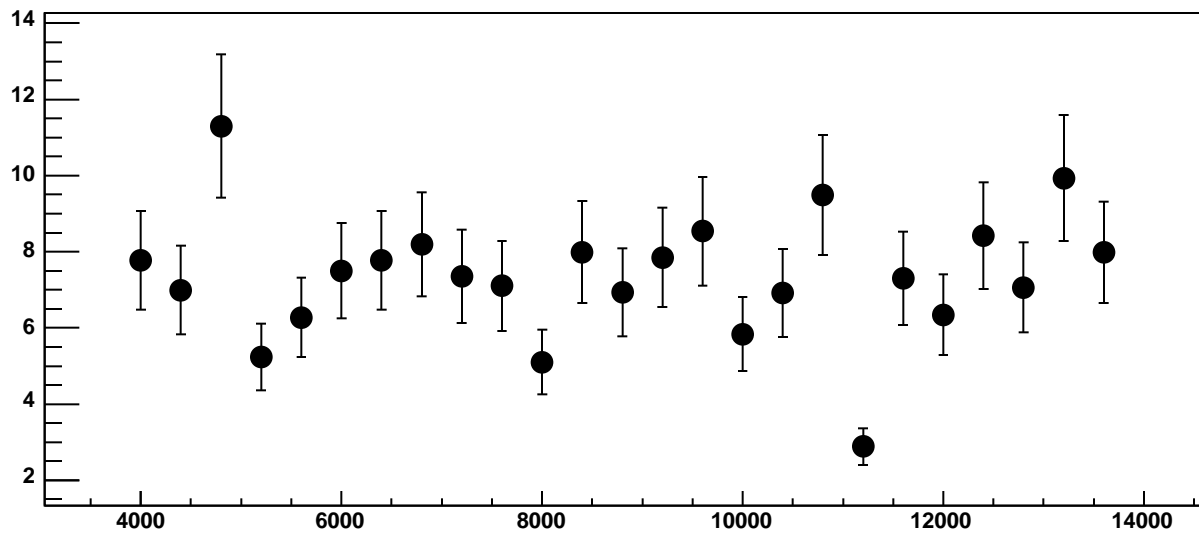
p0

351.6 ± 1.456

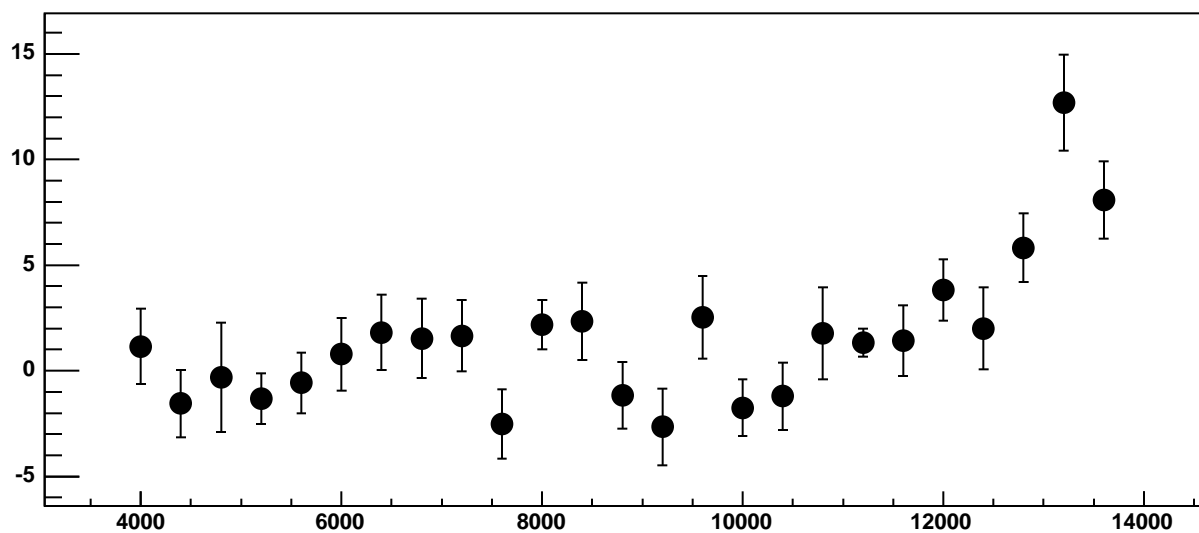
p1

0.01913 ± 0.0001906

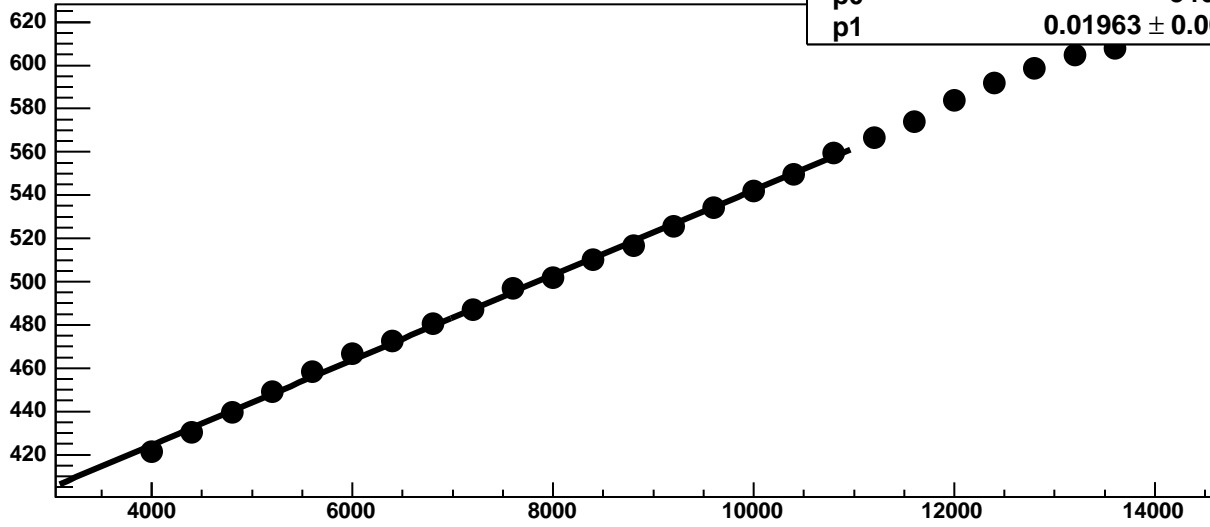
Chip 2, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC

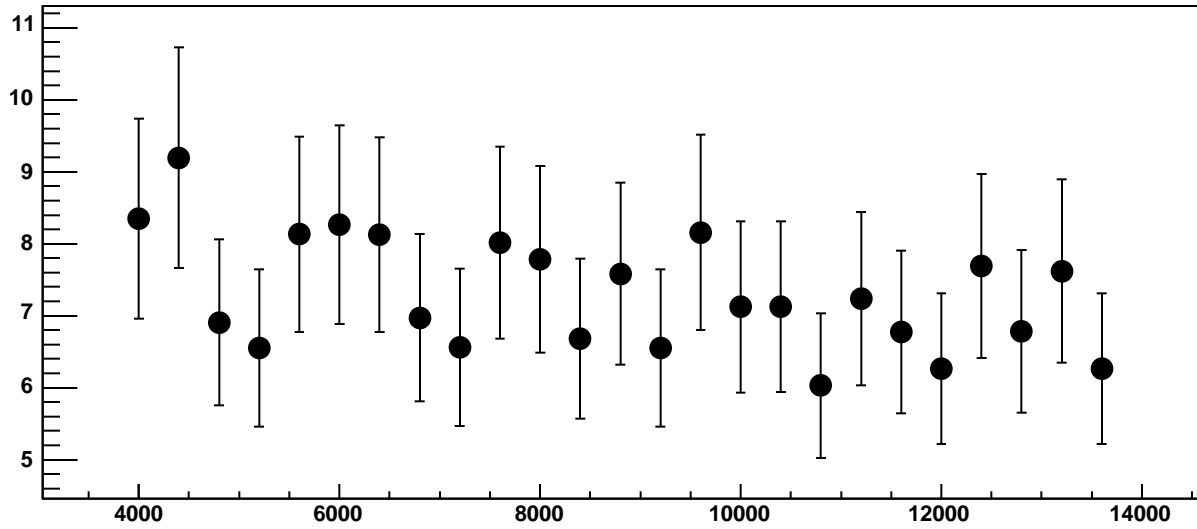


Chip 2, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

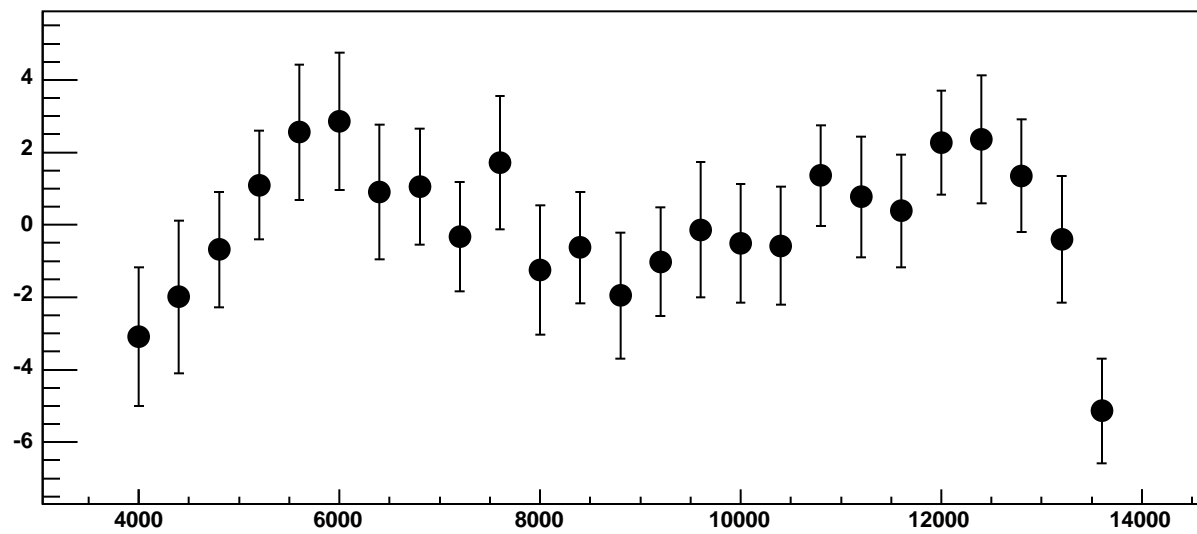


χ^2 / ndf 13.5 / 16
p0 346 ± 1.5
p1 0.01963 ± 0.0001903

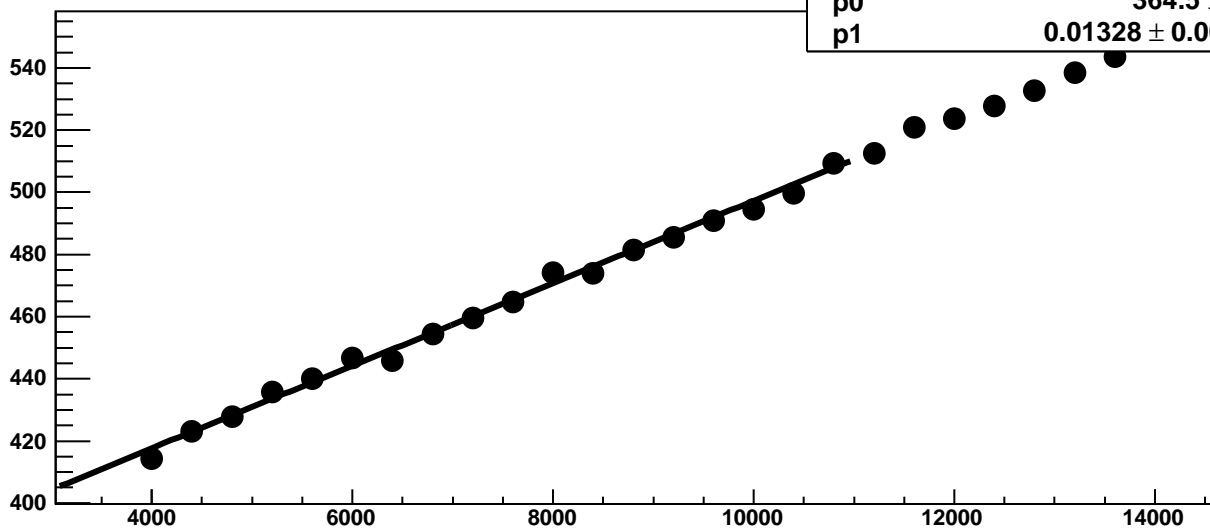
Chip 2, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

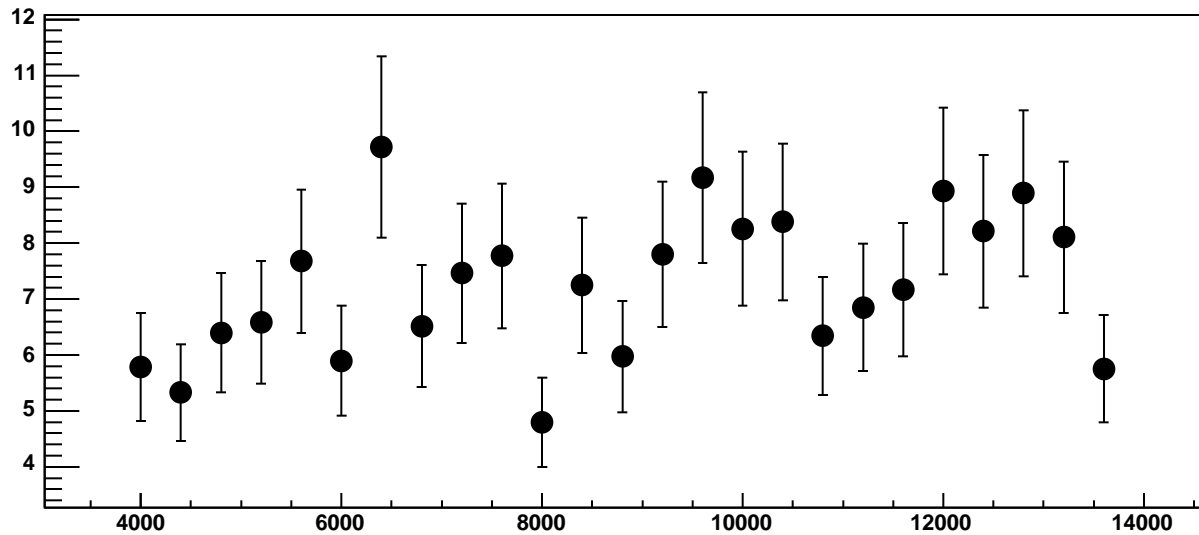


Chip 2, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

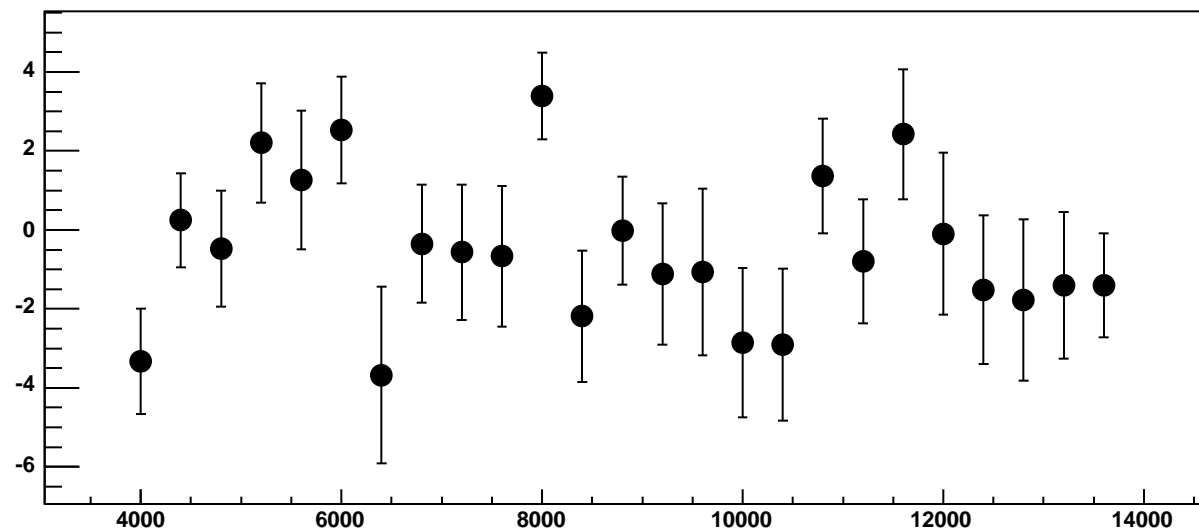


χ^2 / ndf 32.91 / 16
p0 364.5 ± 1.278
p1 0.01328 ± 0.0001725

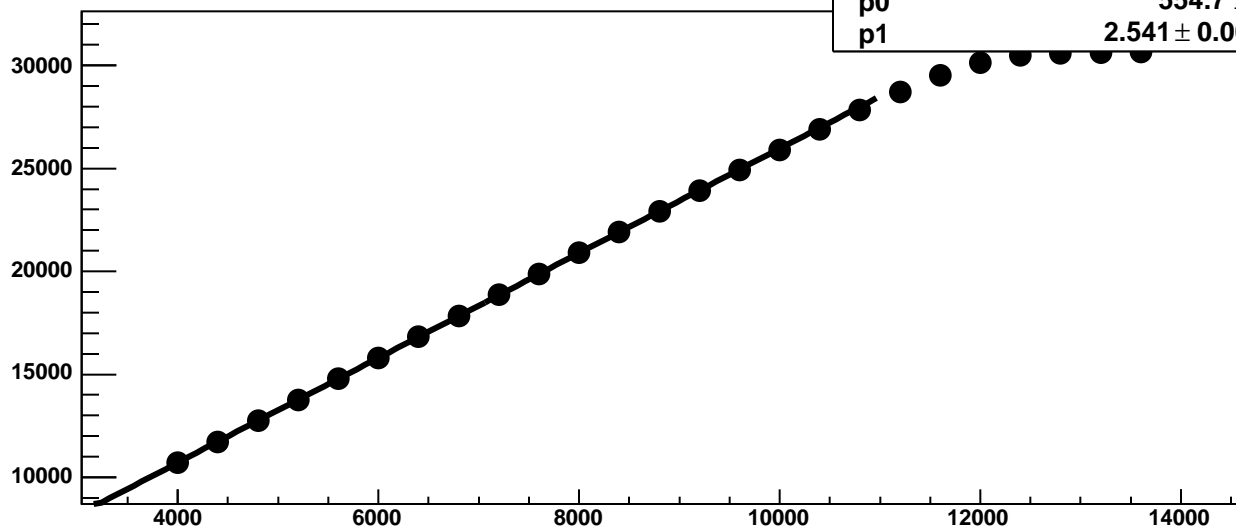
Chip 2, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



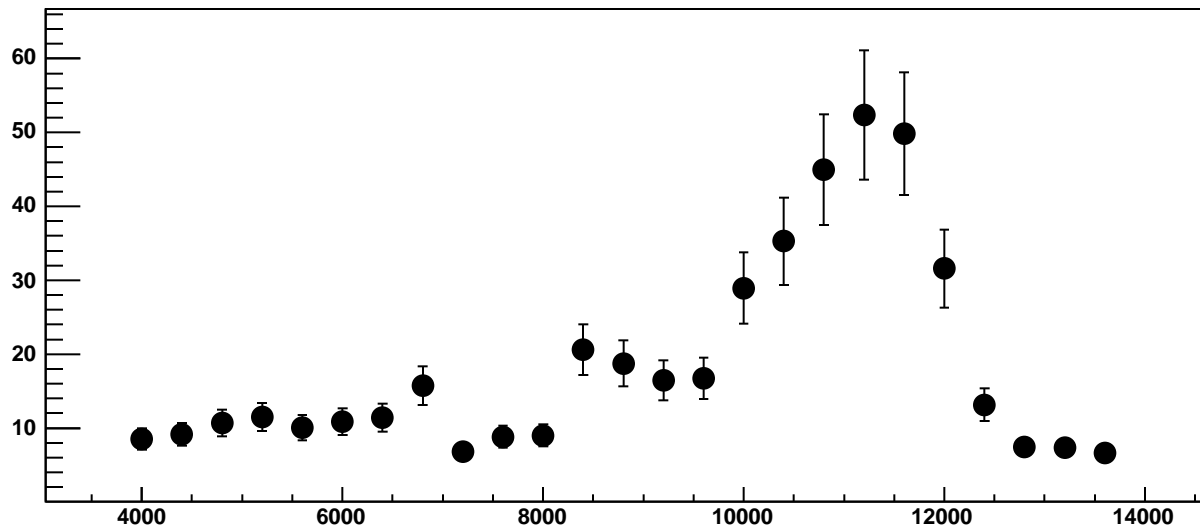
Chip 2, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



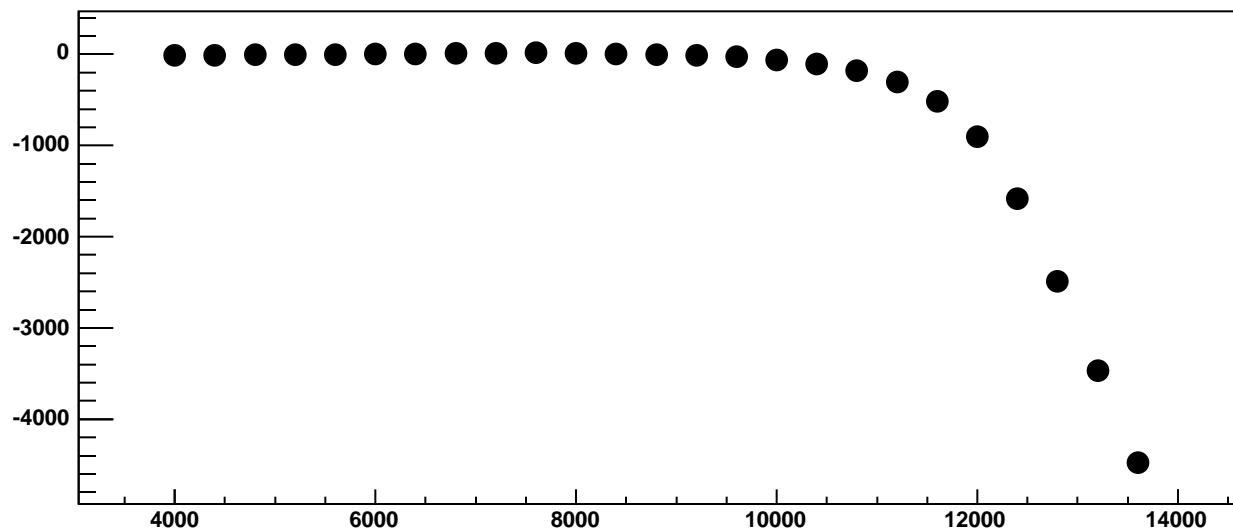
Chip 2, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC



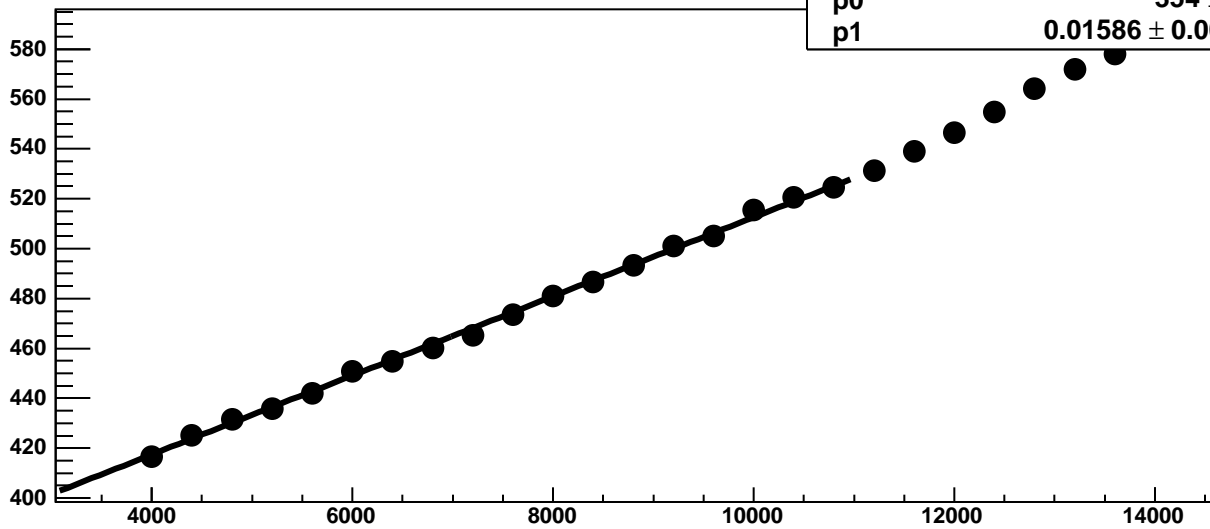
Chip 2, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

11.82 / 16

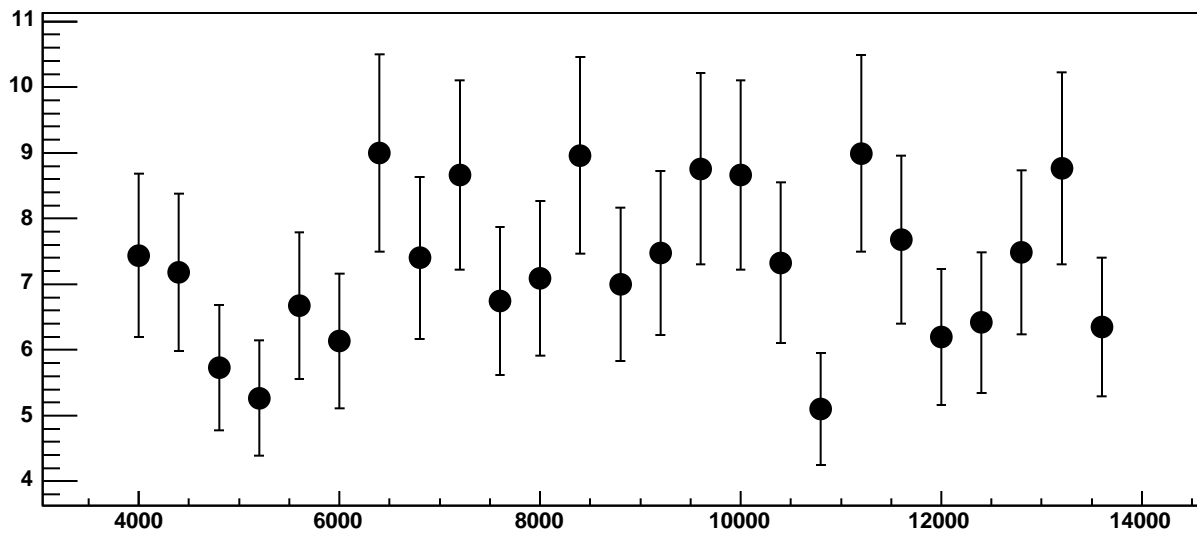
p0

354 ± 1.306

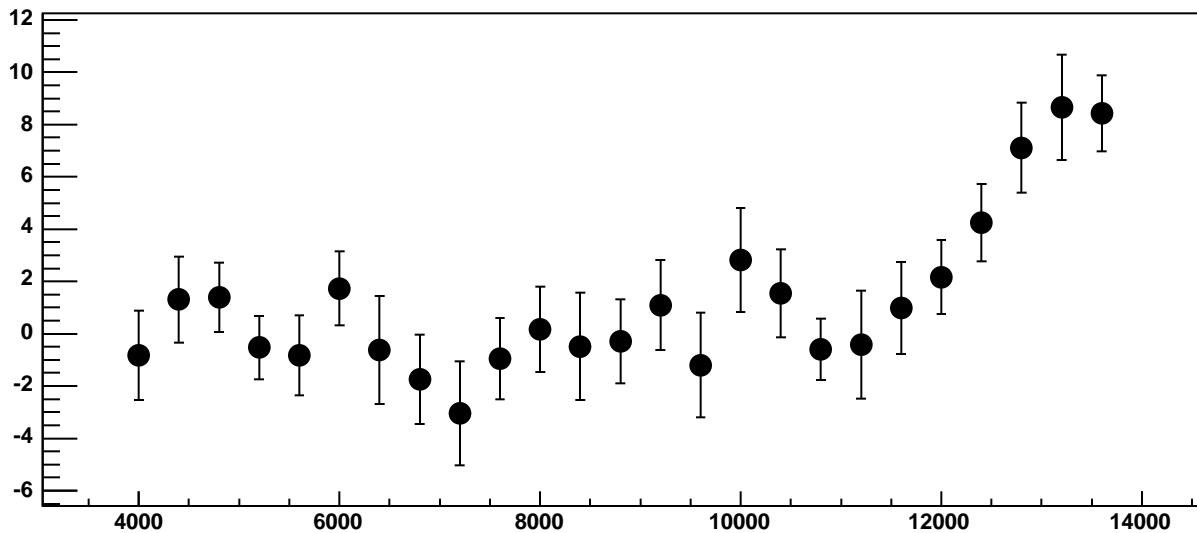
p1

0.01586 ± 0.0001716

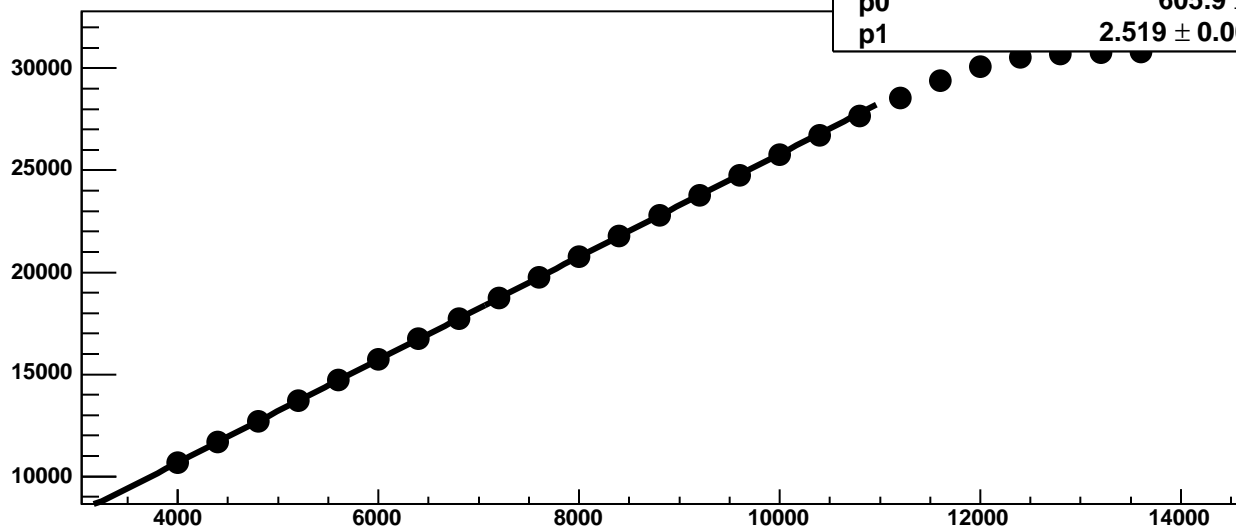
Chip 2, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

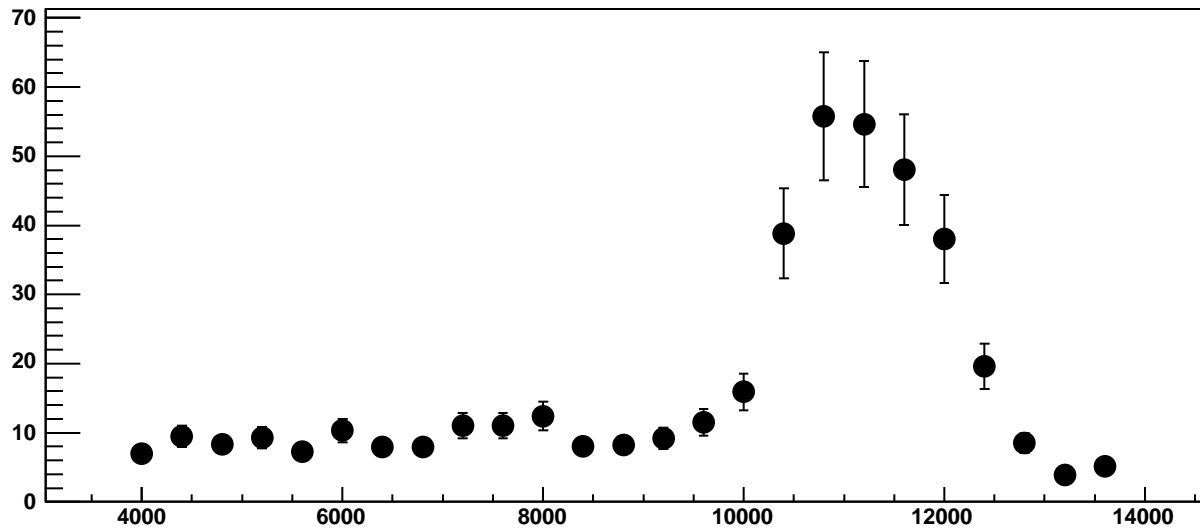


Chip 2, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC

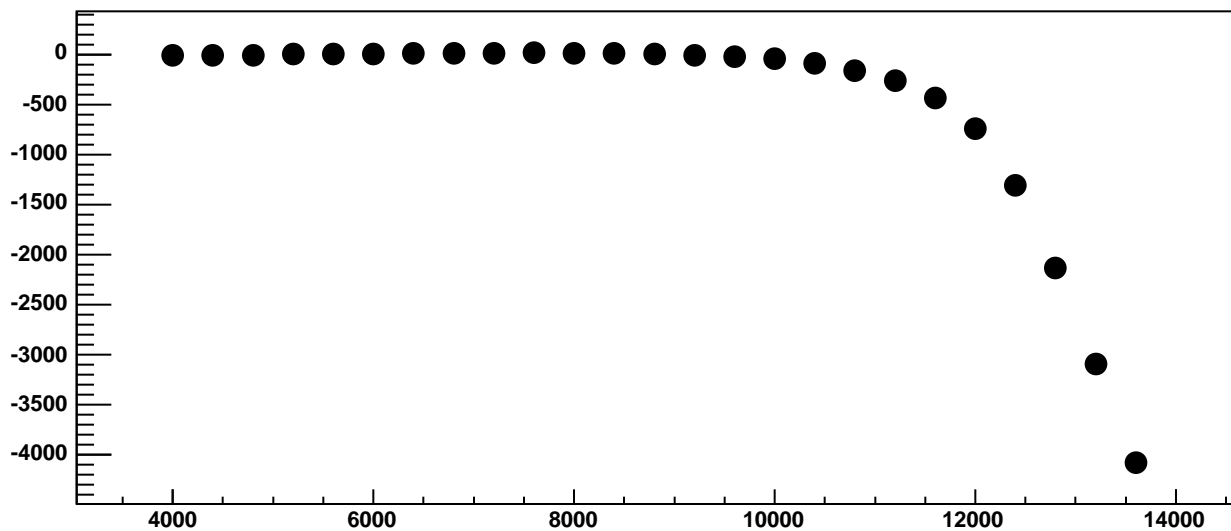


χ^2 / ndf 817.7 / 16
p0 605.9 ± 1.978
p1 2.519 ± 0.0002873

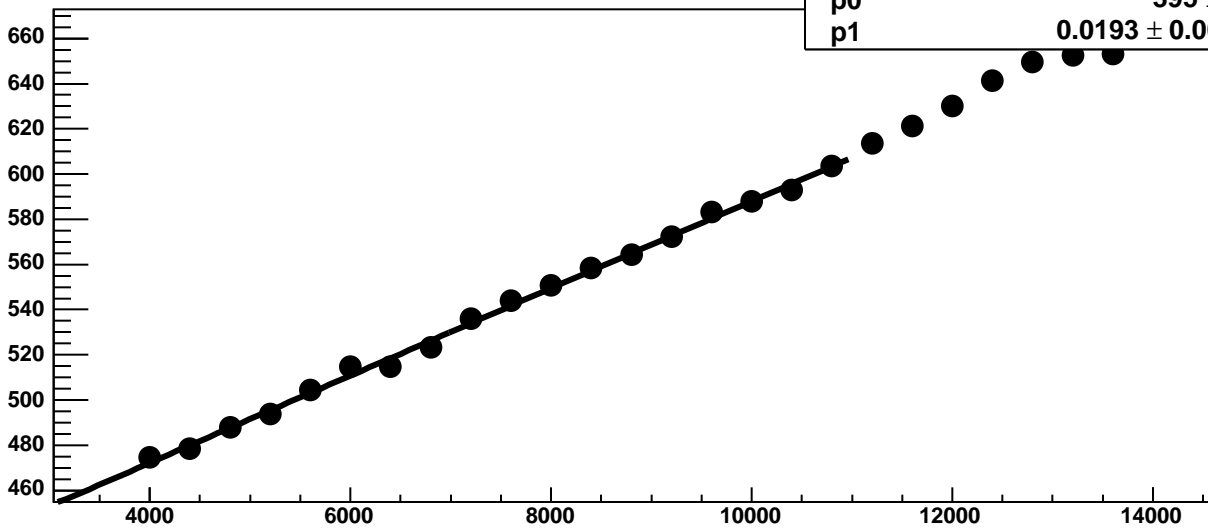
Chip 2, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC

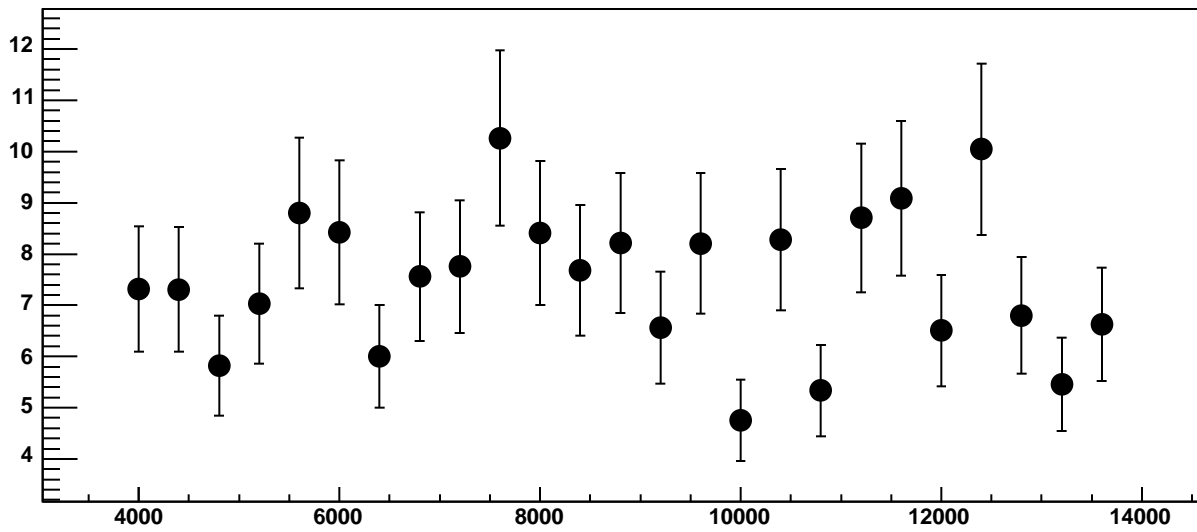


Chip 2, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

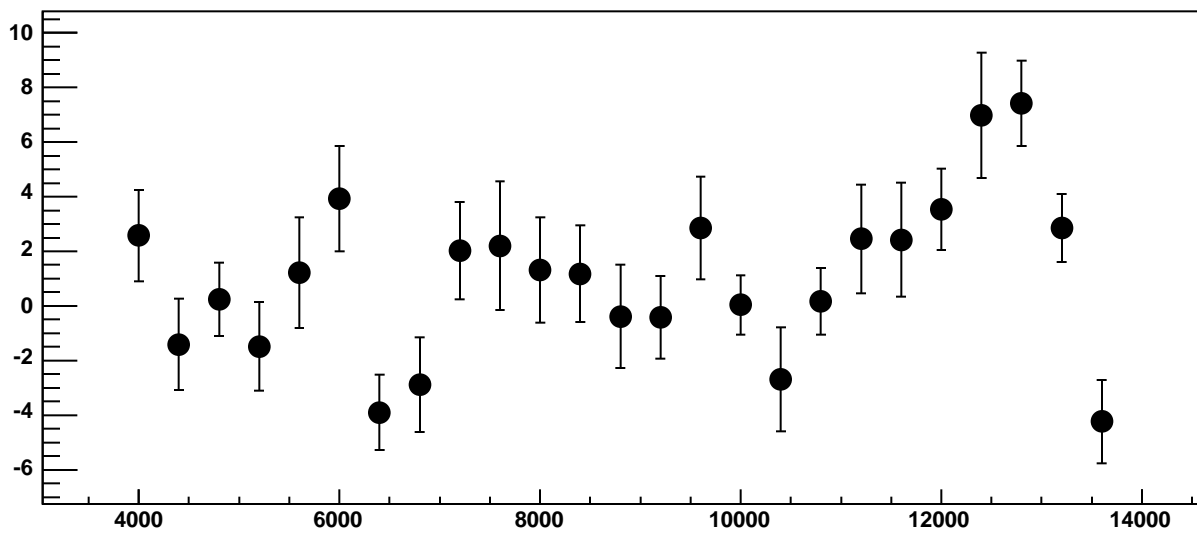


χ^2 / ndf 26.76 / 16
p0 395 ± 1.353
p1 0.0193 ± 0.0001707

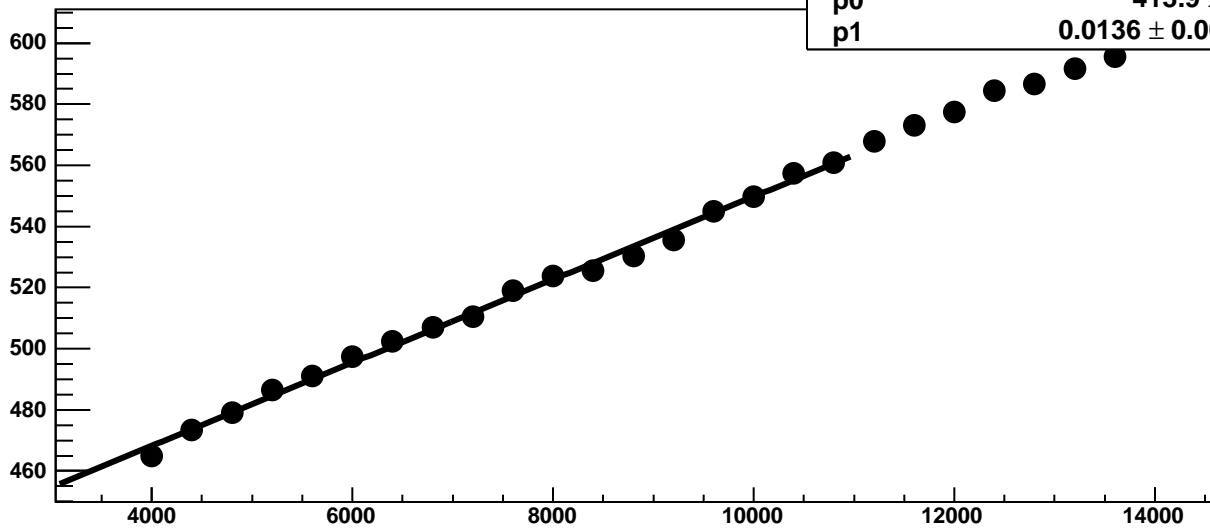
Chip 2, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

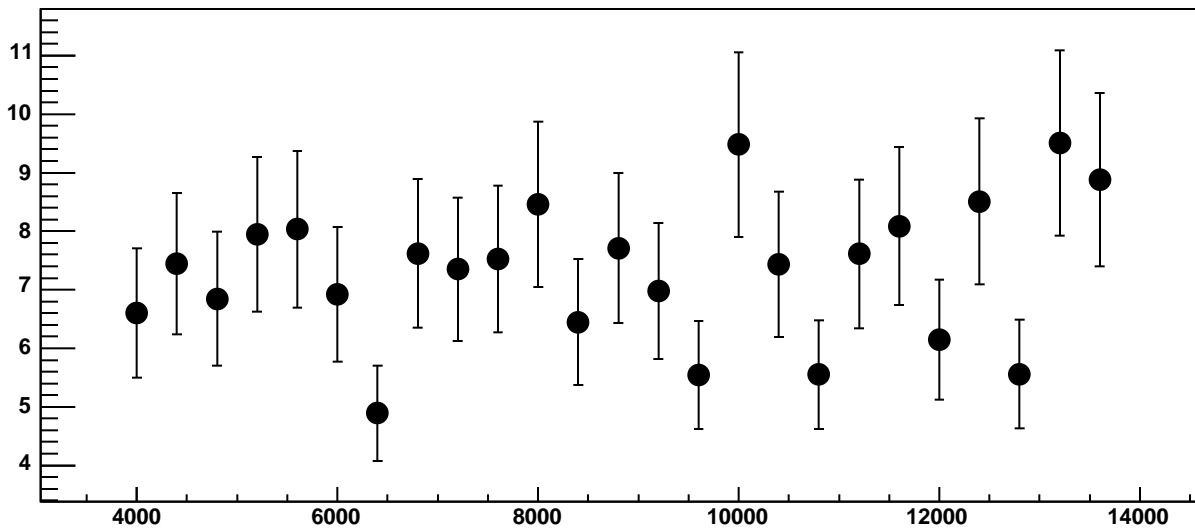


Chip 2, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

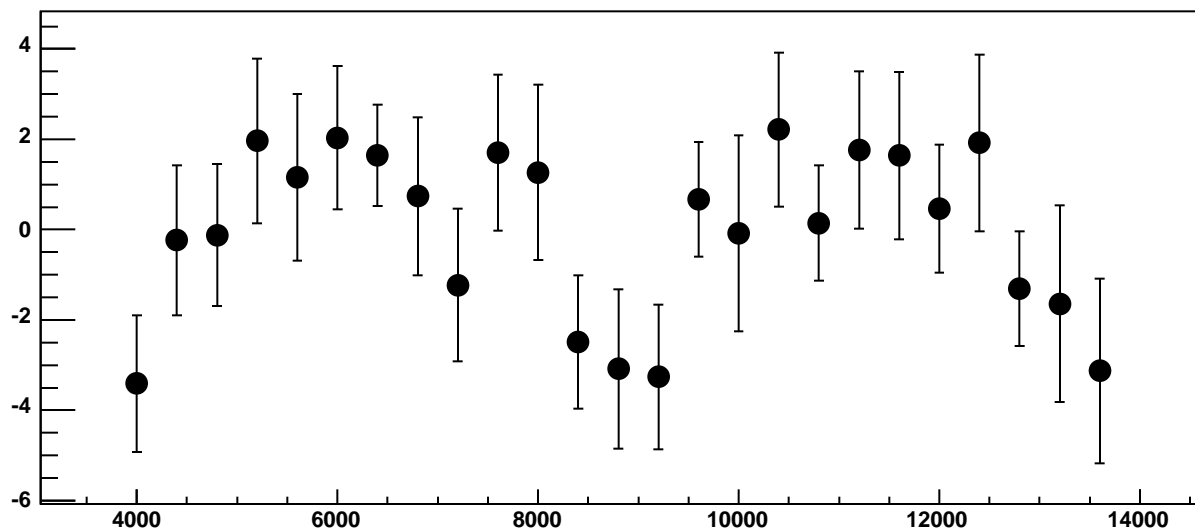


χ^2 / ndf 24.54 / 16
p0 413.9 ± 1.366
p1 0.0136 ± 0.0001762

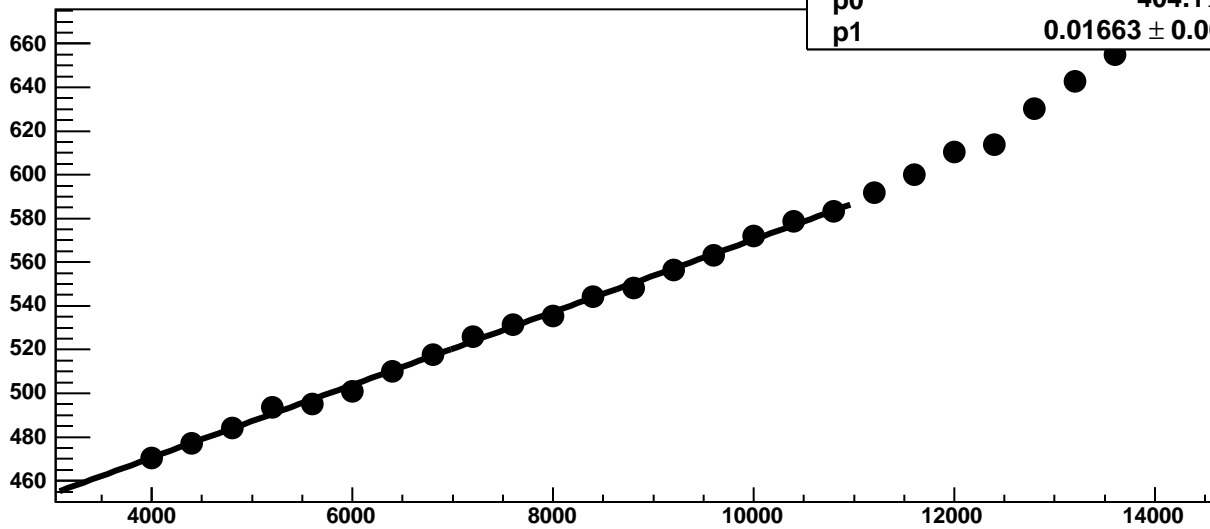
Chip 2, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



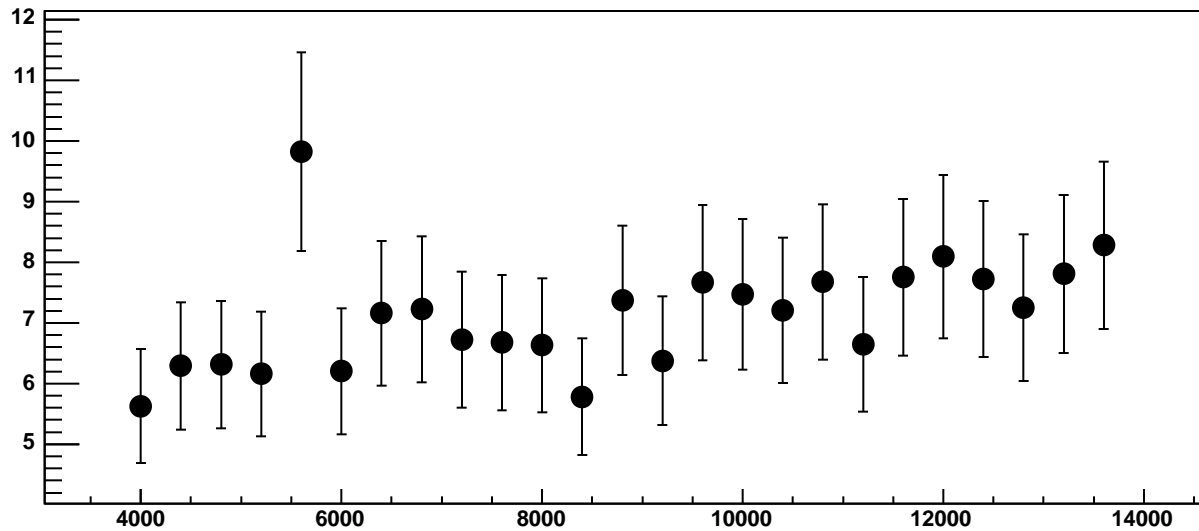
Chip 2, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



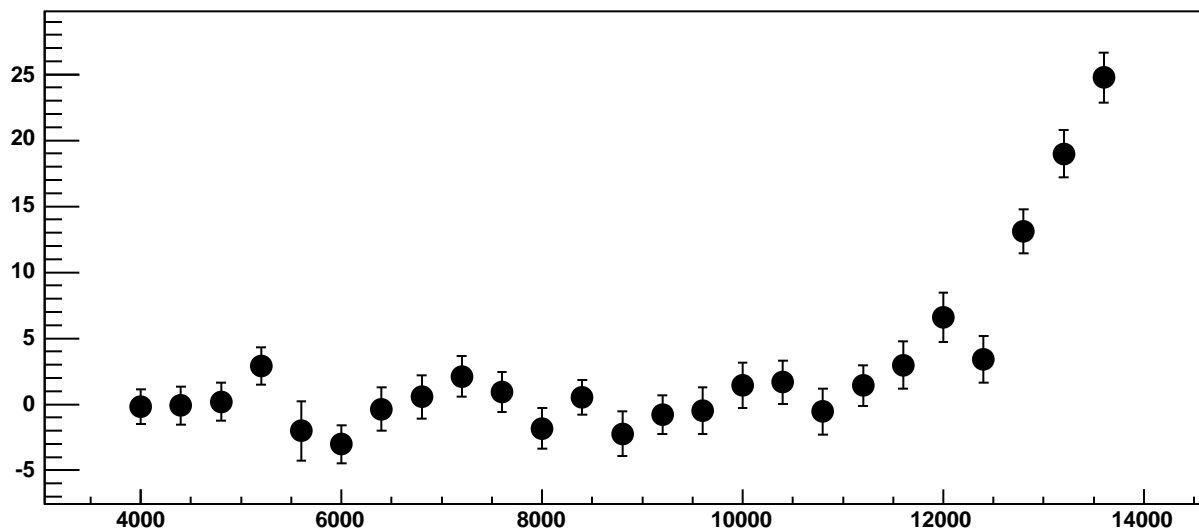
Chip 2, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC



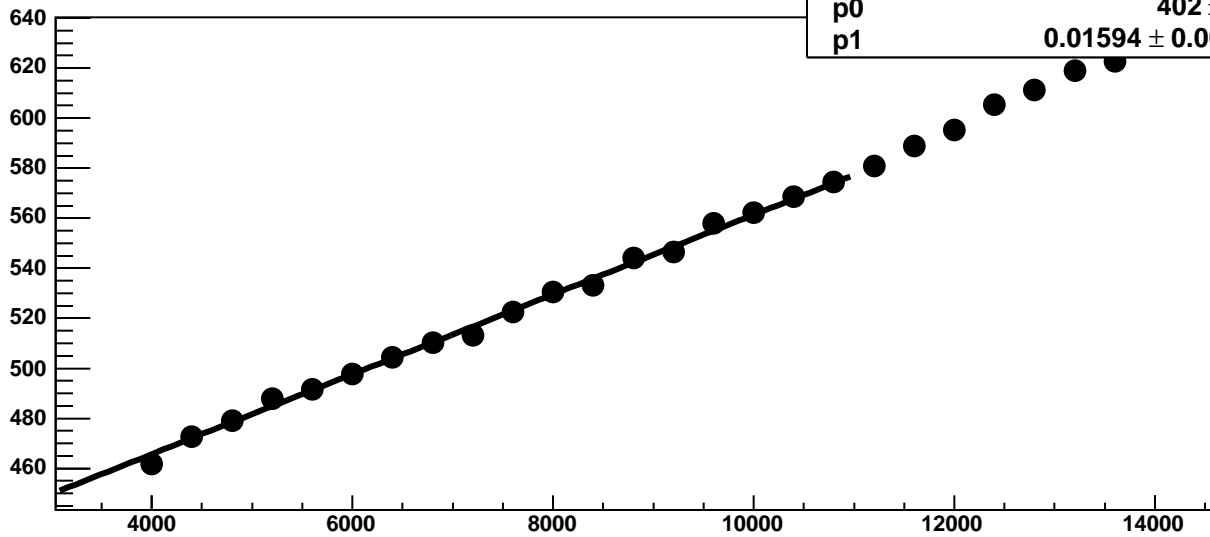
Chip 2, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

19.66 / 16

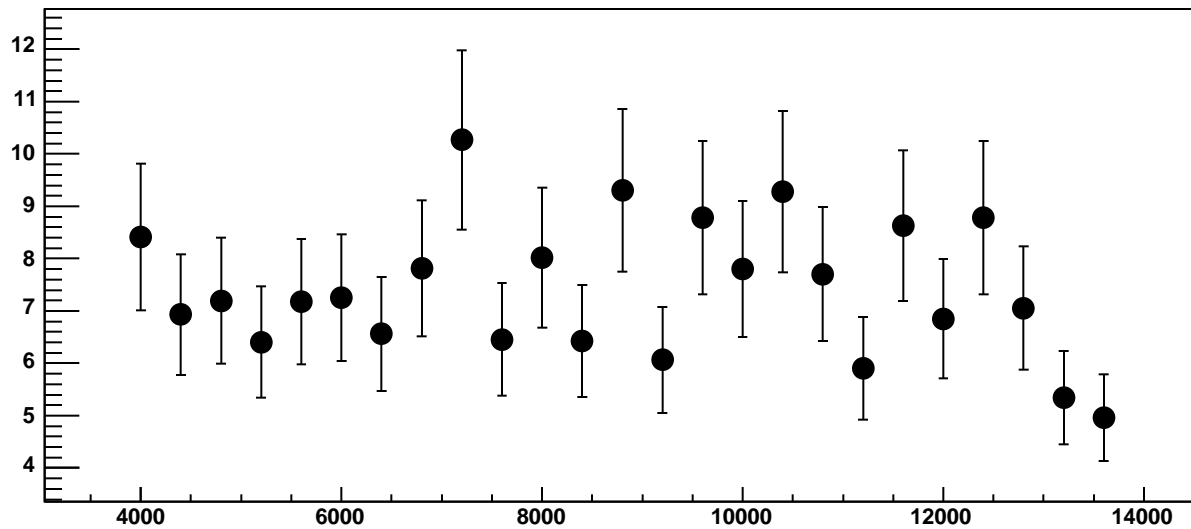
p0

402 ± 1.496

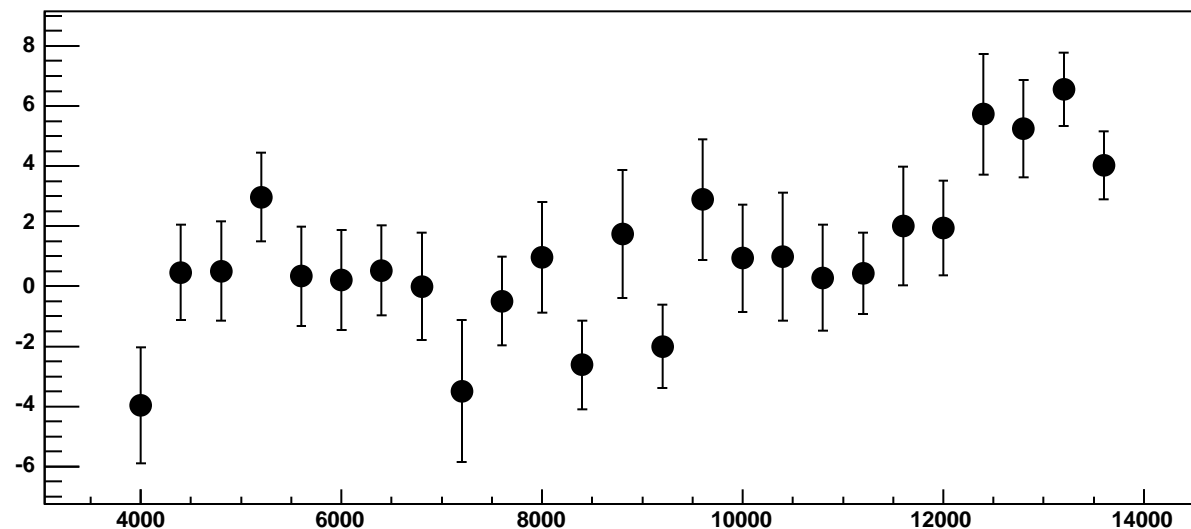
p1

0.01594 ± 0.0001984

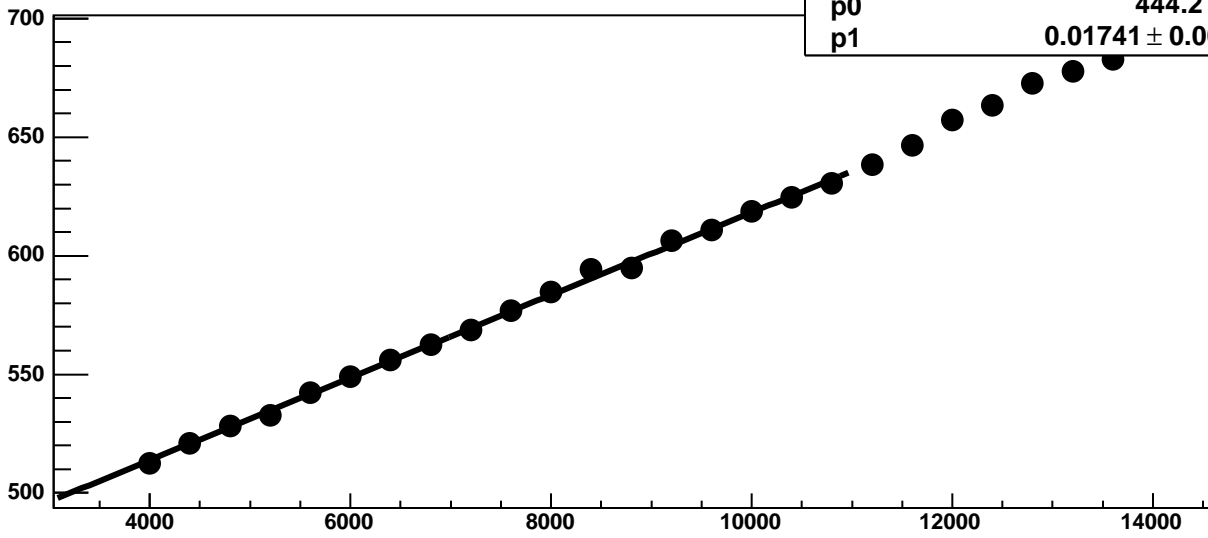
Chip 2, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



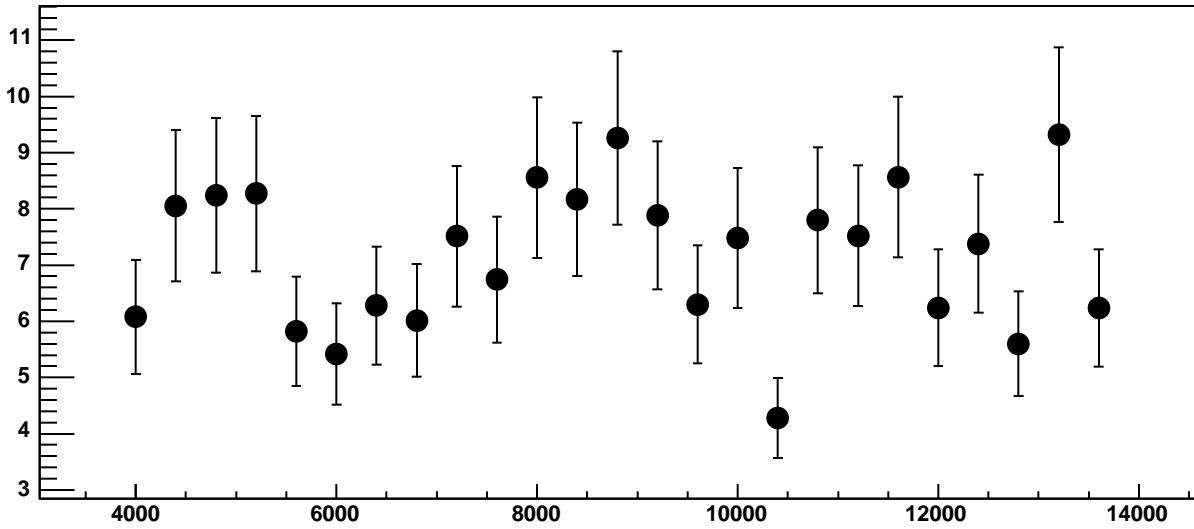
Chip 2, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



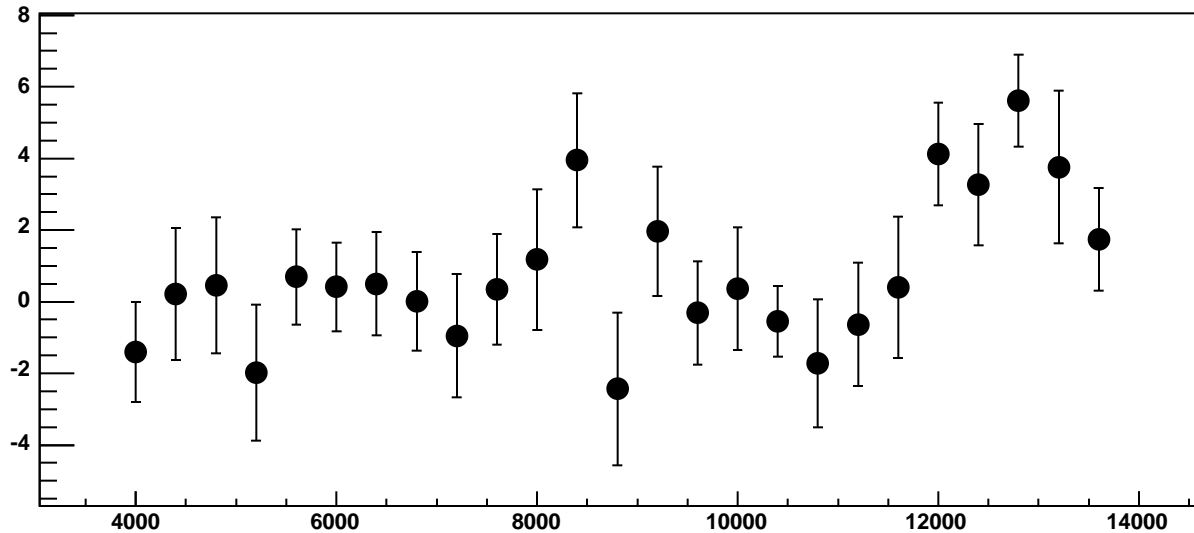
Chip 2, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



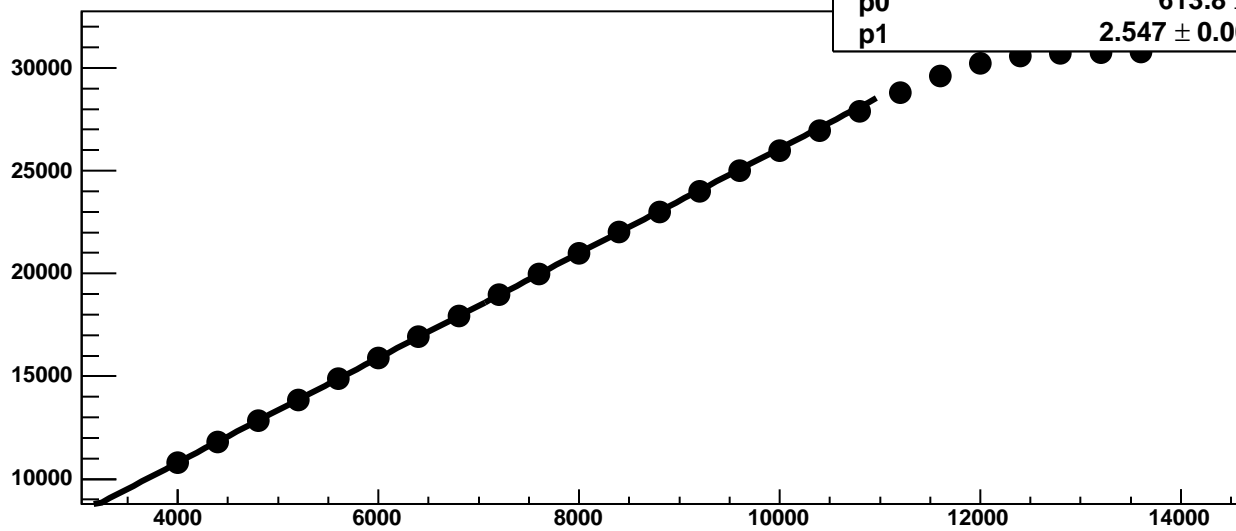
Chip 2, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

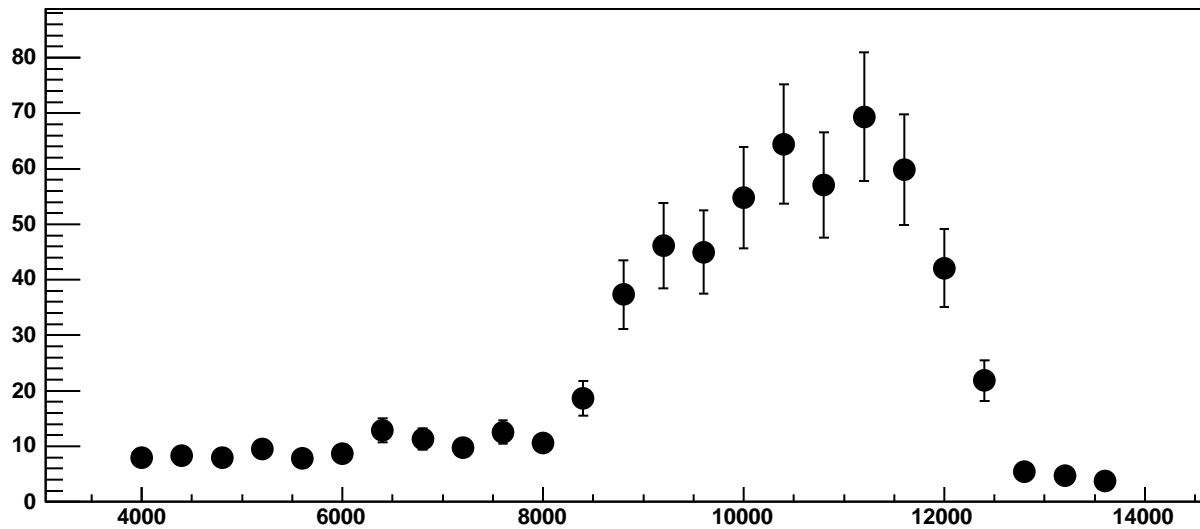


Chip 2, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC

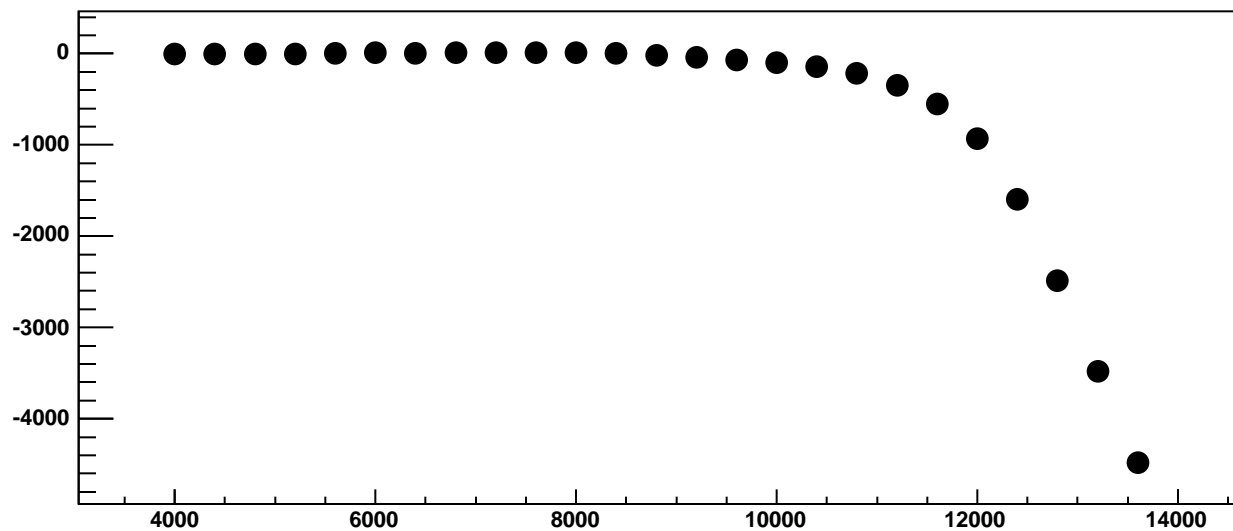


χ^2 / ndf 594.5 / 16
p0 613.8 ± 2.733
p1 2.547 ± 0.0004568

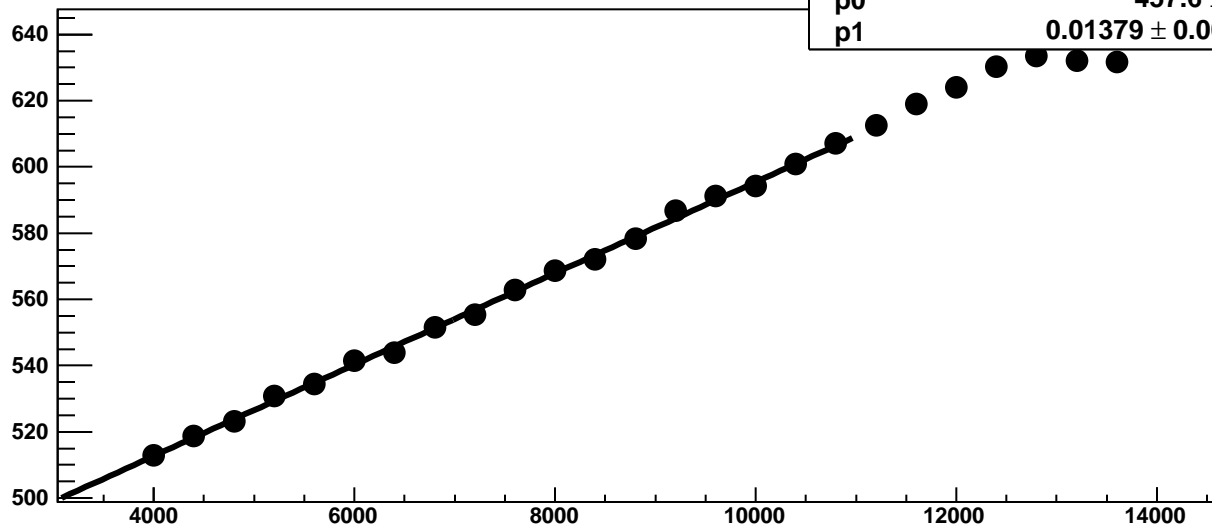
Chip 2, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC

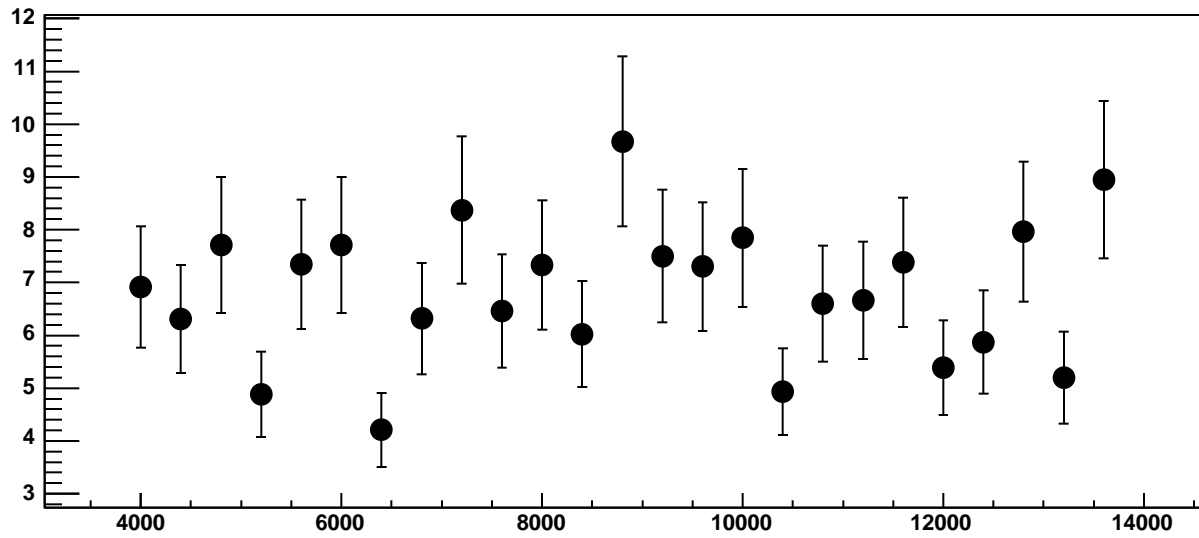


Chip 2, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

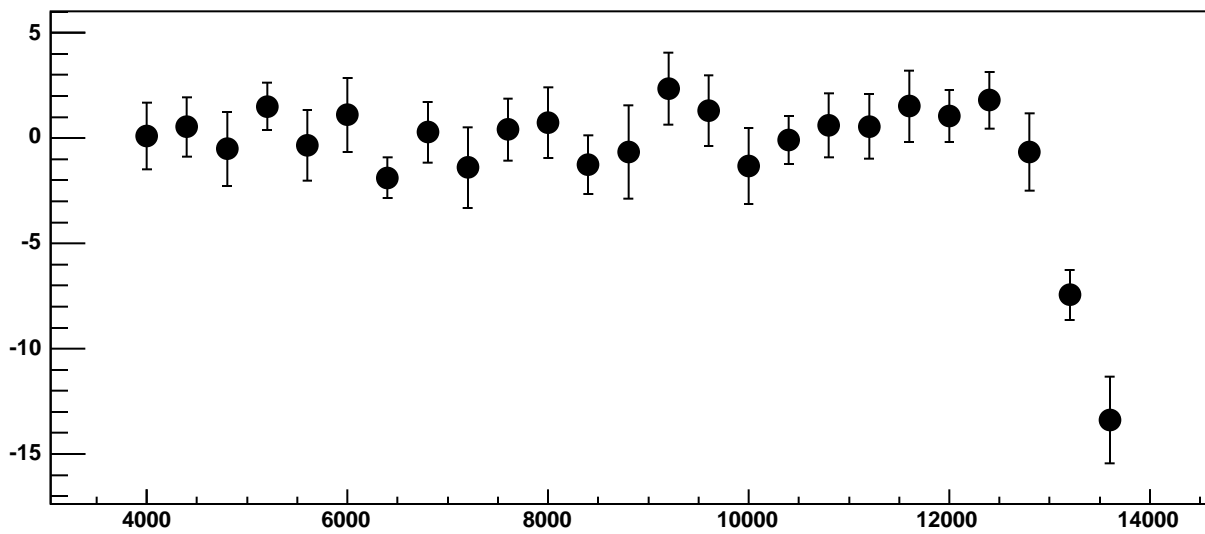


χ^2 / ndf 11.18 / 16
p0 457.6 ± 1.264
p1 0.01379 ± 0.0001664

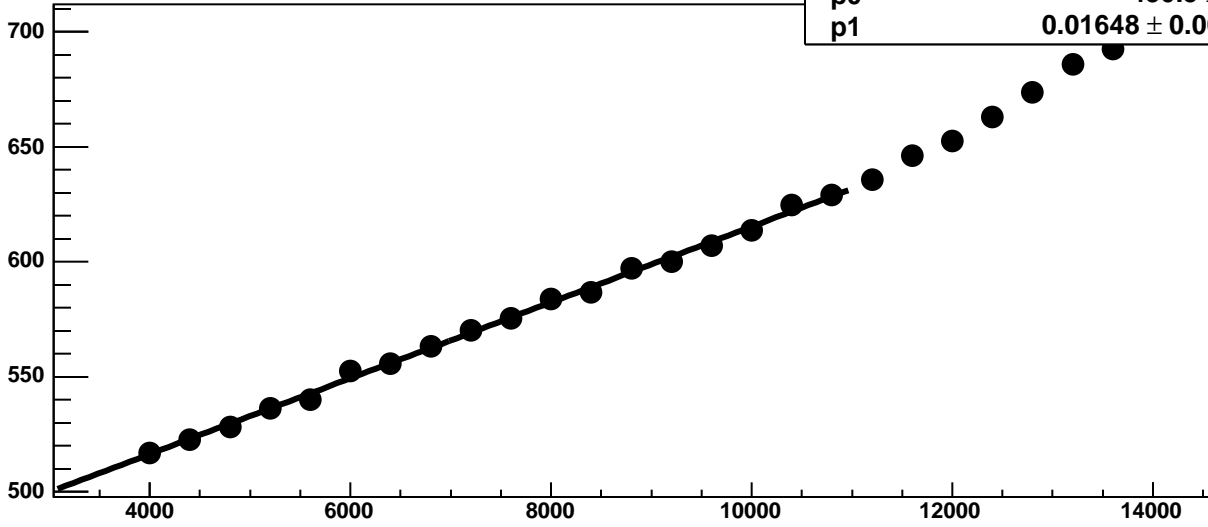
Chip 2, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

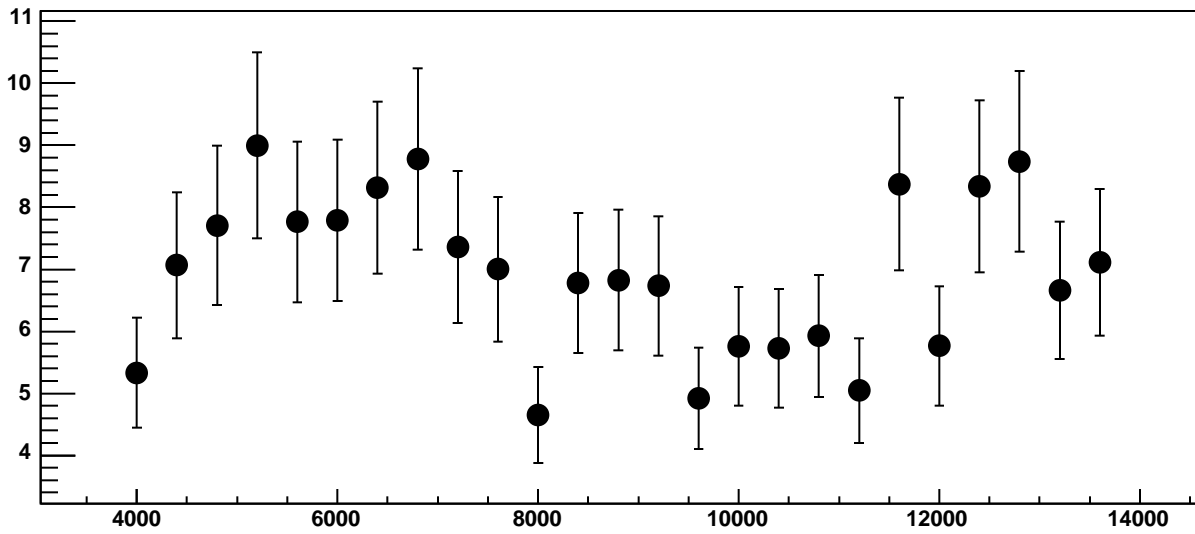


Chip 2, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

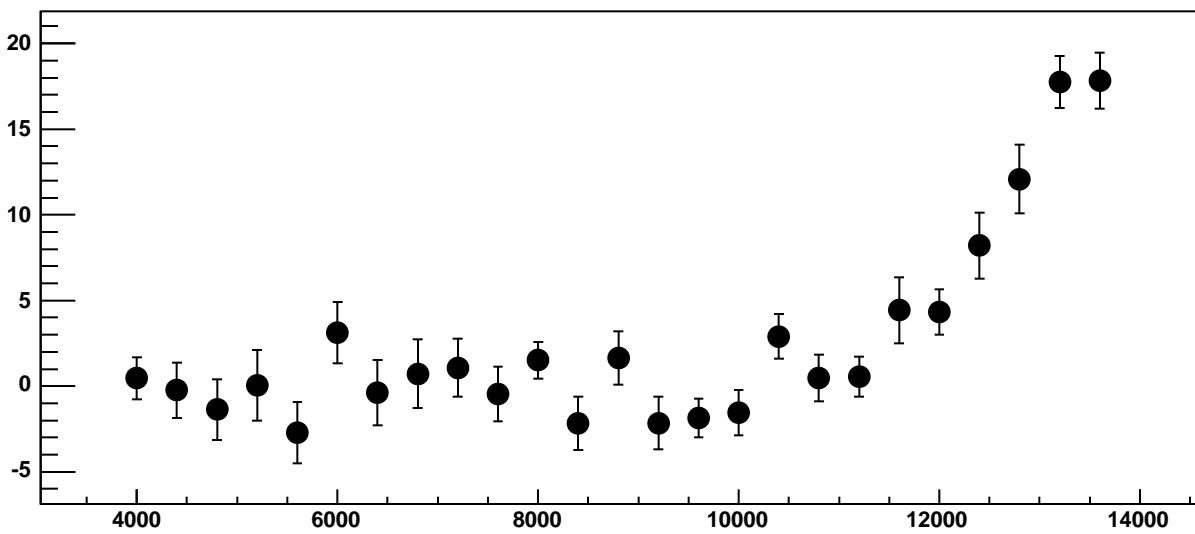


χ^2 / ndf 22.9 / 16
p0 450.5 ± 1.315
p1 0.01648 ± 0.0001634

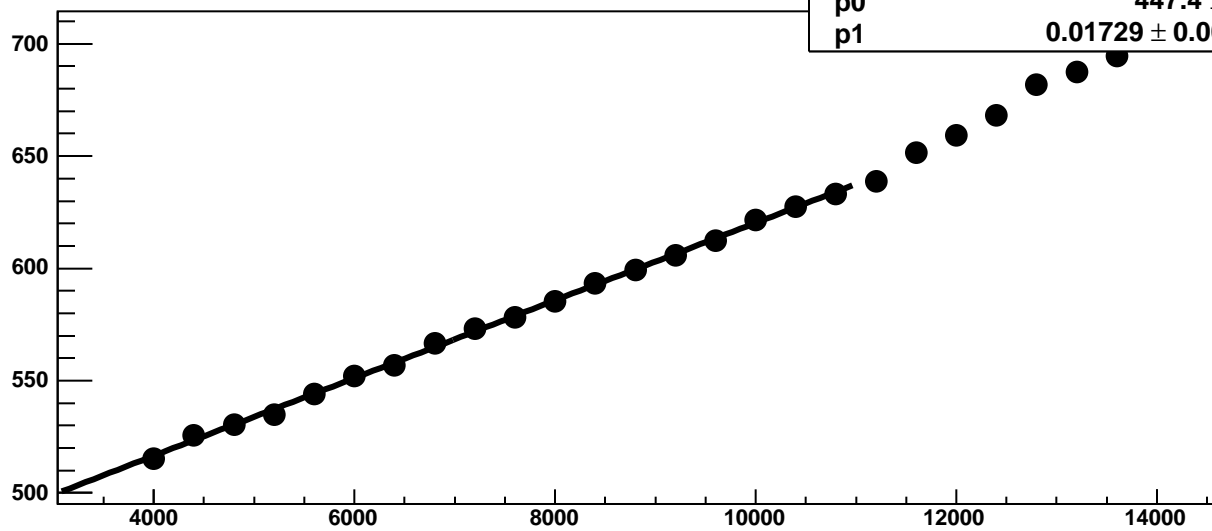
Chip 2, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



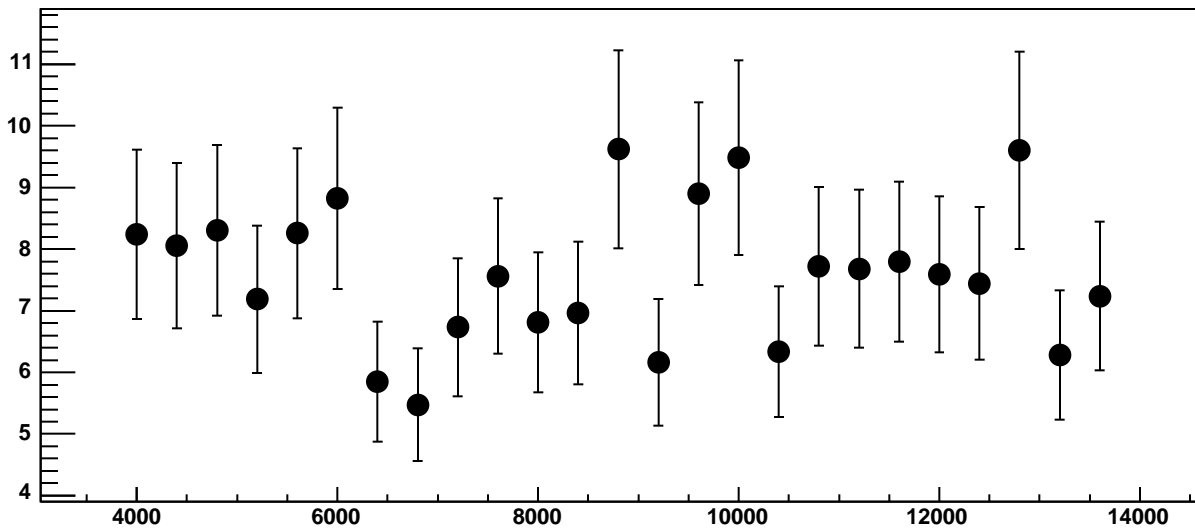
Chip 2, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



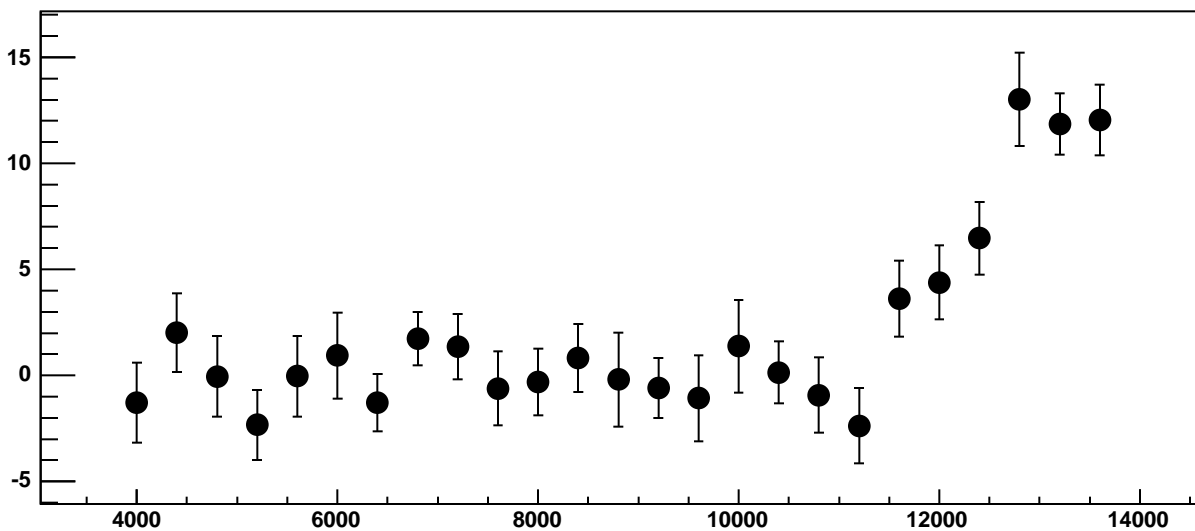
Chip 2, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC



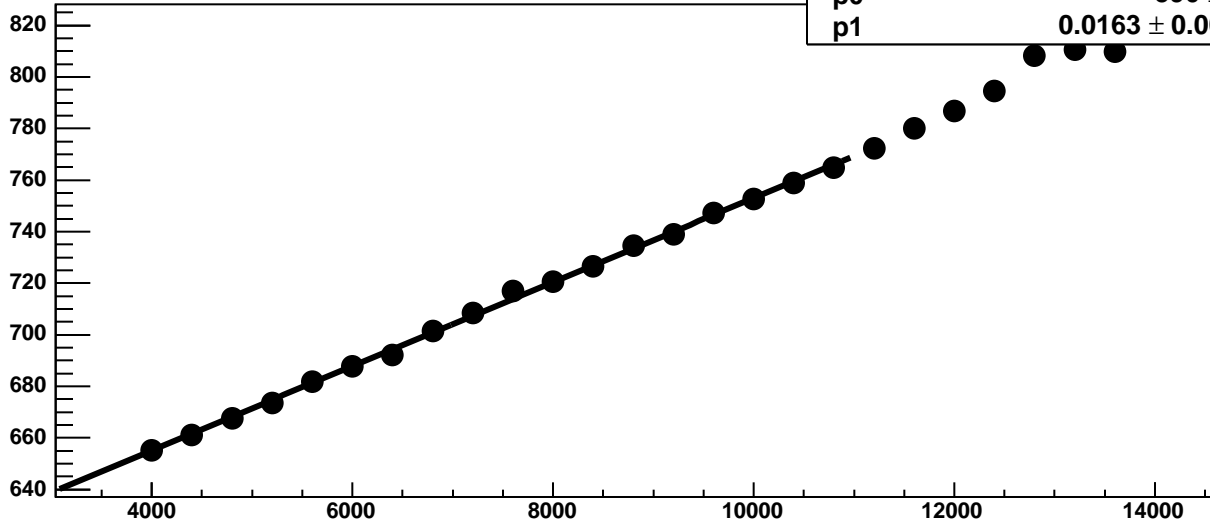
Chip 2, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



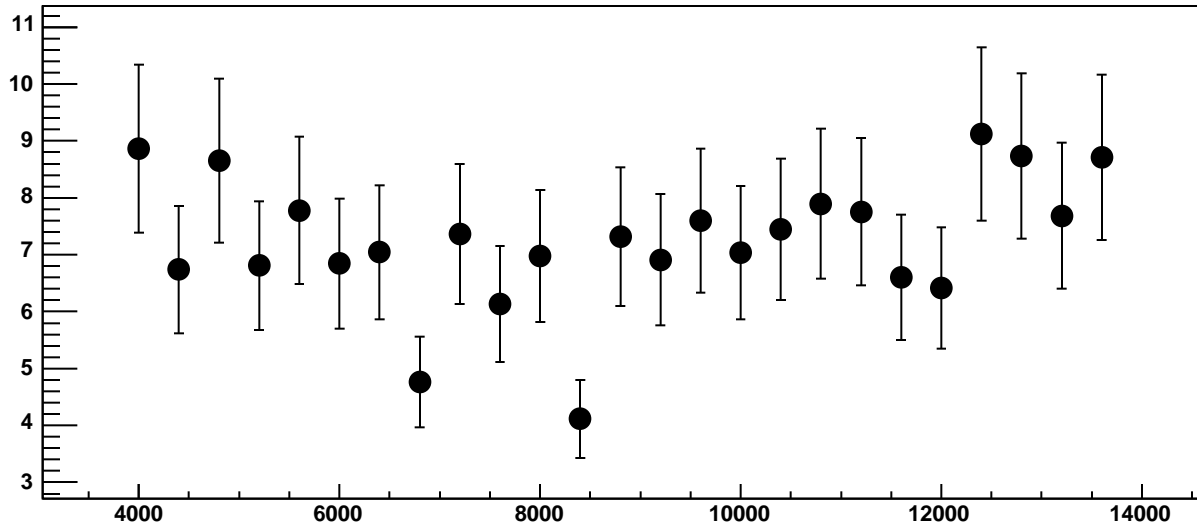
Chip 2, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC



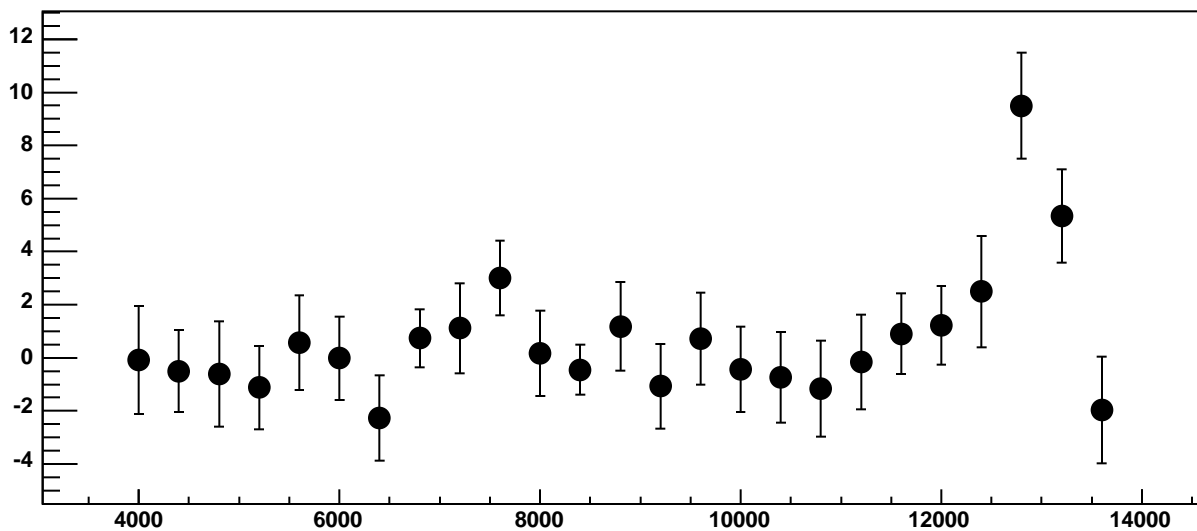
Chip 2, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



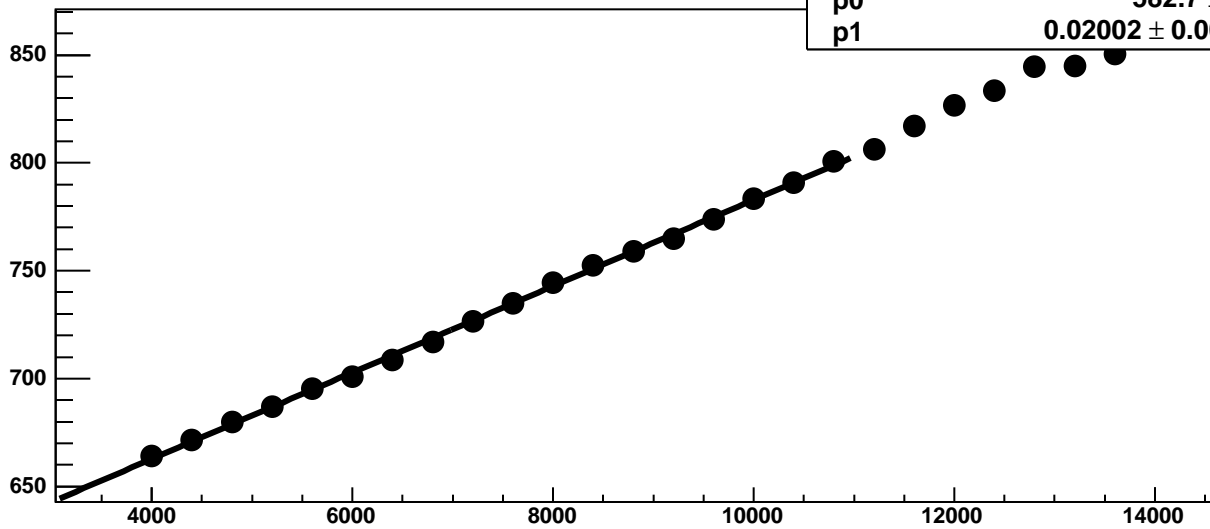
Chip 2, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

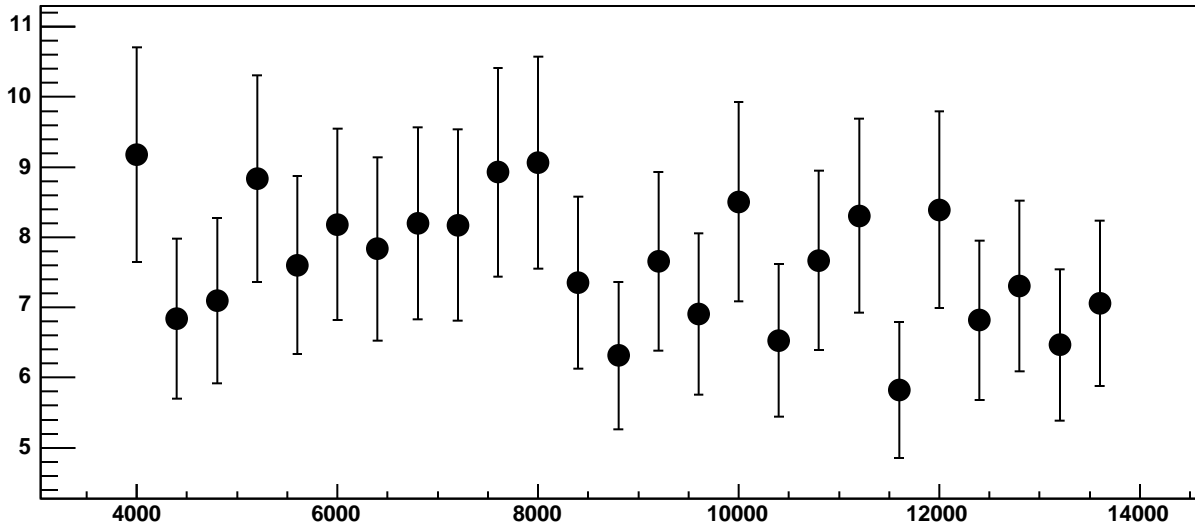


Chip 2, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

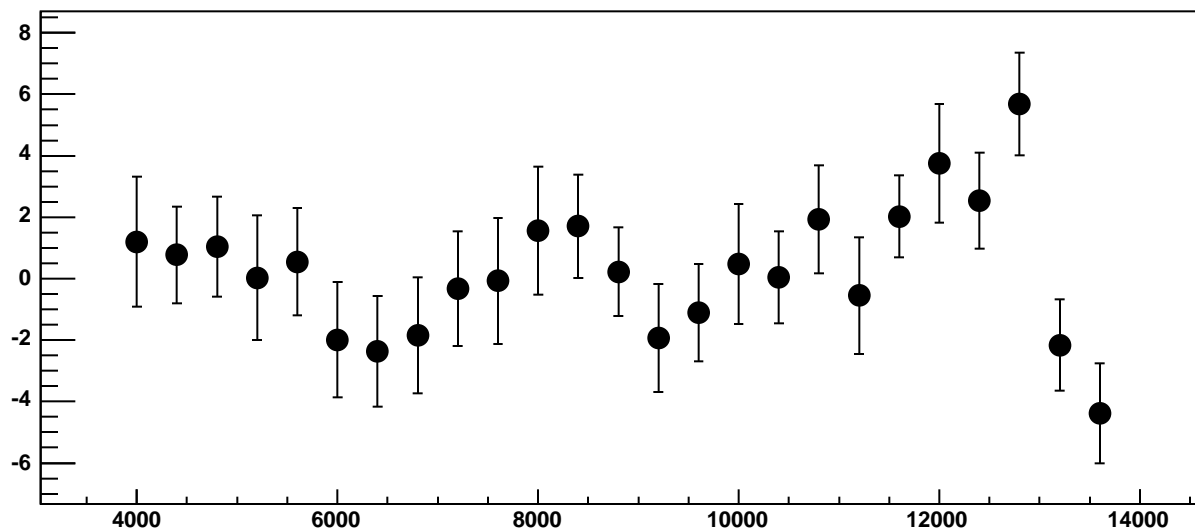


χ^2 / ndf 9.488 / 16
p0 582.7 ± 1.539
p1 0.02002 ± 0.0001968

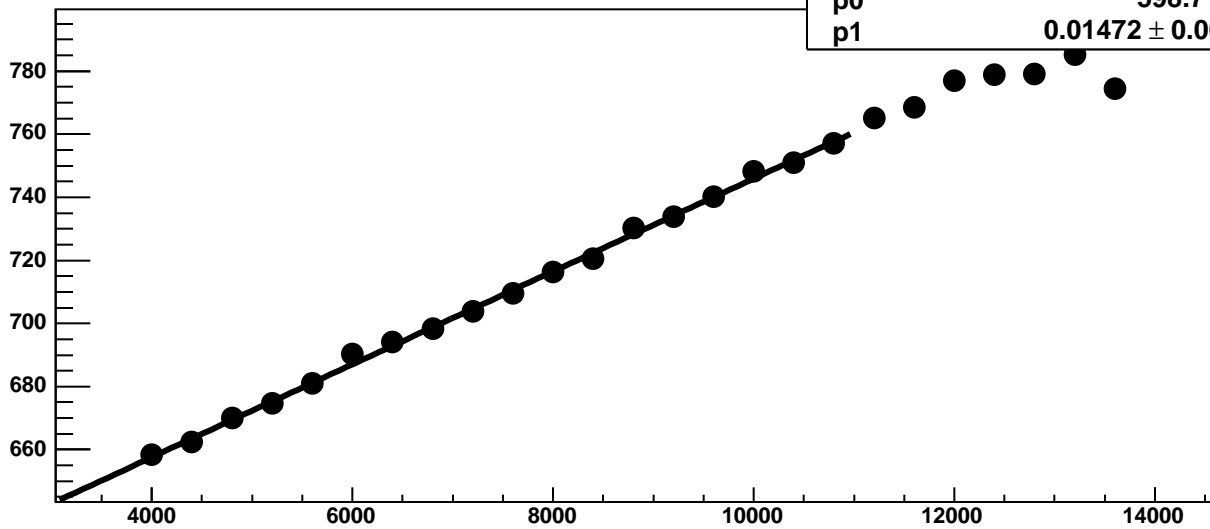
Chip 2, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



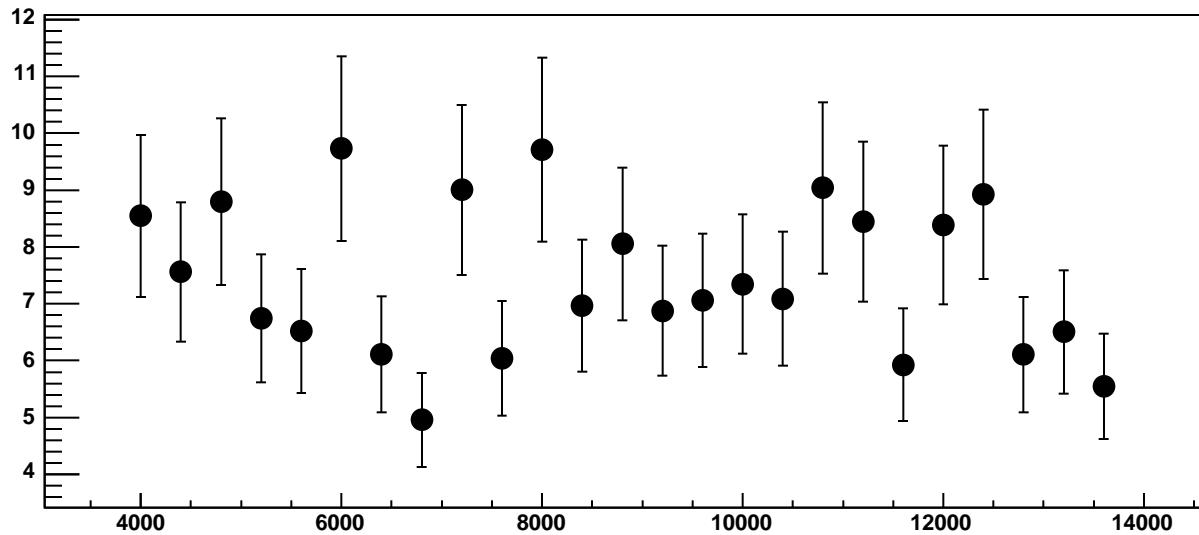
Chip 2, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC



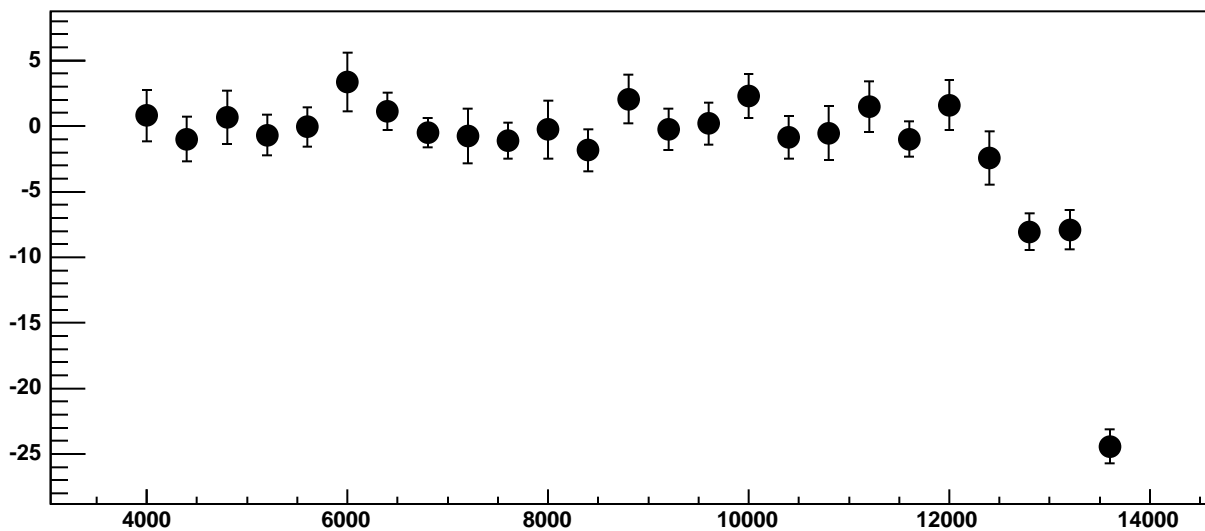
Chip 2, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



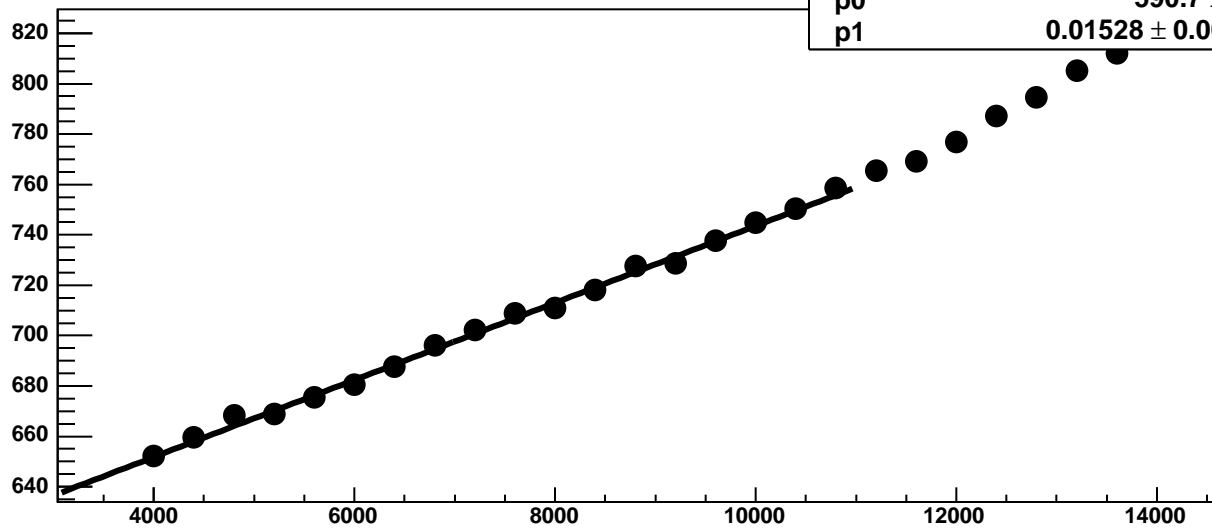
Chip 2, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

21.87 / 16

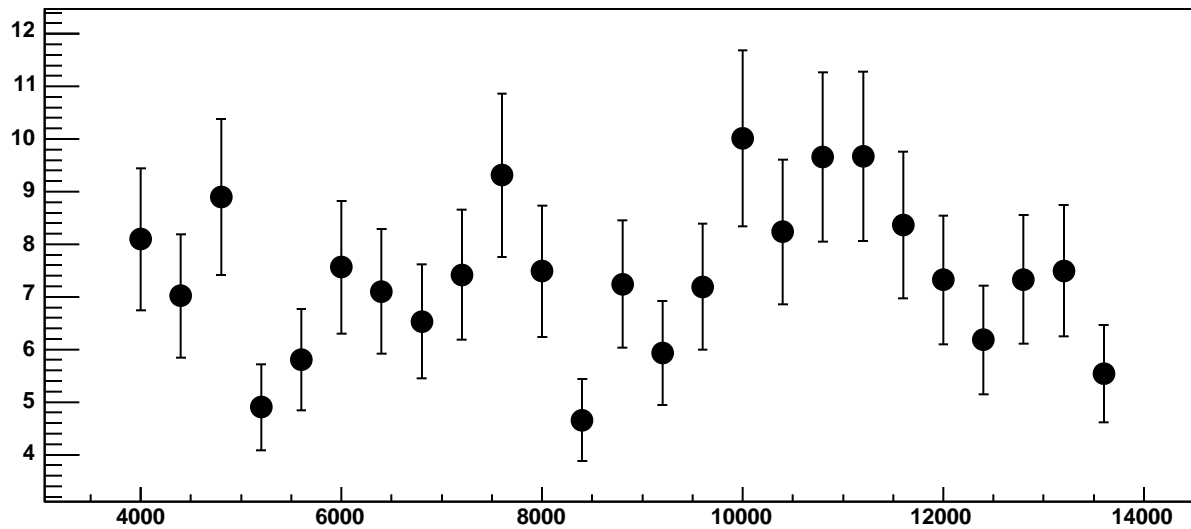
p0

590.7 ± 1.453

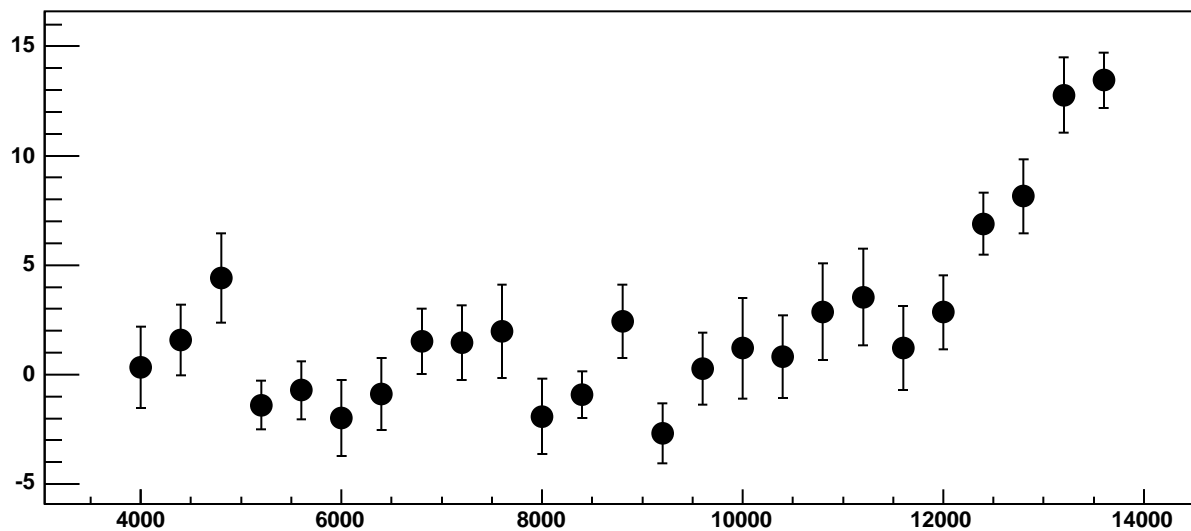
p1

0.01528 ± 0.0001944

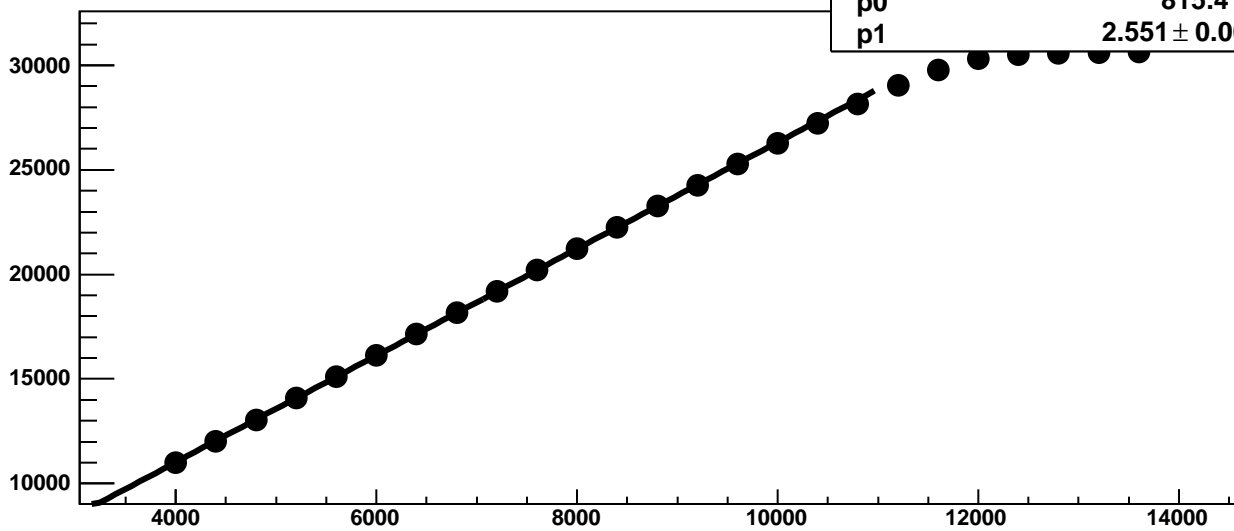
Chip 2, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

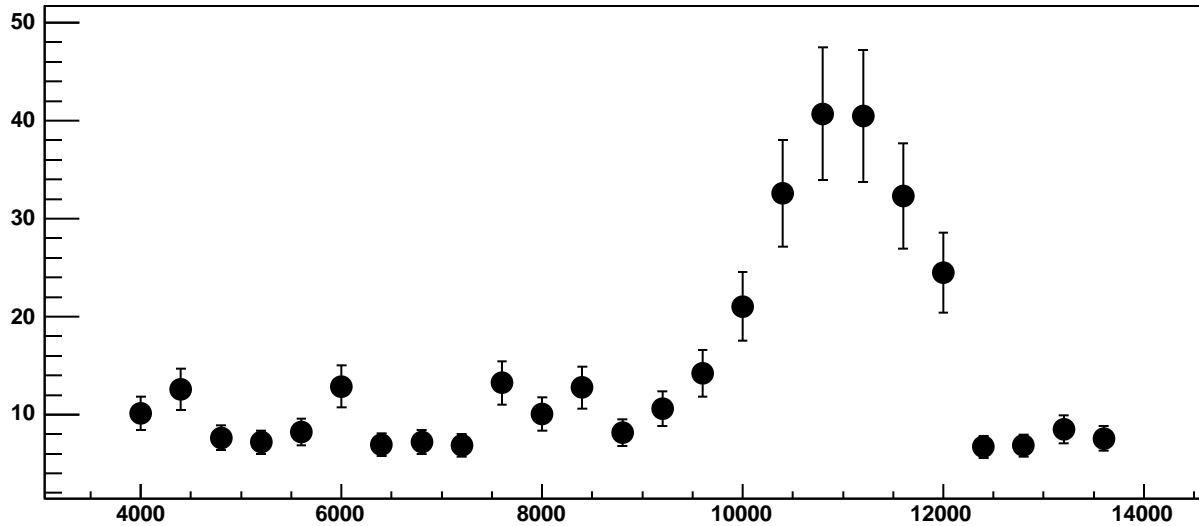


Chip 2, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC

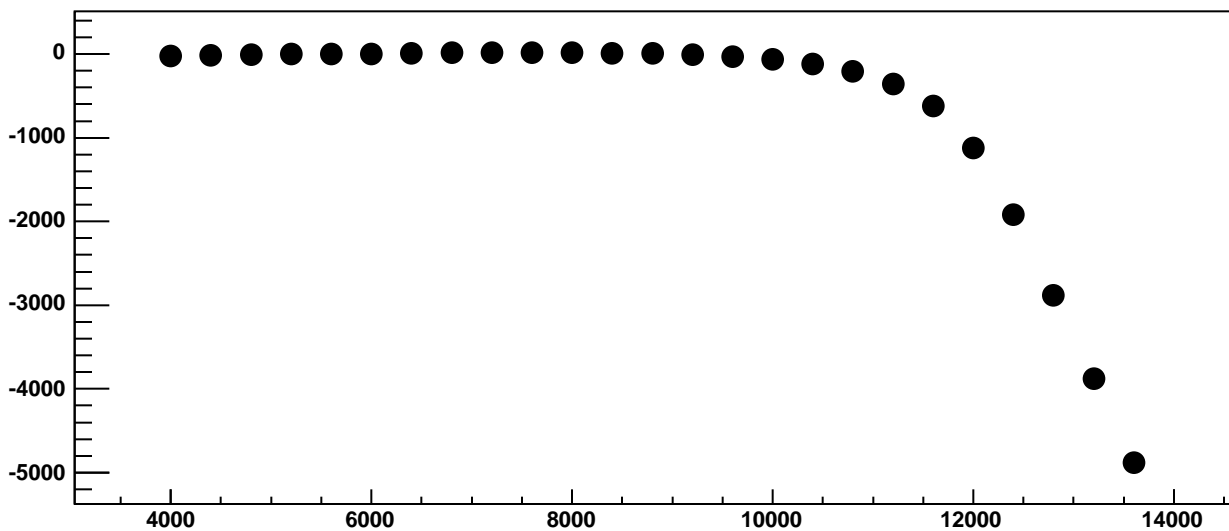


χ^2 / ndf 1478 / 16
p0 815.4 ± 2.251
p1 2.551 ± 0.0003285

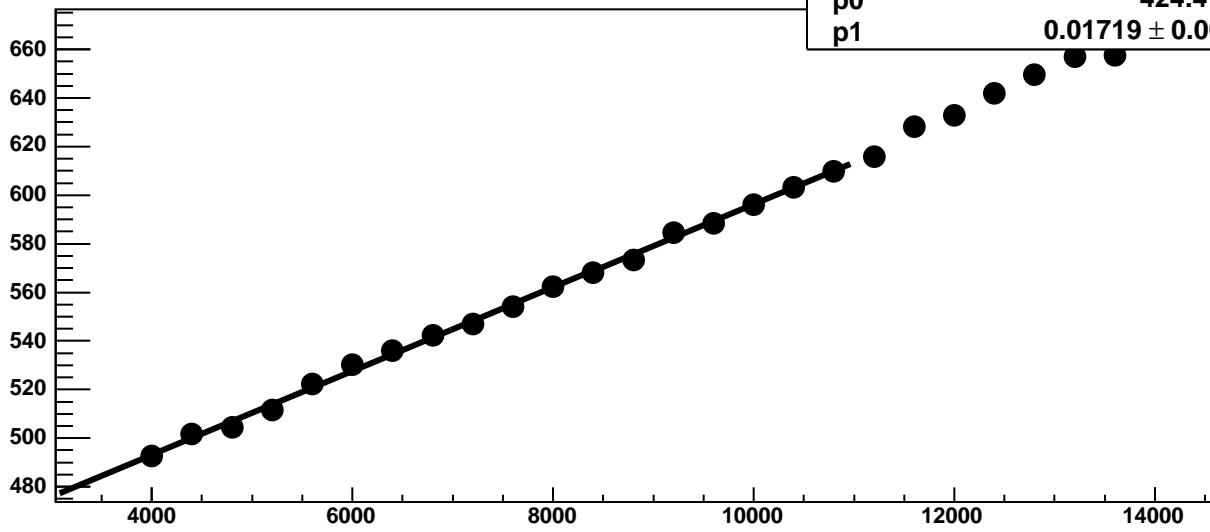
Chip 2, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

16.33 / 16

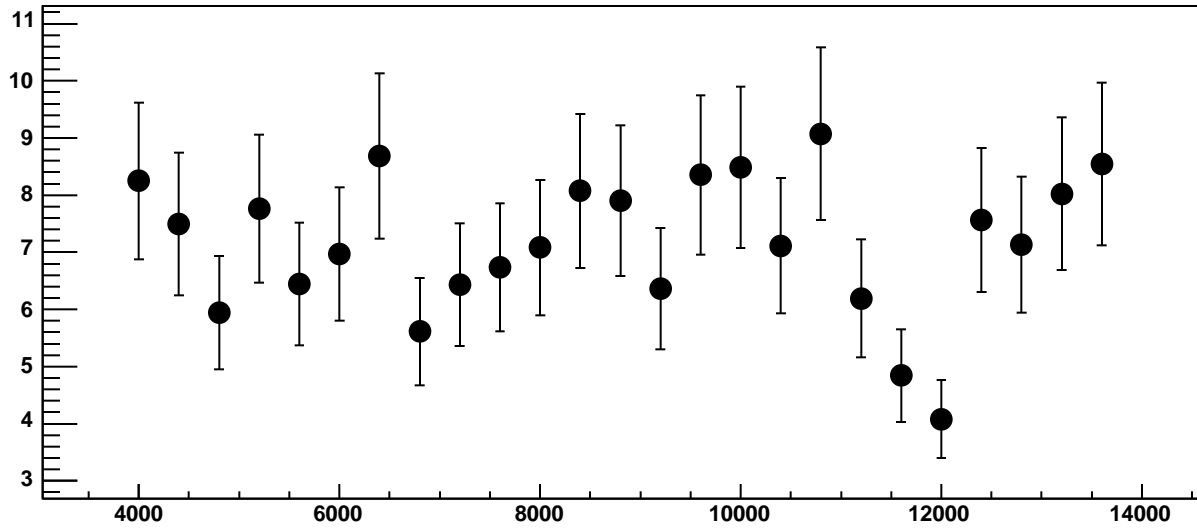
p0

424.4 ± 1.48

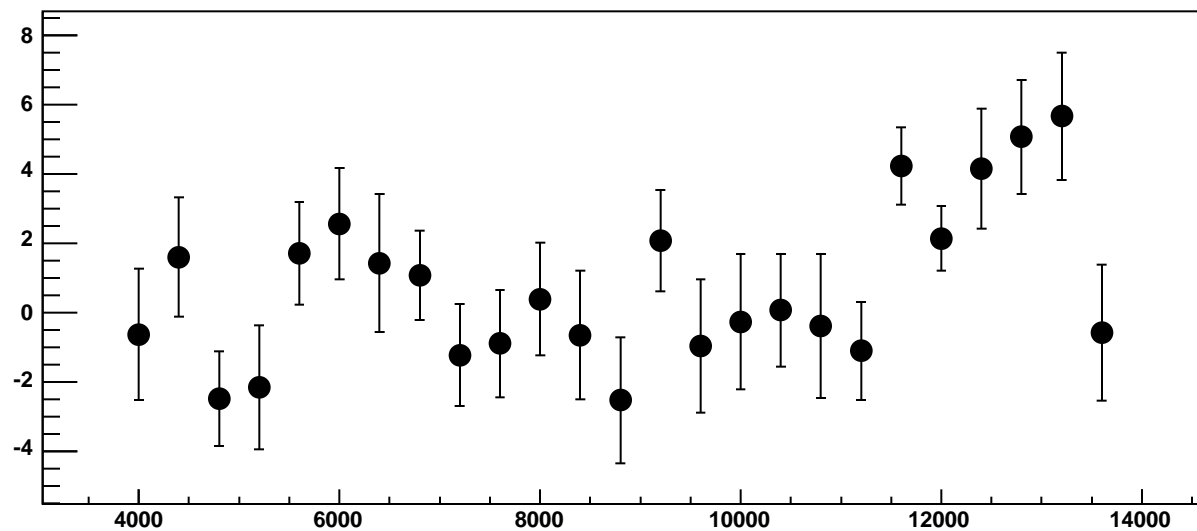
p1

0.01719 ± 0.0001972

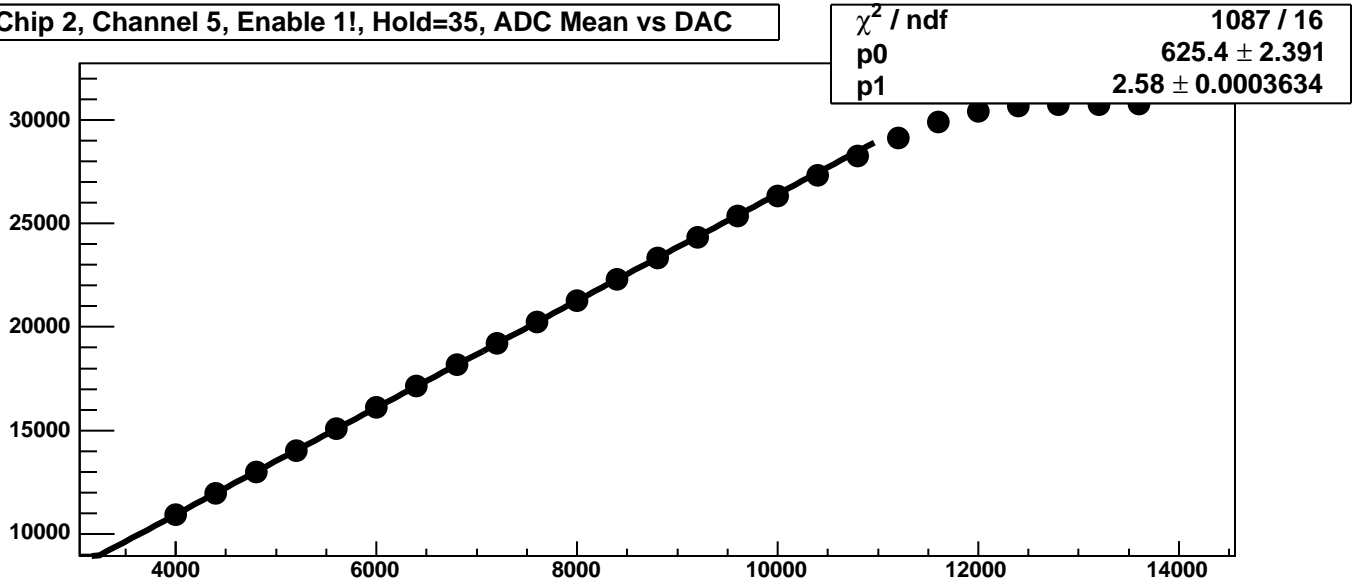
Chip 2, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



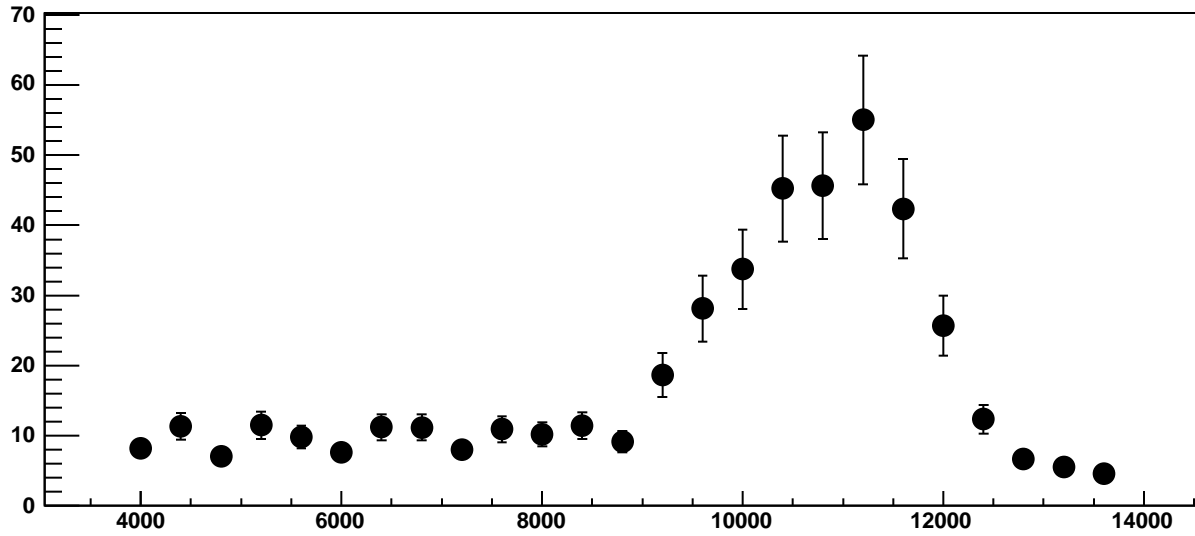
Chip 2, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



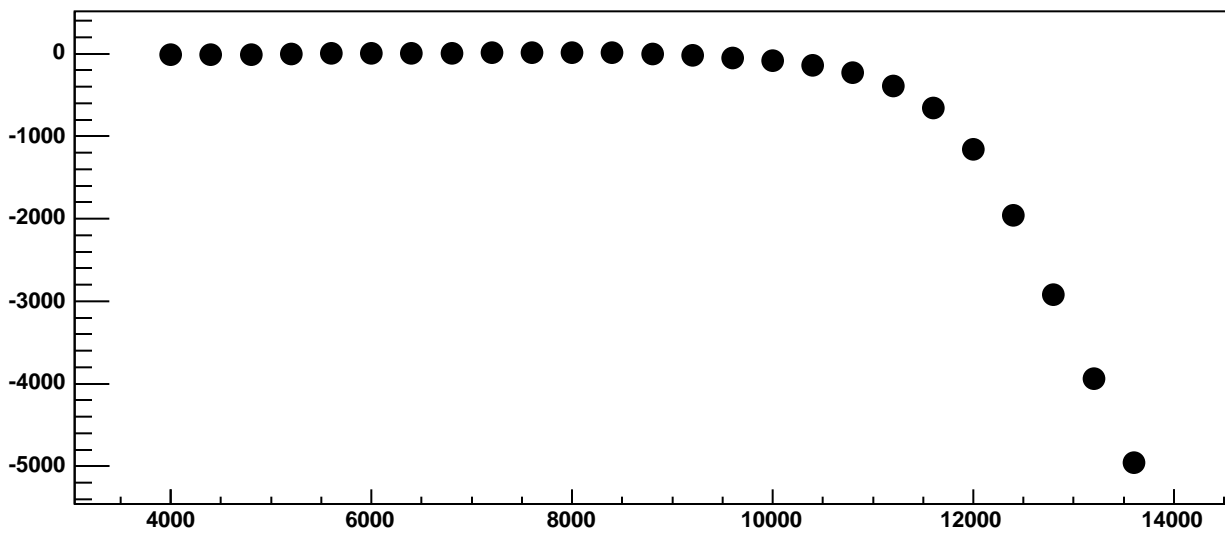
Chip 2, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC



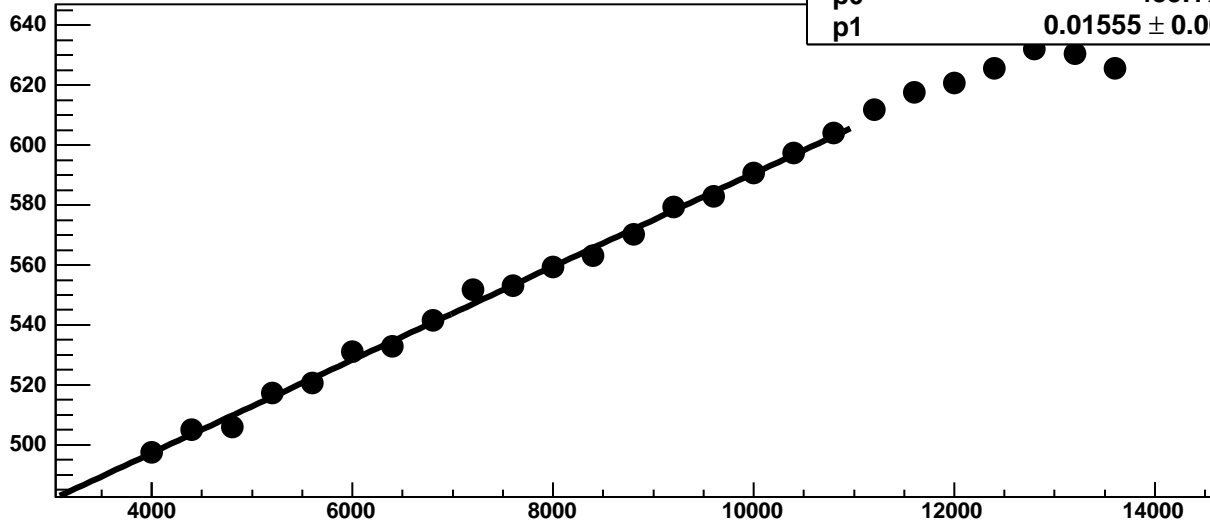
Chip 2, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



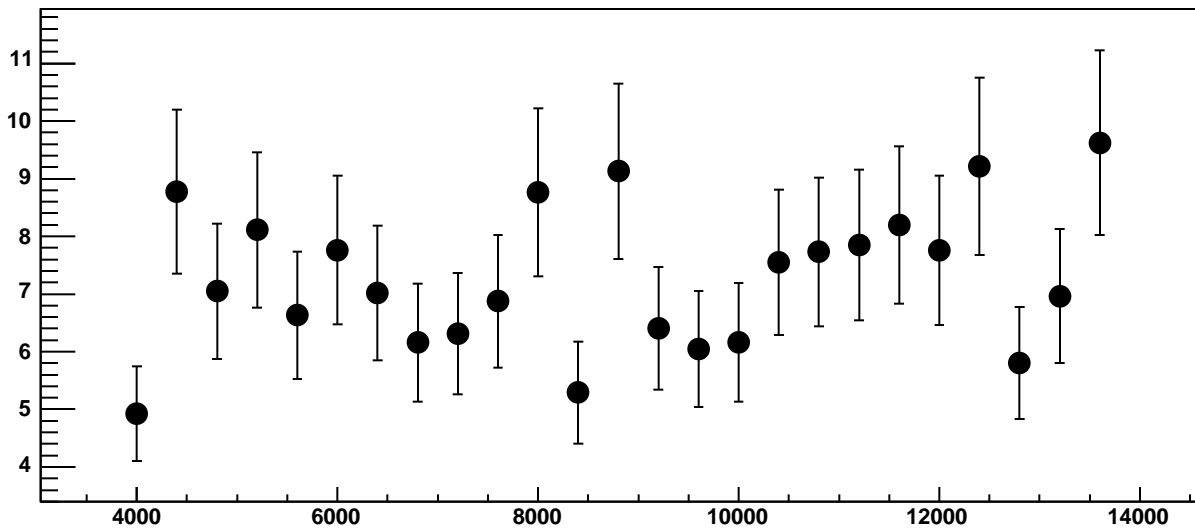
Chip 2, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC



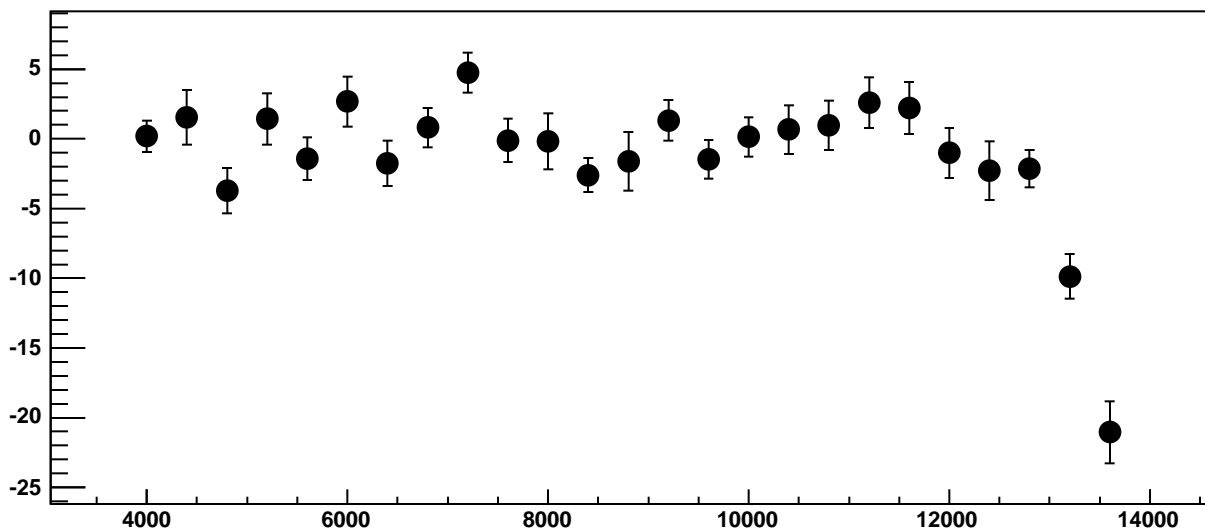
Chip 2, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



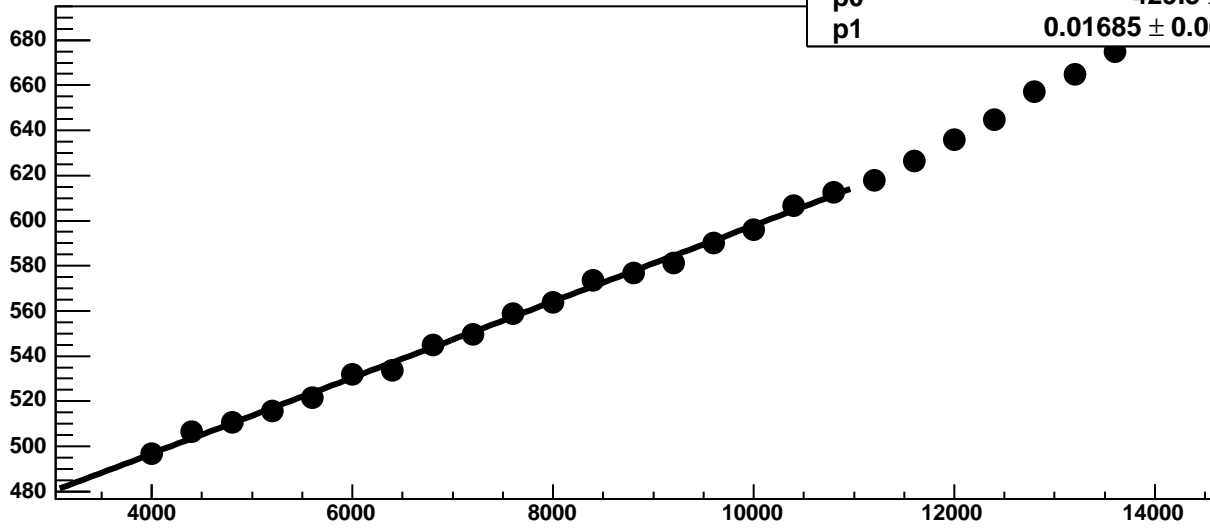
Chip 2, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

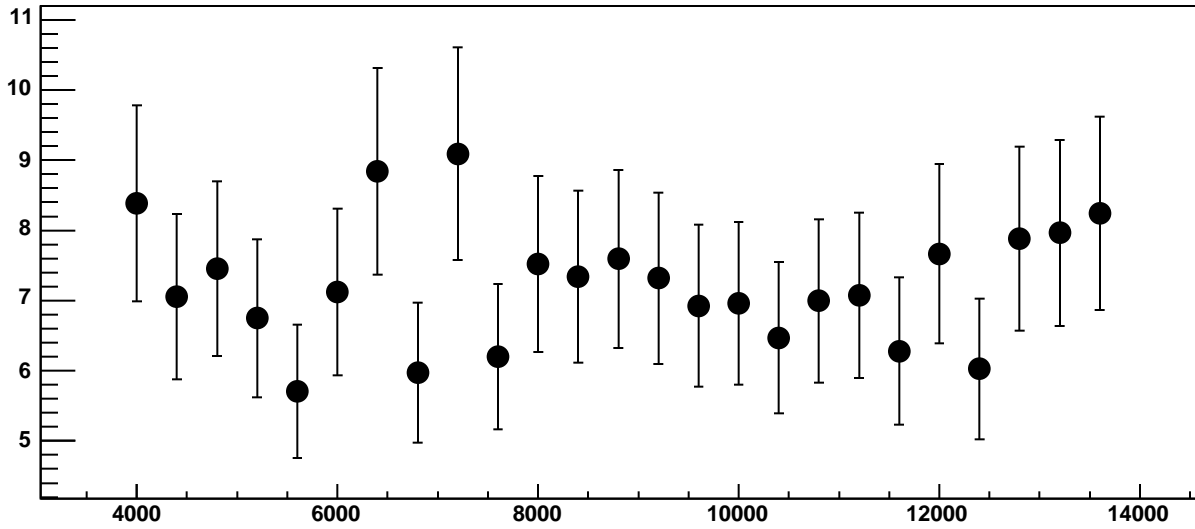


Chip 2, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

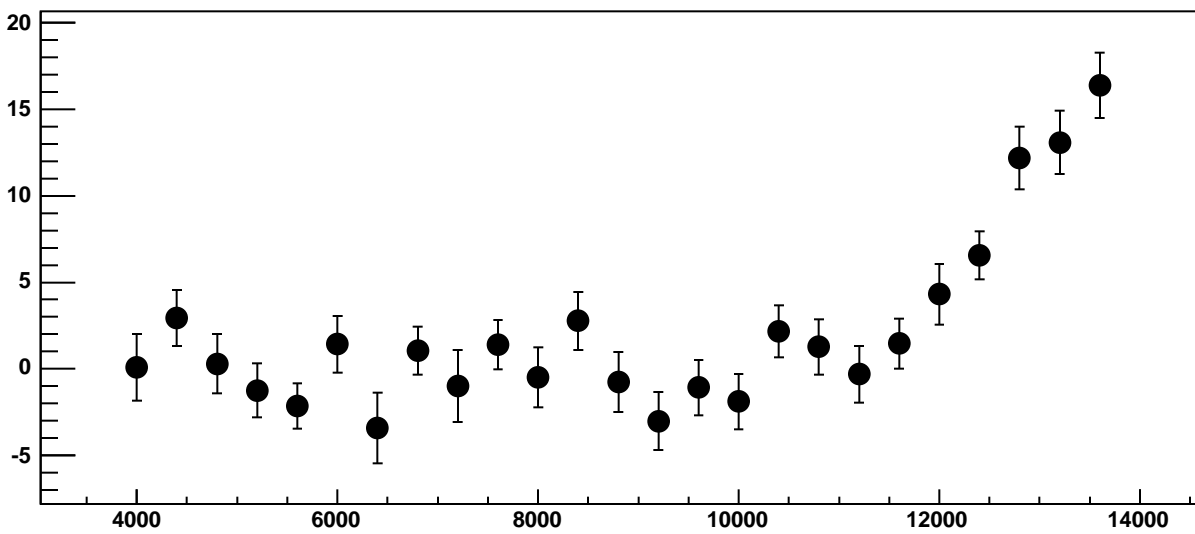


χ^2 / ndf 22.86 / 16
p0 429.3 ± 1.425
p1 0.01685 ± 0.0001845

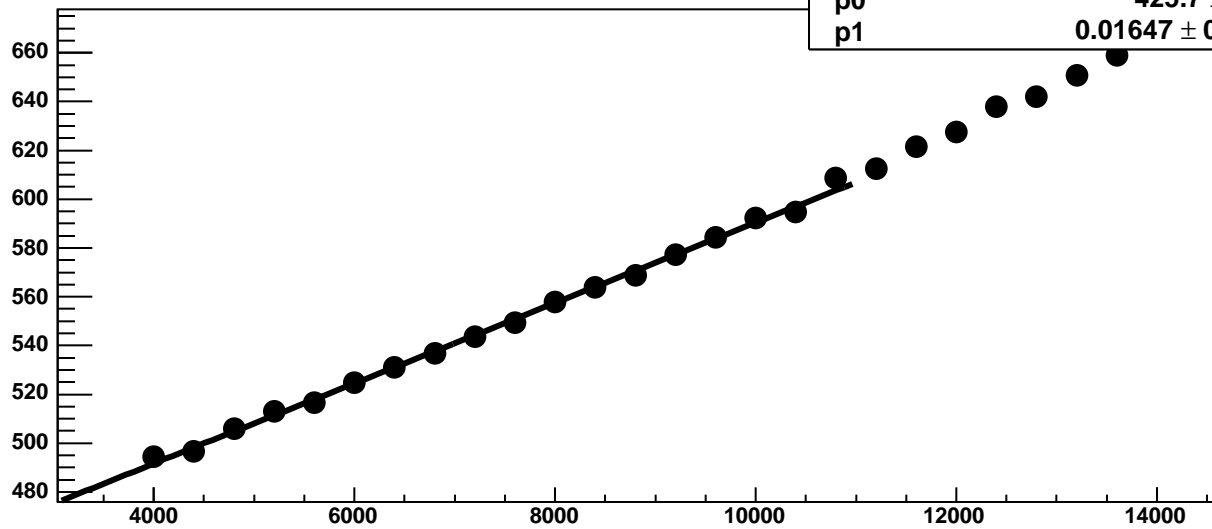
Chip 2, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

19.08 / 16

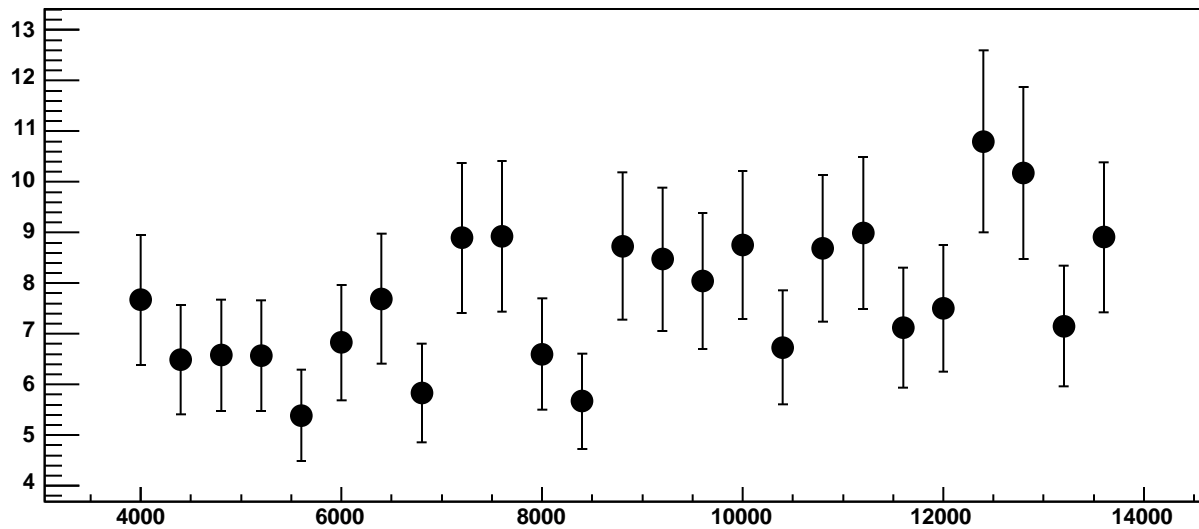
p0

425.7 ± 1.406

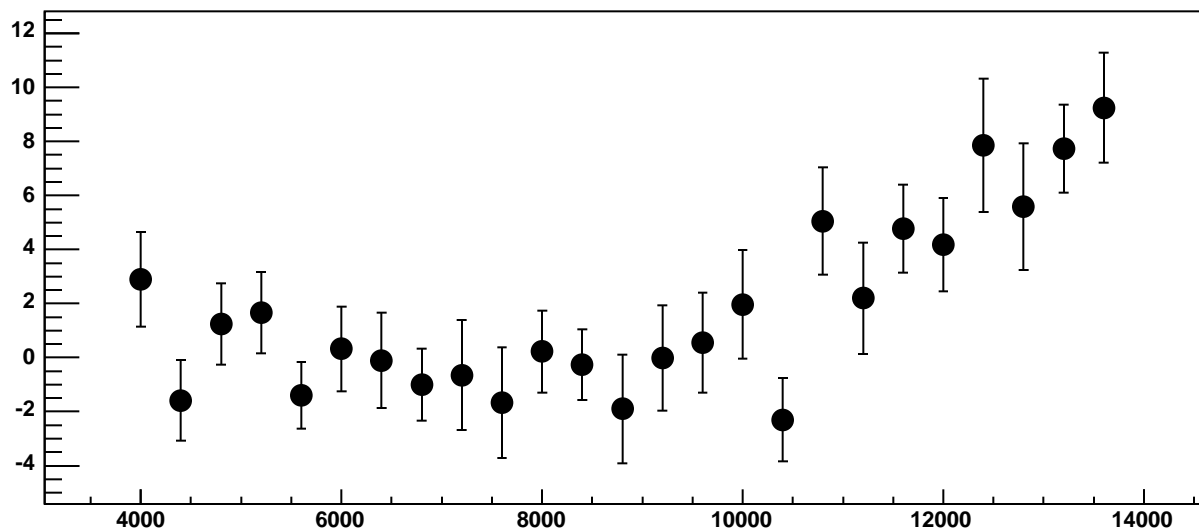
p1

0.01647 ± 0.00019

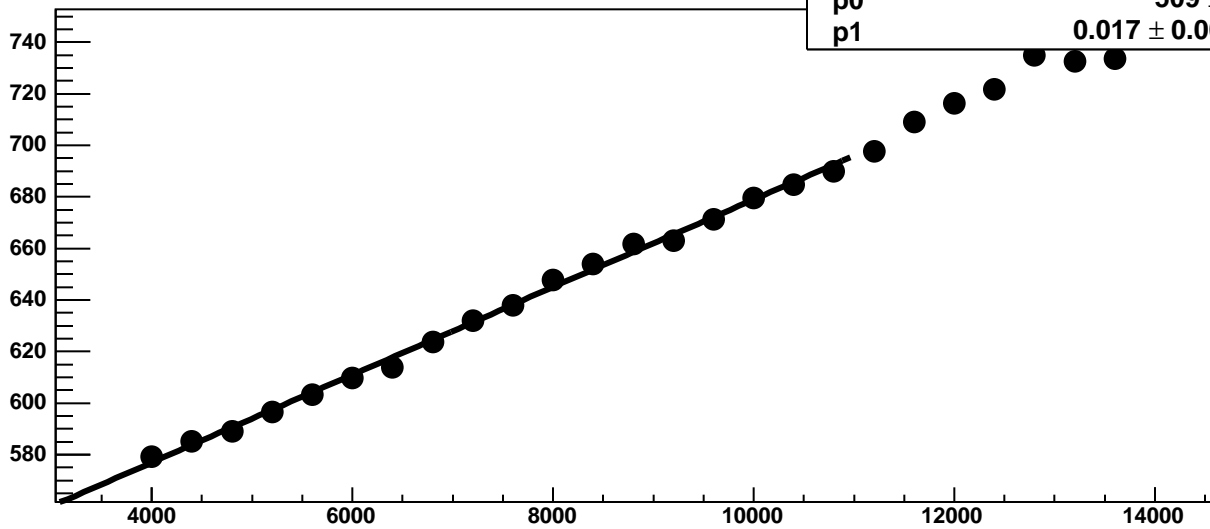
Chip 2, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

29.95 / 16

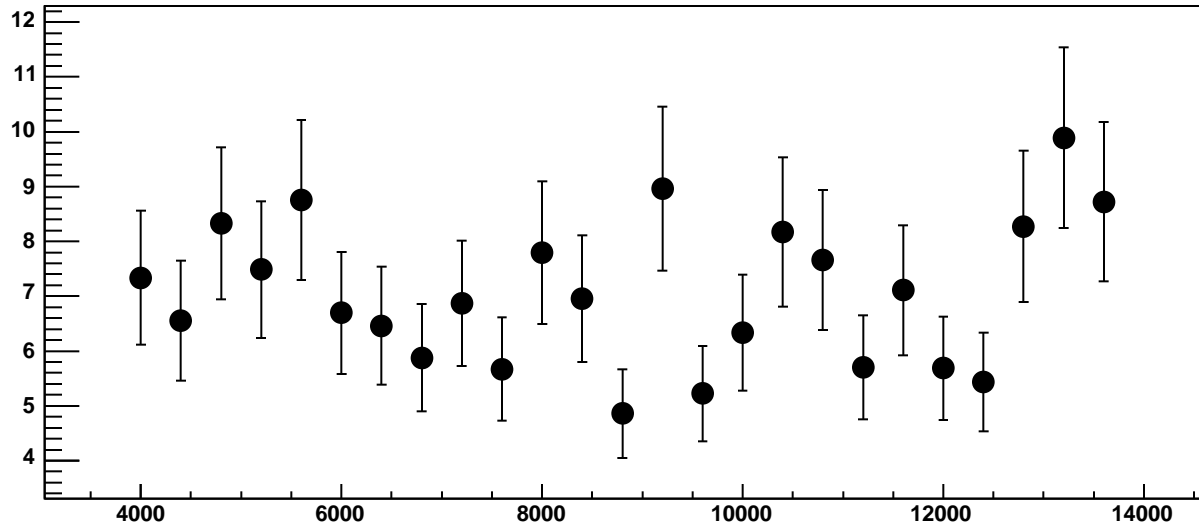
p0

509 ± 1.436

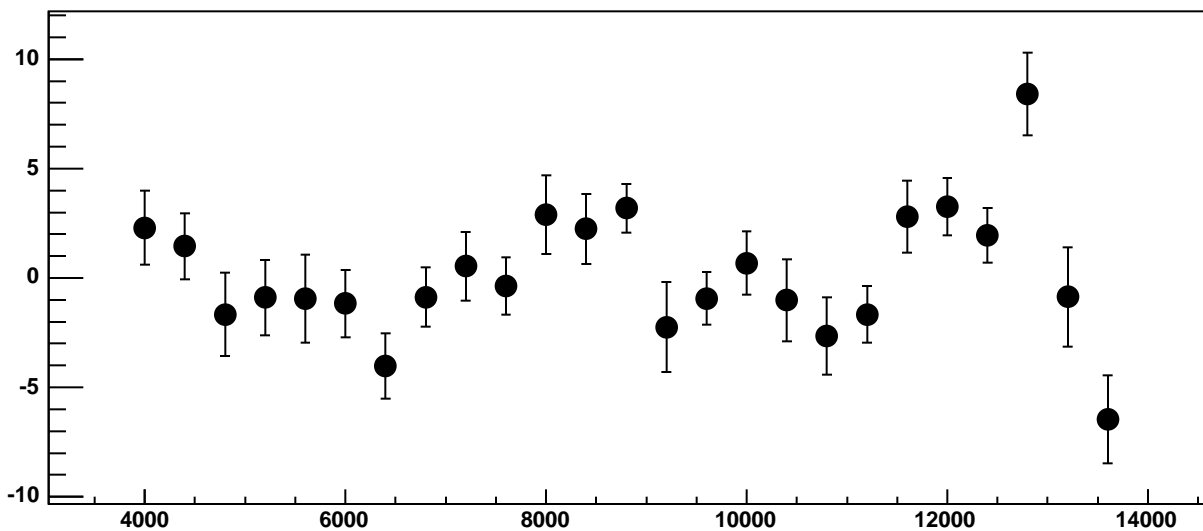
p1

0.017 ± 0.0001843

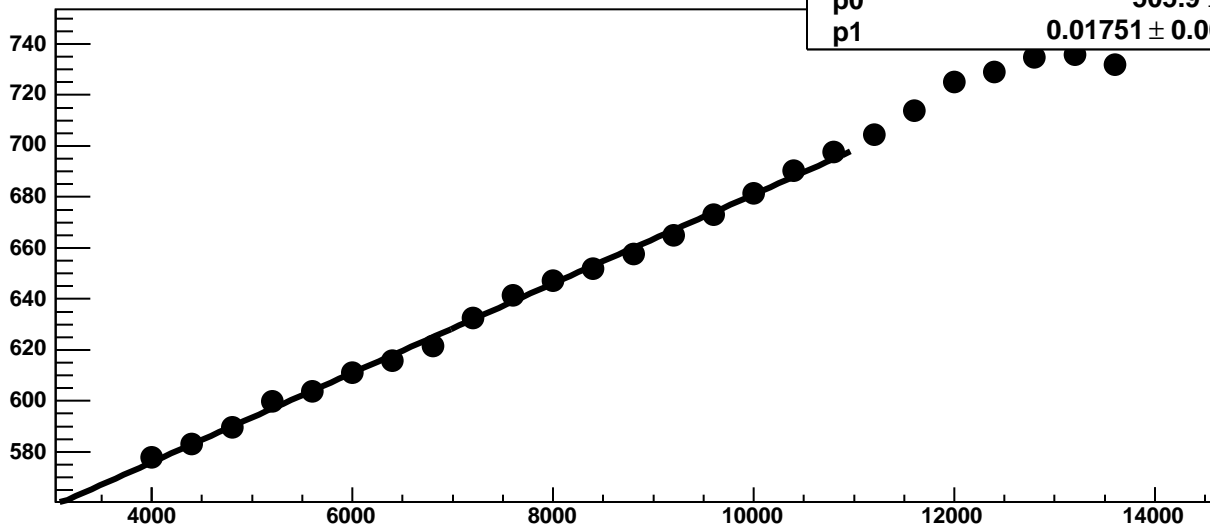
Chip 2, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



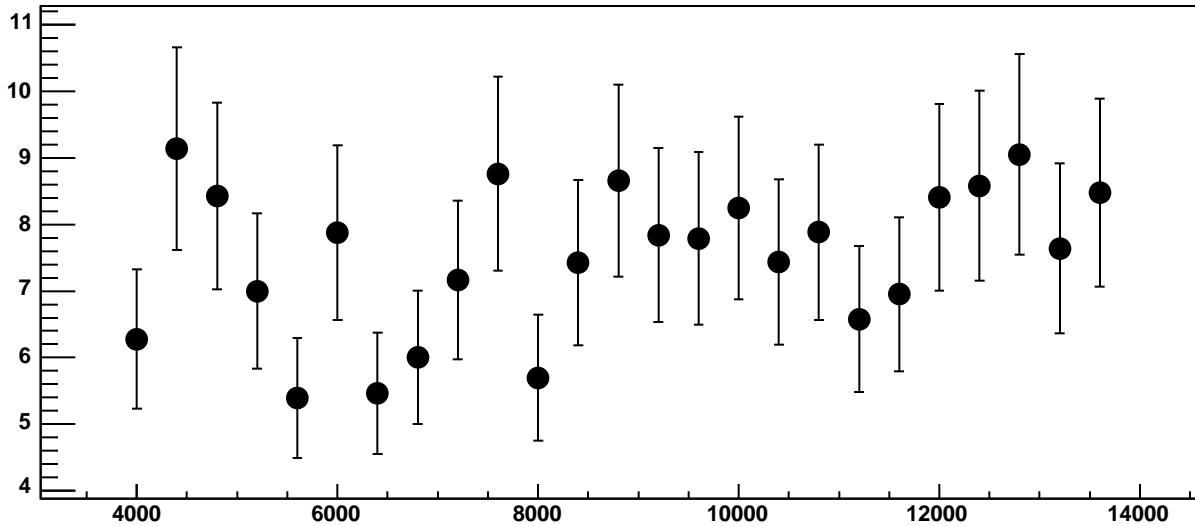
Chip 2, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



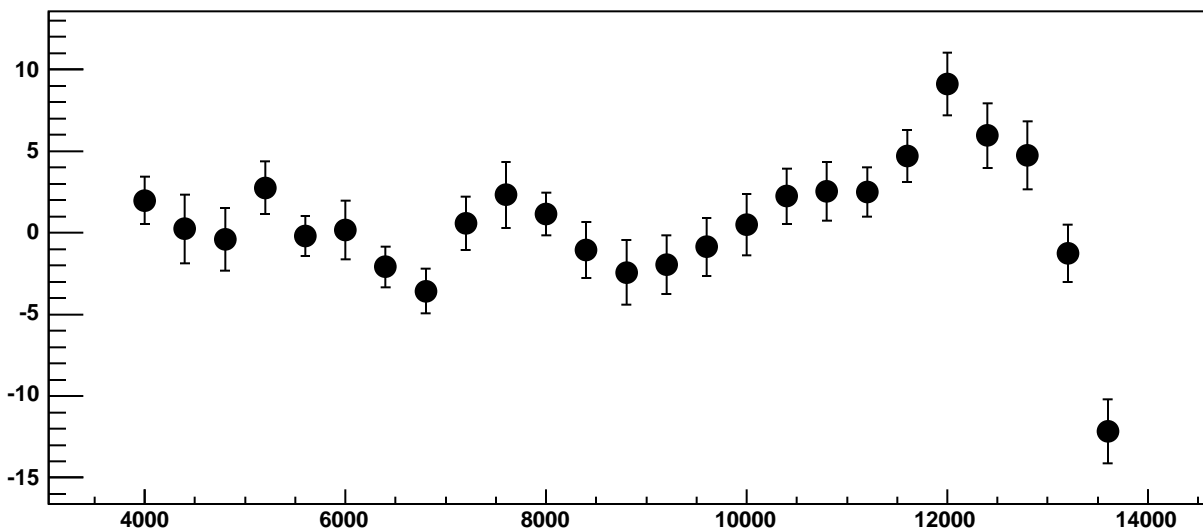
Chip 2, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



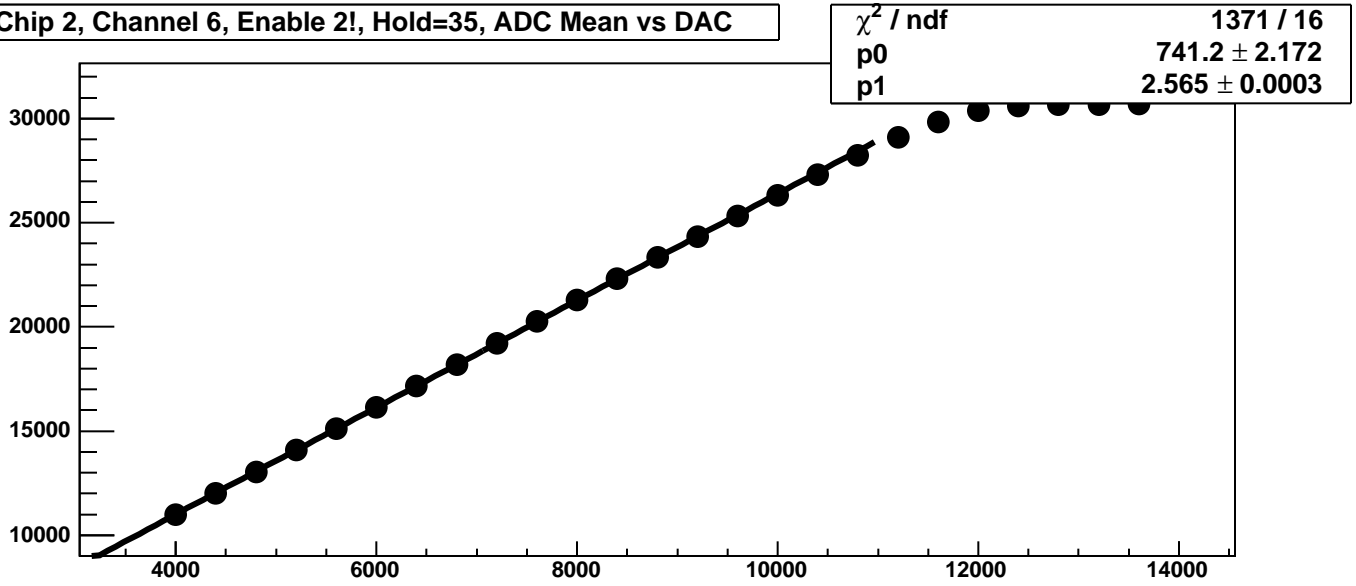
Chip 2, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



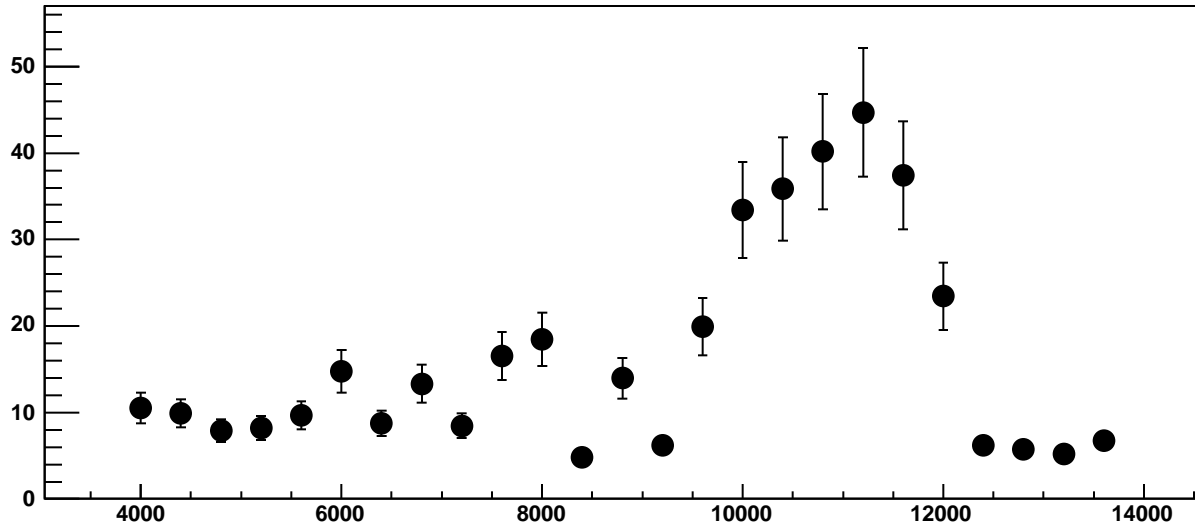
Chip 2, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



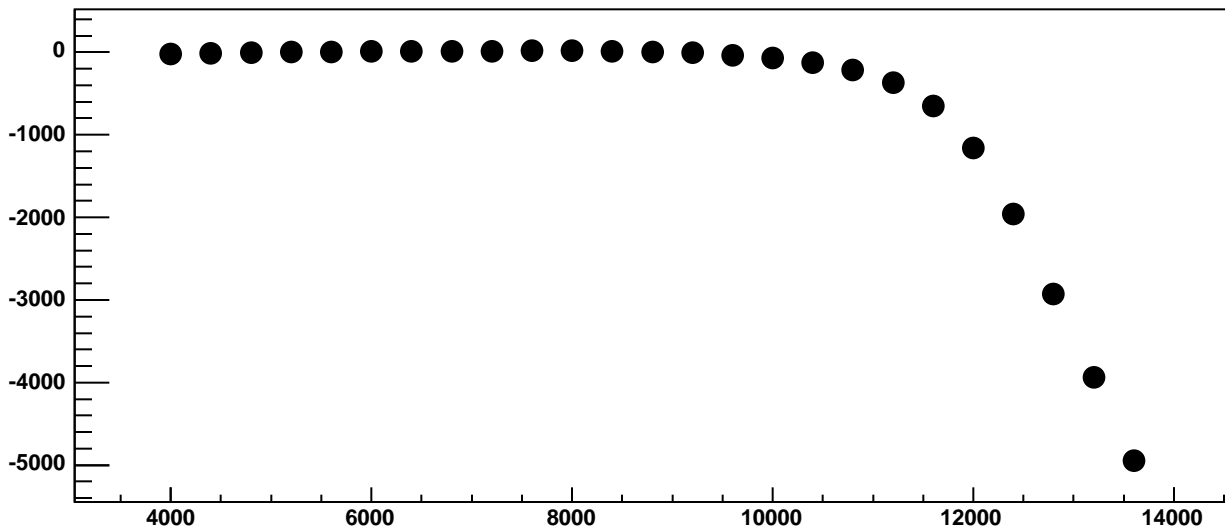
Chip 2, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



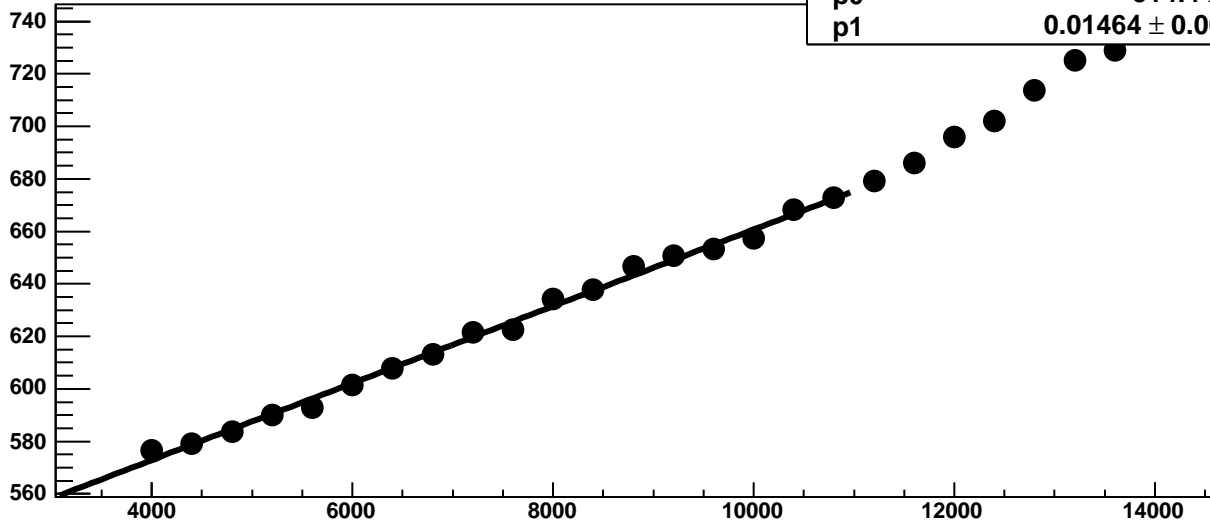
Chip 2, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



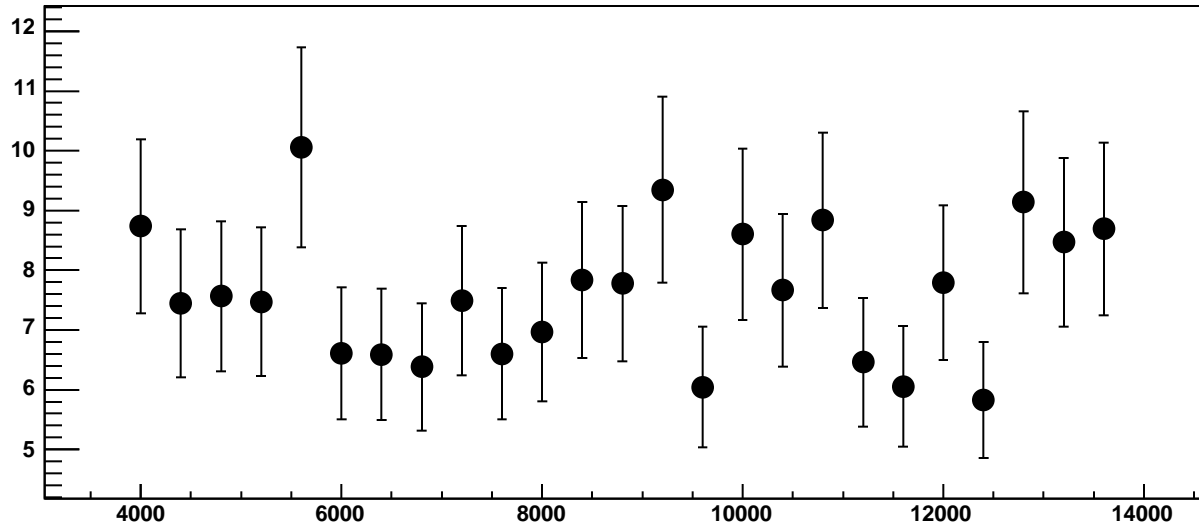
Chip 2, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC



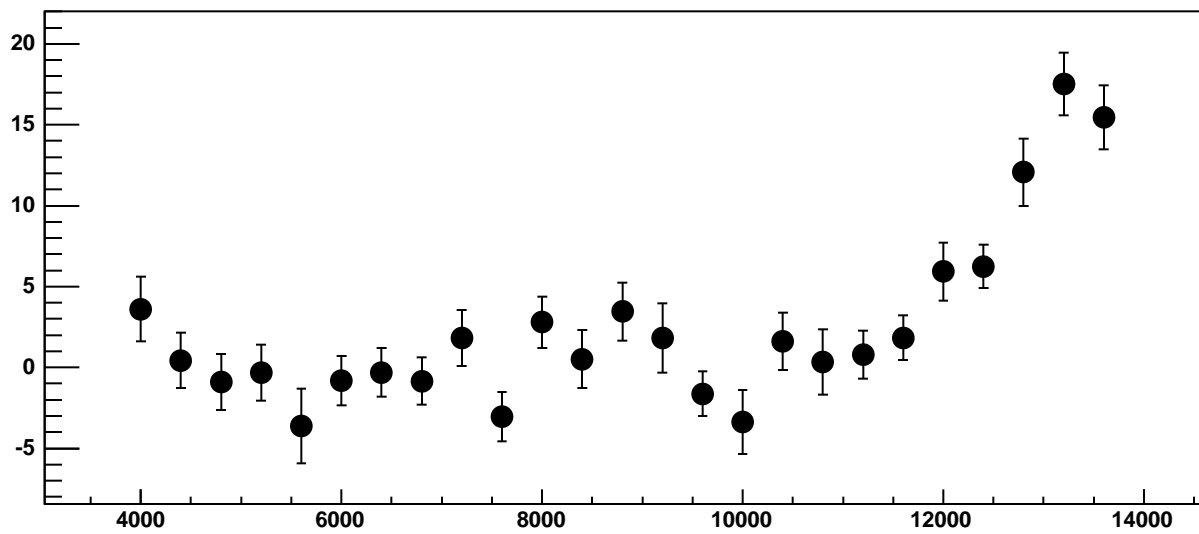
Chip 2, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



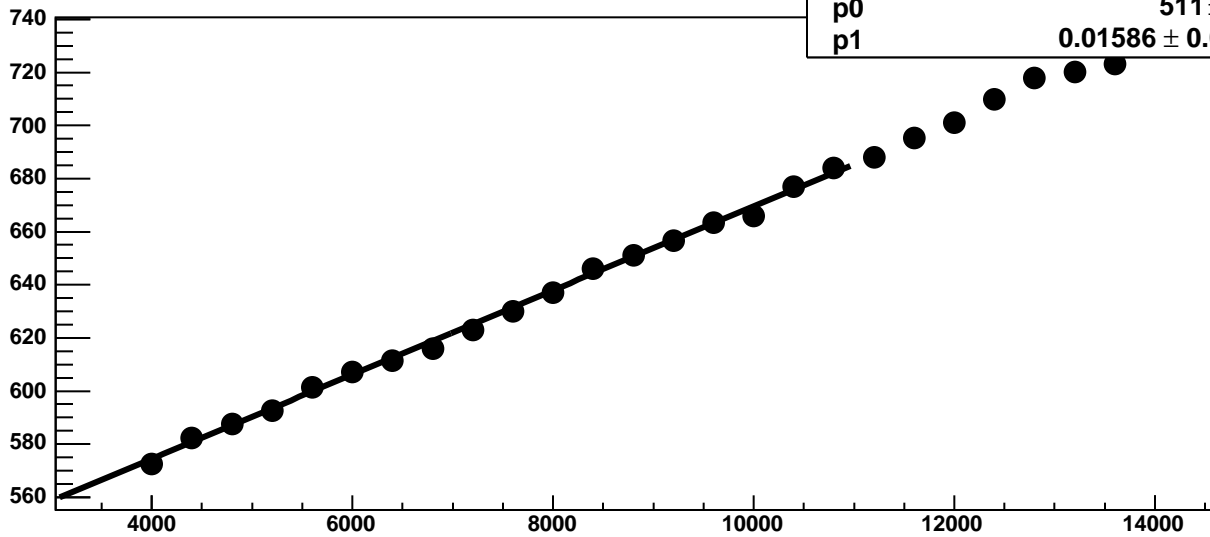
Chip 2, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

18.67 / 16

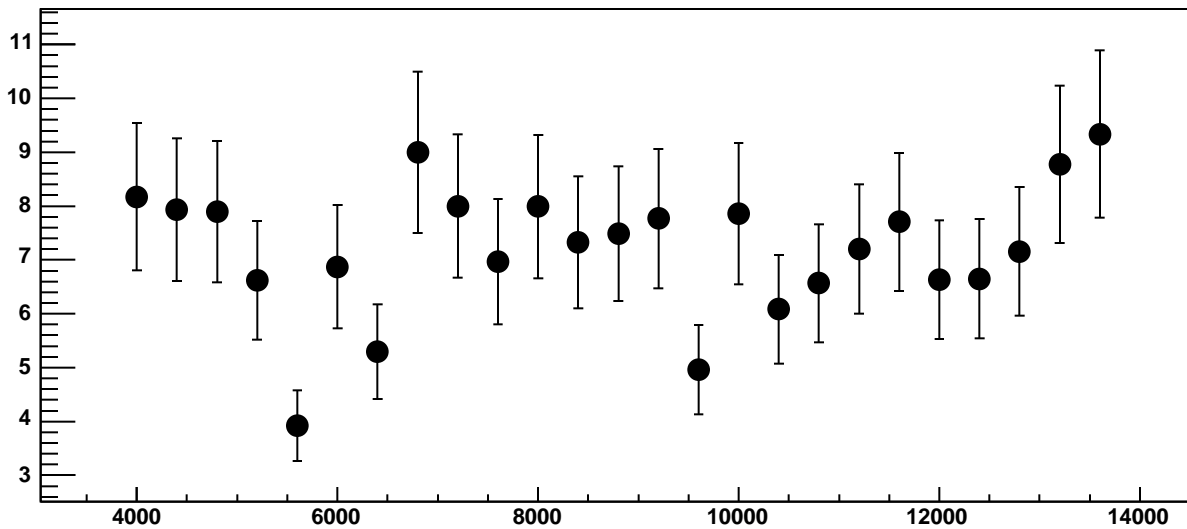
p0

511 ± 1.327

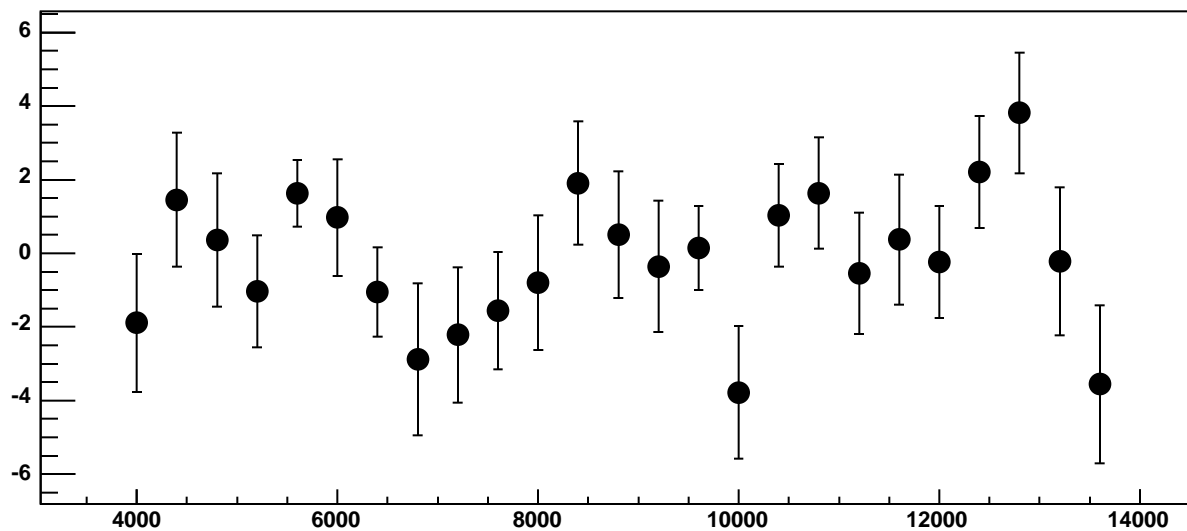
p1

0.01586 ± 0.000173

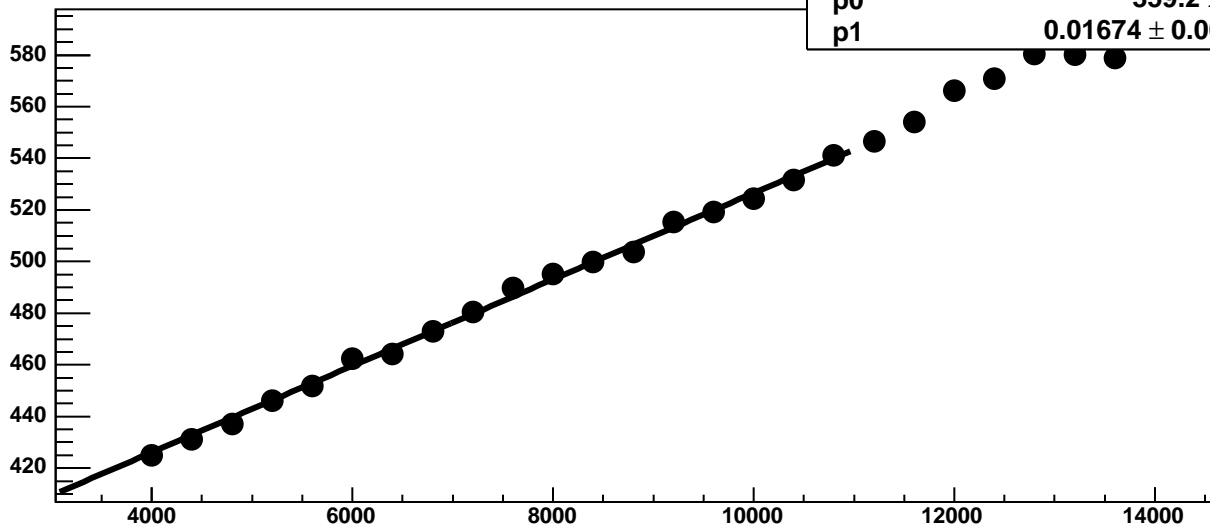
Chip 2, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



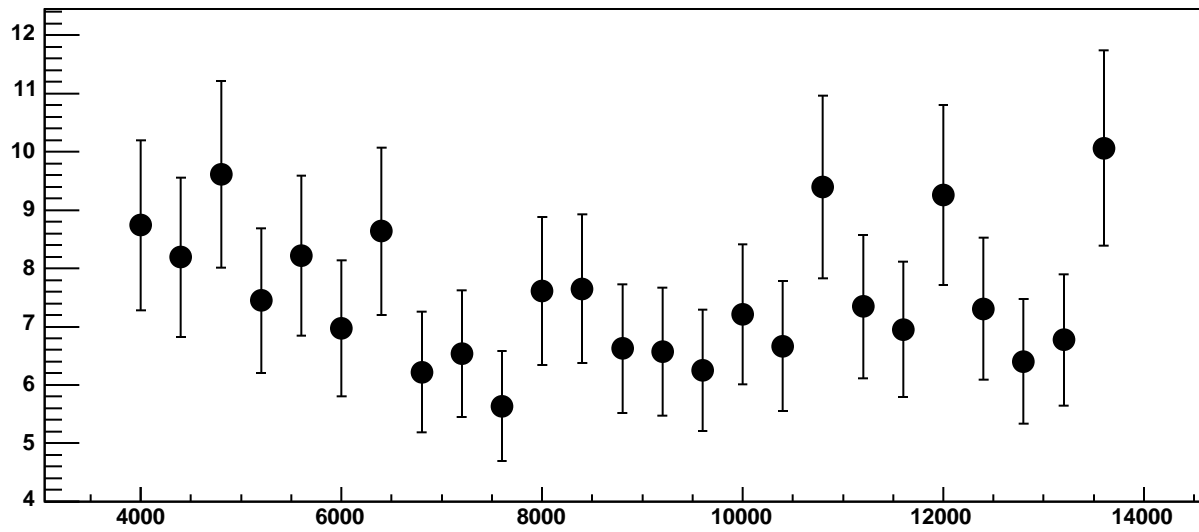
Chip 2, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



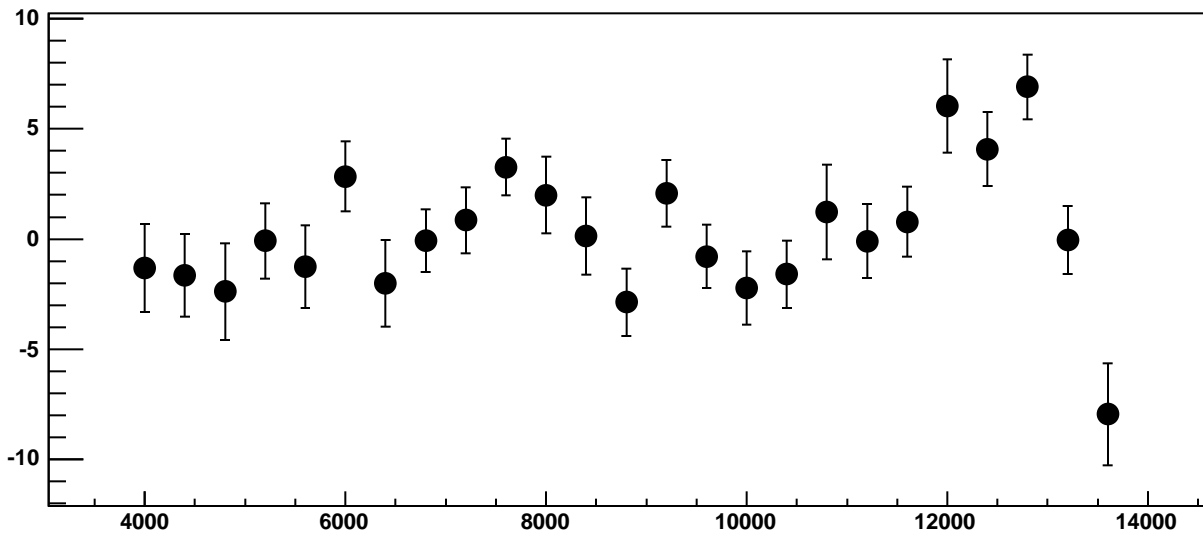
Chip 2, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



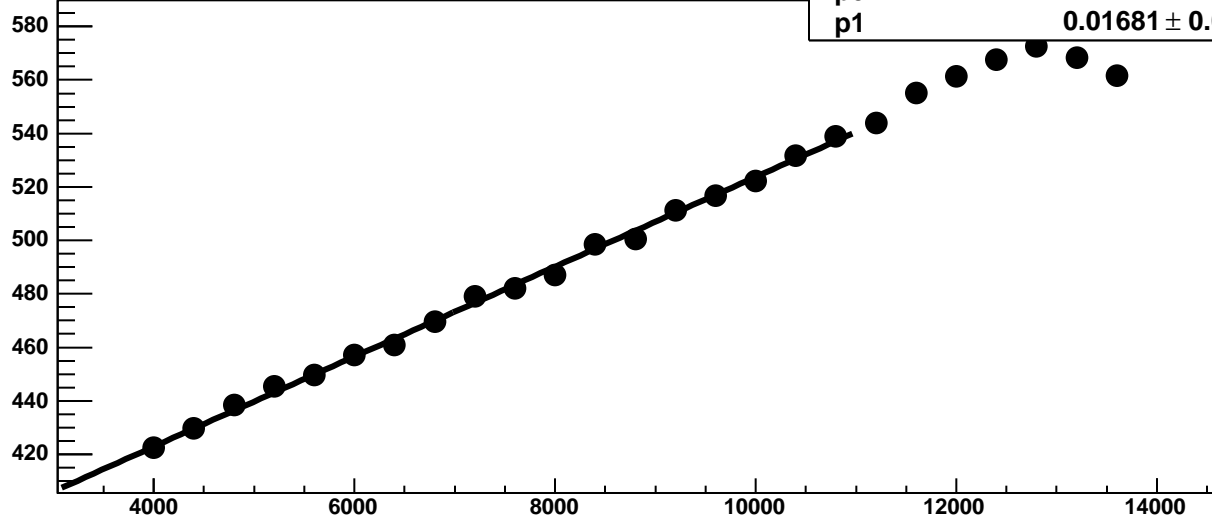
Chip 2, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



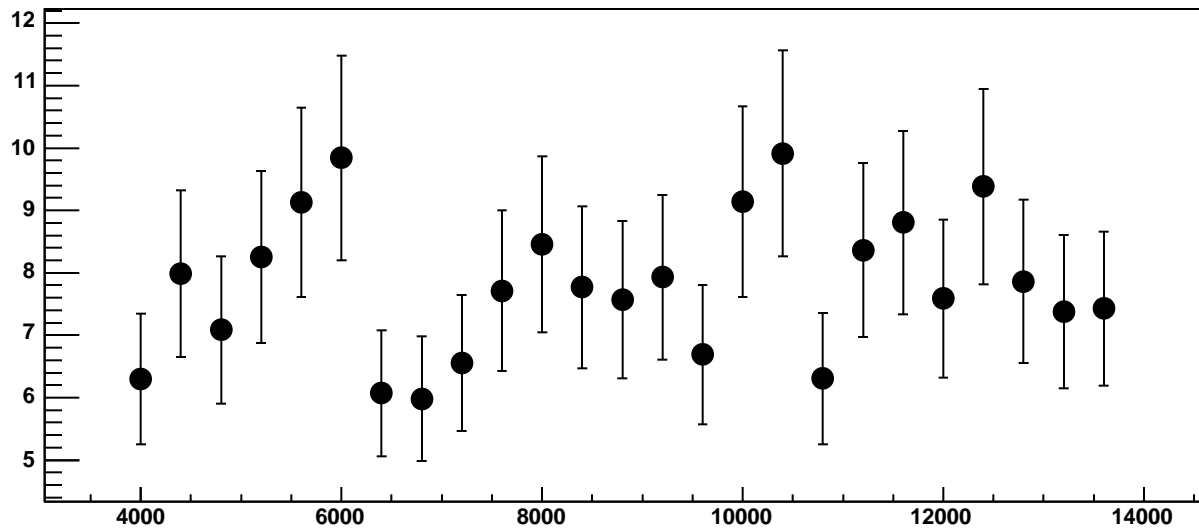
Chip 2, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



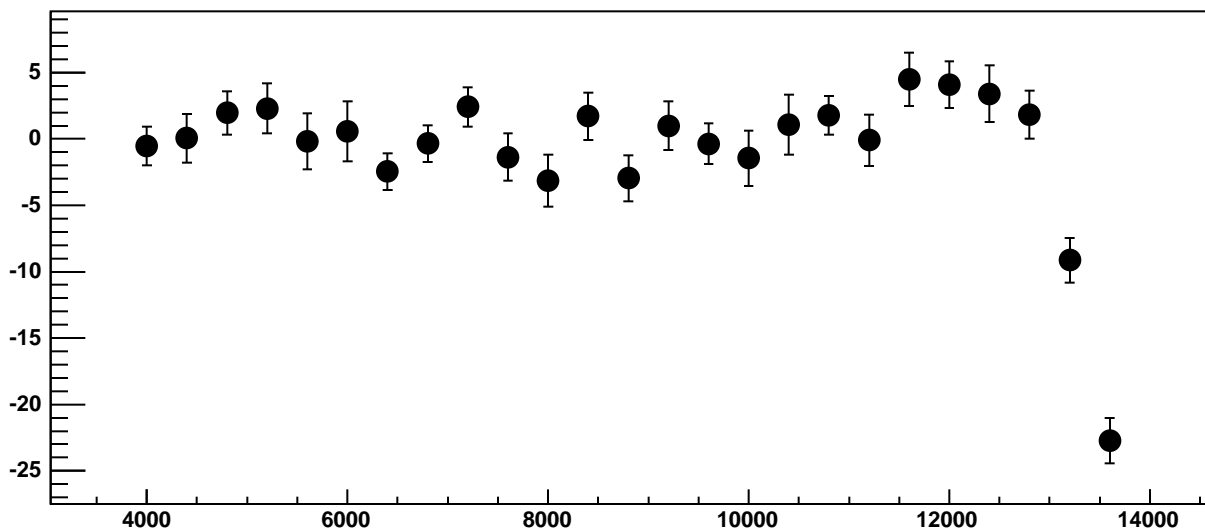
Chip 2, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



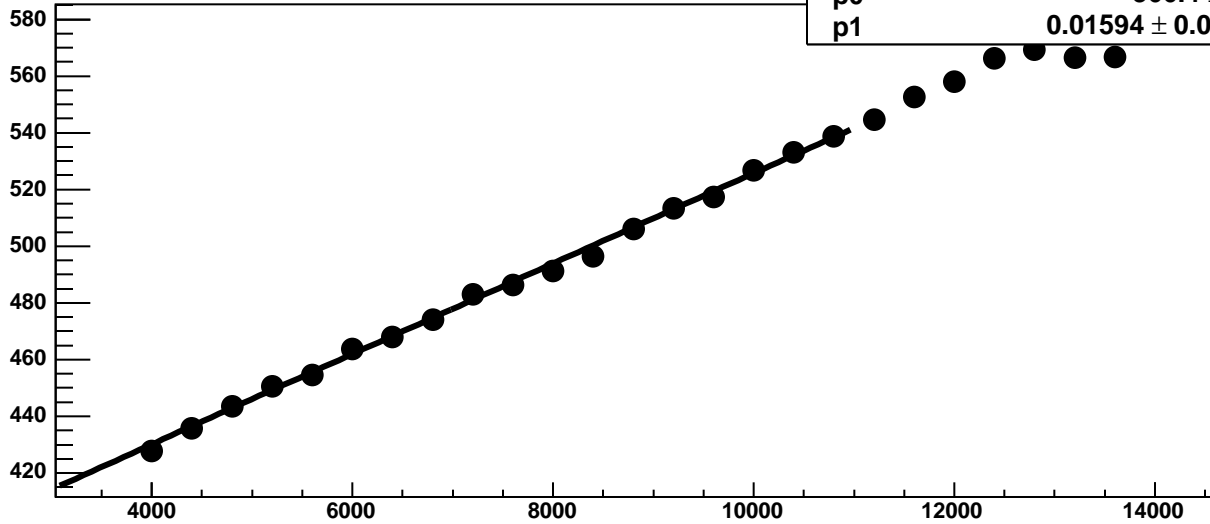
Chip 2, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



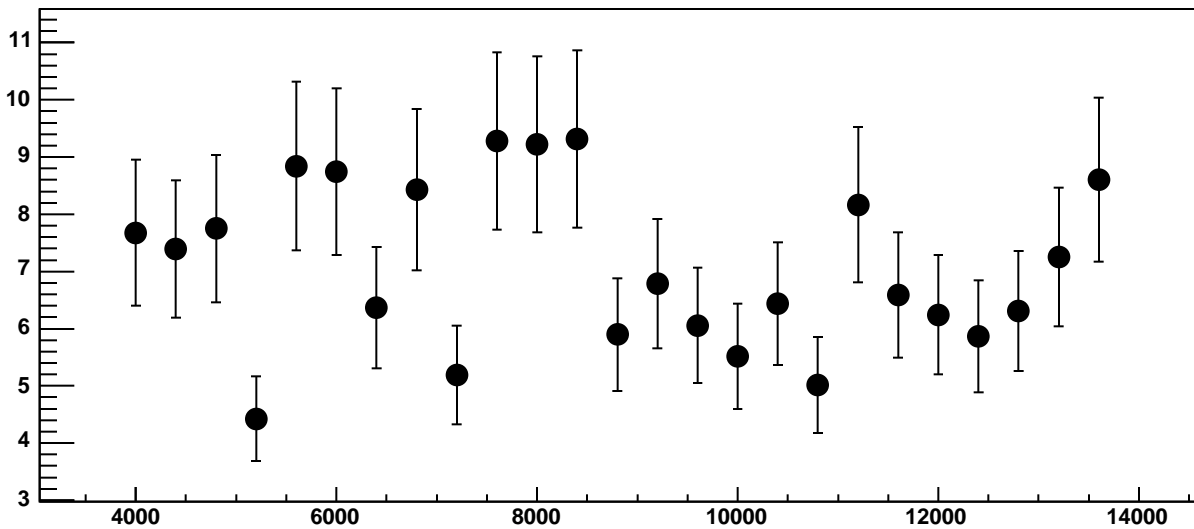
Chip 2, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



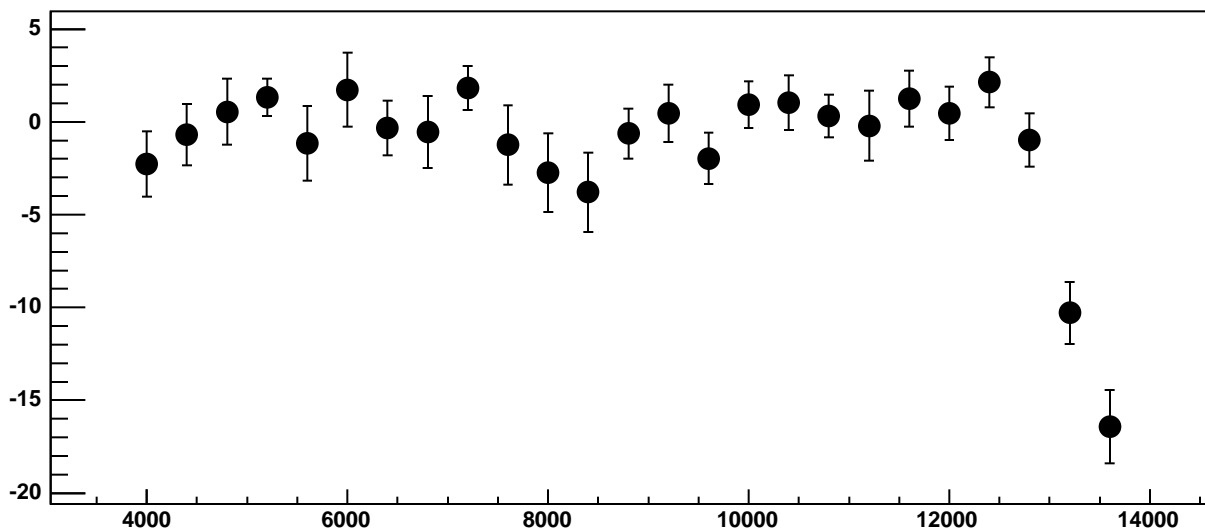
Chip 2, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC



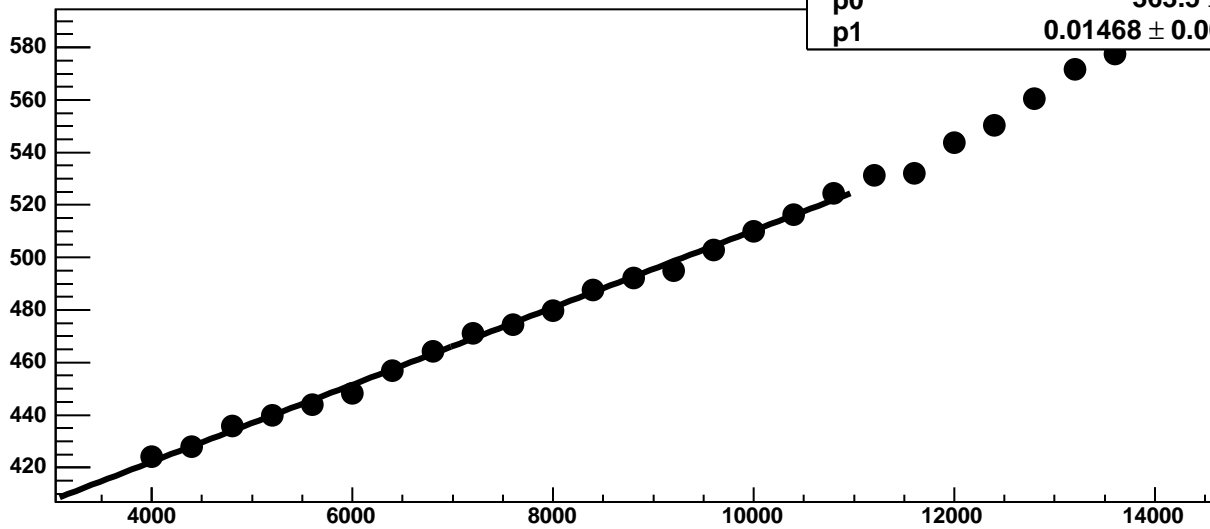
Chip 2, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

20.5 / 16

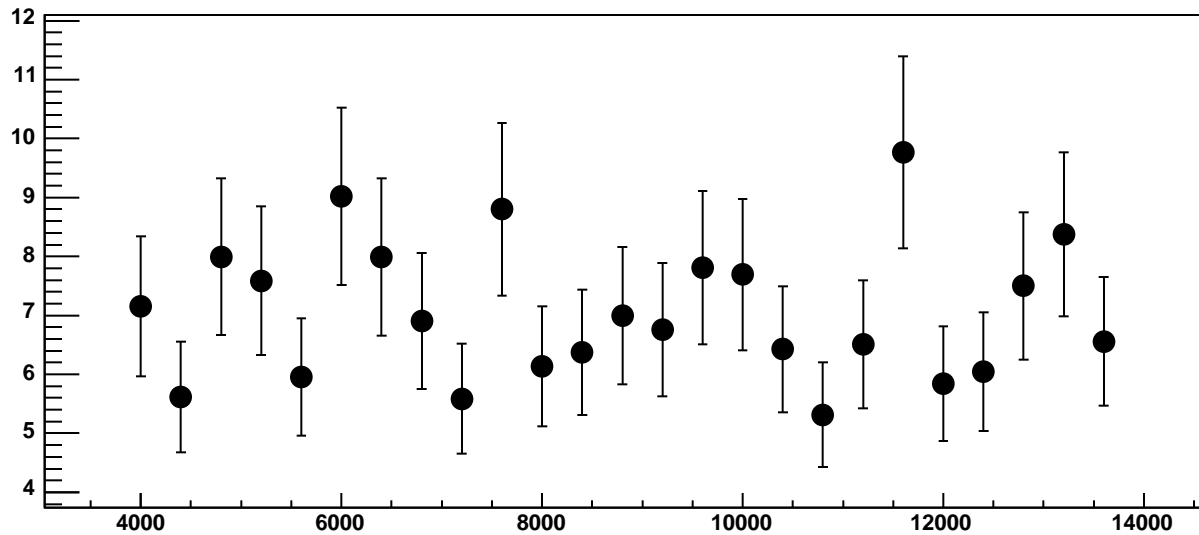
p0

363.5 ± 1.333

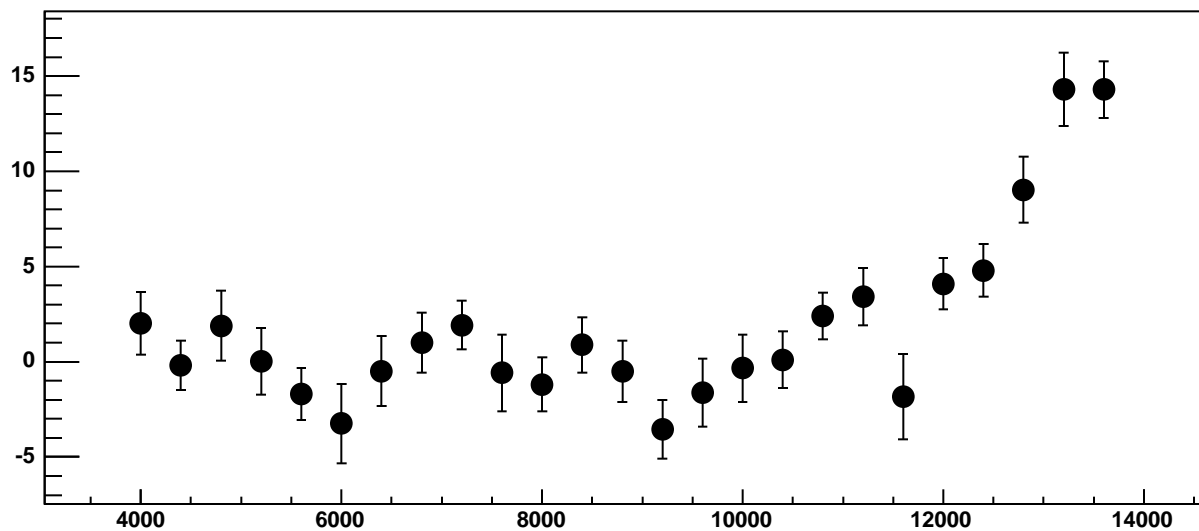
p1

0.01468 ± 0.0001709

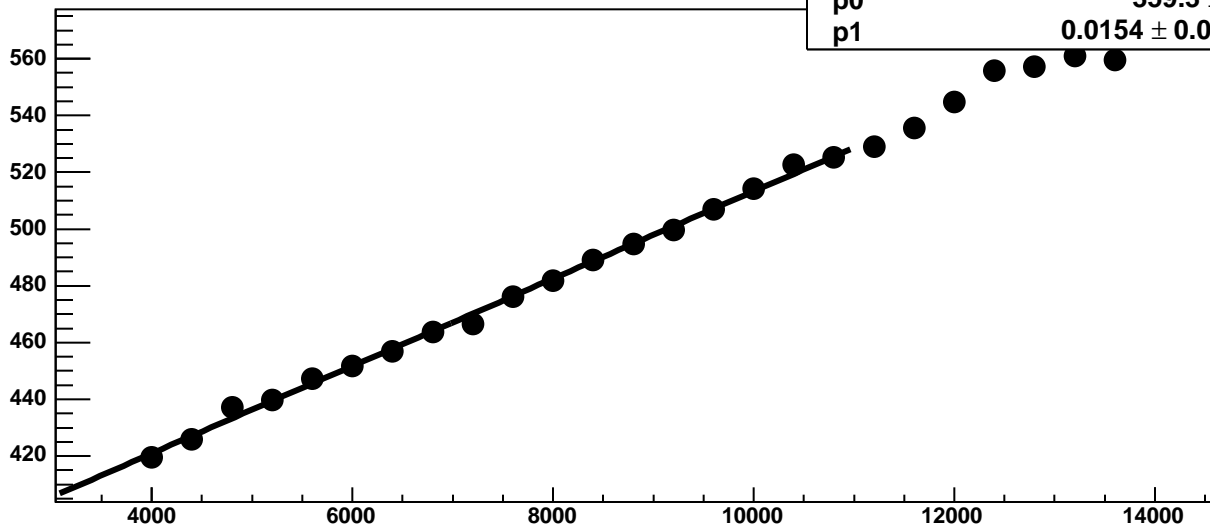
Chip 2, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



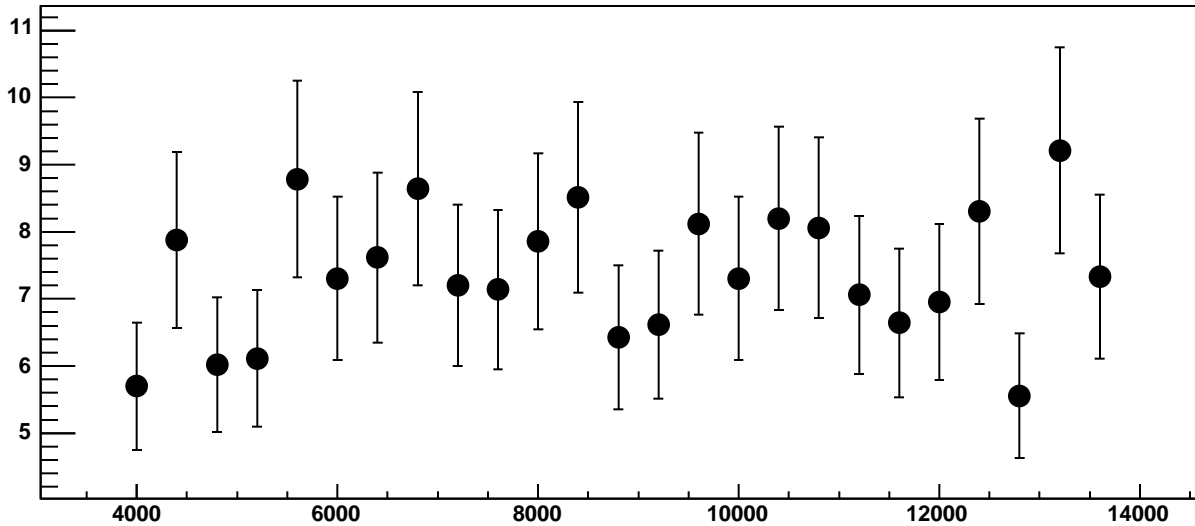
Chip 2, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



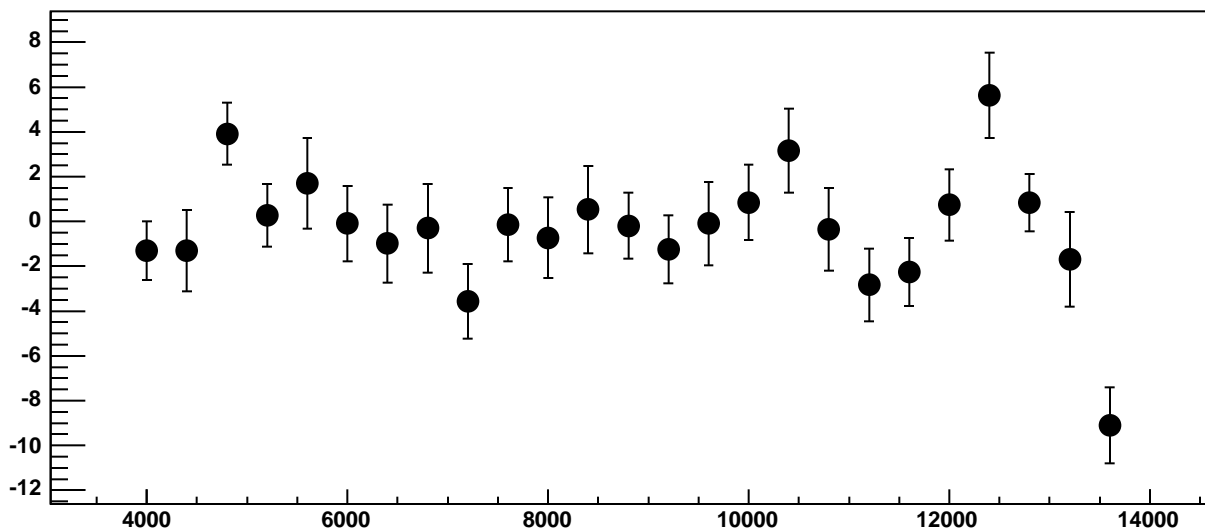
Chip 2, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



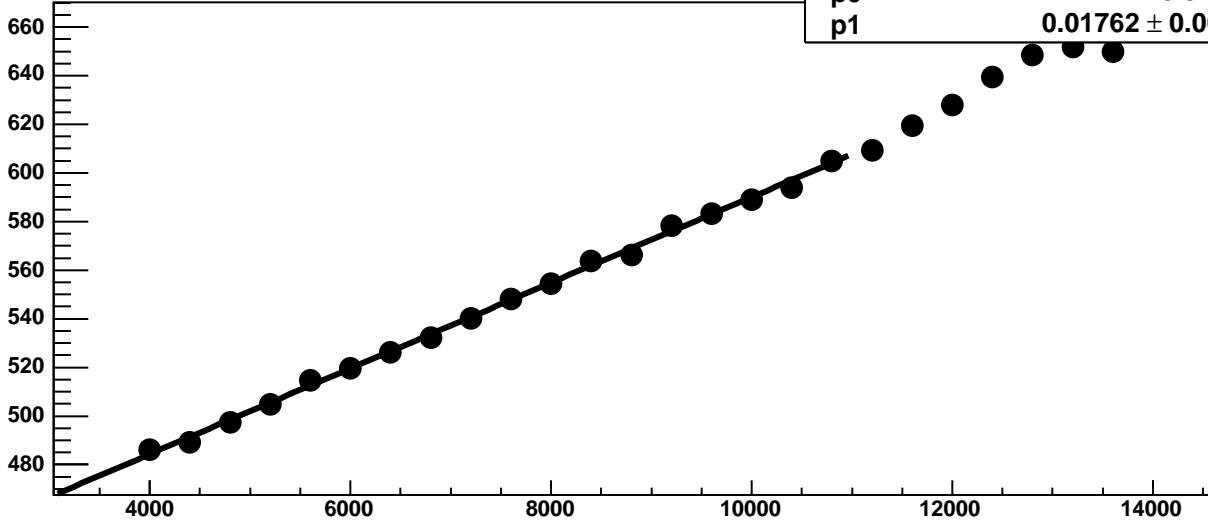
Chip 2, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

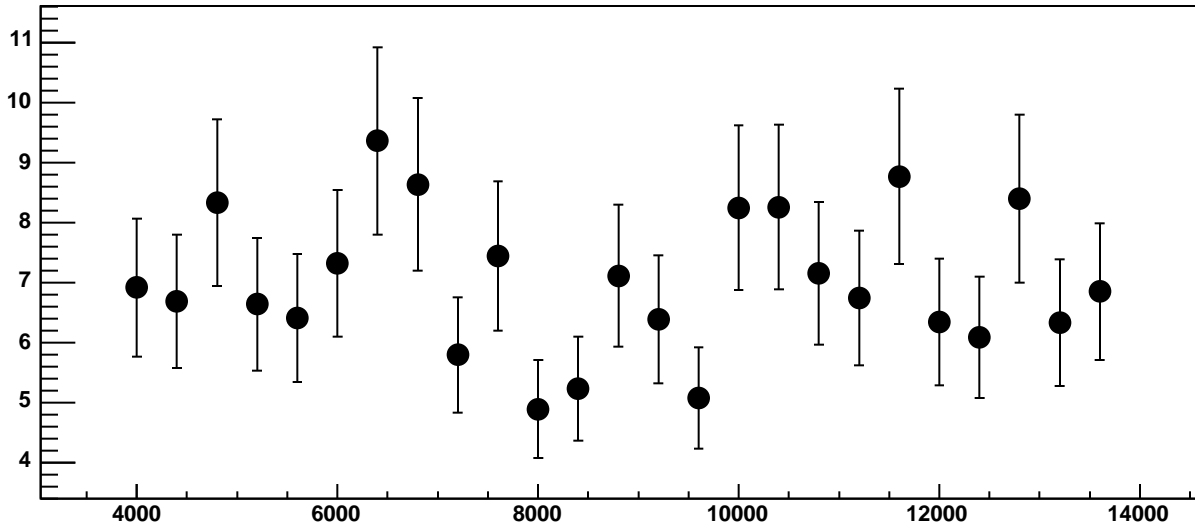


Chip 2, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

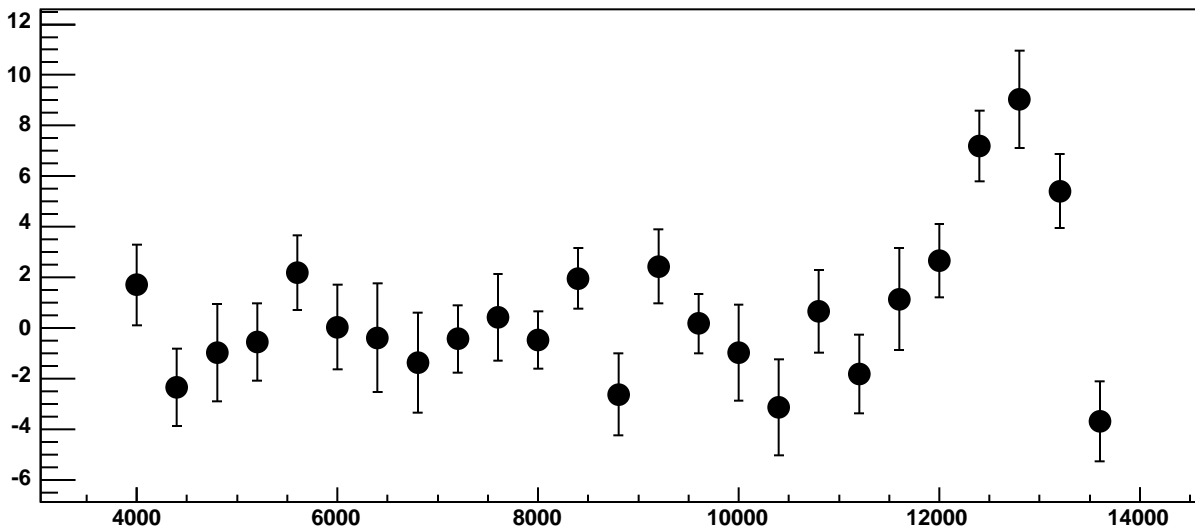


χ^2 / ndf 18.13 / 16
p0 413.9 ± 1.414
p1 0.01762 ± 0.0001818

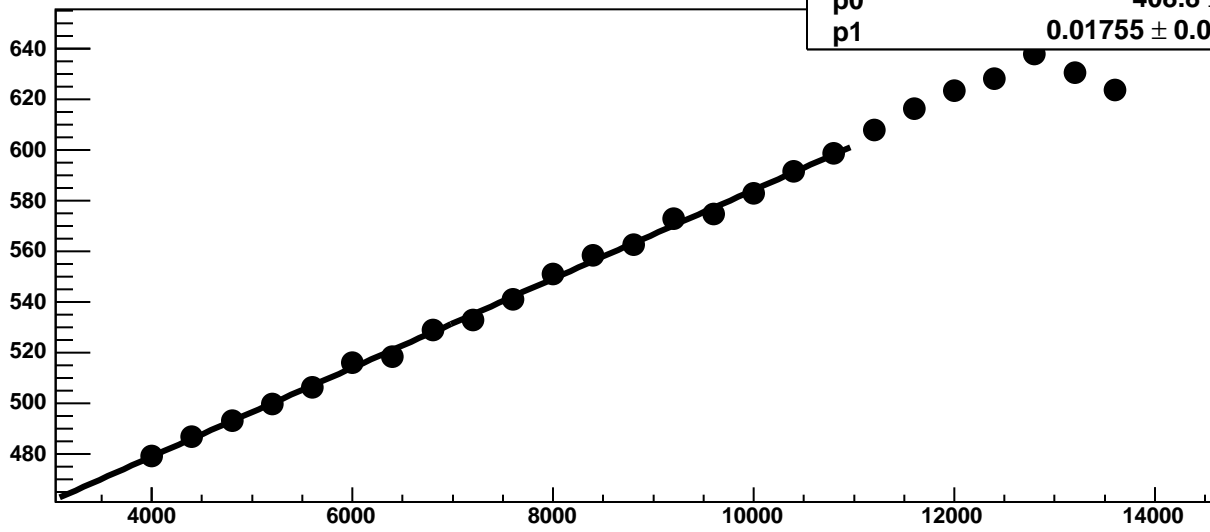
Chip 2, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



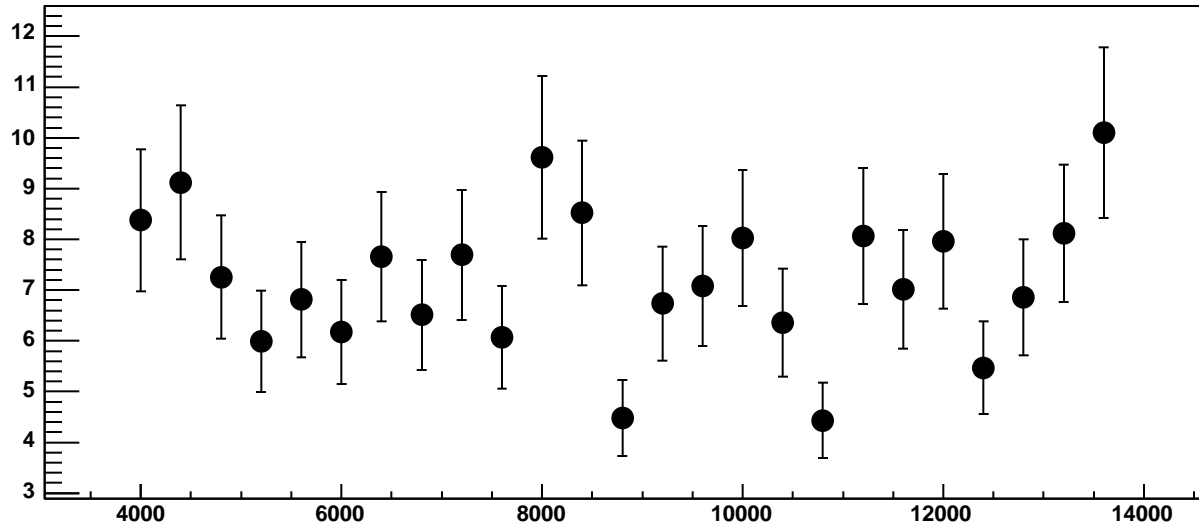
Chip 2, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



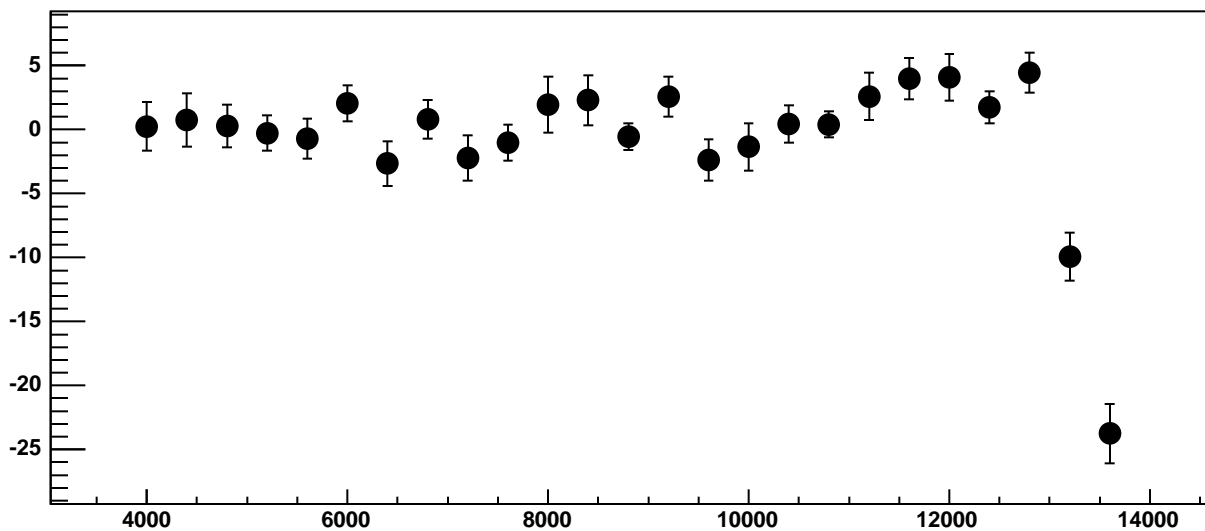
Chip 2, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



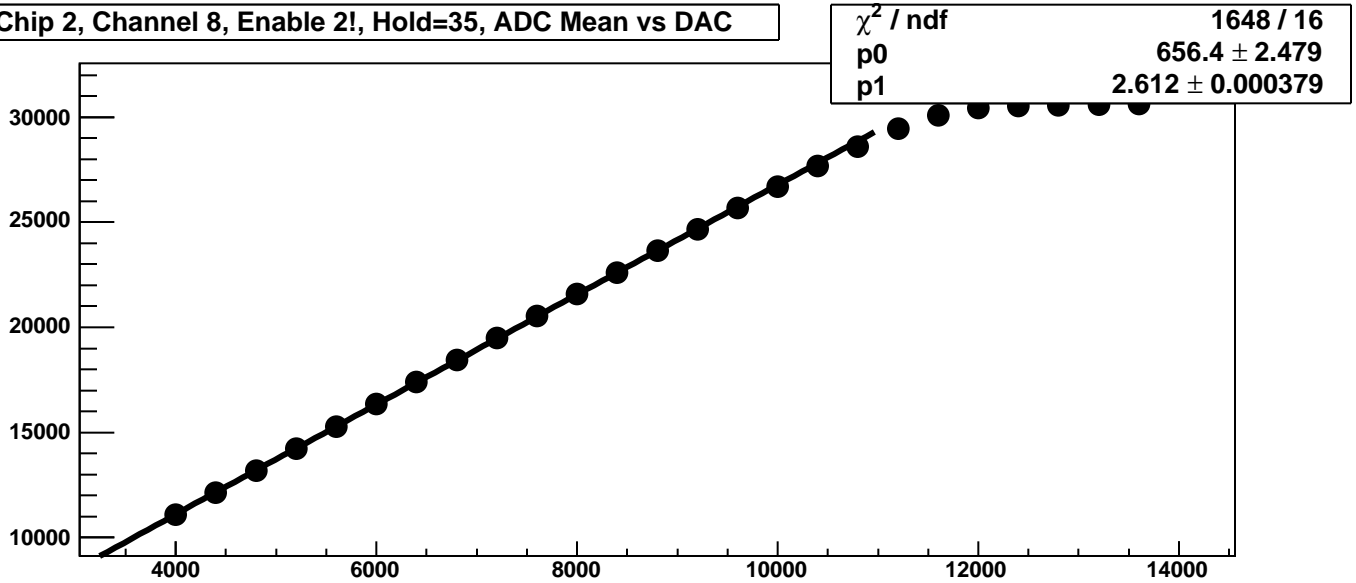
Chip 2, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



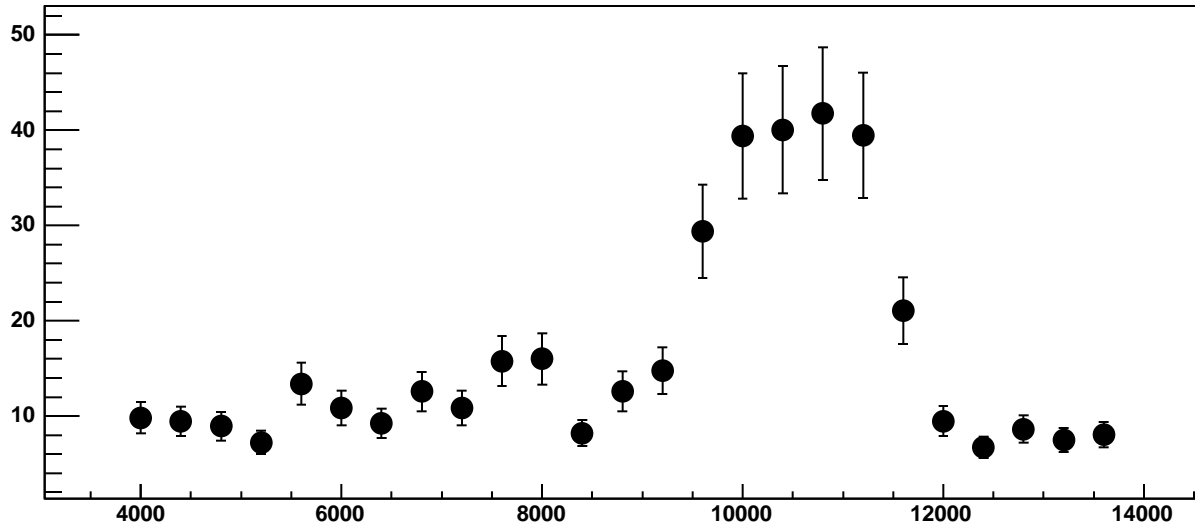
Chip 2, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



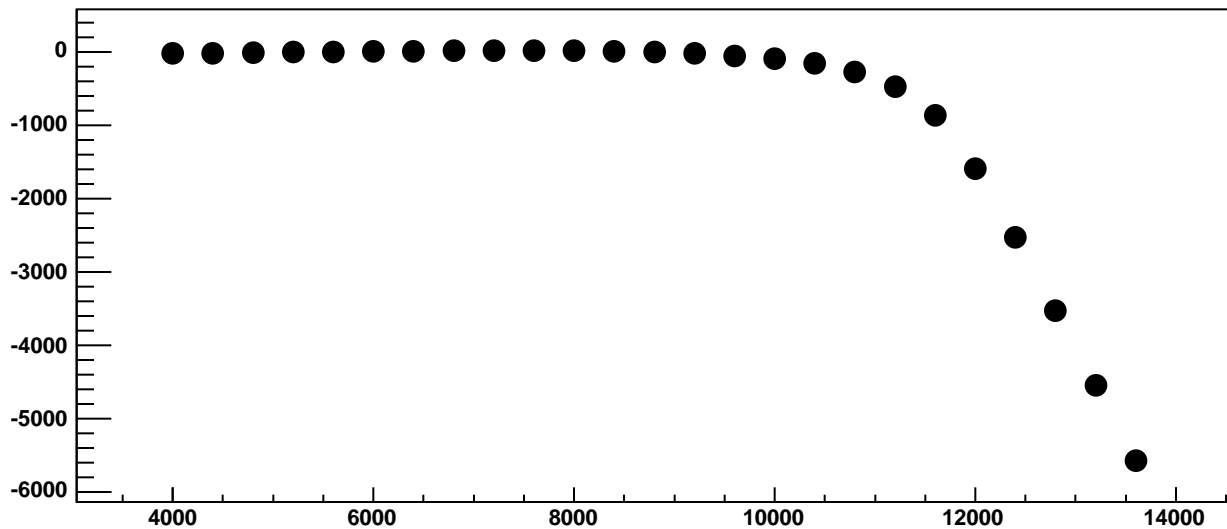
Chip 2, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC



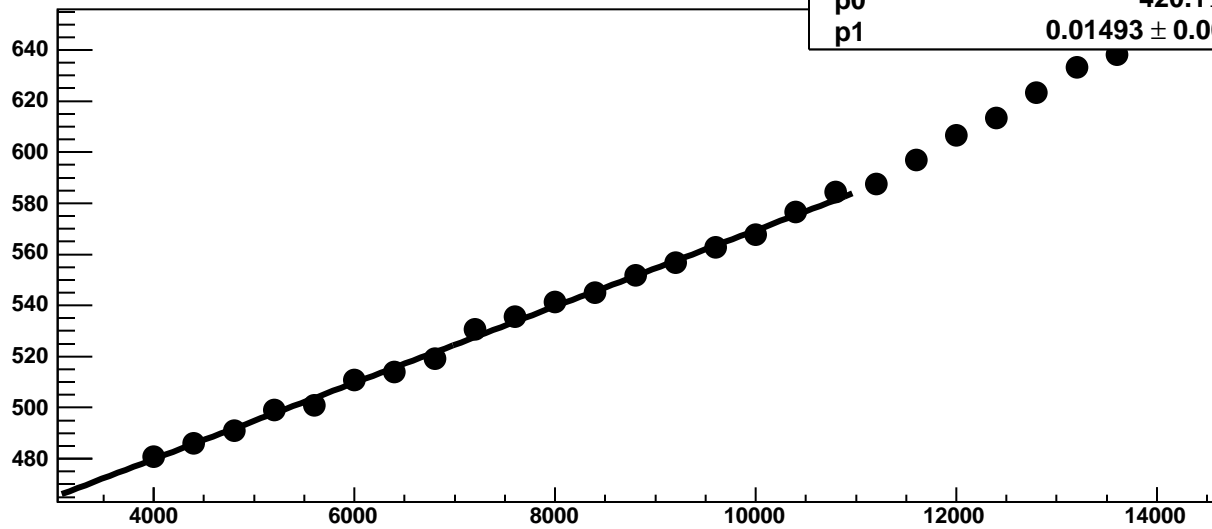
Chip 2, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

18.95 / 16

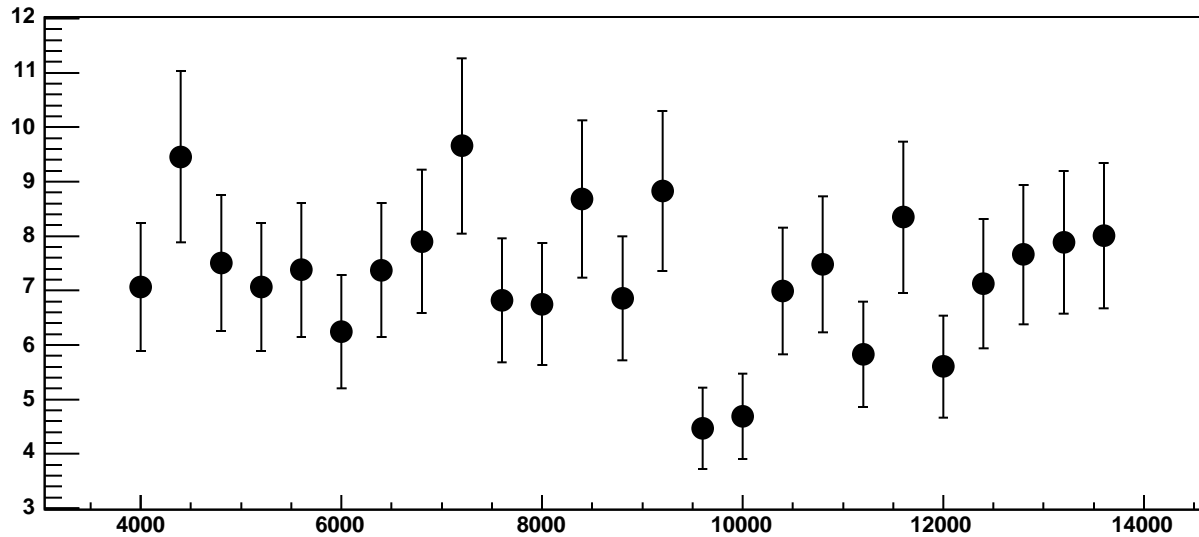
p0

420.1 ± 1.426

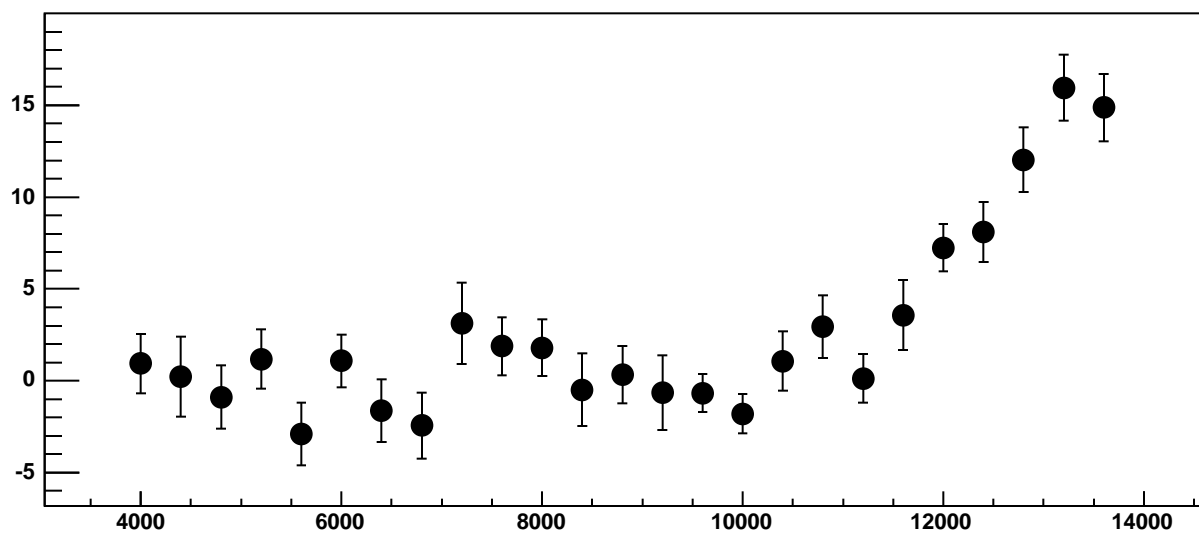
p1

0.01493 ± 0.0001772

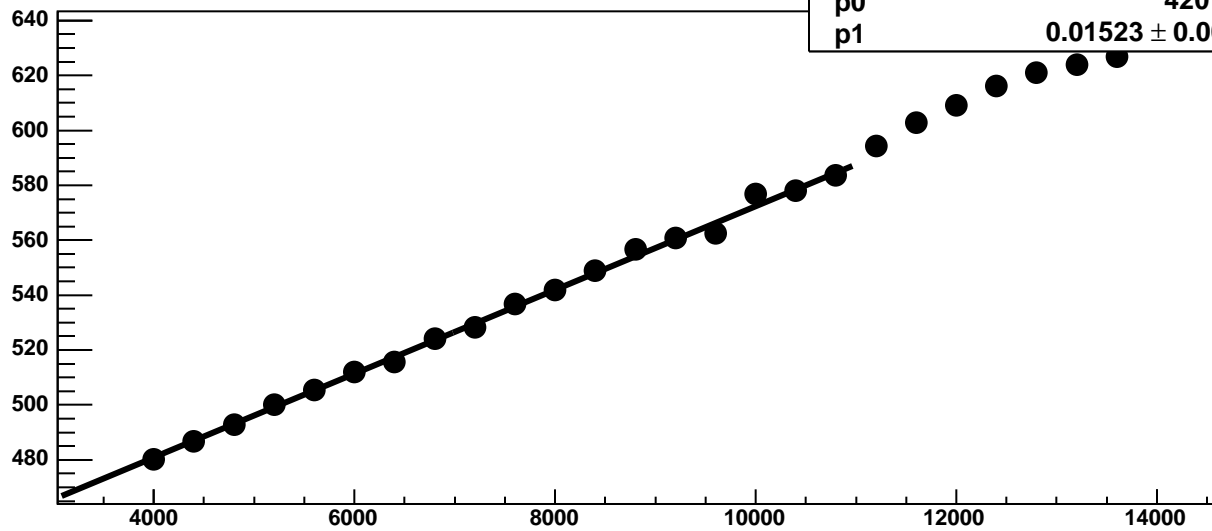
Chip 2, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



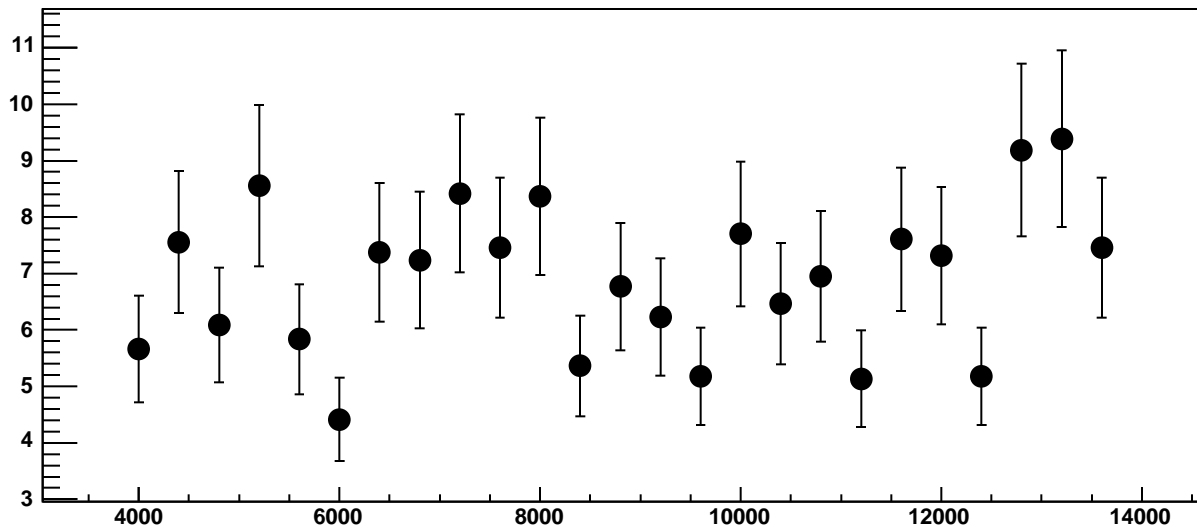
Chip 2, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



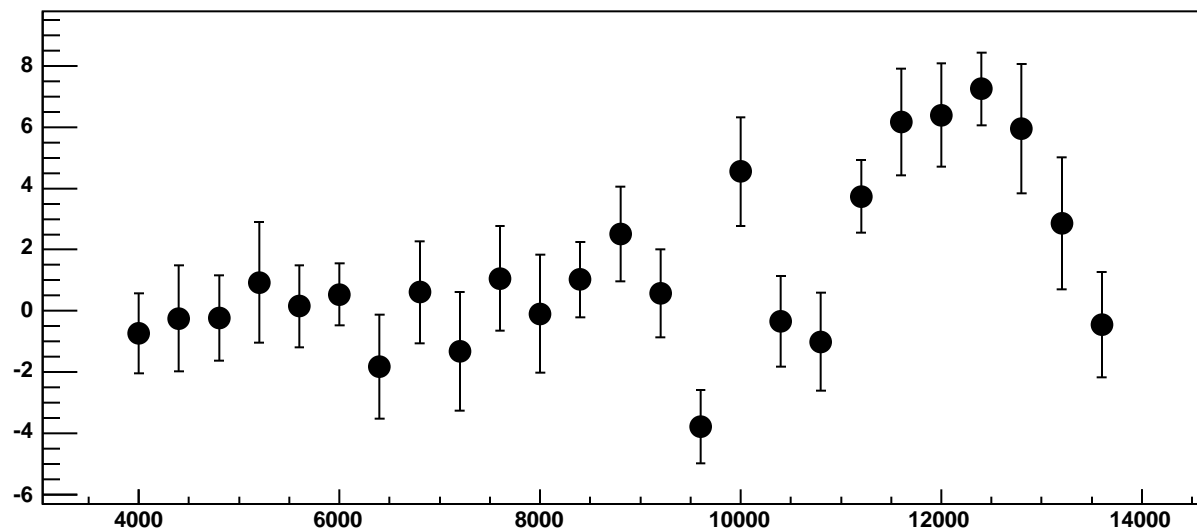
Chip 2, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



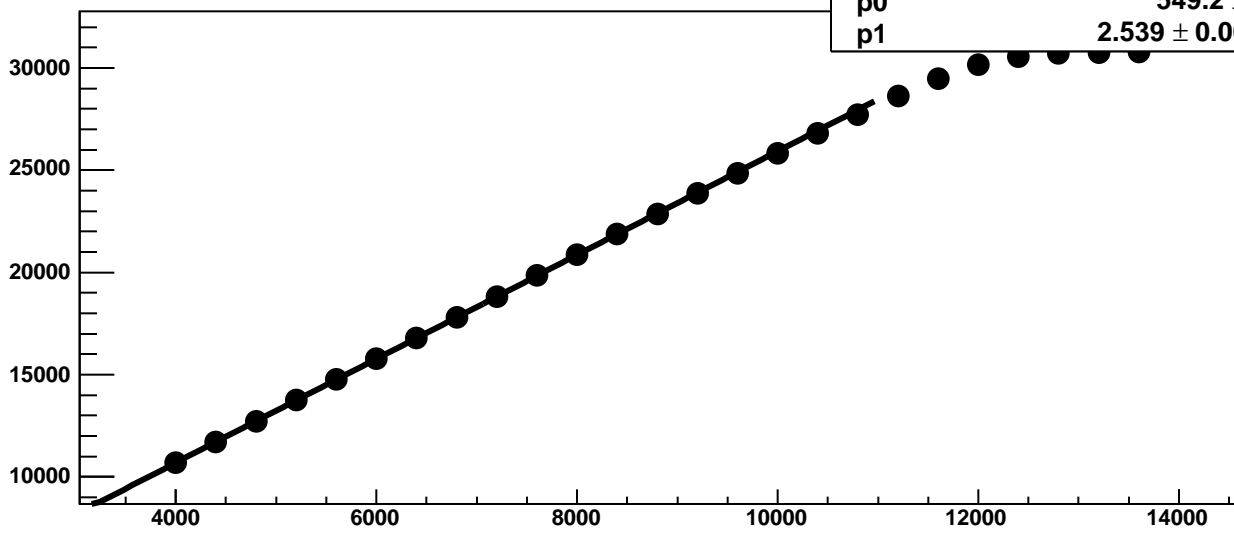
Chip 2, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



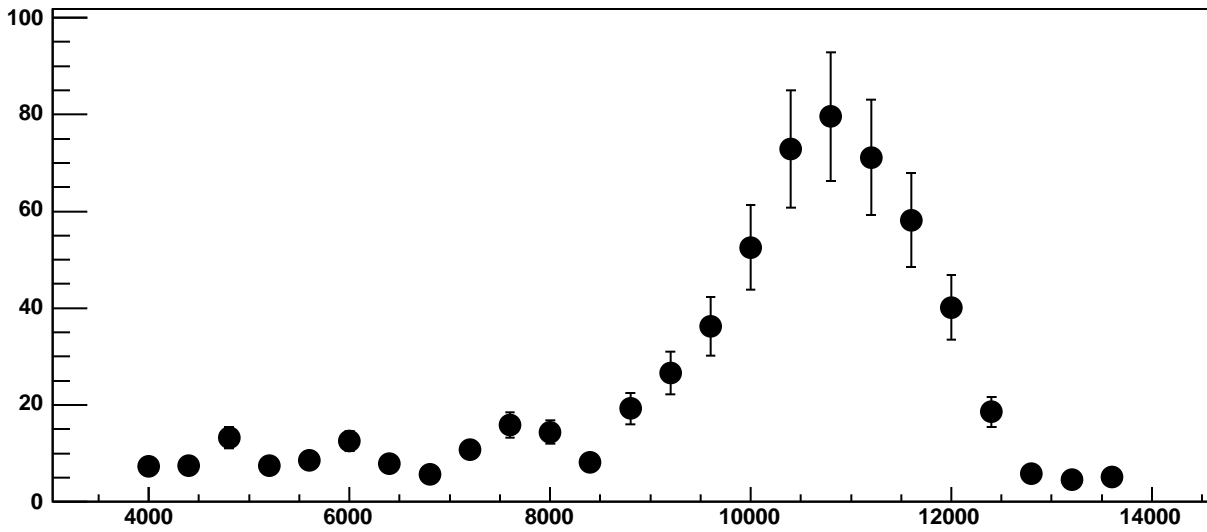
Chip 2, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



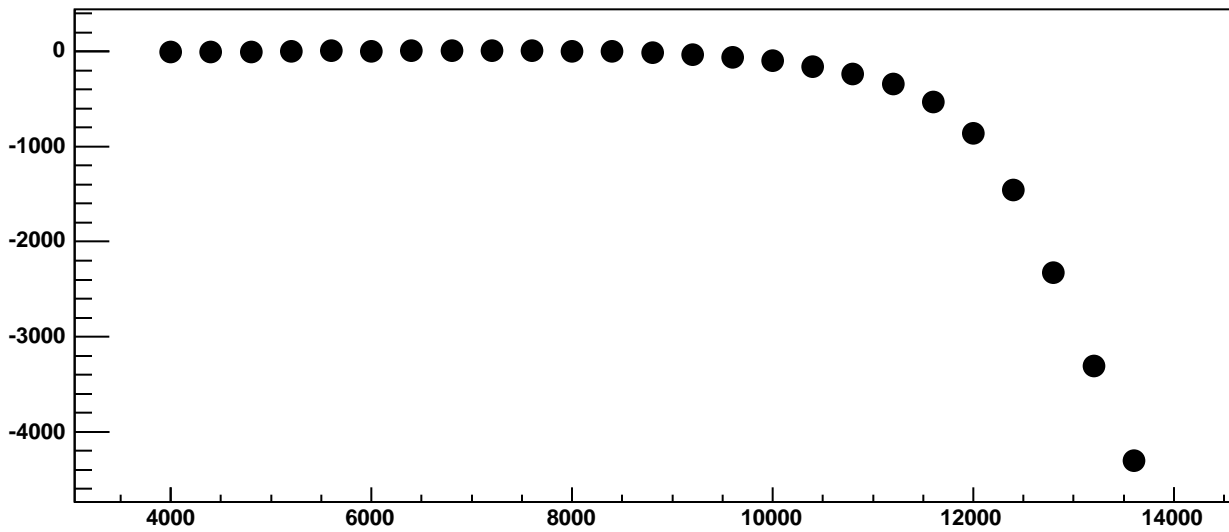
Chip 2, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



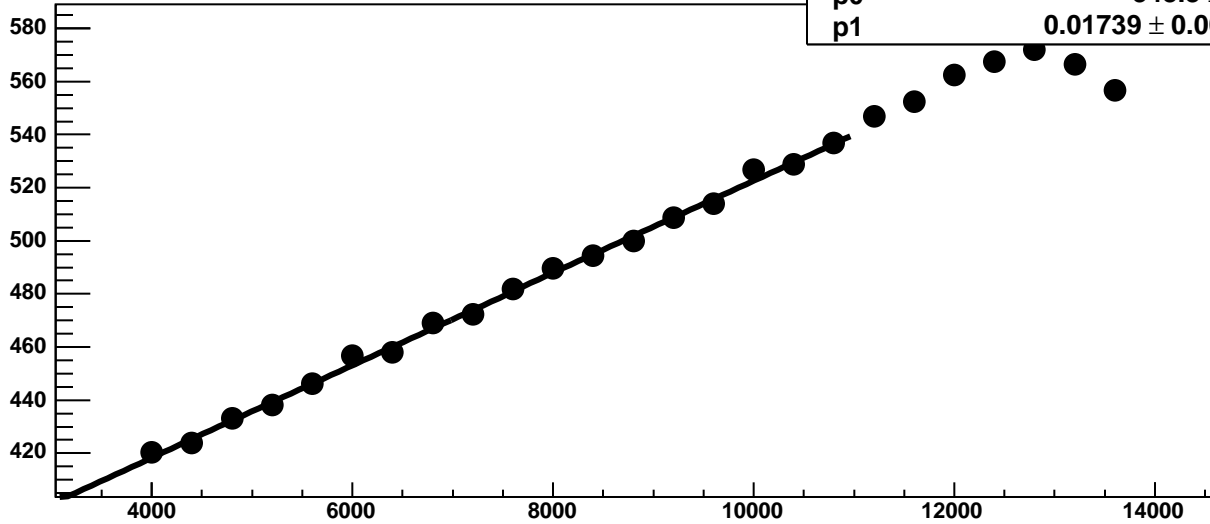
Chip 2, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



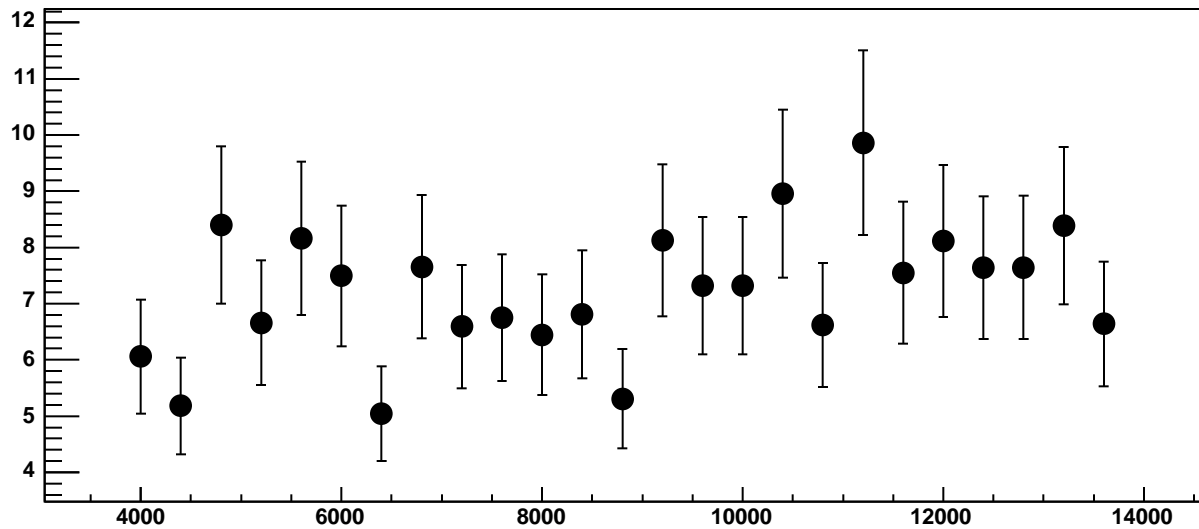
Chip 2, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC



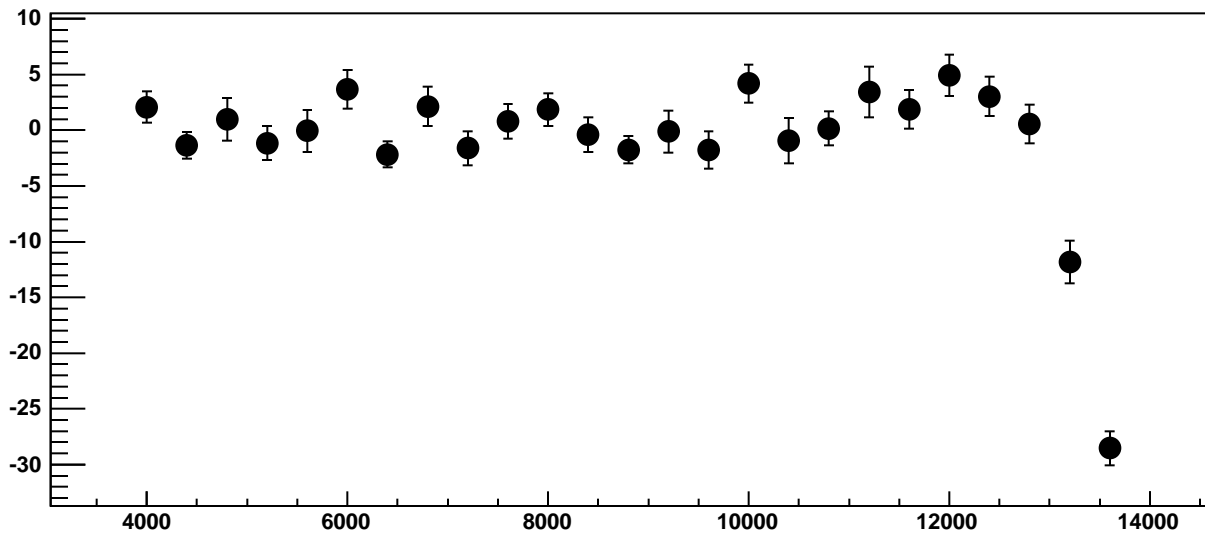
Chip 2, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



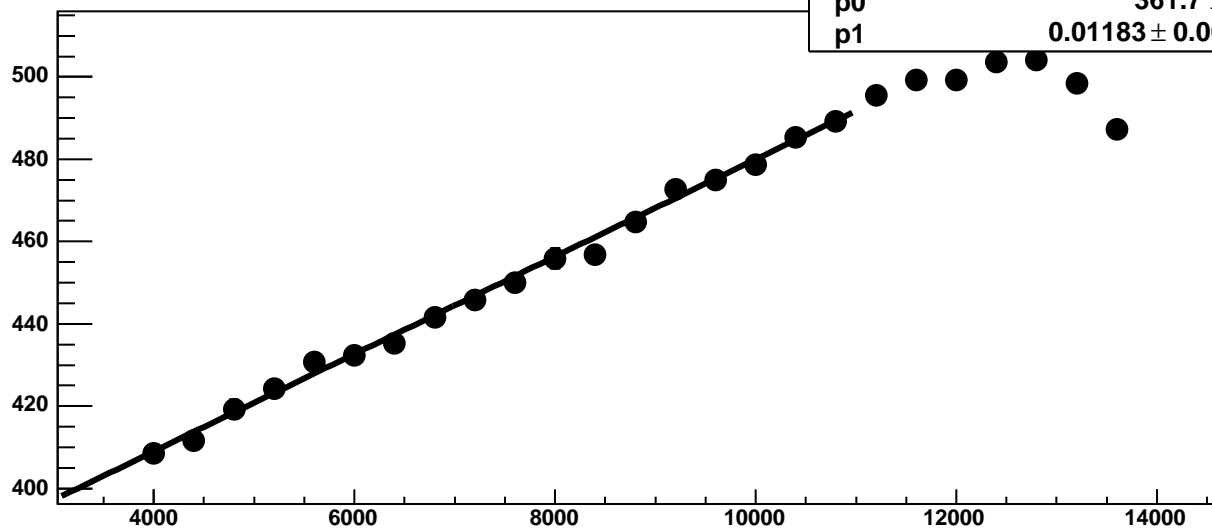
Chip 2, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



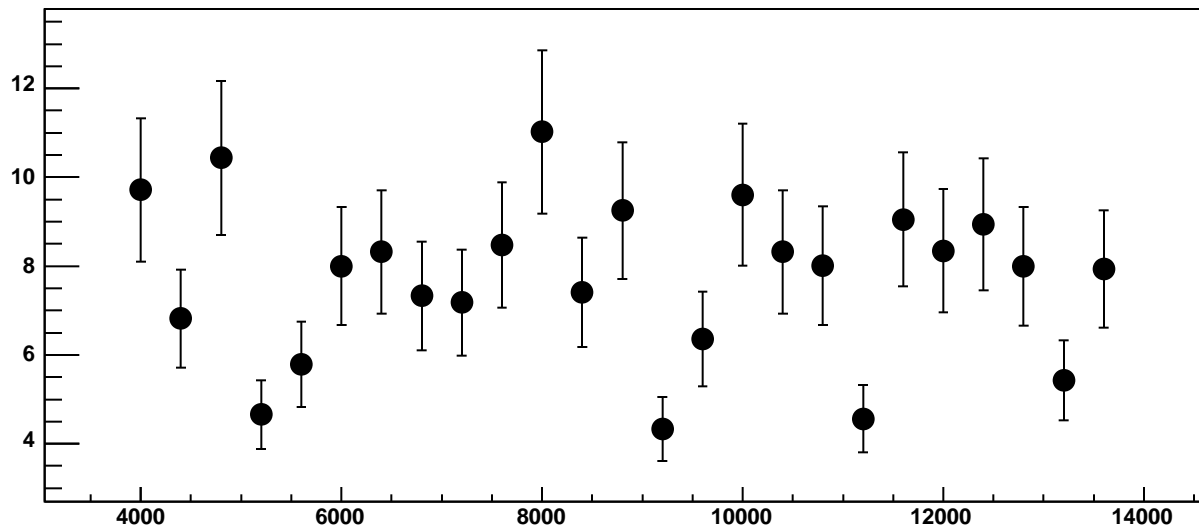
Chip 2, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



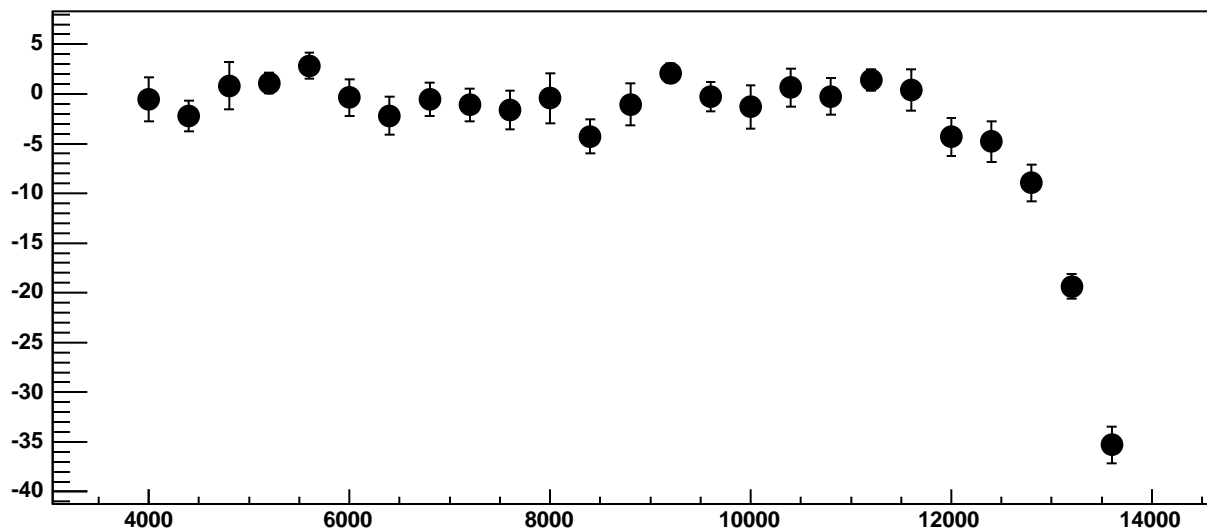
Chip 2, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



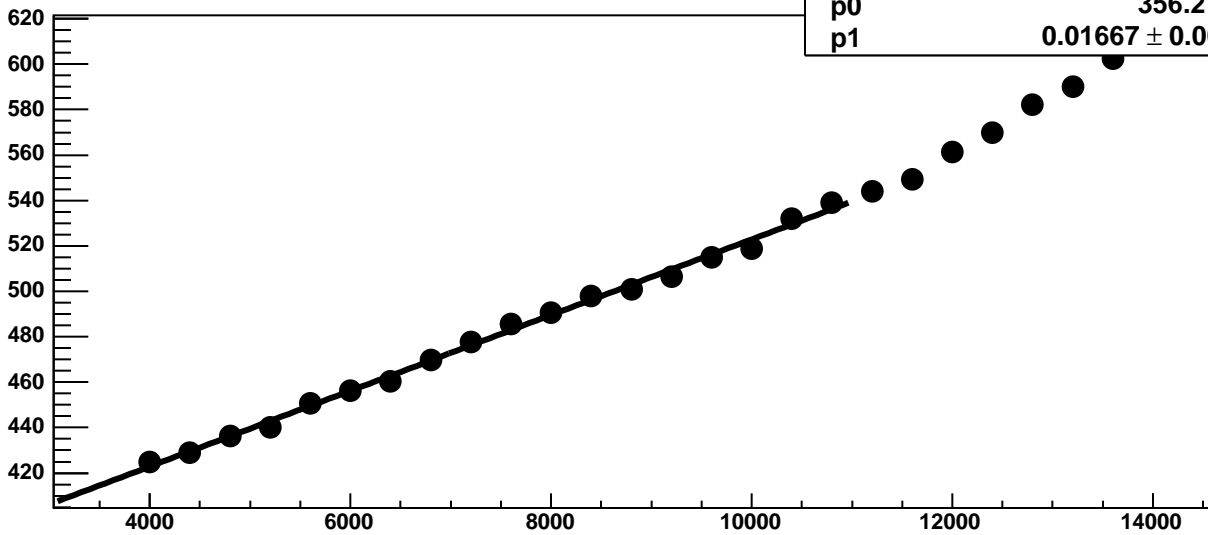
Chip 2, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

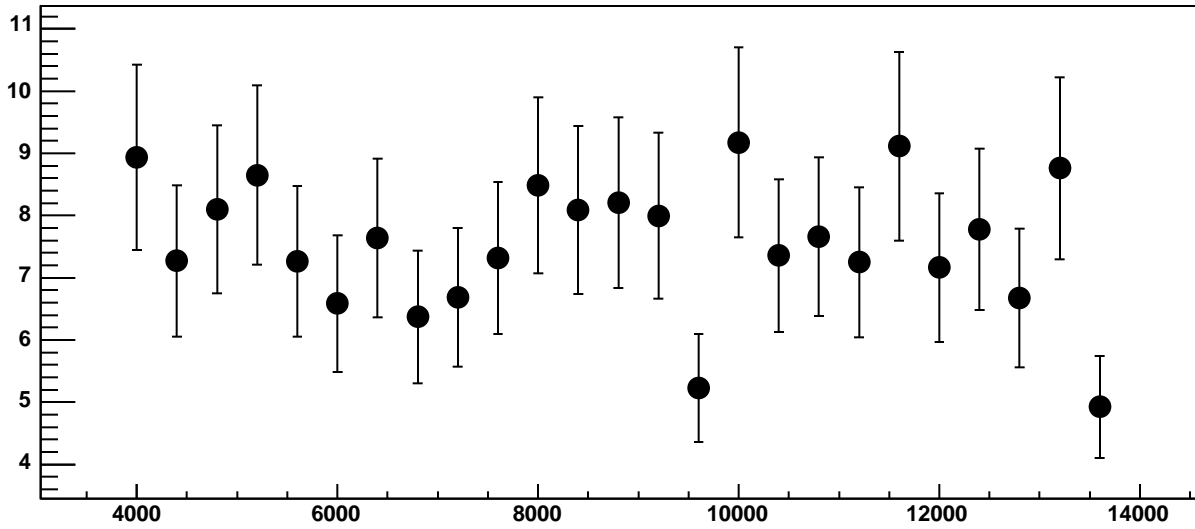


Chip 2, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

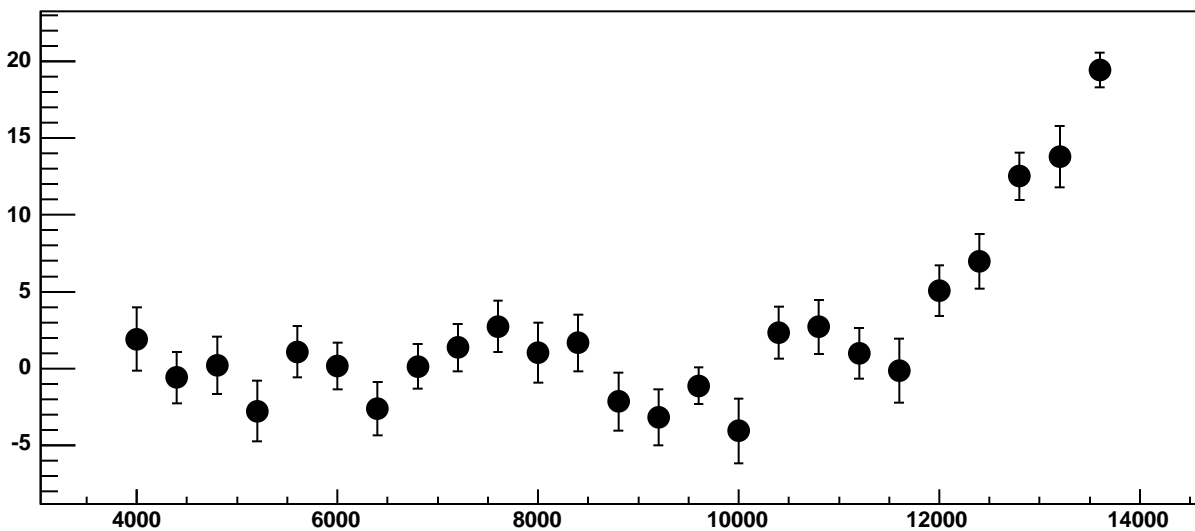


χ^2 / ndf 23.41 / 16
p0 356.2 ± 1.54
p1 0.01667 ± 0.0001985

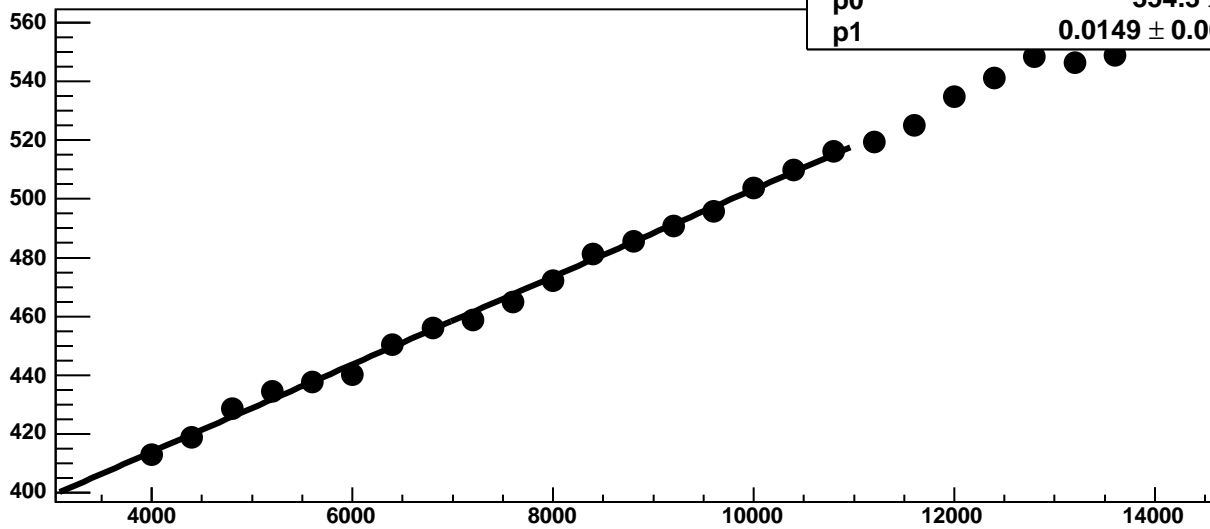
Chip 2, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

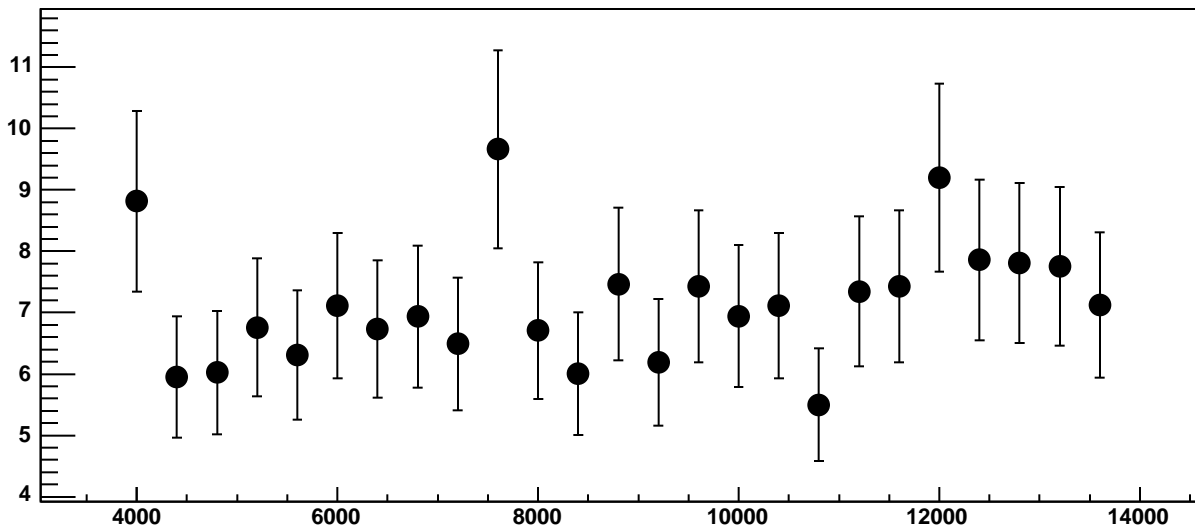


Chip 2, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

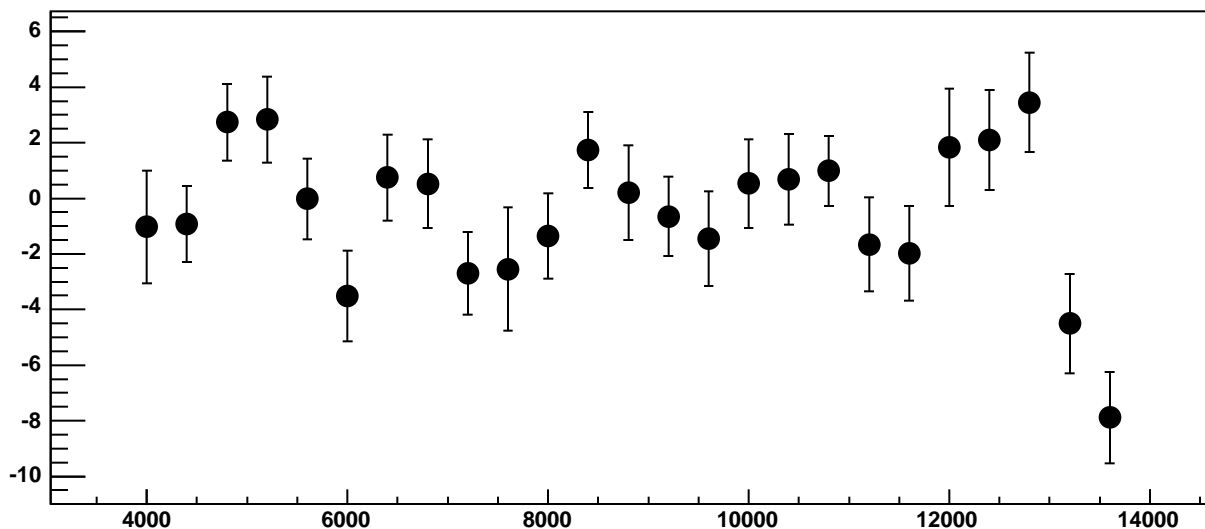


χ^2 / ndf 21.78 / 16
p0 354.3 ± 1.329
p1 0.0149 ± 0.0001716

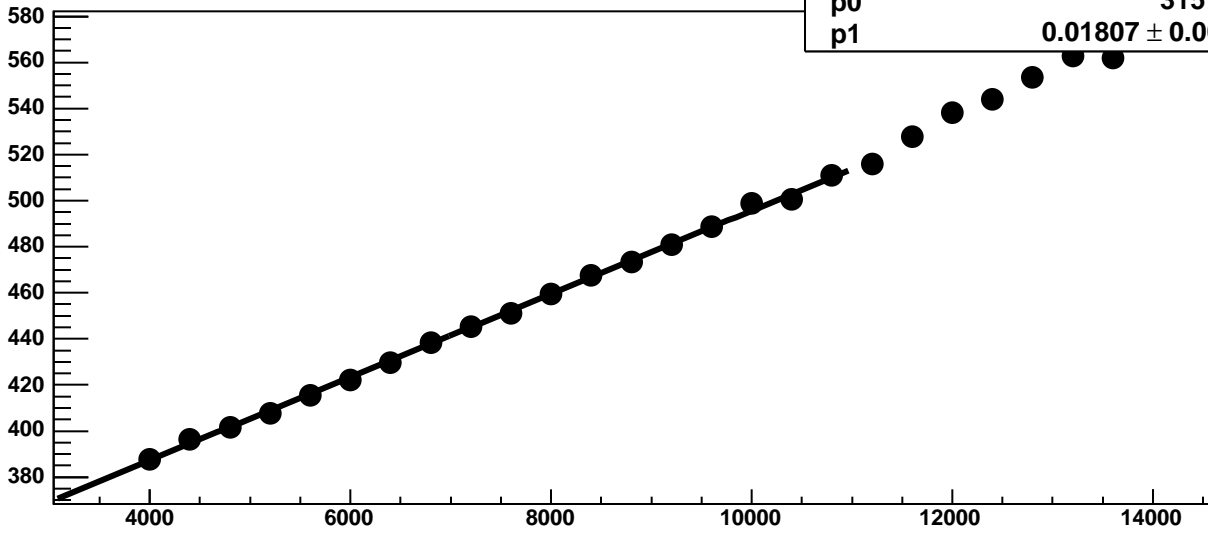
Chip 2, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



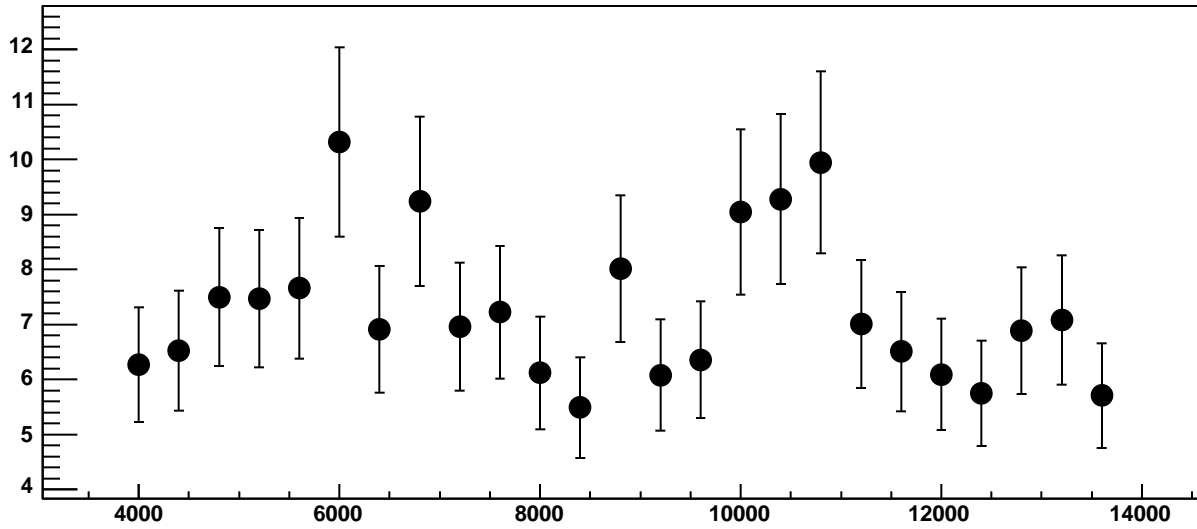
Chip 2, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



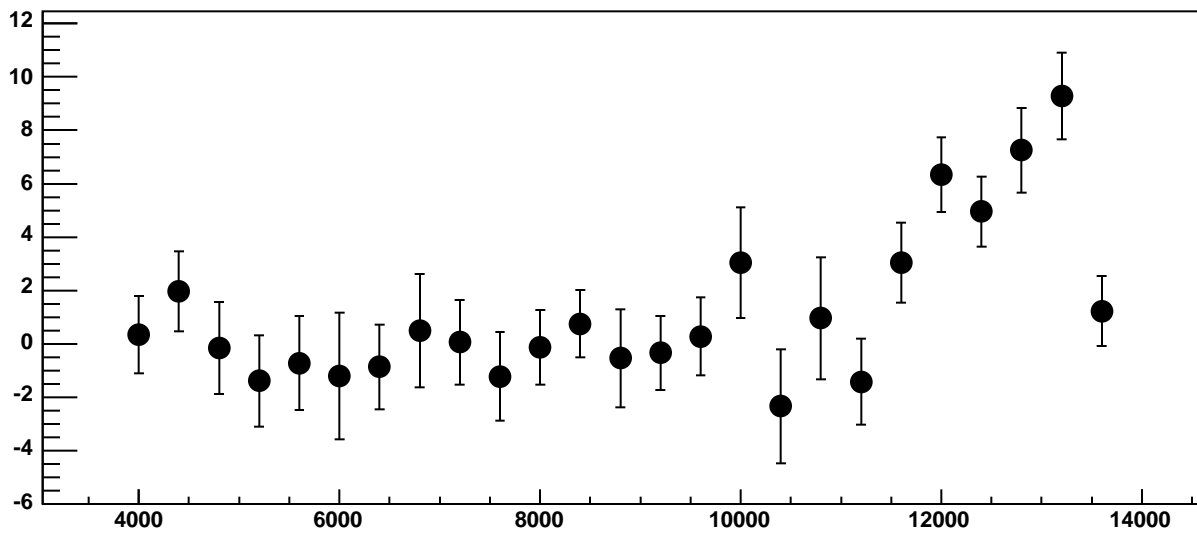
Chip 2, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC



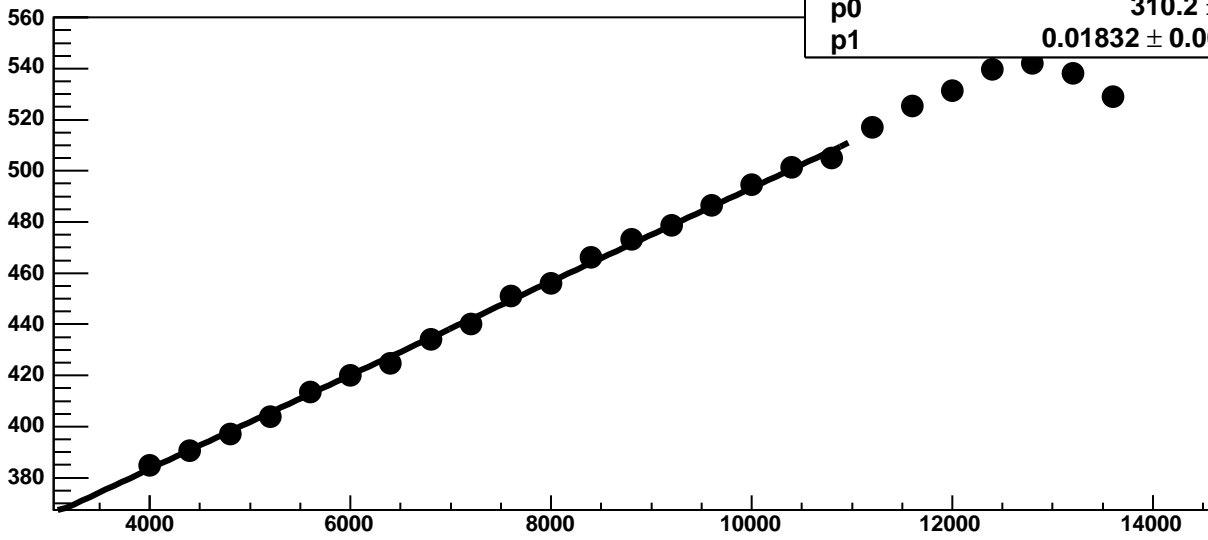
Chip 2, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC

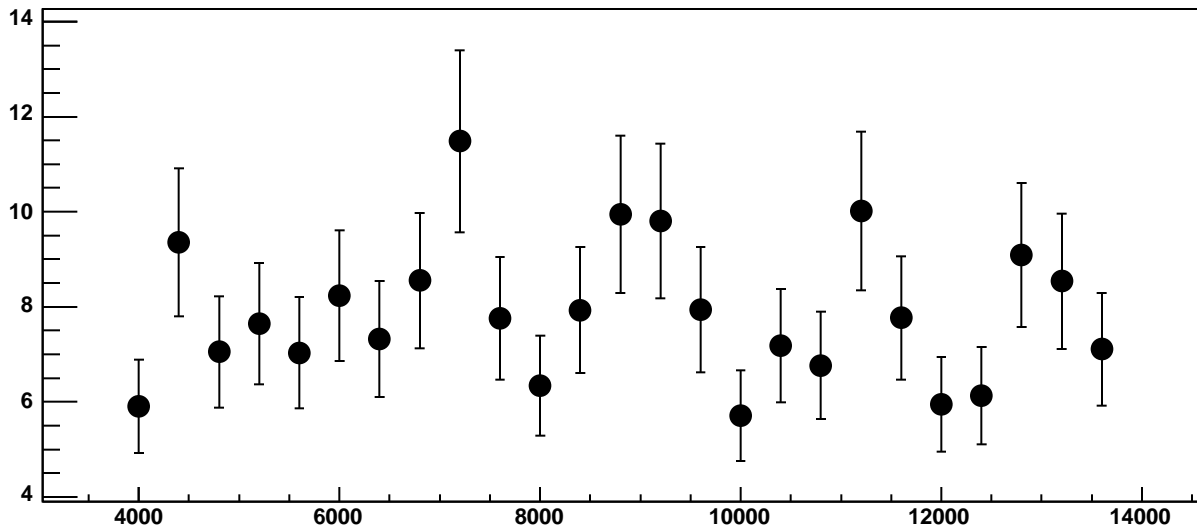


Chip 2, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

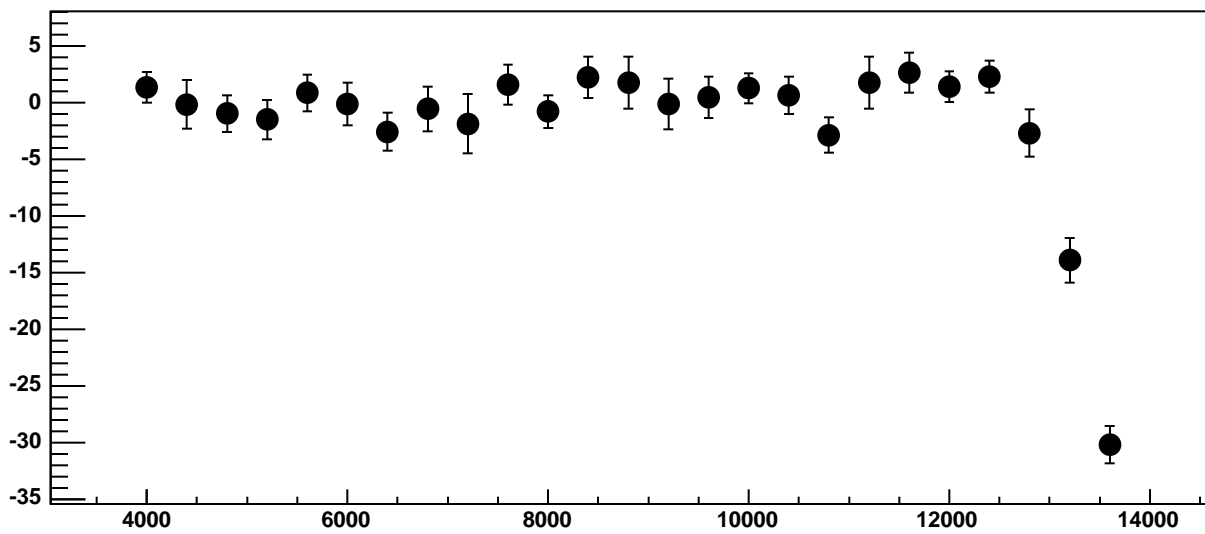


χ^2 / ndf 13.13 / 16
p0 310.2 ± 1.422
p1 0.01832 ± 0.0001836

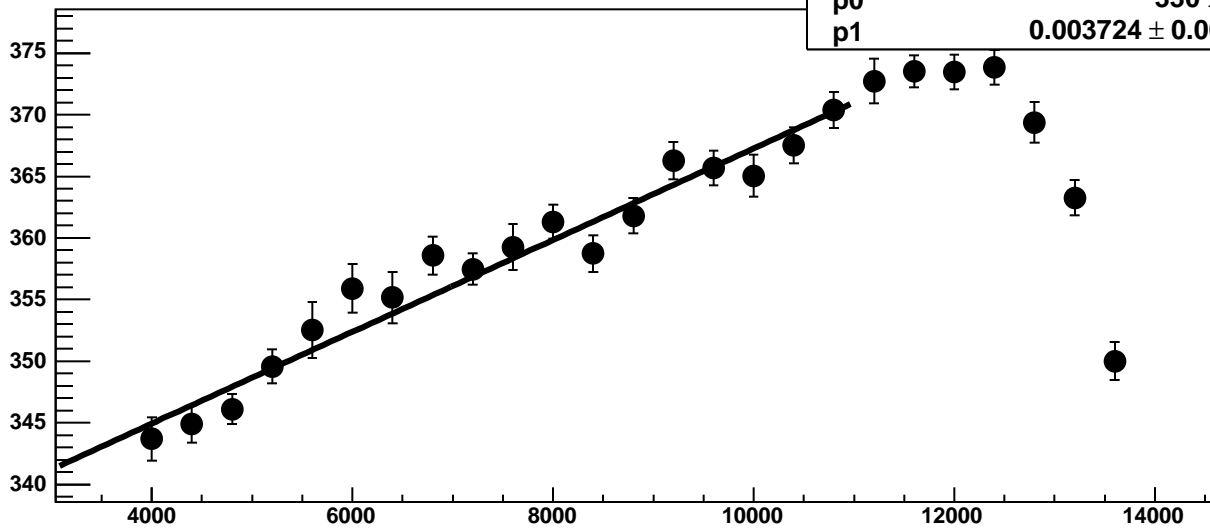
Chip 2, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



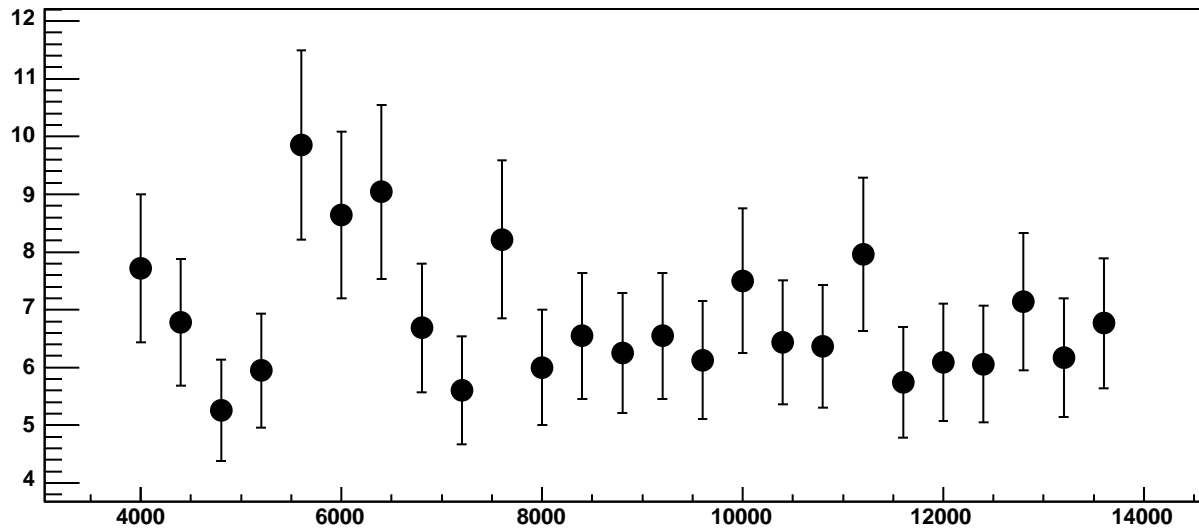
Chip 2, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



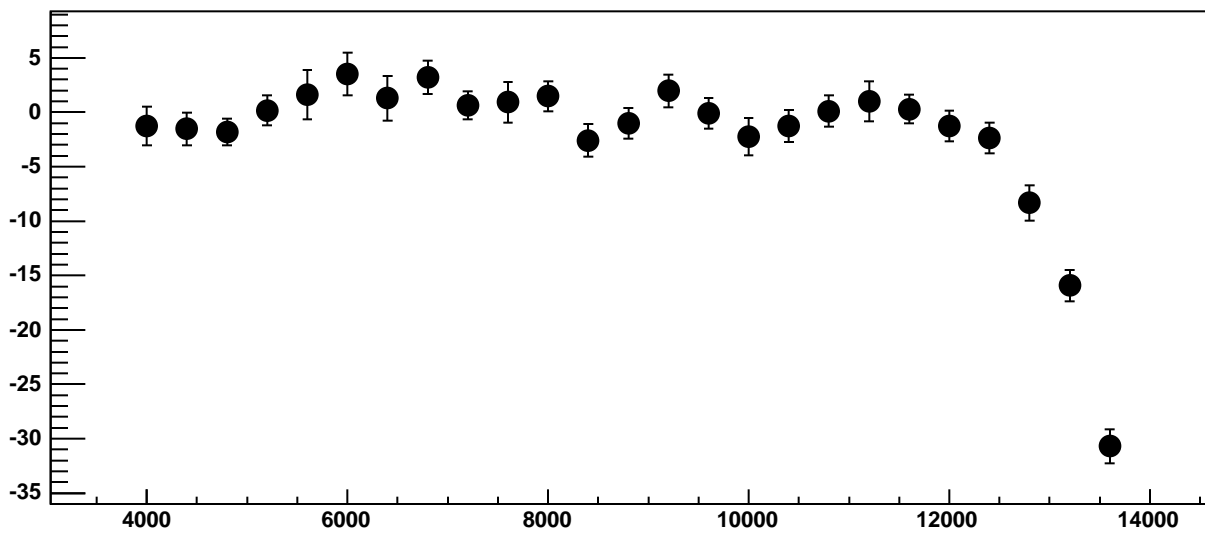
Chip 2, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



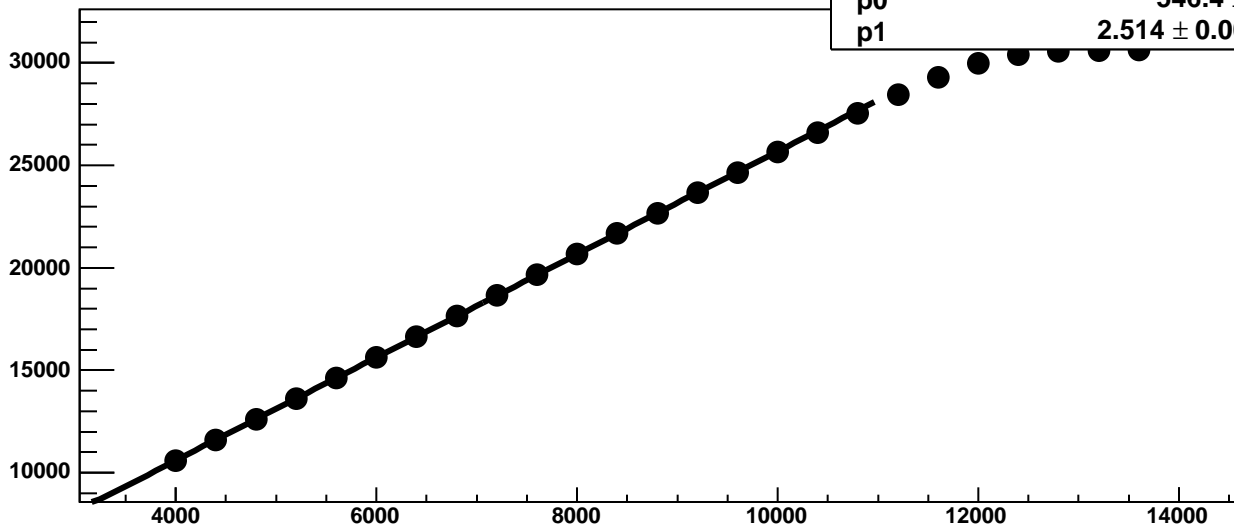
Chip 2, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



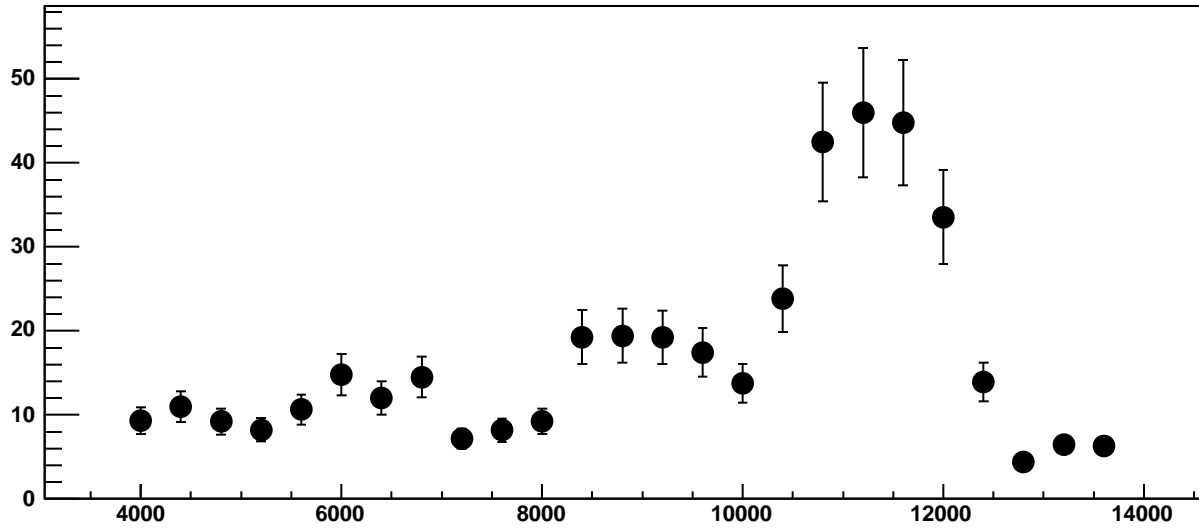
Chip 2, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



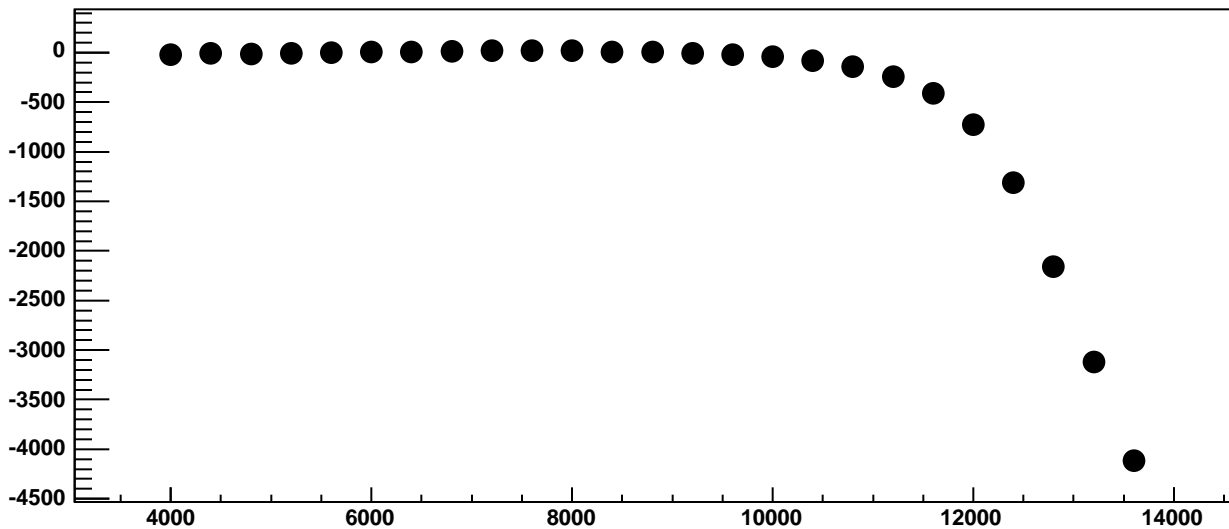
Chip 2, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC



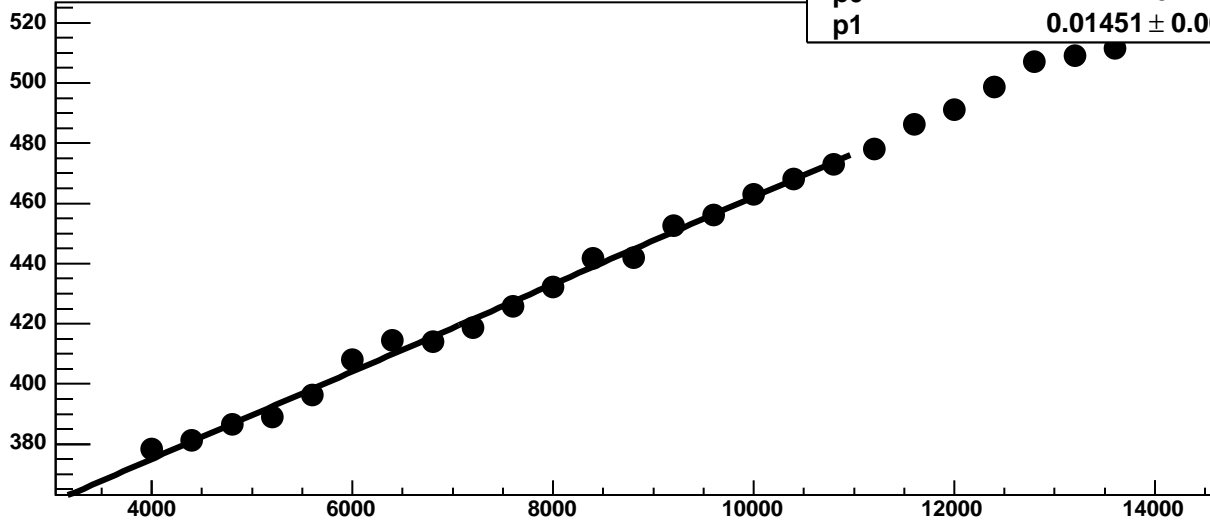
Chip 2, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

37.48 / 16

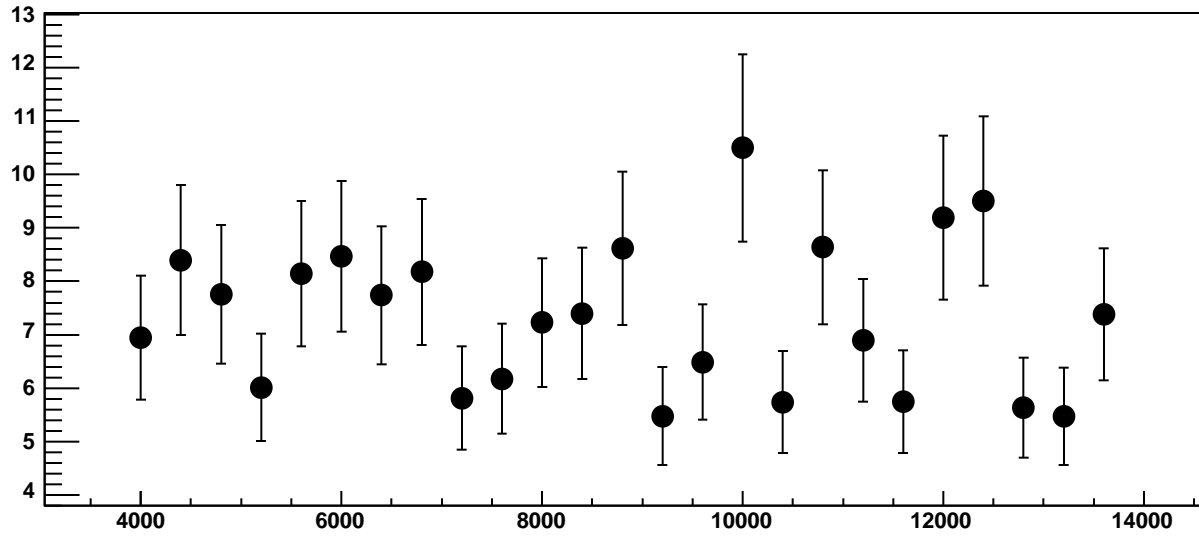
p0

317 ± 1.465

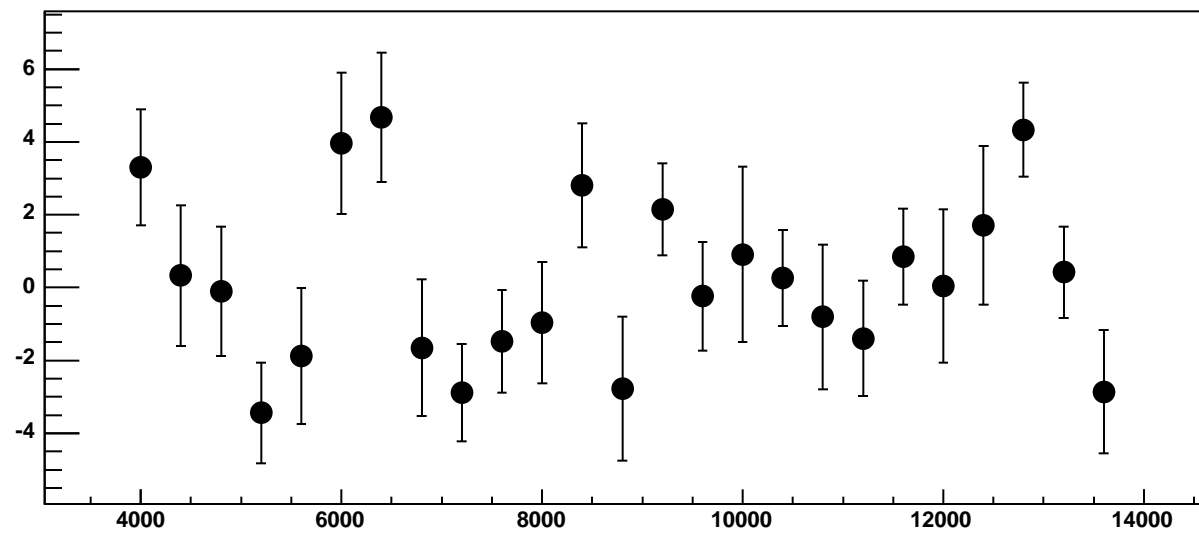
p1

0.01451 ± 0.0001889

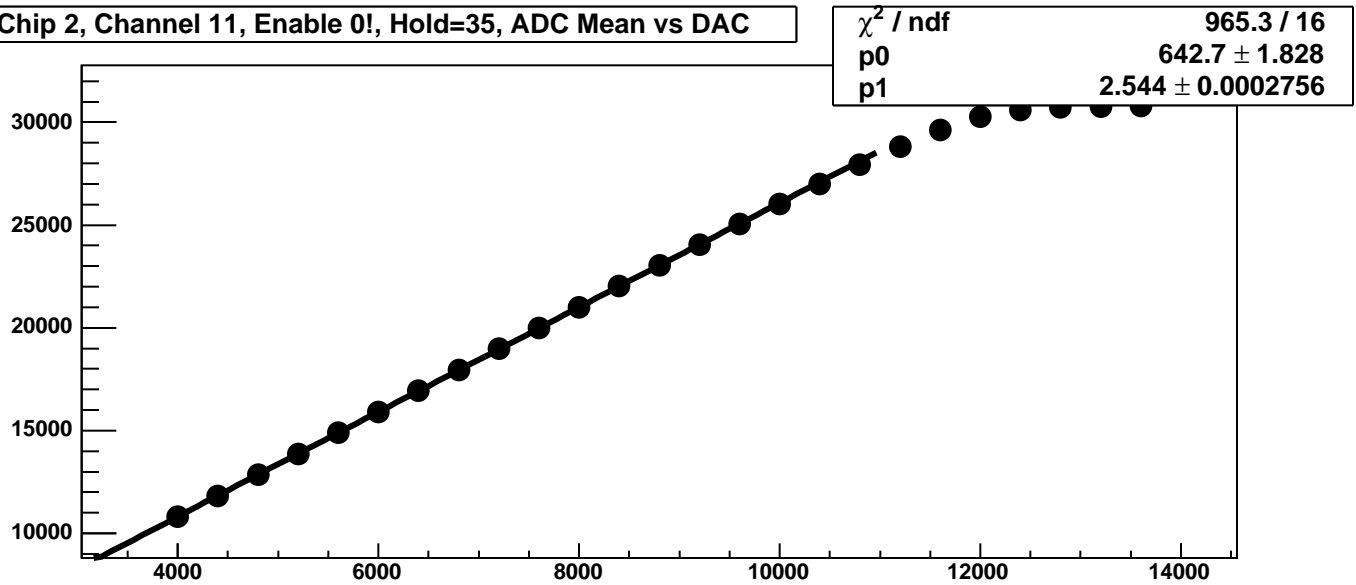
Chip 2, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



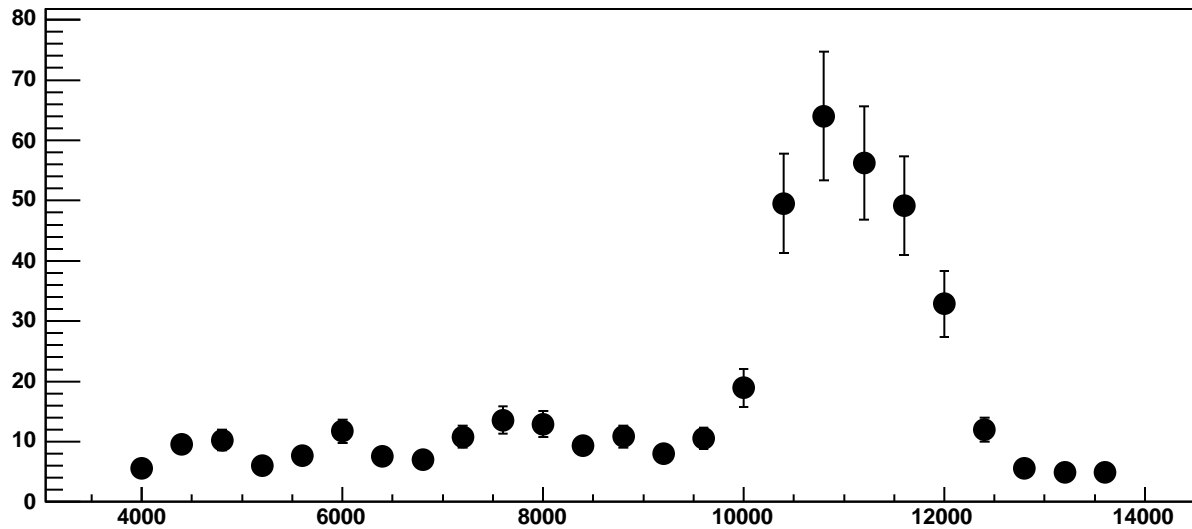
Chip 2, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



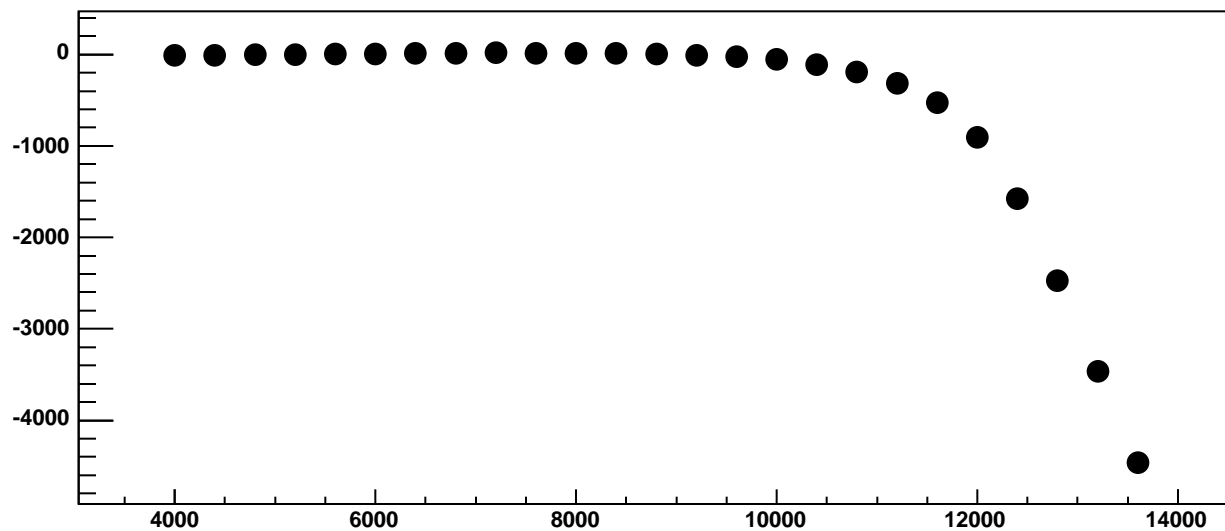
Chip 2, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC



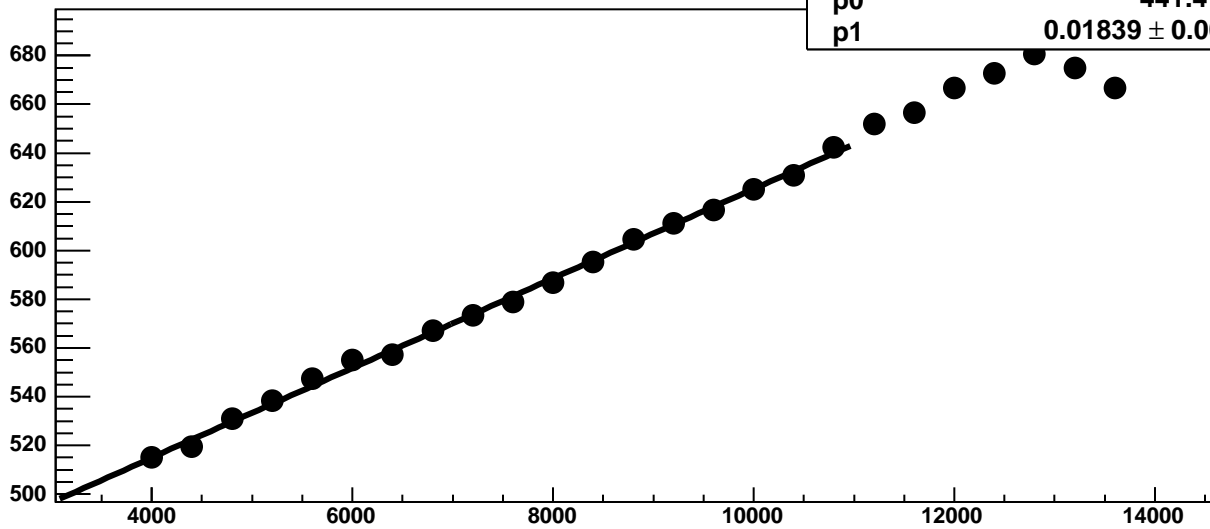
Chip 2, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



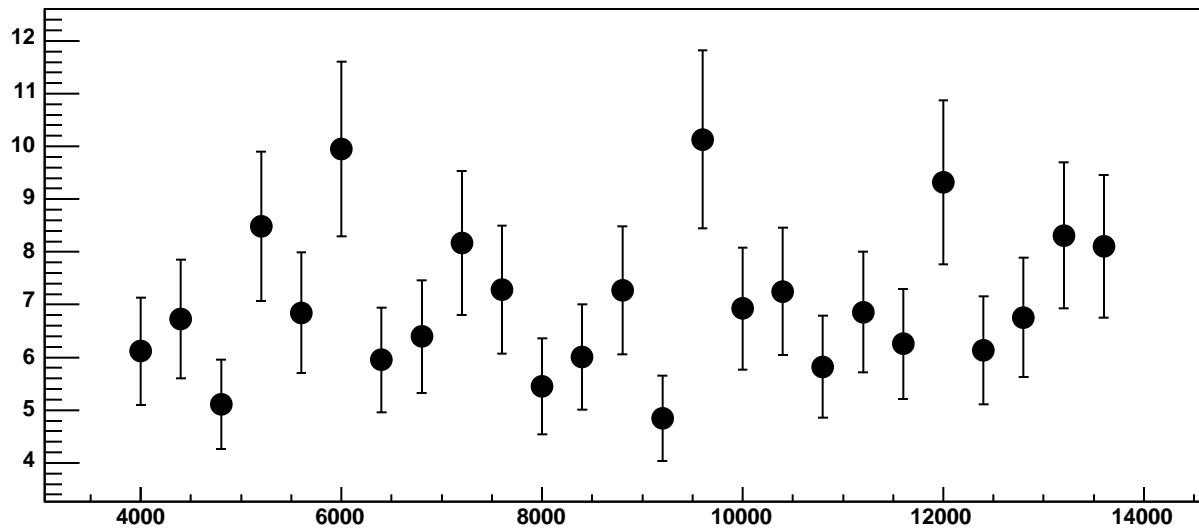
Chip 2, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC



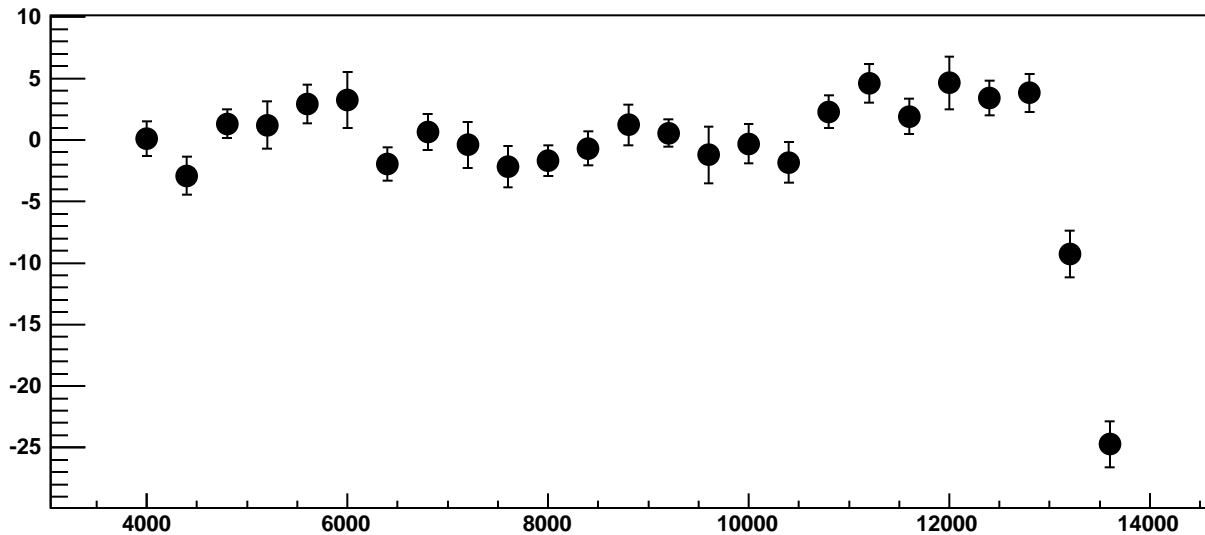
Chip 2, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



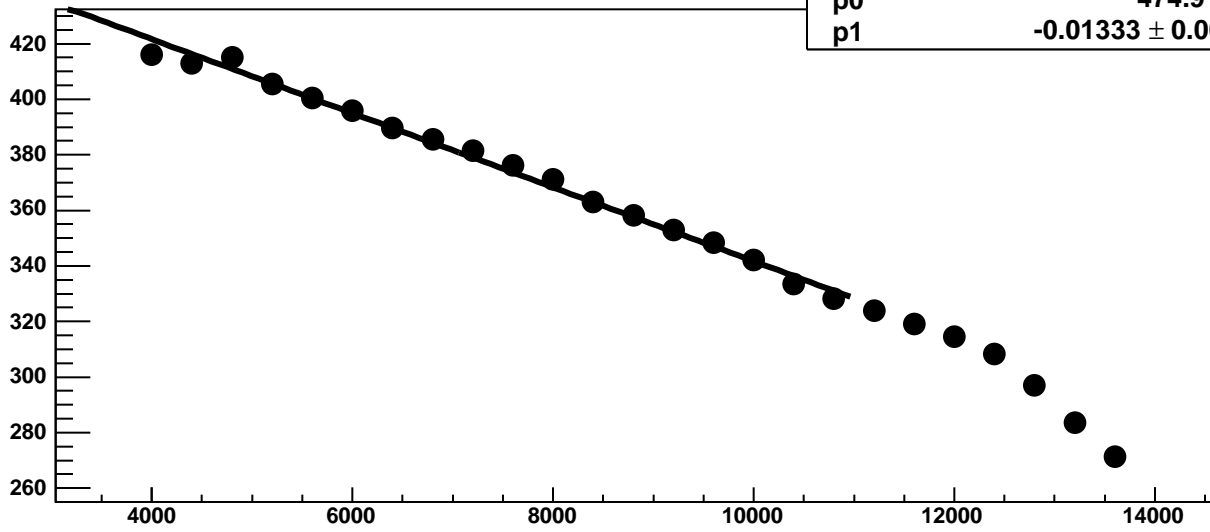
Chip 2, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



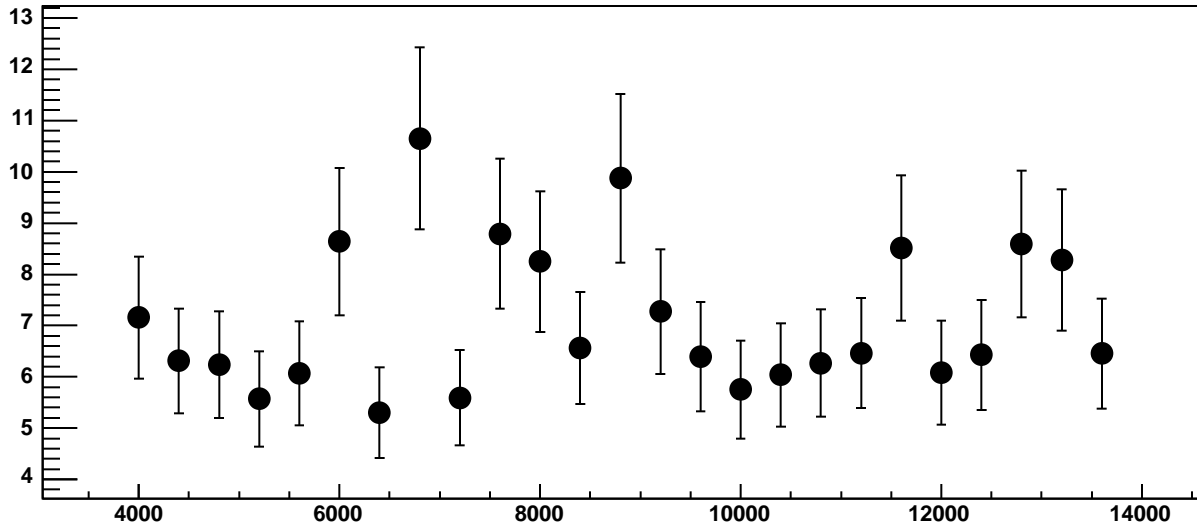
Chip 2, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



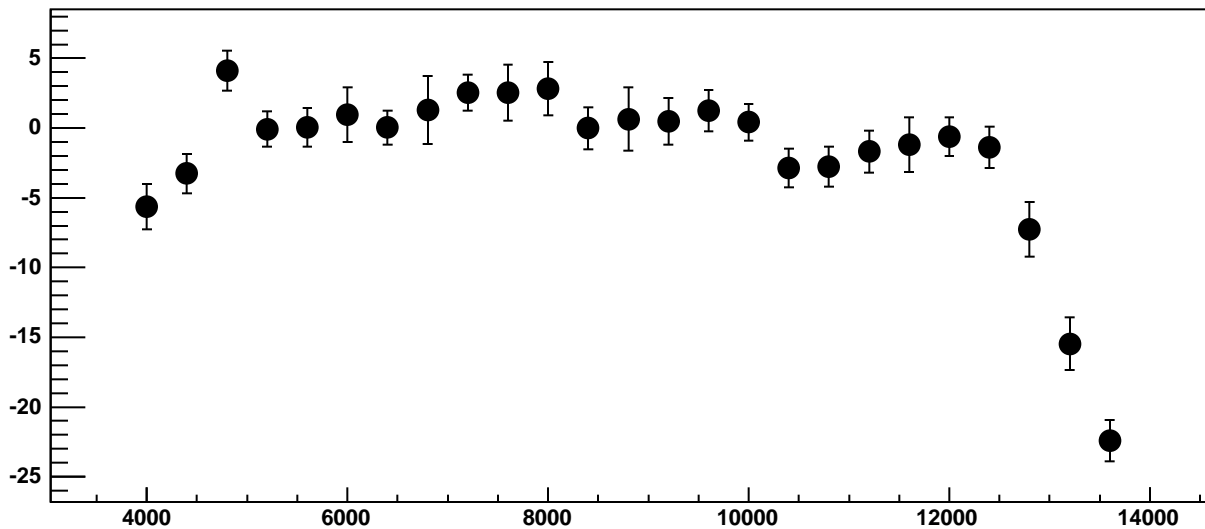
Chip 2, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



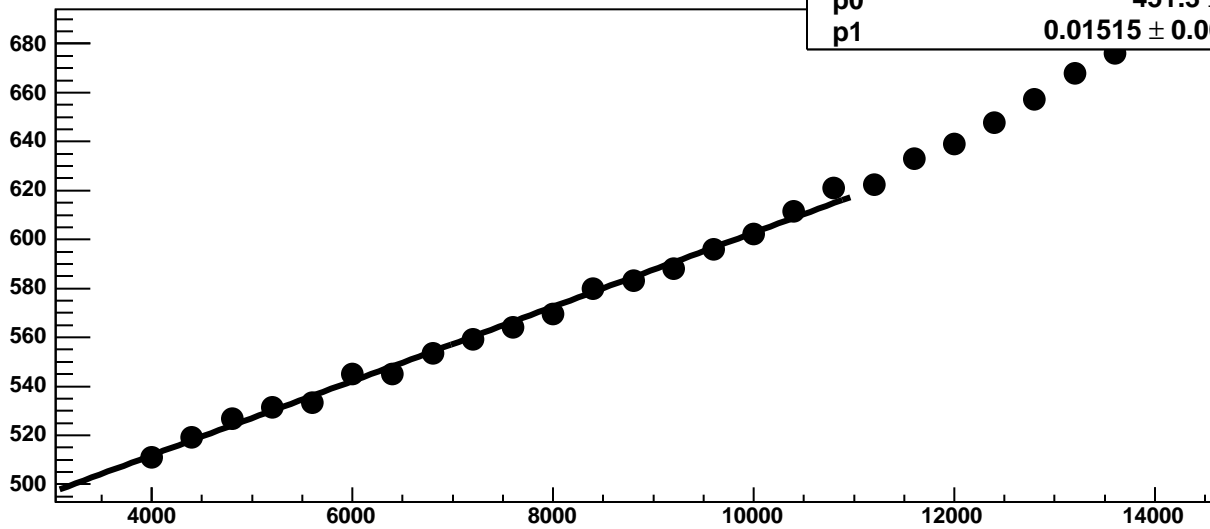
Chip 2, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

38.45 / 16

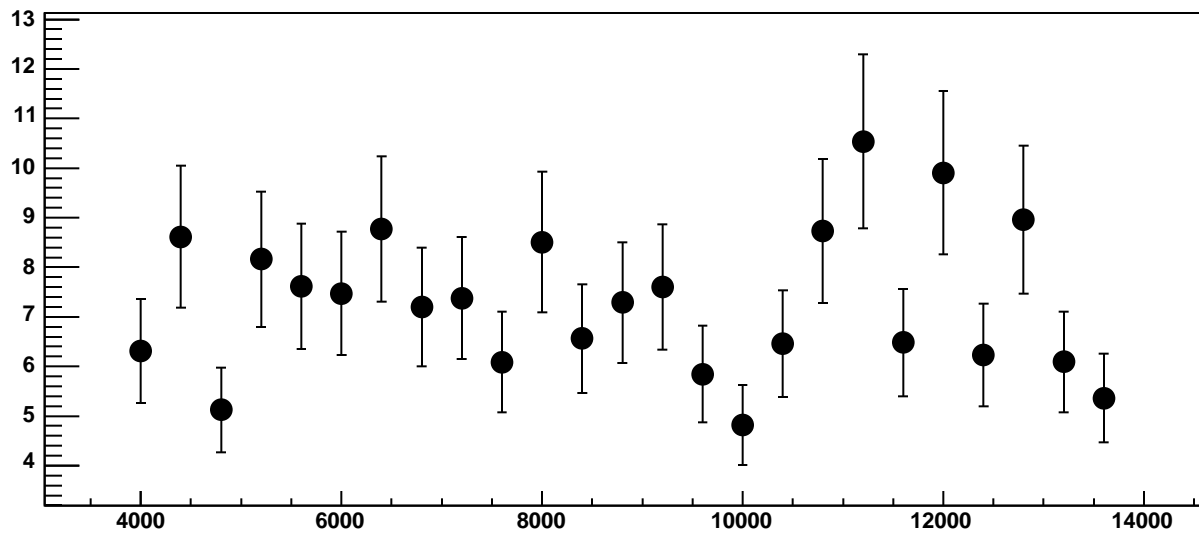
p0

451.3 ± 1.347

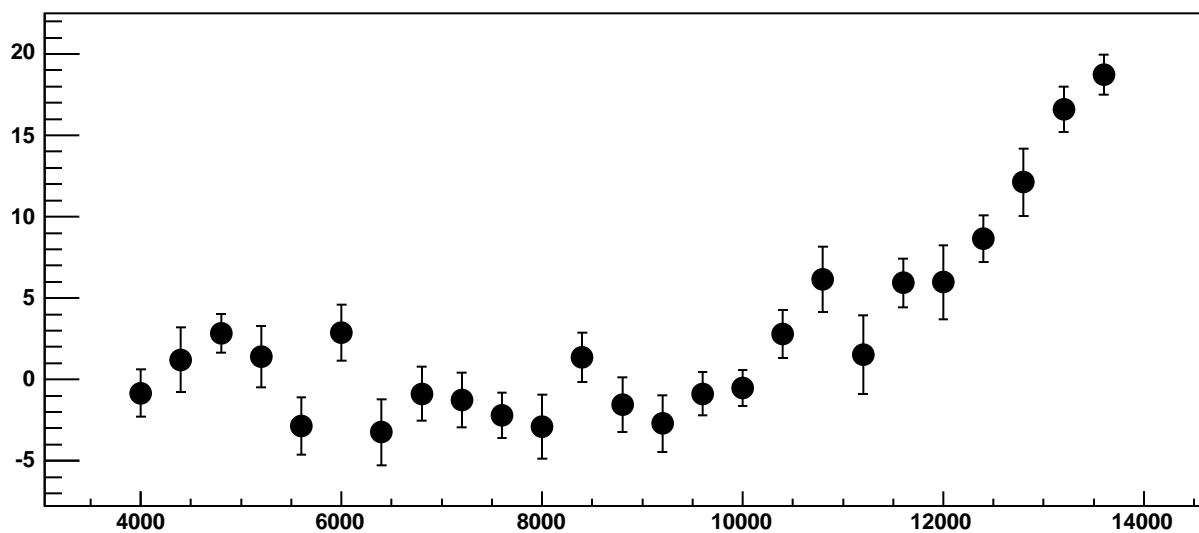
p1

0.01515 ± 0.0001724

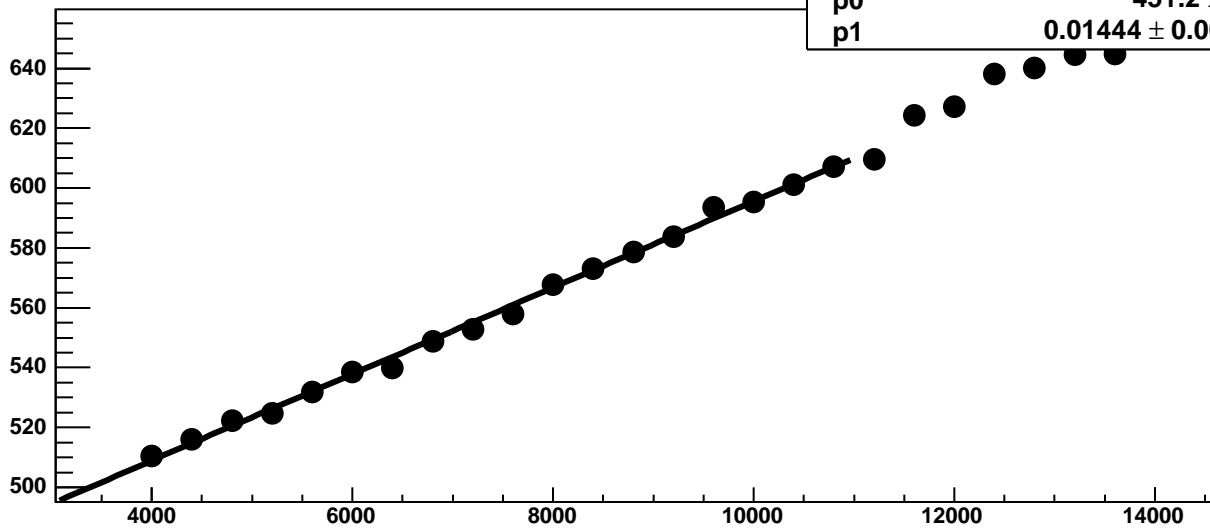
Chip 2, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



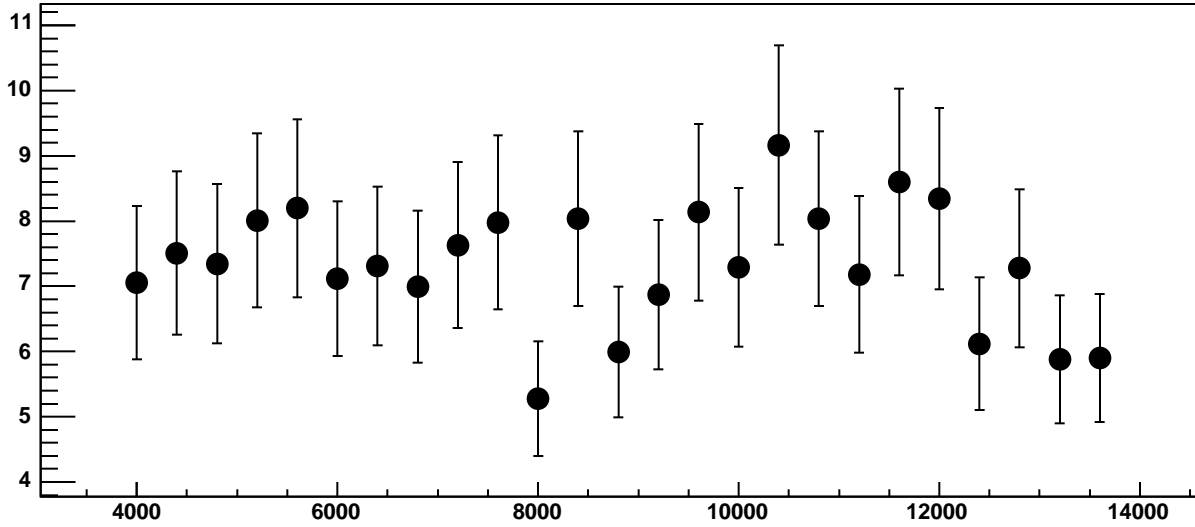
Chip 2, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC



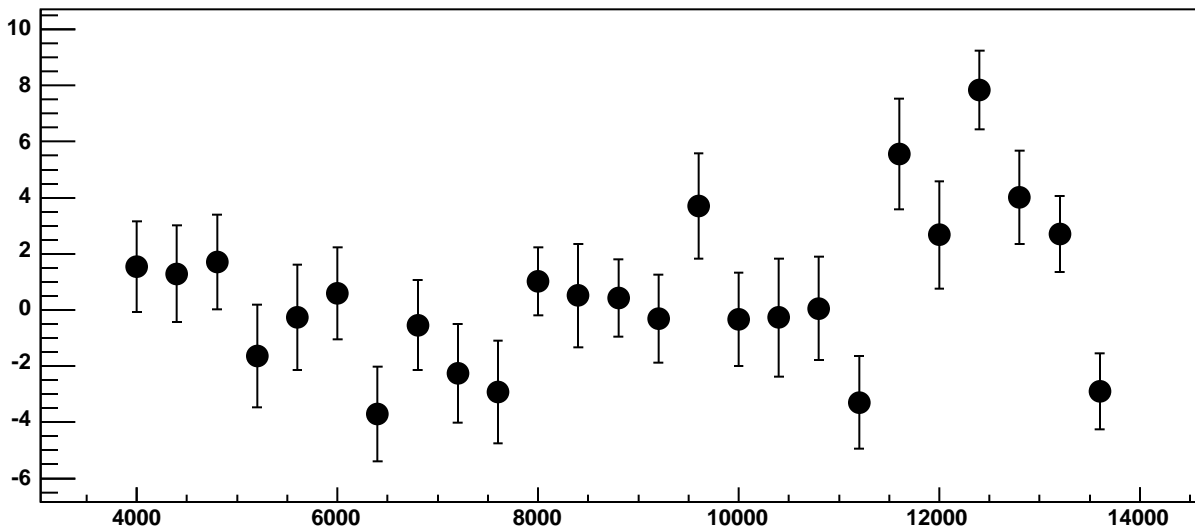
Chip 2, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



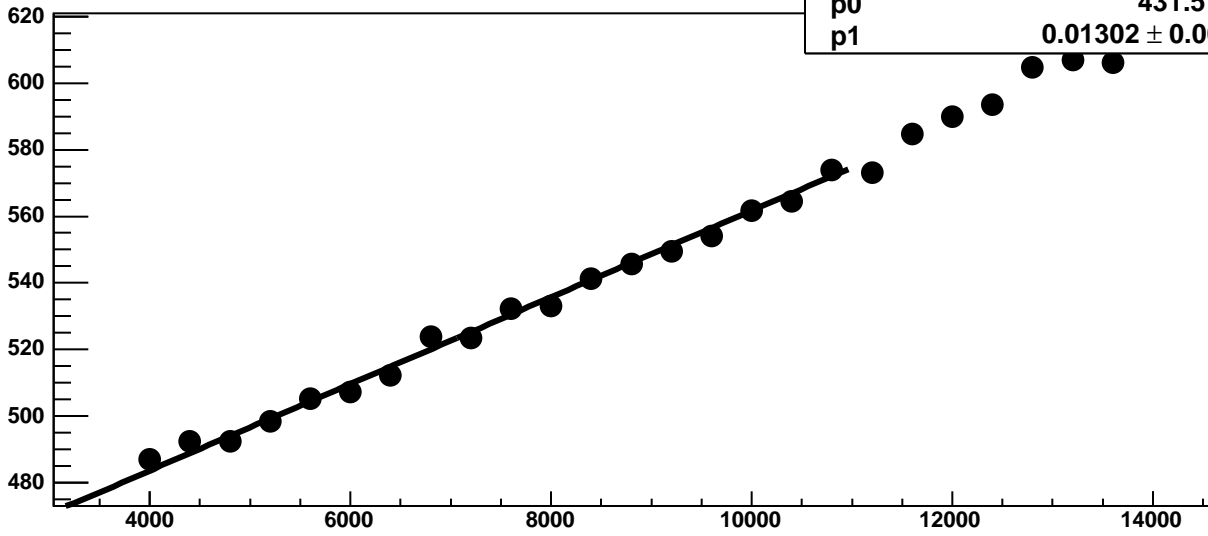
Chip 2, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



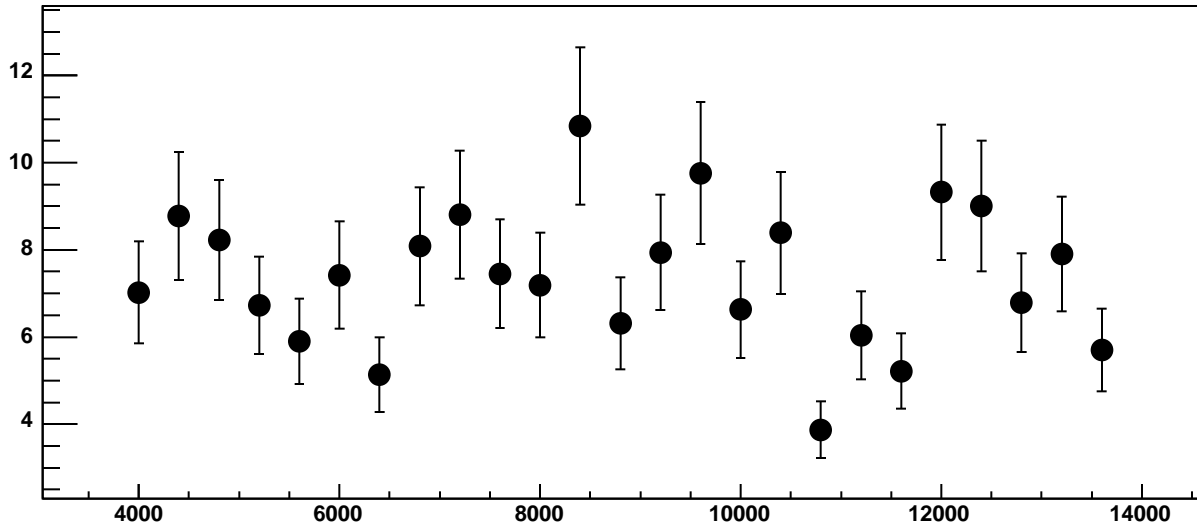
Chip 2, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



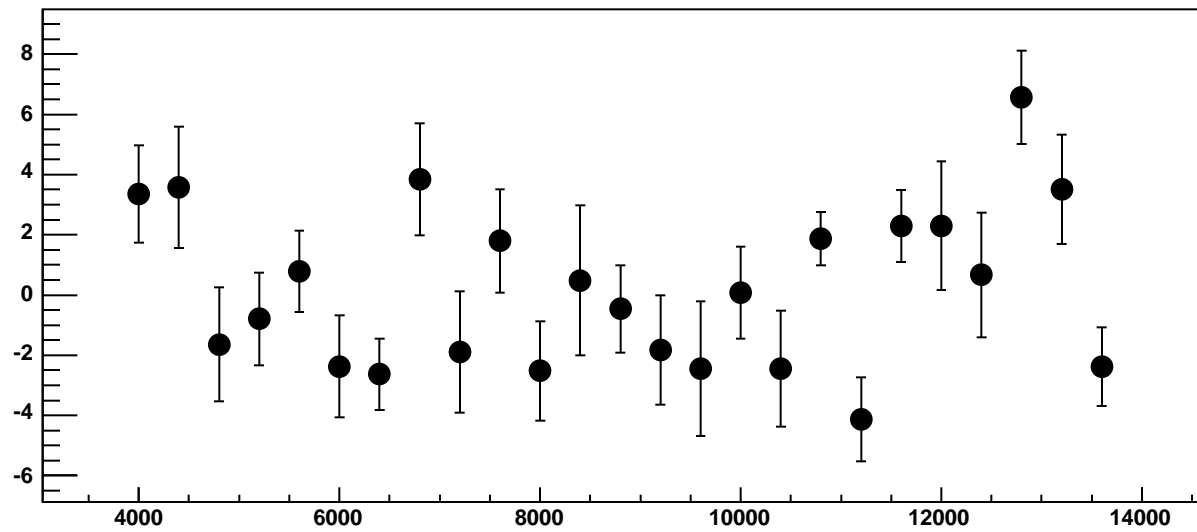
Chip 2, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



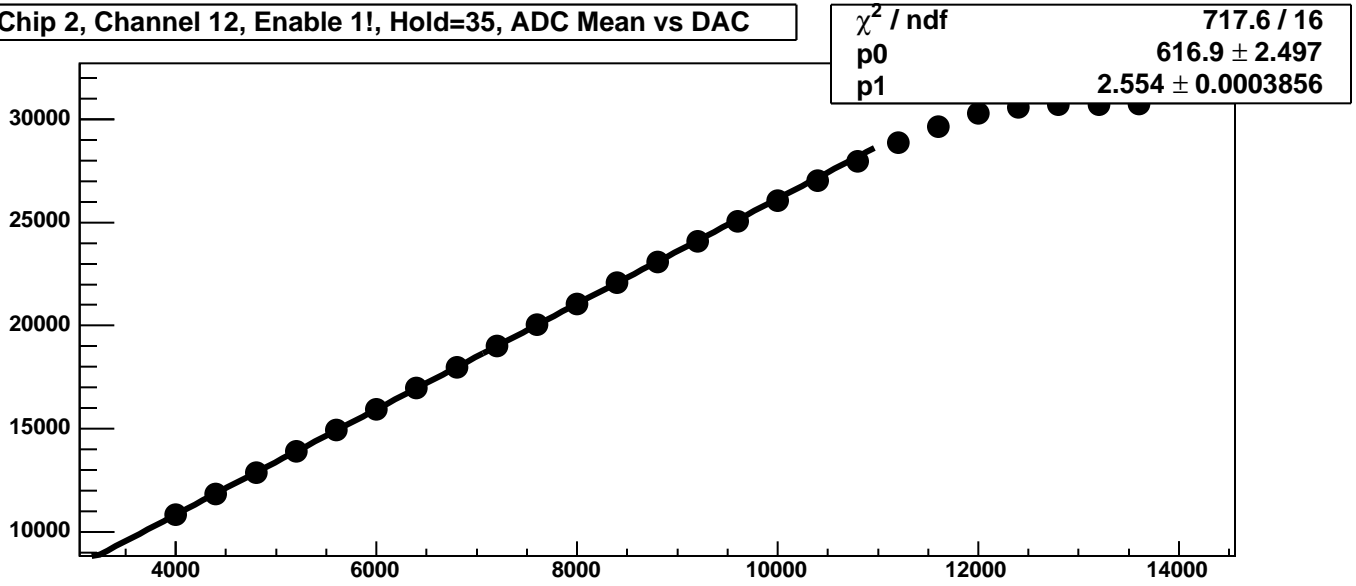
Chip 2, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



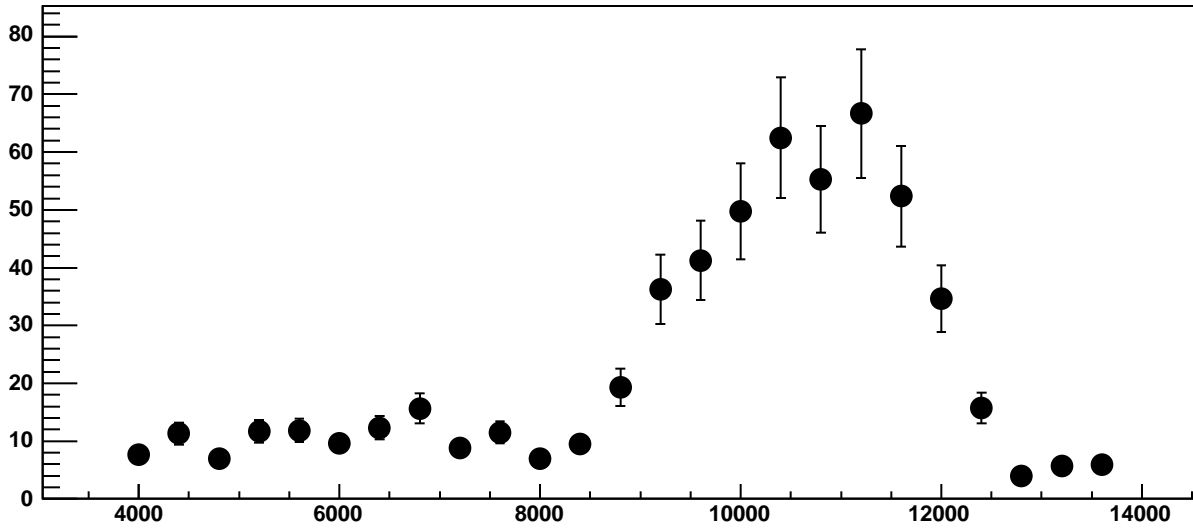
Chip 2, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



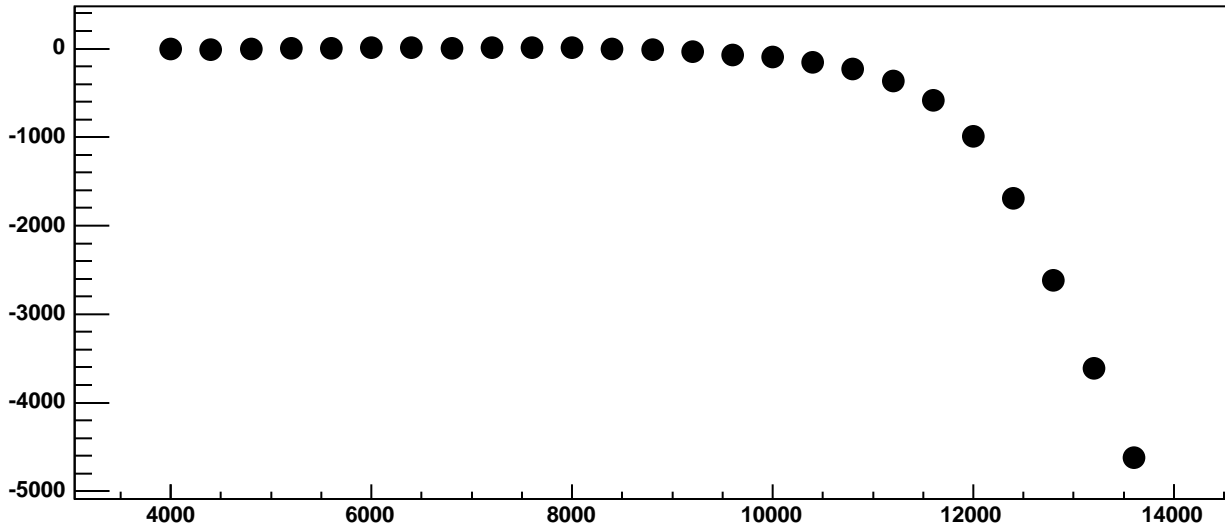
Chip 2, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC



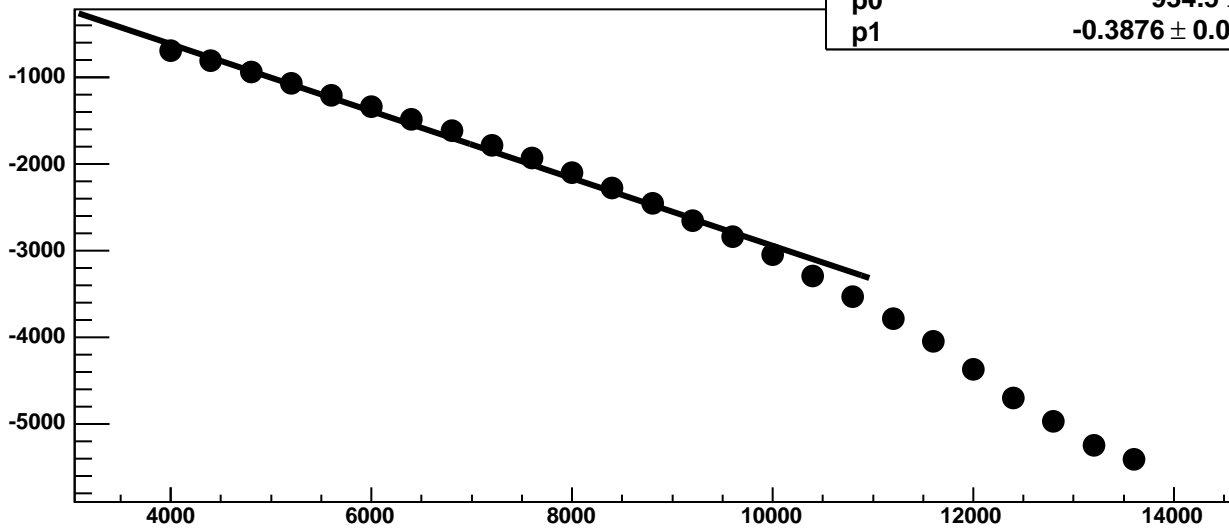
Chip 2, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



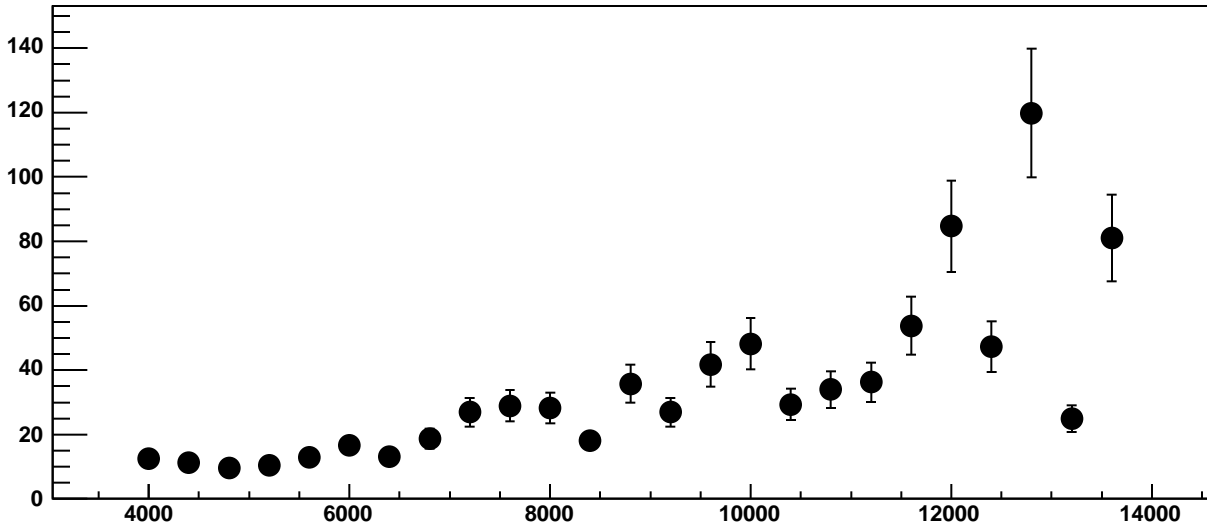
Chip 2, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC



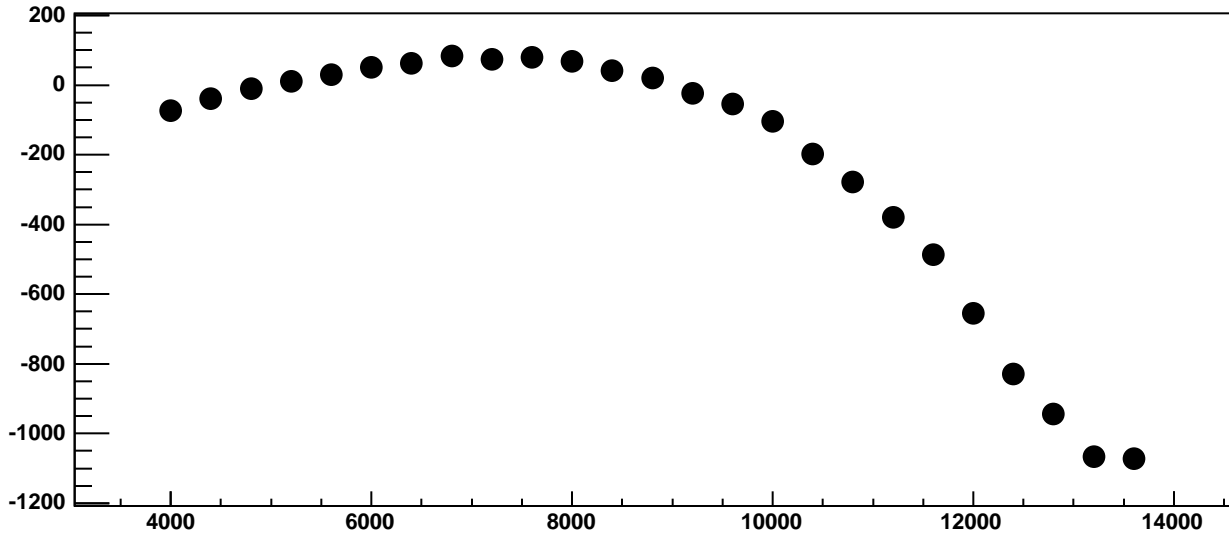
Chip 2, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



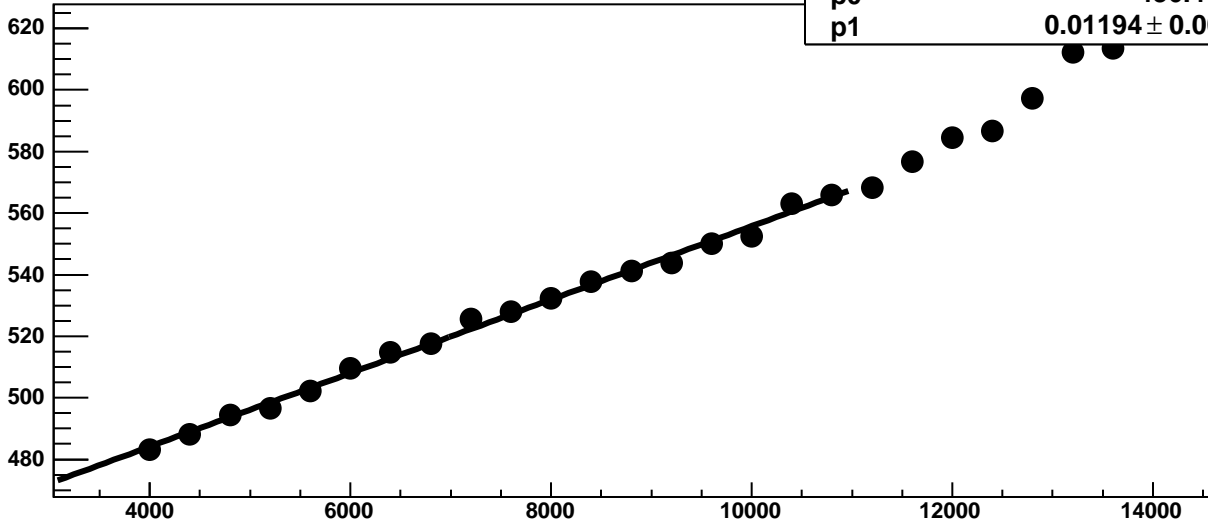
Chip 2, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

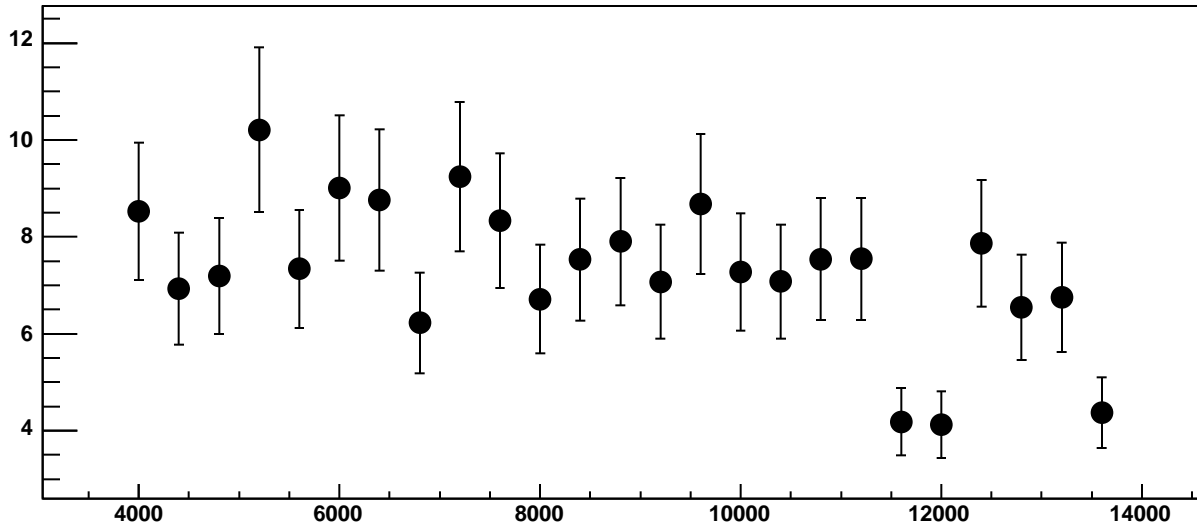


Chip 2, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

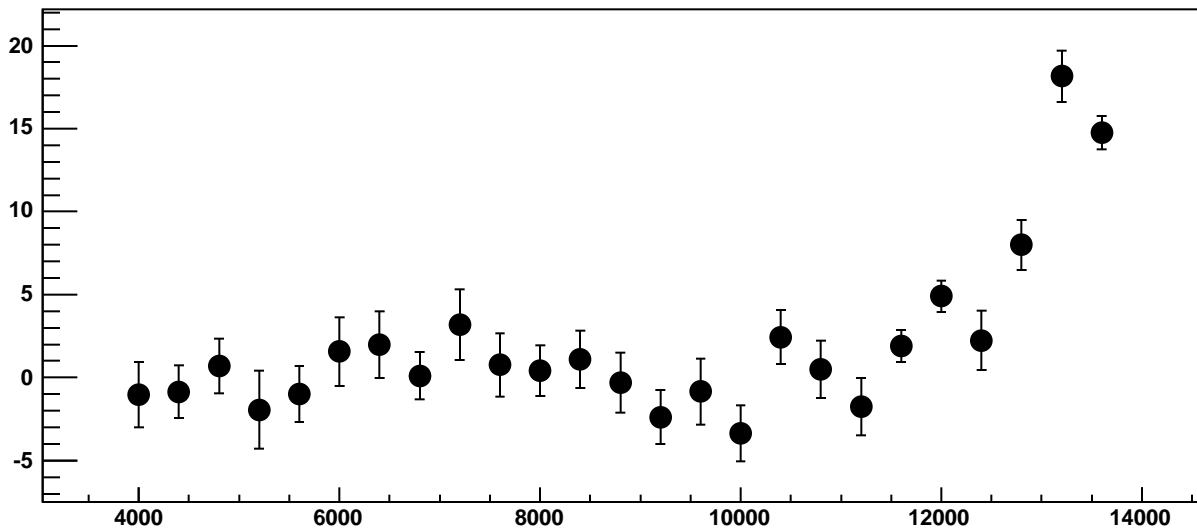


χ^2 / ndf 14.99 / 16
p0 436.4 ± 1.55
p1 0.01194 ± 0.0001993

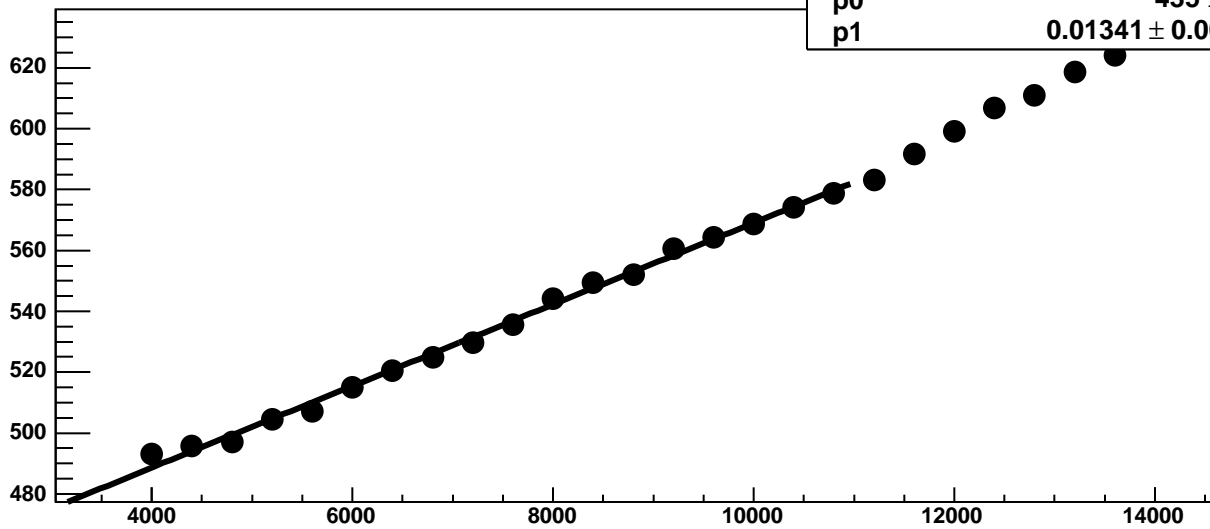
Chip 2, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



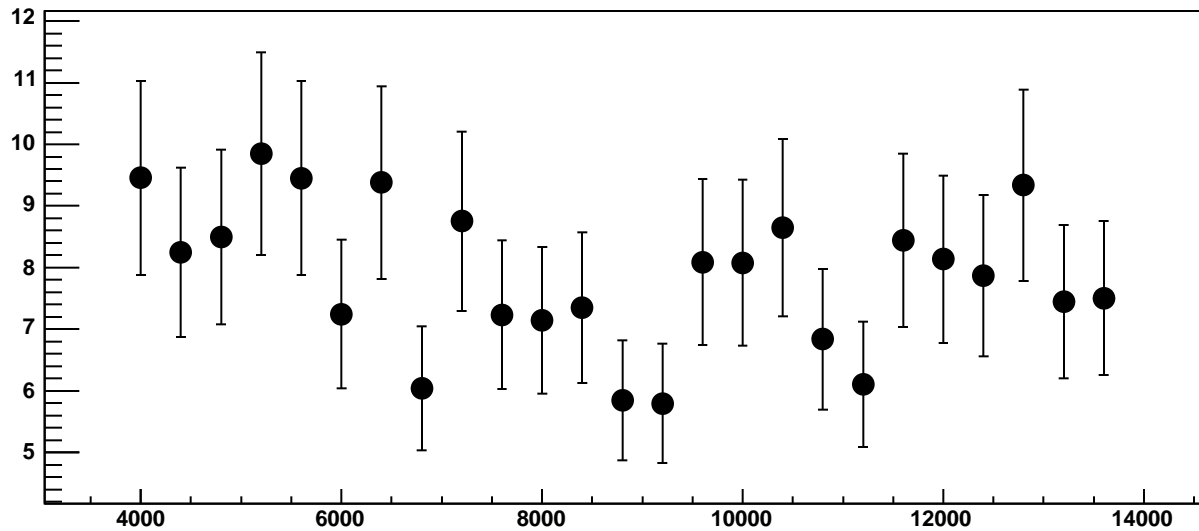
Chip 2, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



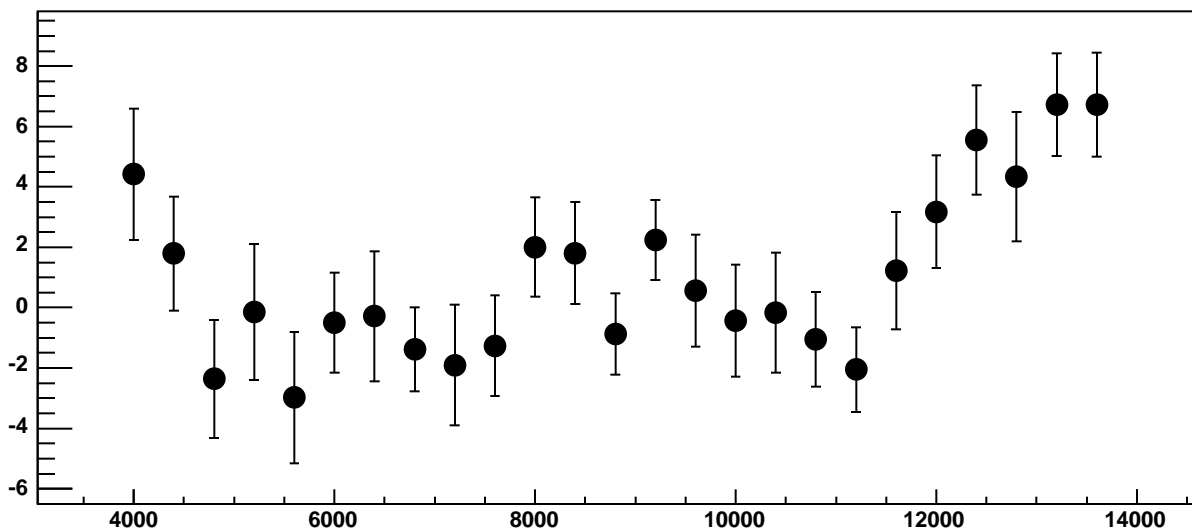
Chip 2, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC



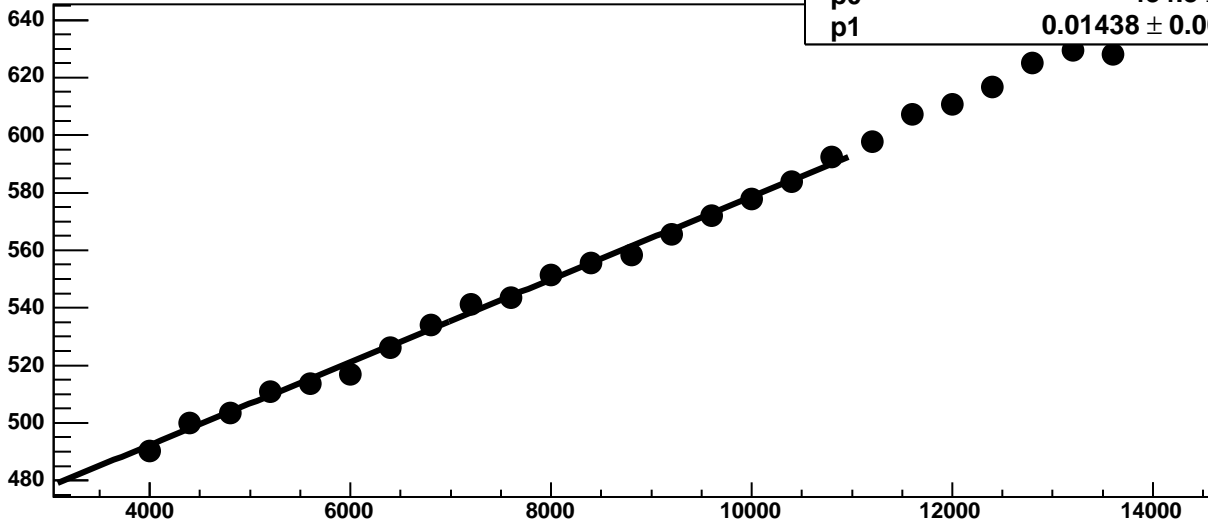
Chip 2, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

20.81 / 16

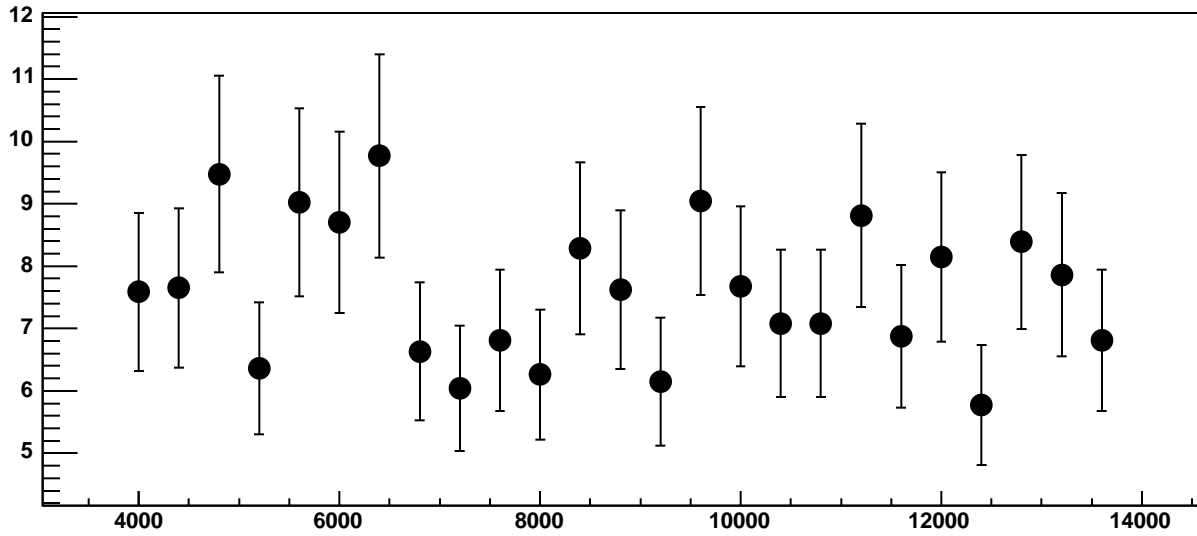
p0

434.8 ± 1.534

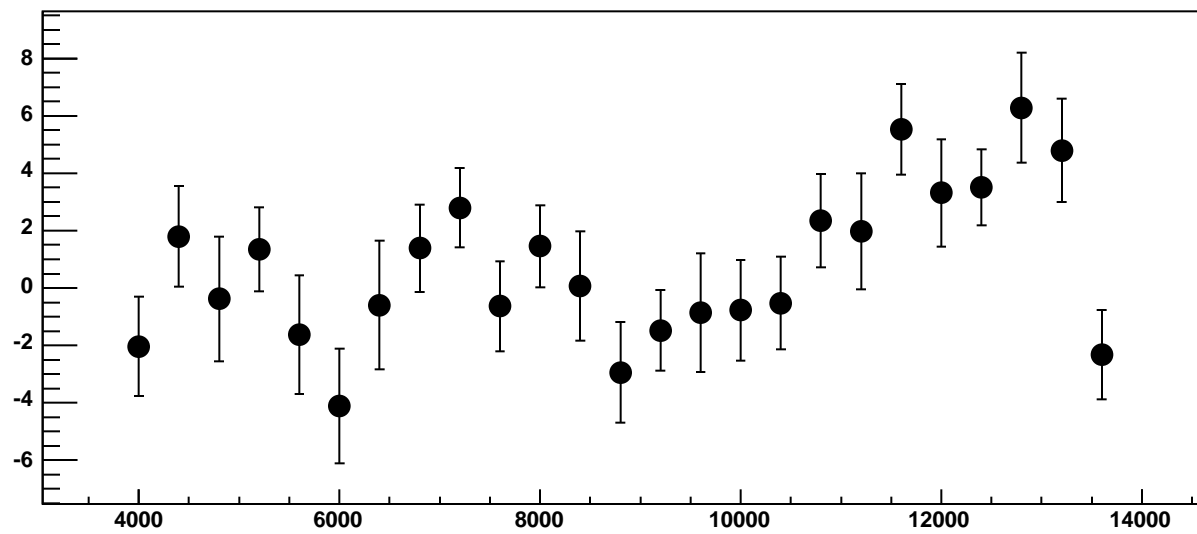
p1

0.01438 ± 0.0001969

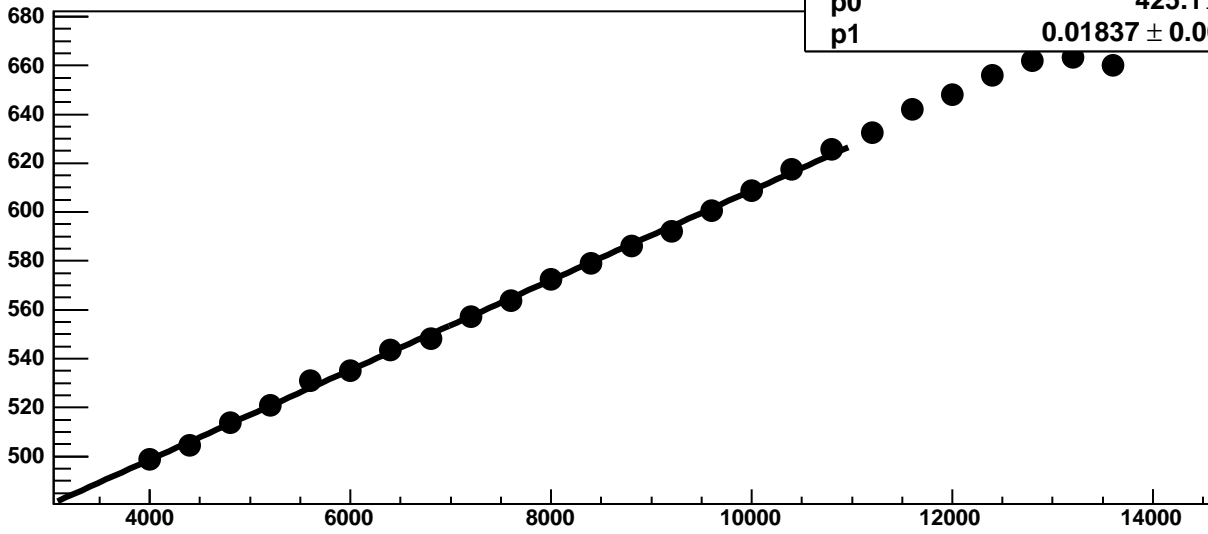
Chip 2, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



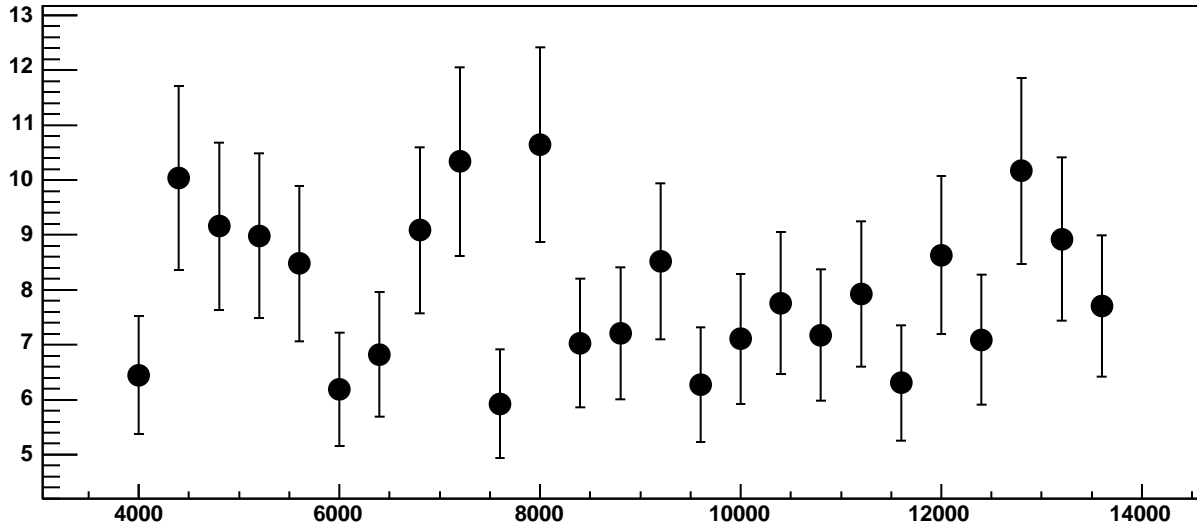
Chip 2, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



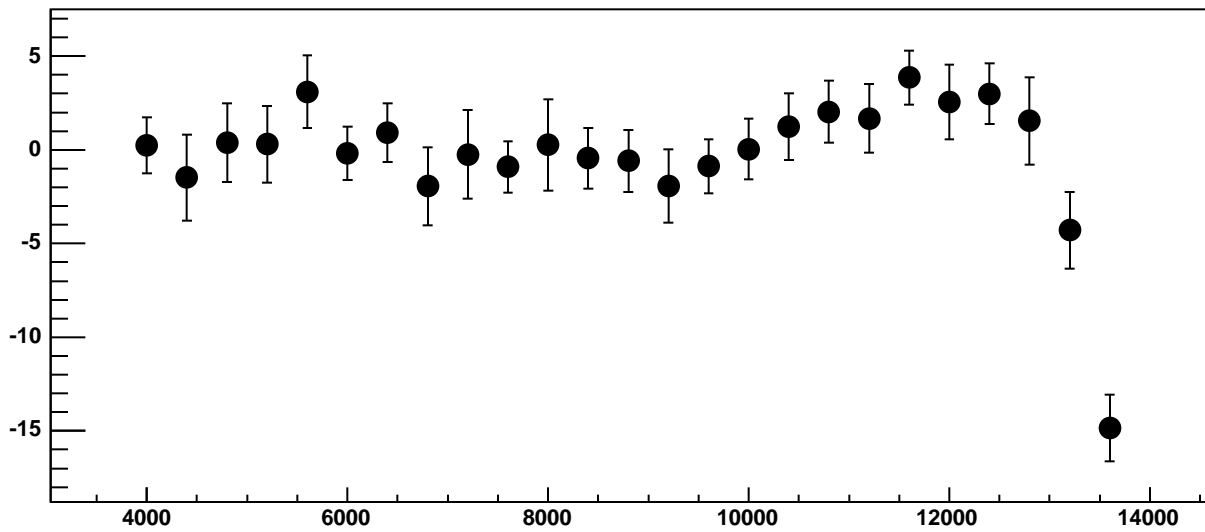
Chip 2, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC



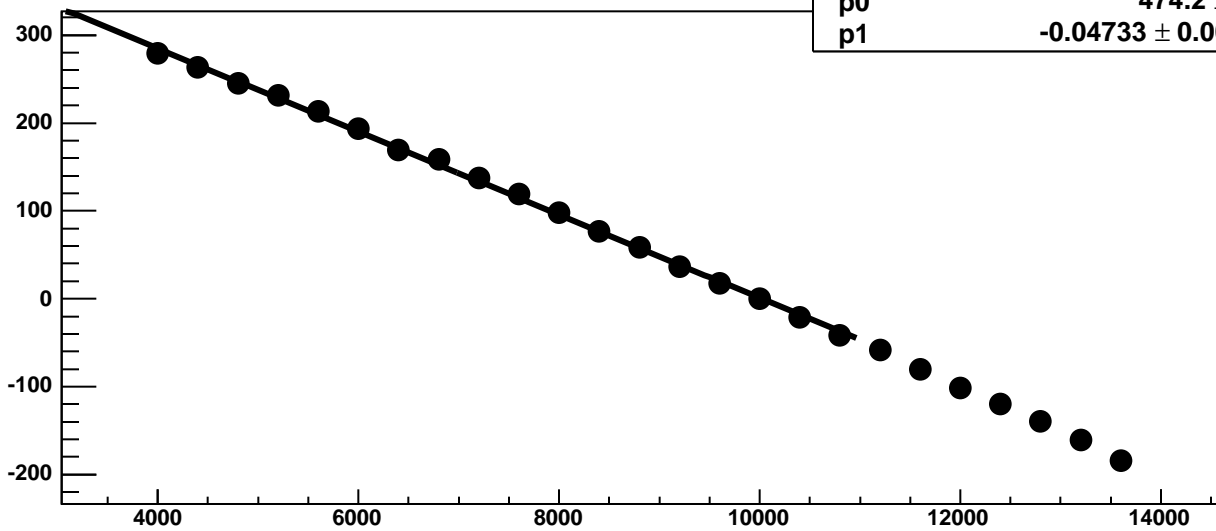
Chip 2, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

80.62 / 16

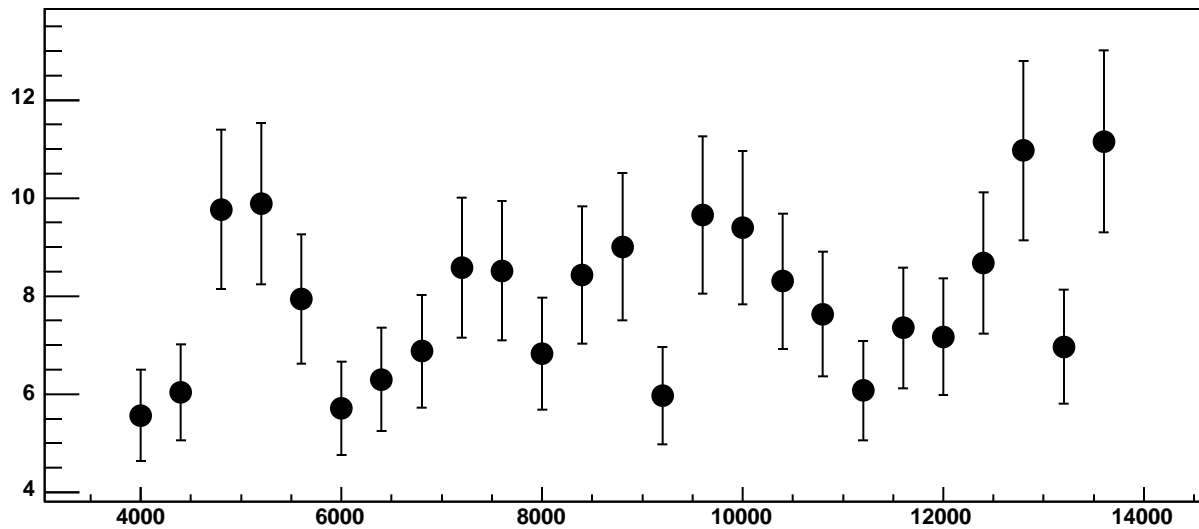
p0

474.2 ± 1.402

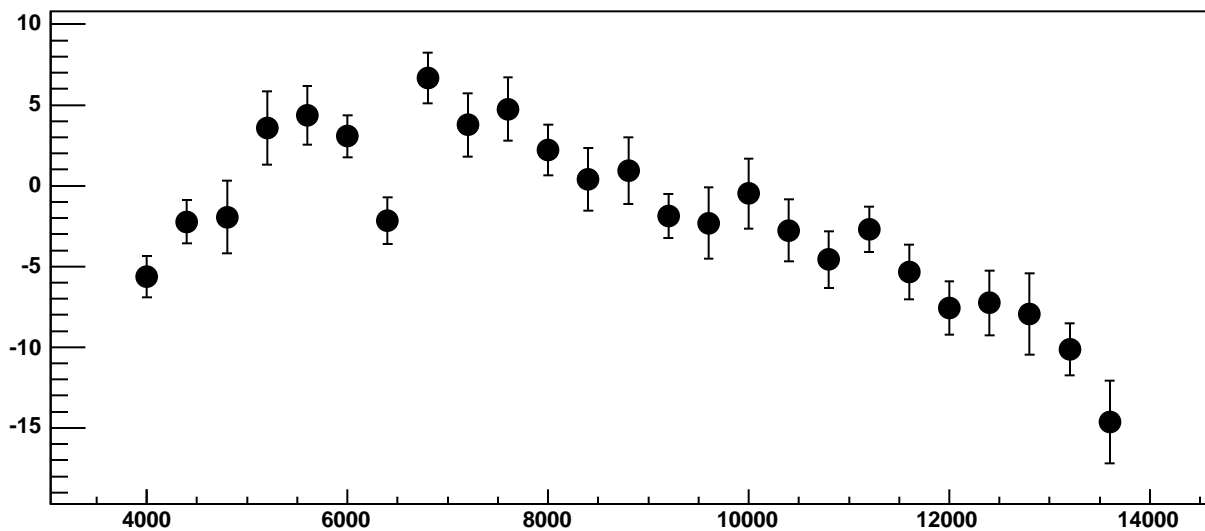
p1

-0.04733 ± 0.0001899

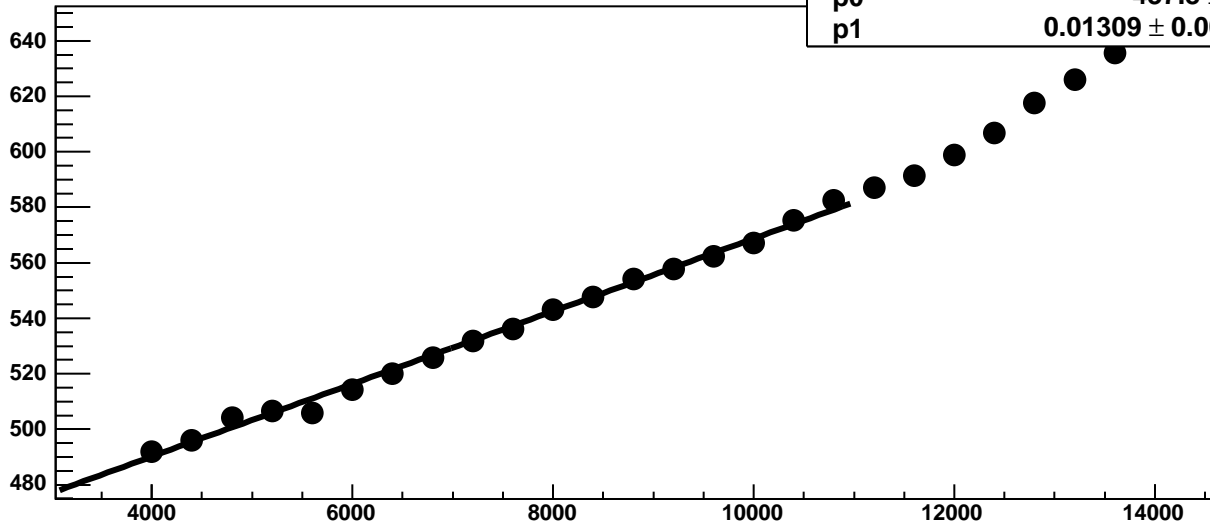
Chip 2, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



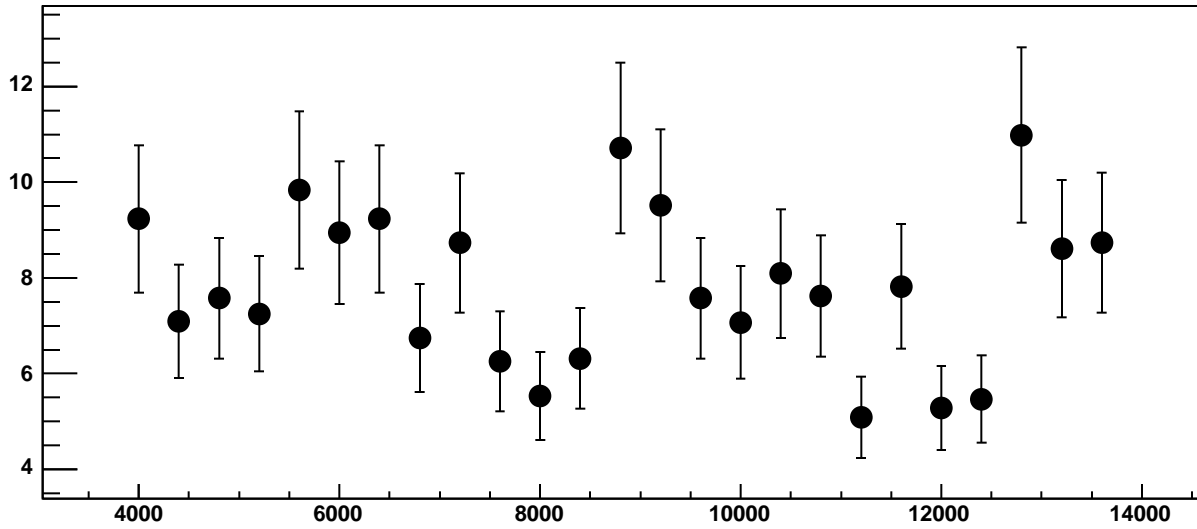
Chip 2, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



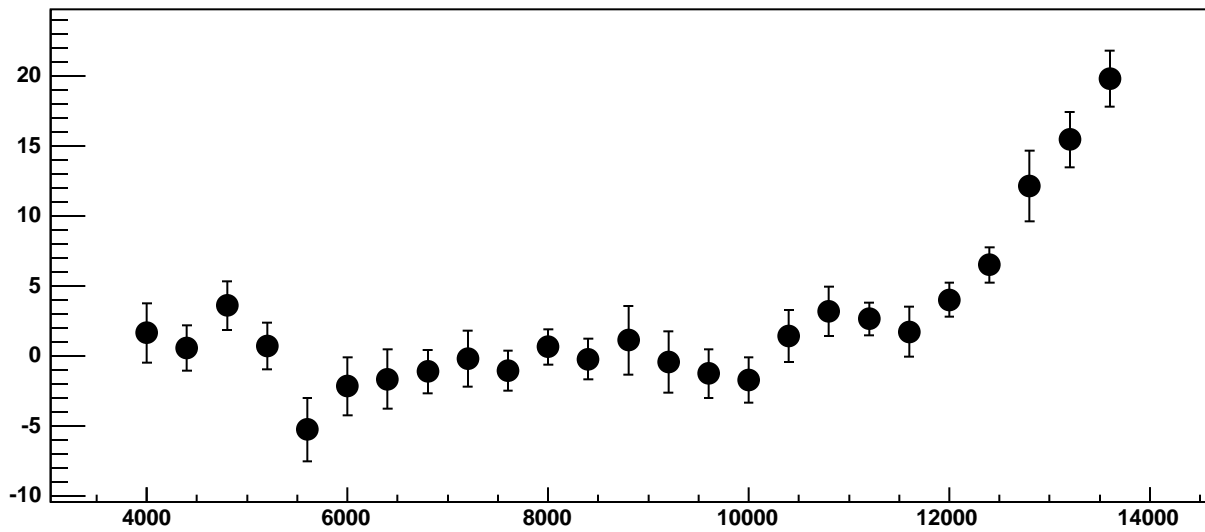
Chip 2, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC



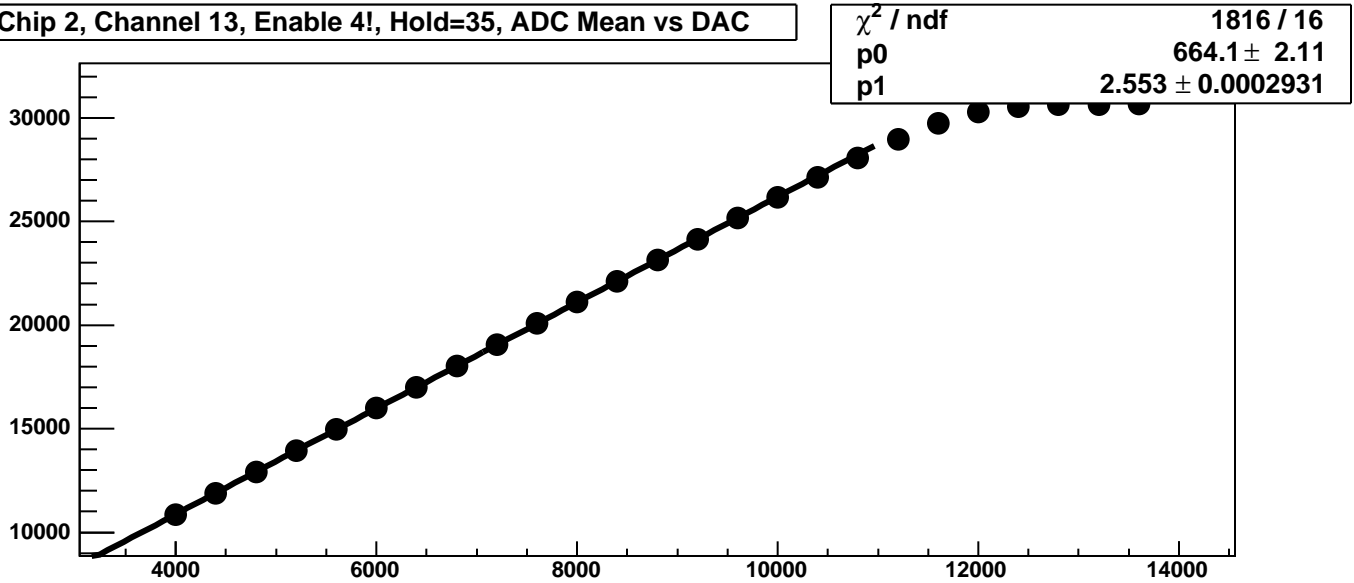
Chip 2, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



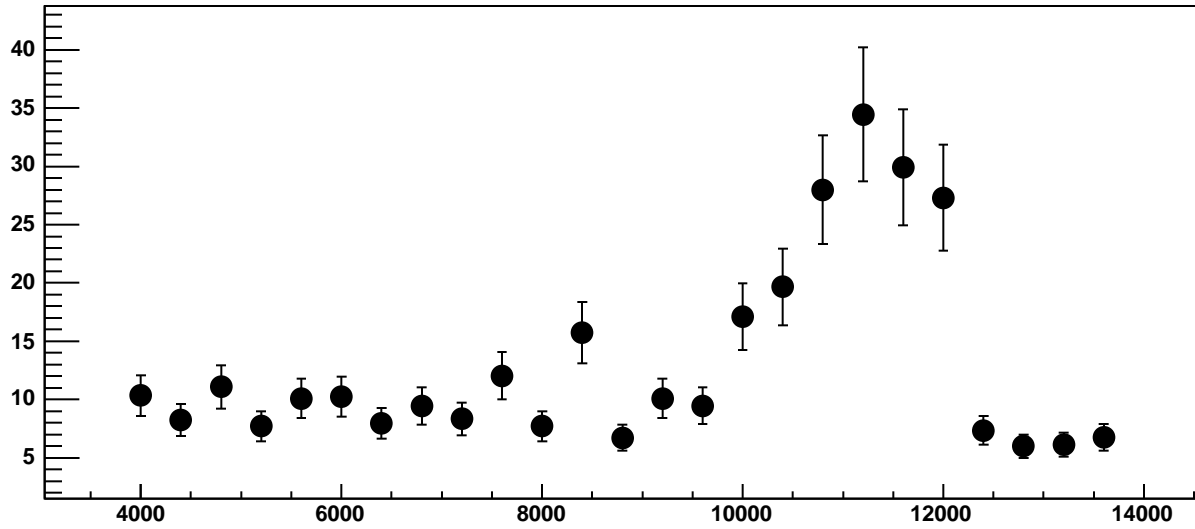
Chip 2, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



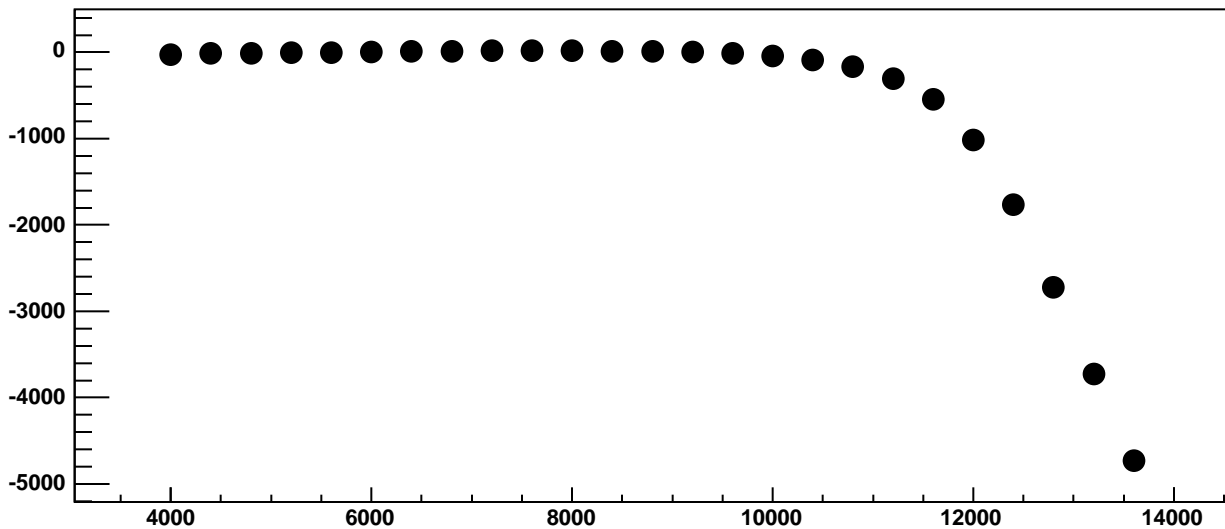
Chip 2, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC



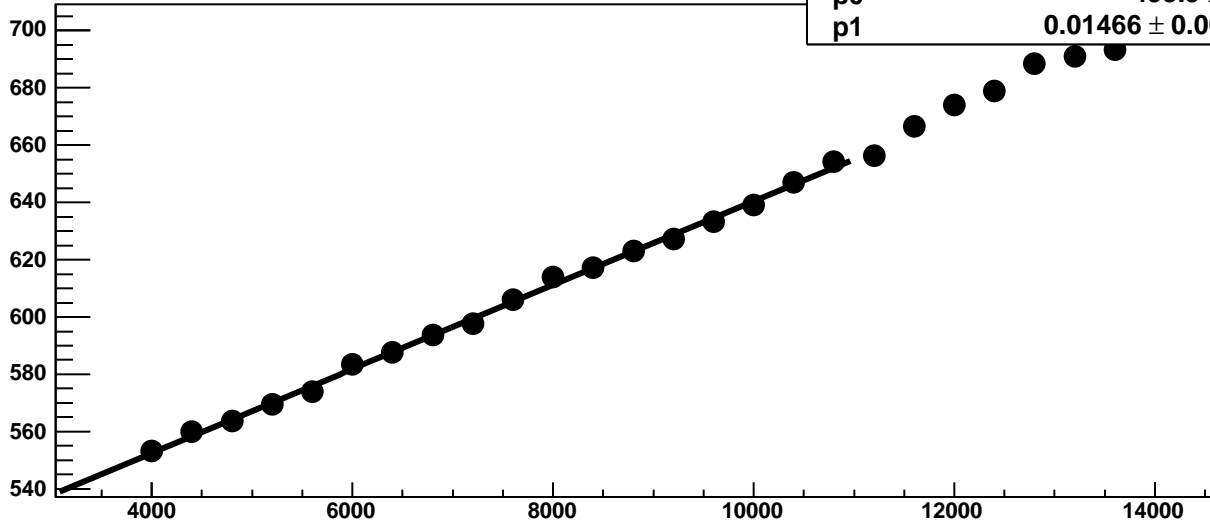
Chip 2, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC



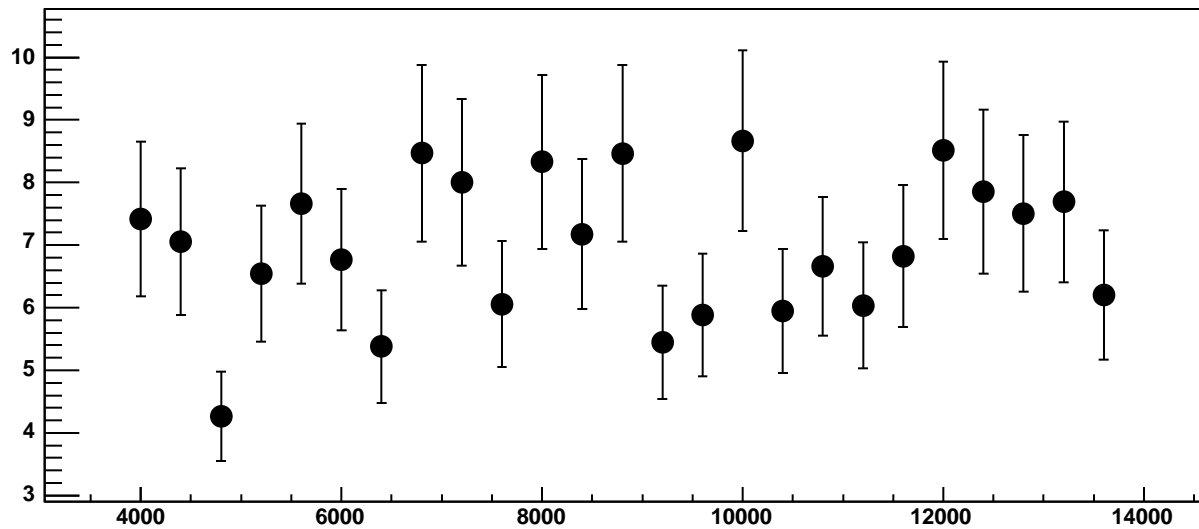
Chip 2, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC



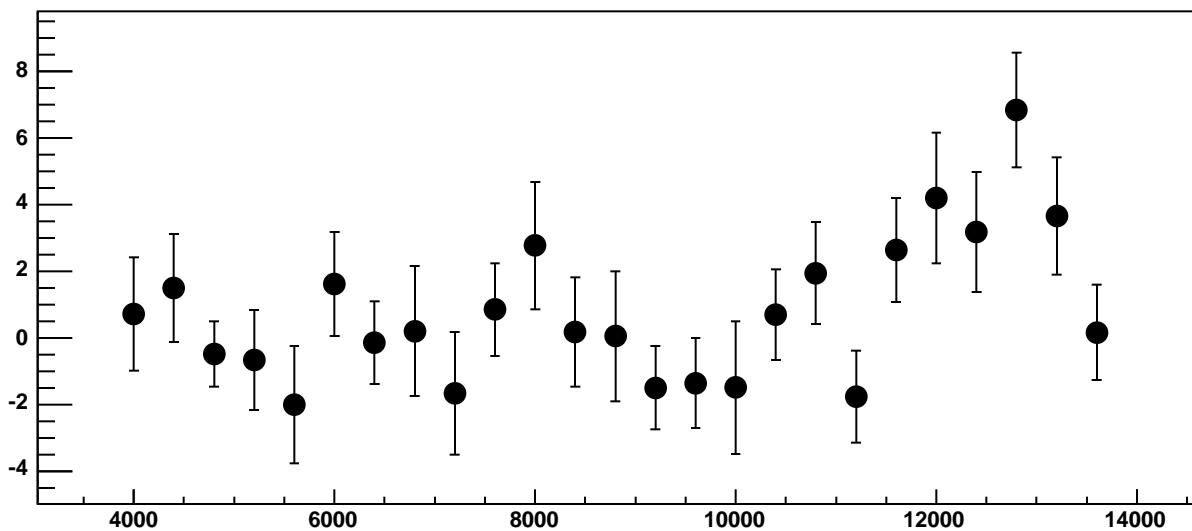
Chip 2, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



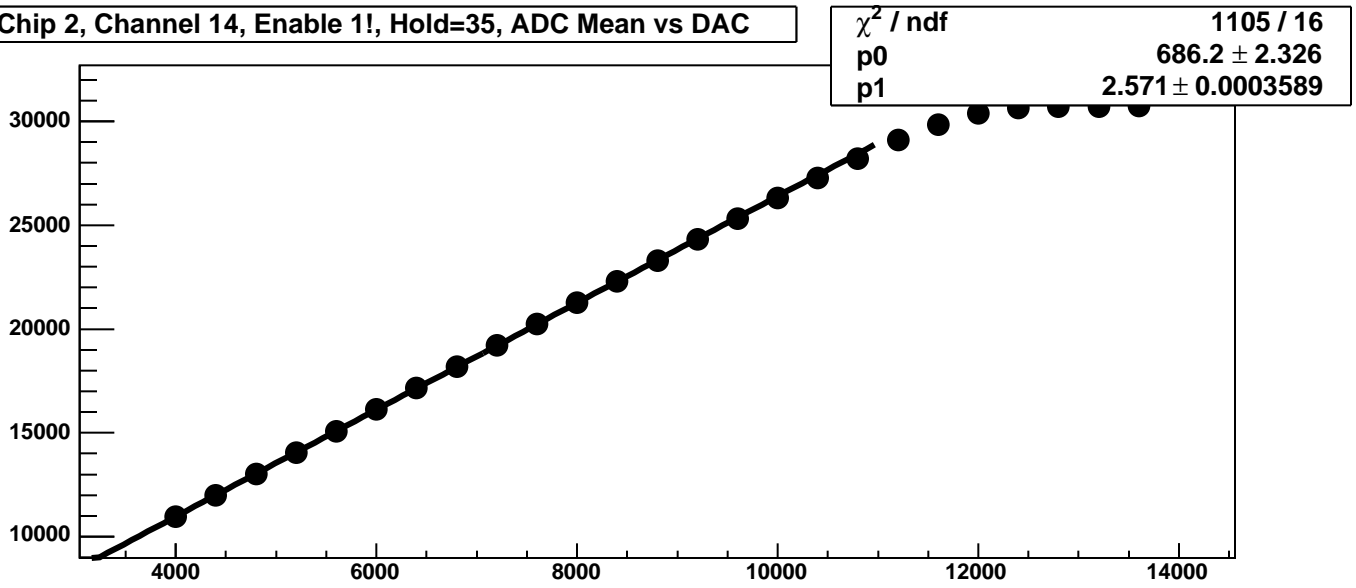
Chip 2, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



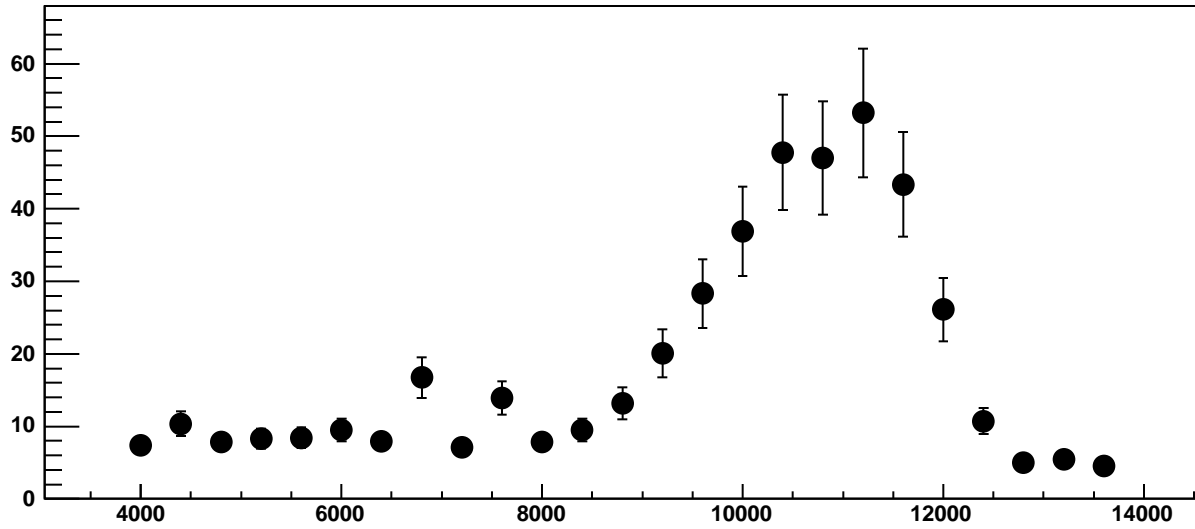
Chip 2, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



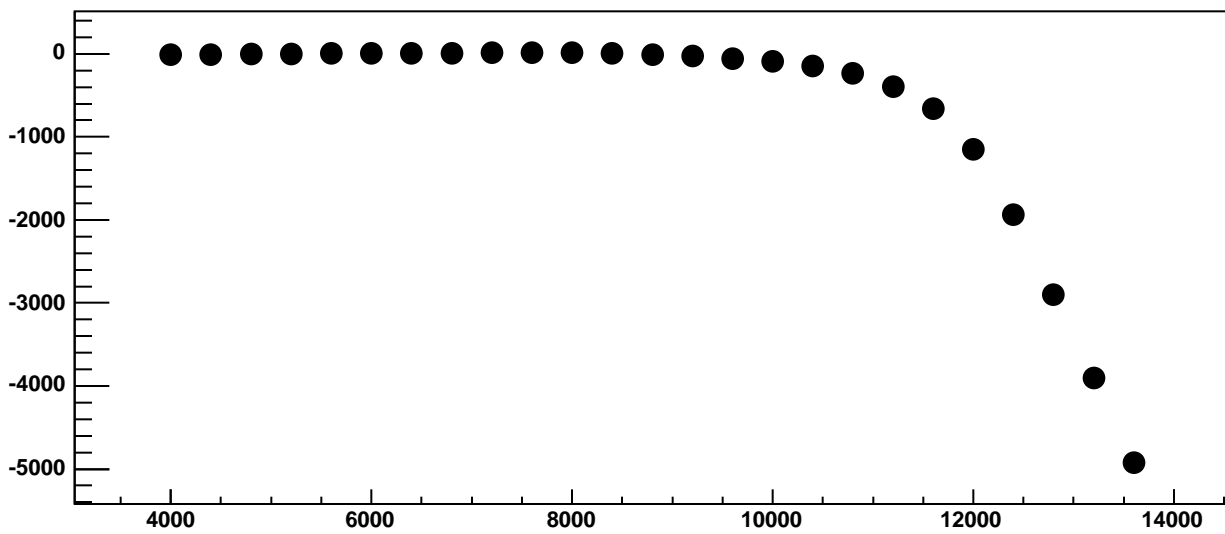
Chip 2, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC



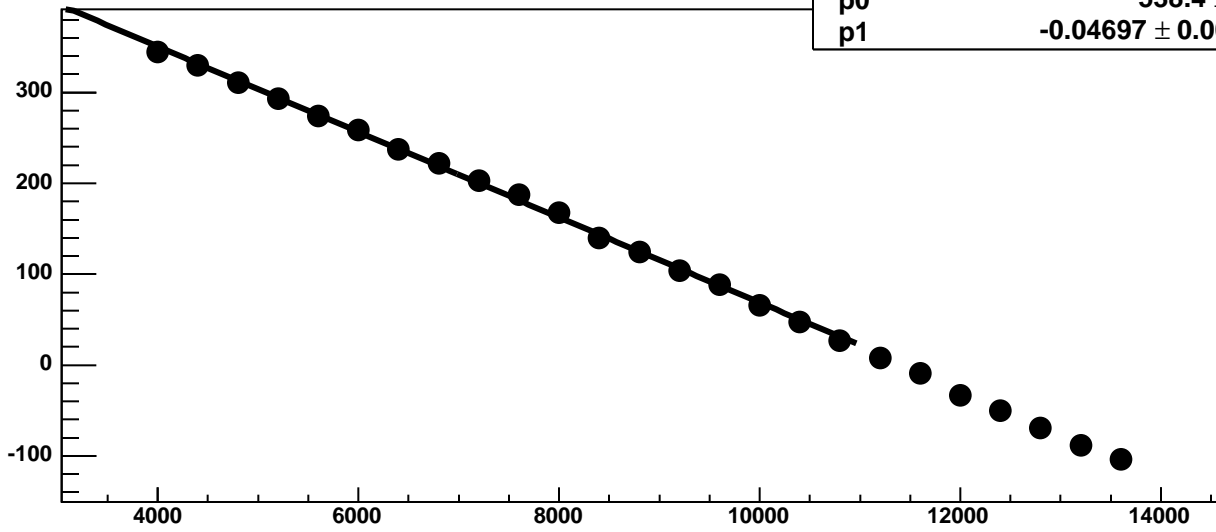
Chip 2, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



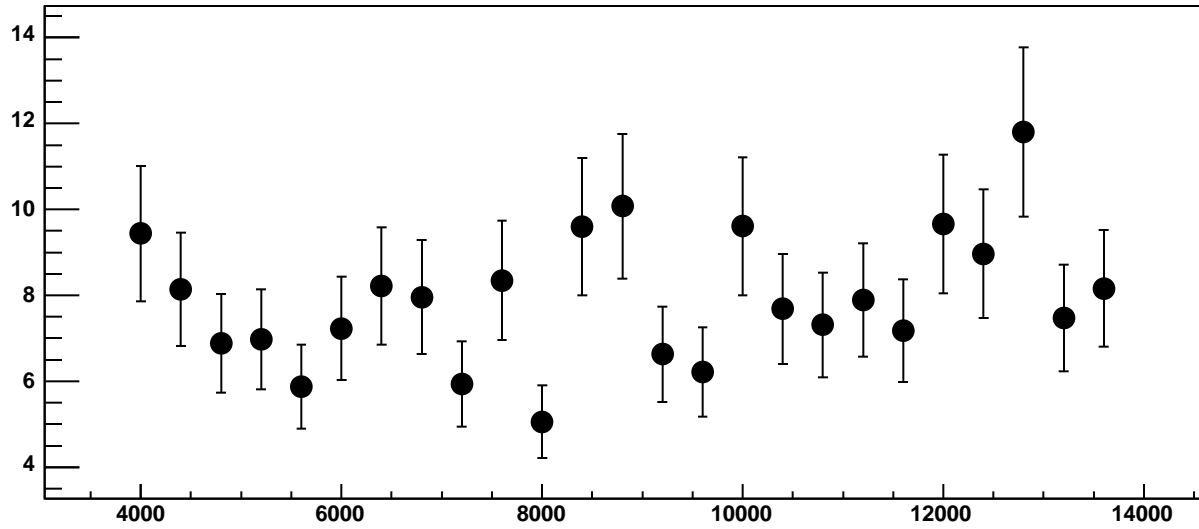
Chip 2, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC



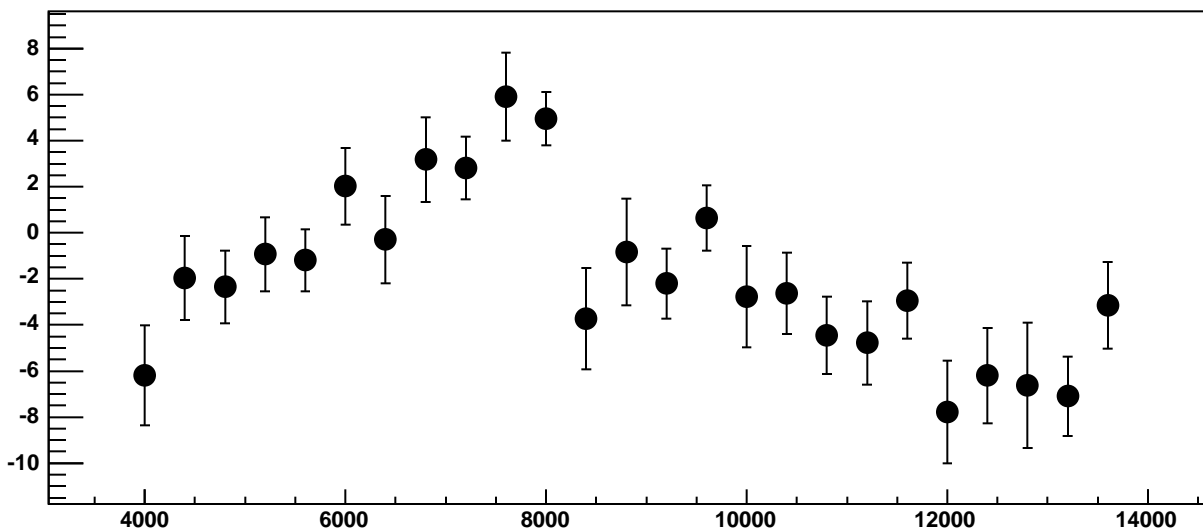
Chip 2, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



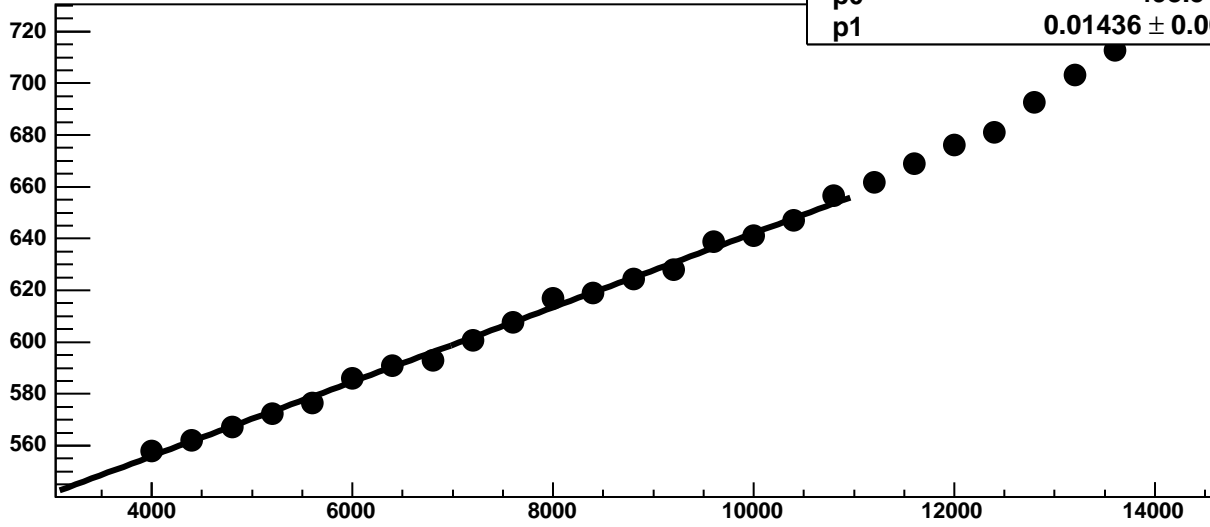
Chip 2, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

19.11 / 16

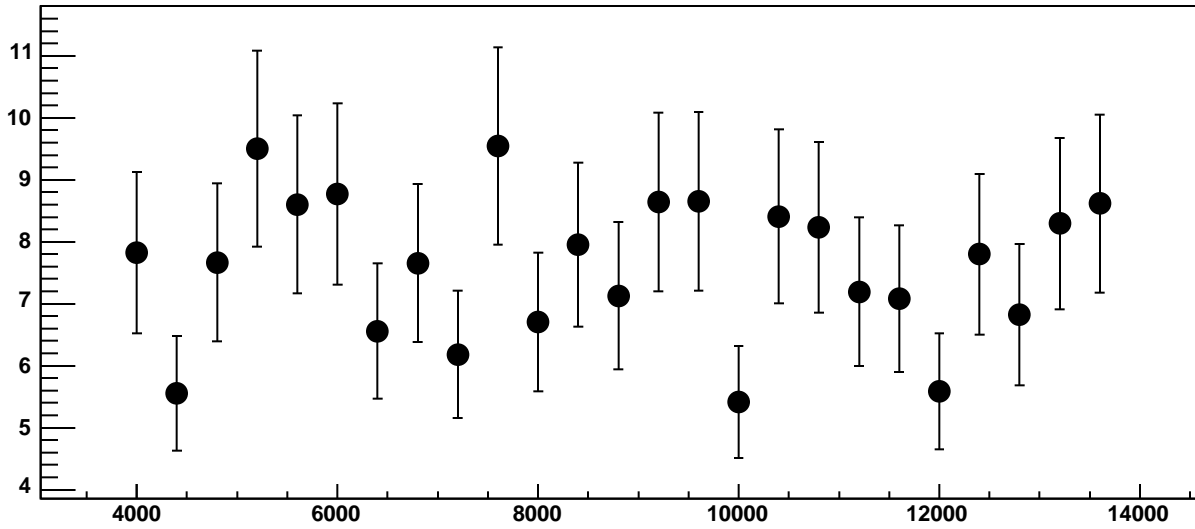
p0

498.5 ± 1.461

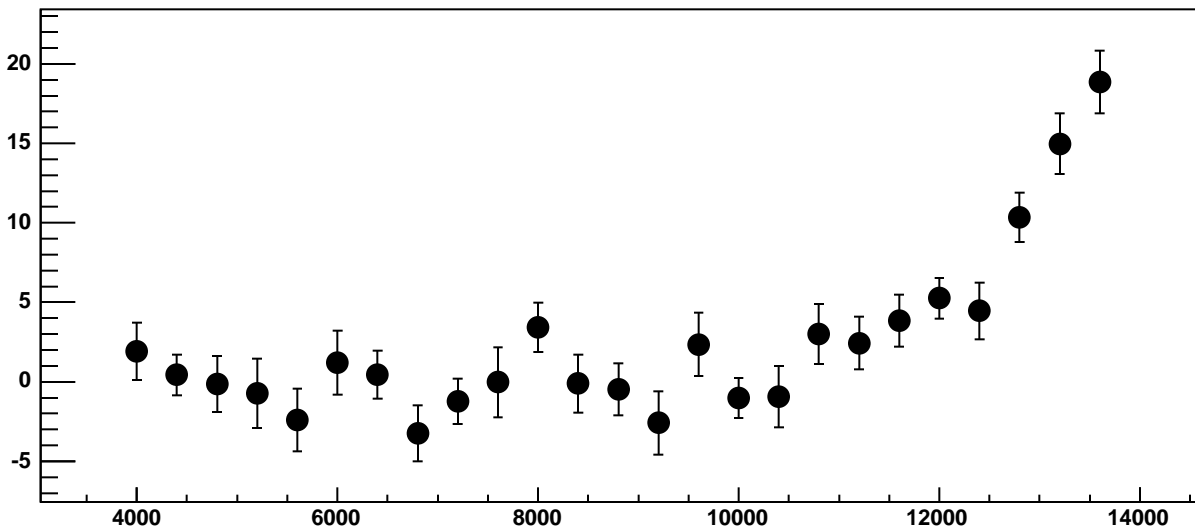
p1

0.01436 ± 0.0001906

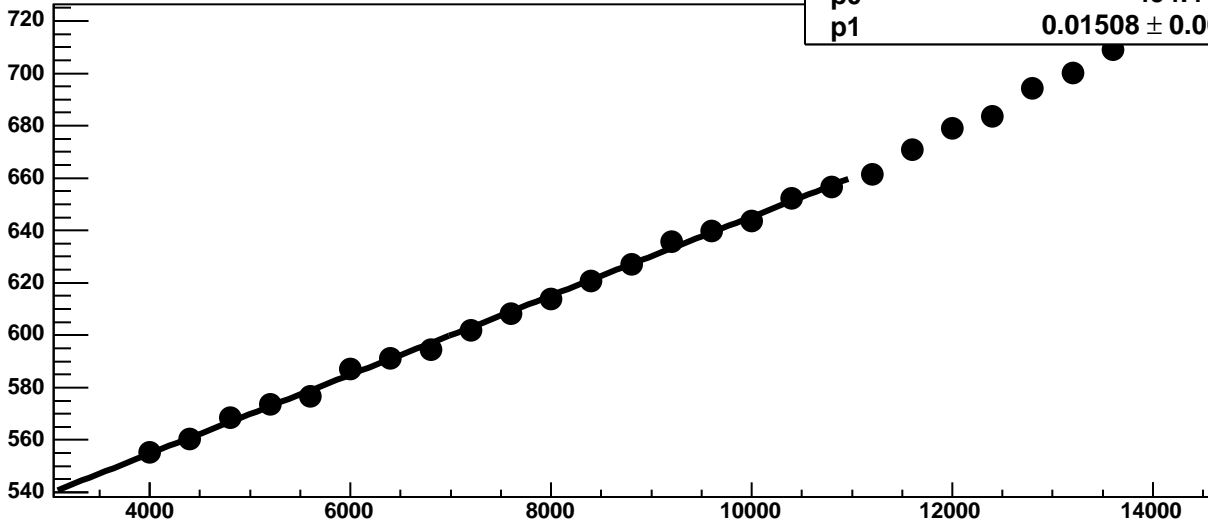
Chip 2, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

13.38 / 16

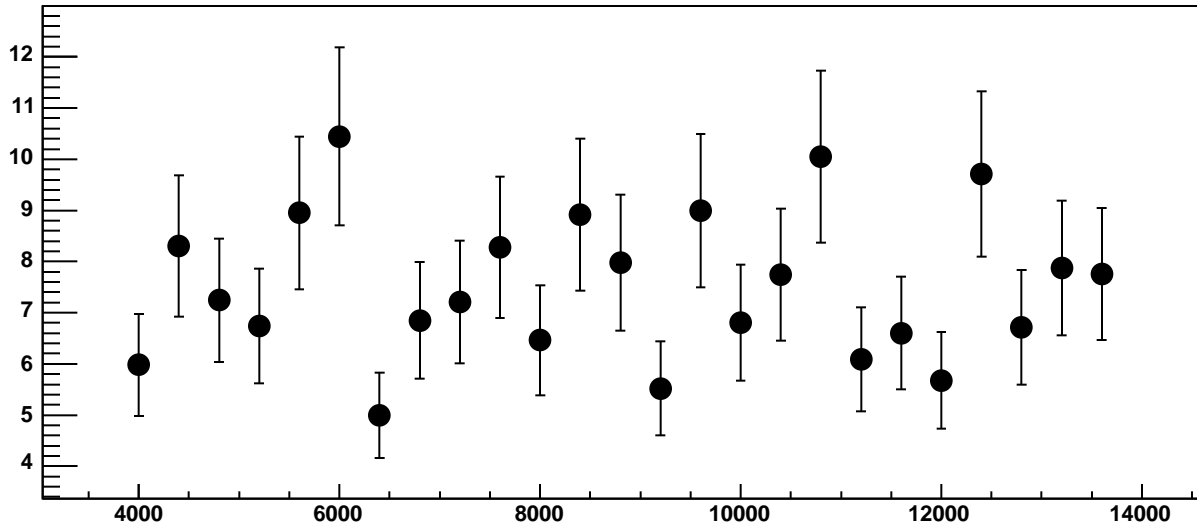
p0

494.4 ± 1.451

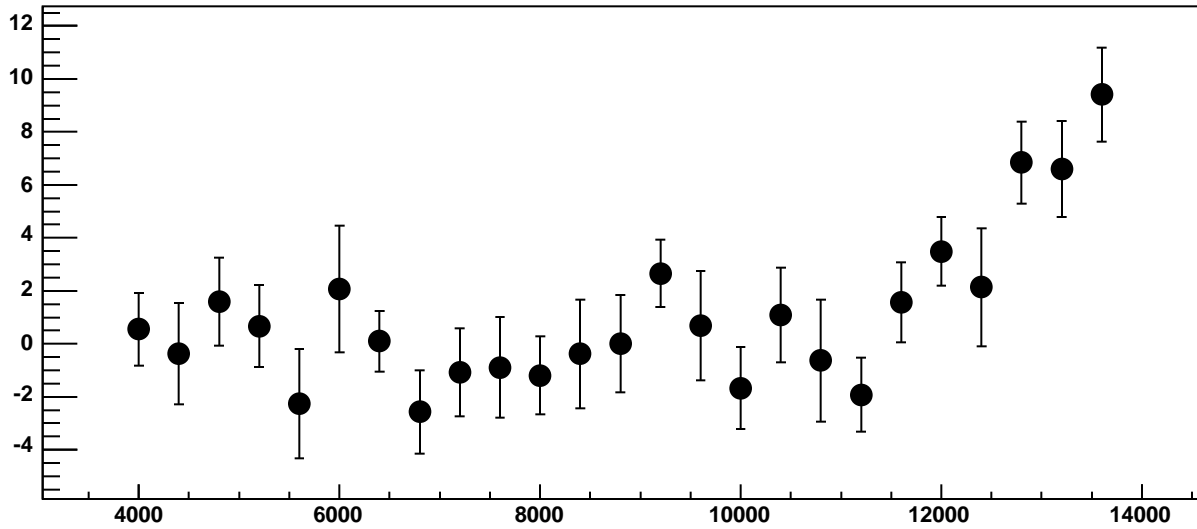
p1

0.01508 ± 0.0001927

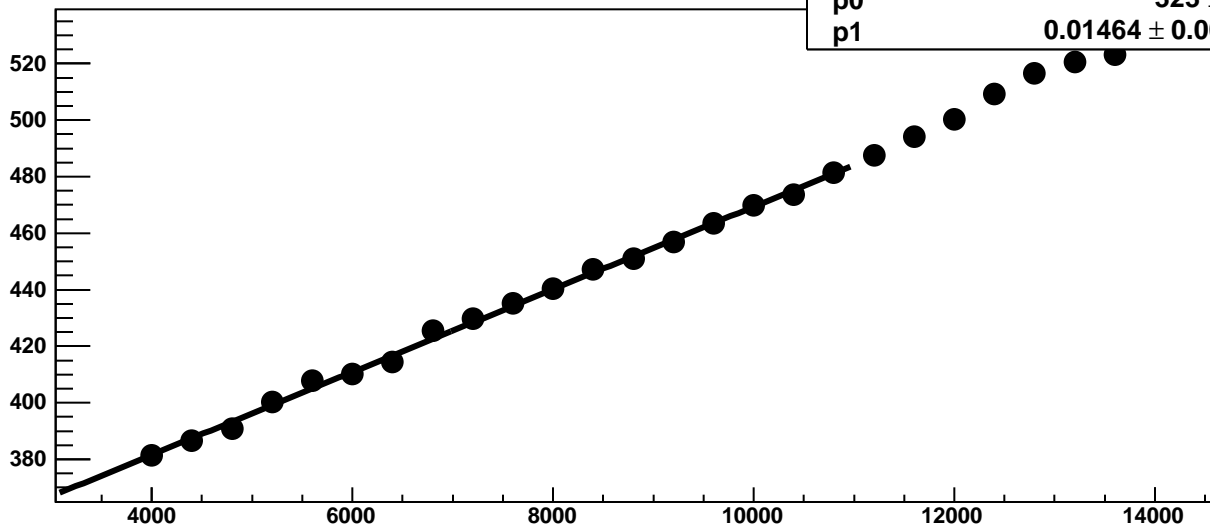
Chip 2, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



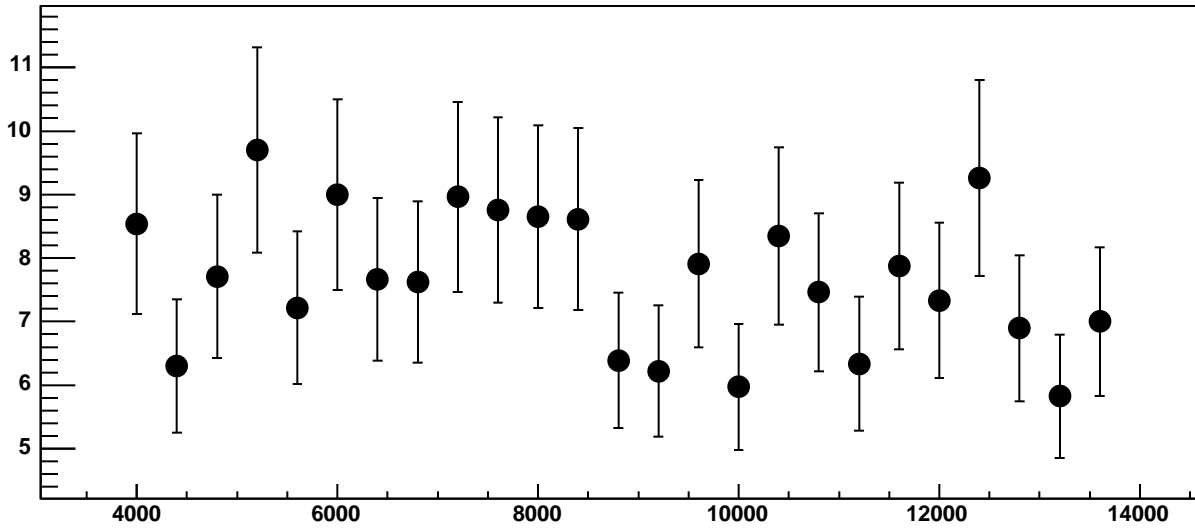
Chip 2, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC



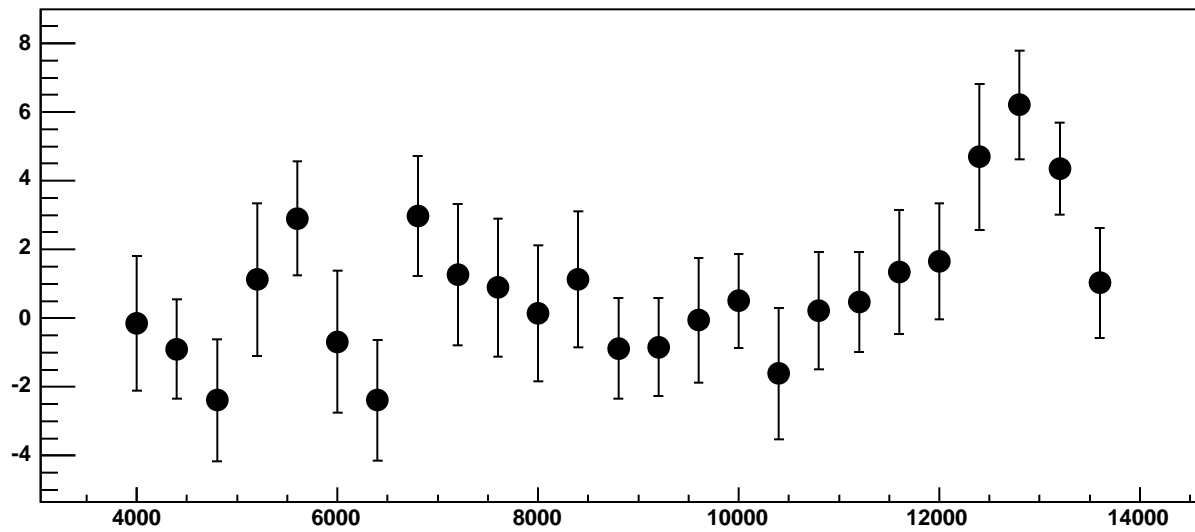
Chip 2, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



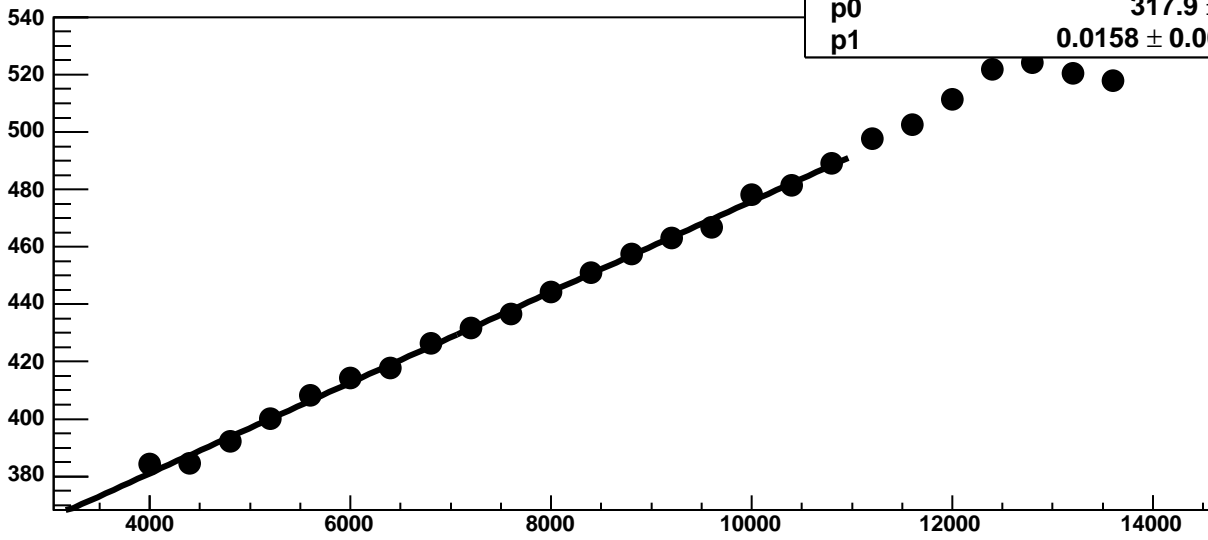
Chip 2, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

15.61 / 16

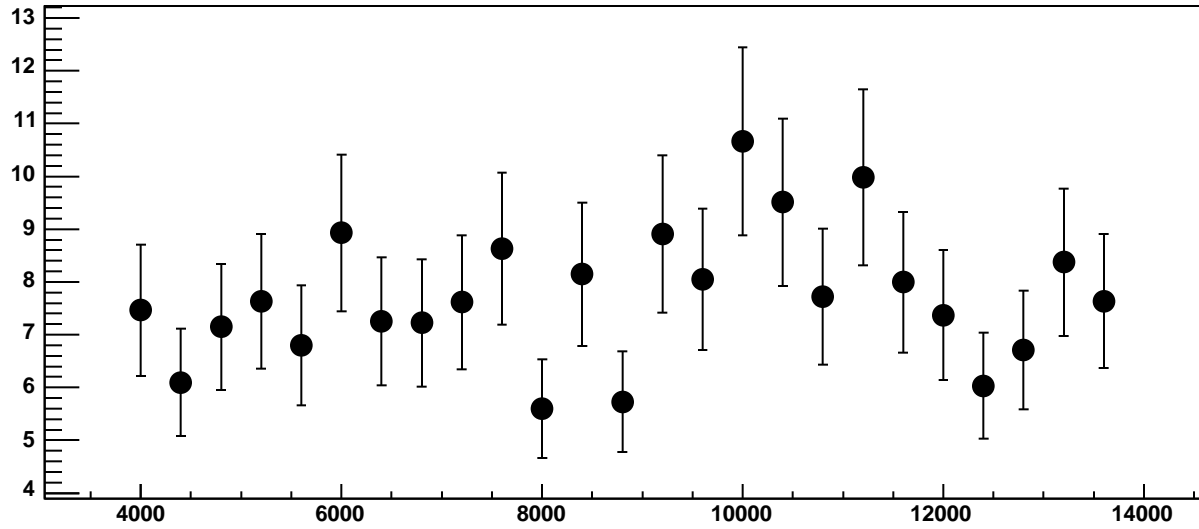
p0

317.9 ± 1.486

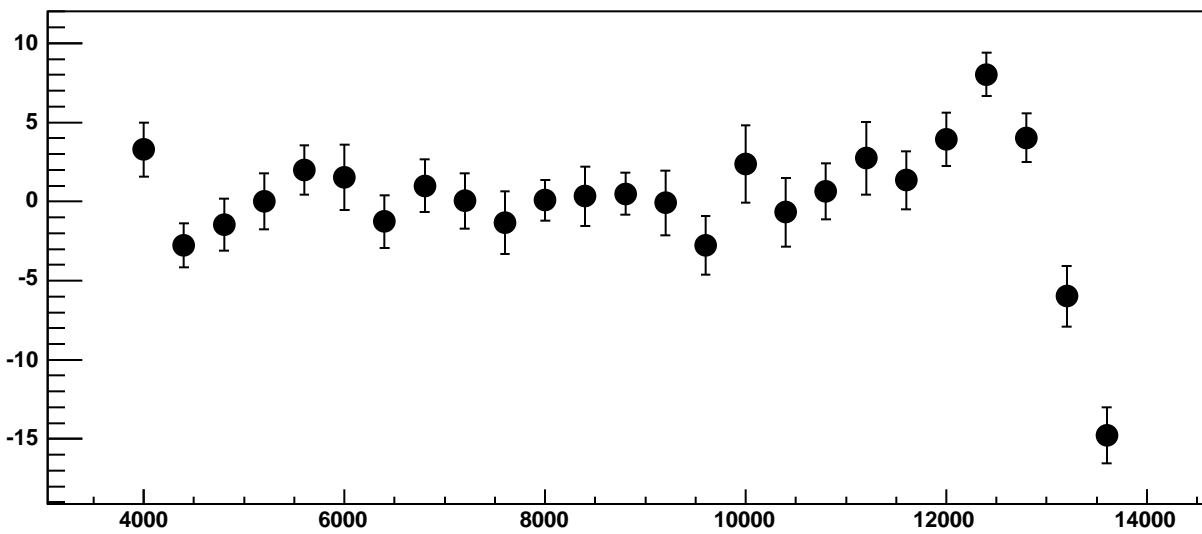
p1

0.0158 ± 0.0001992

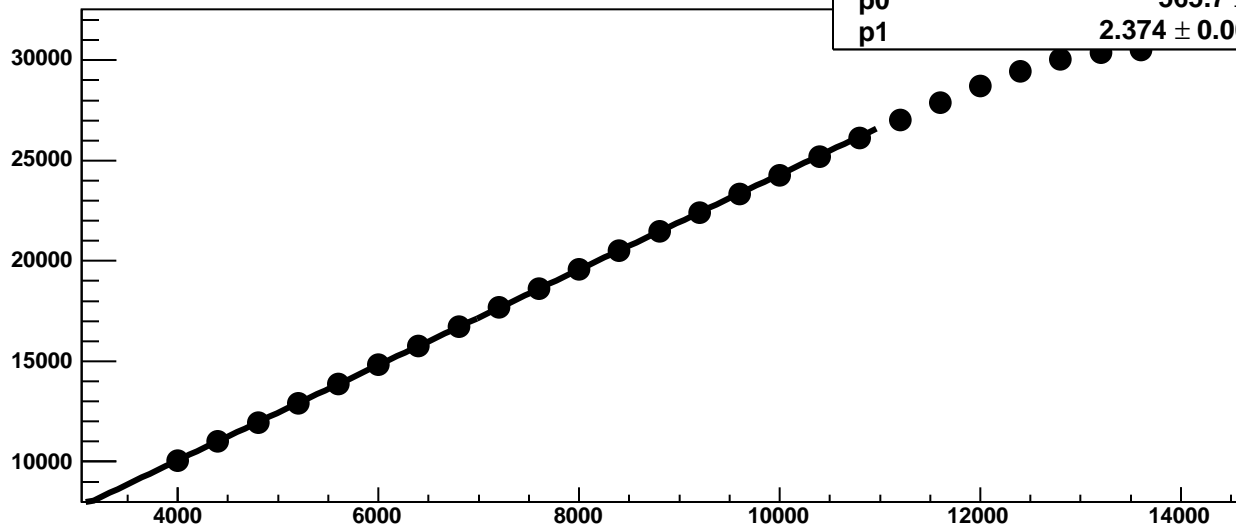
Chip 2, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



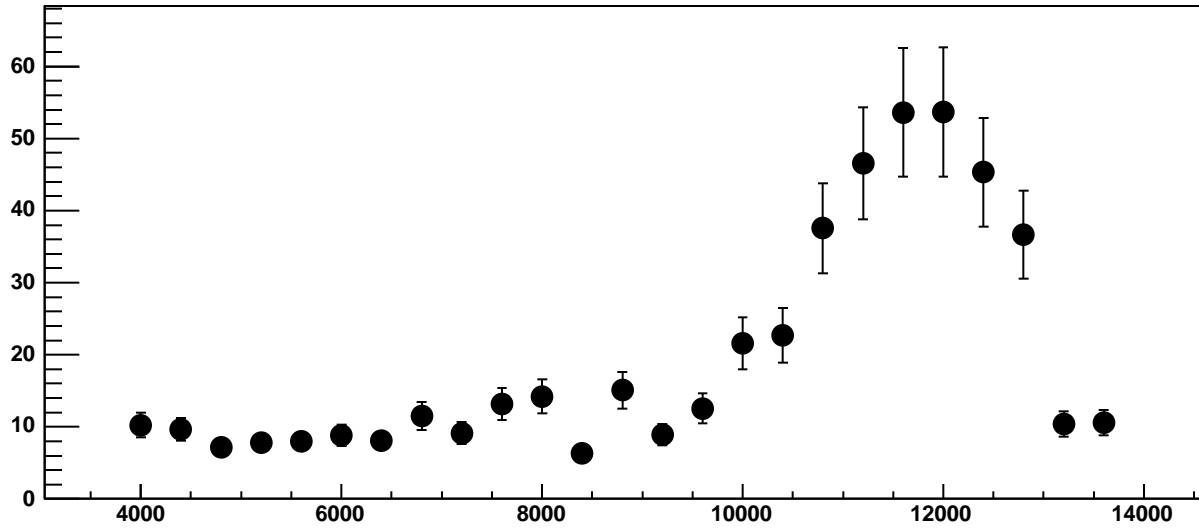
Chip 2, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



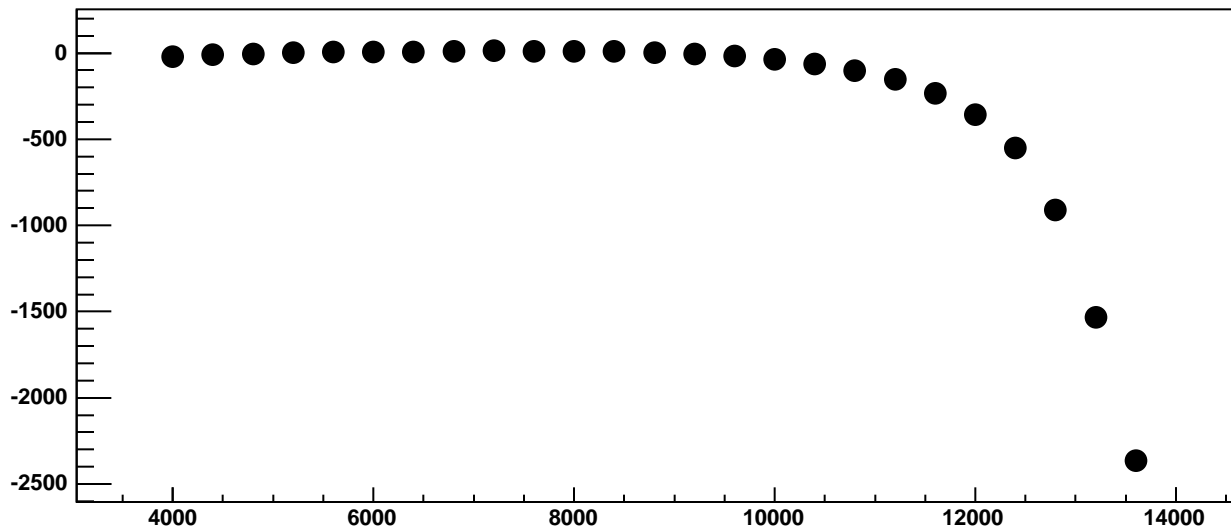
Chip 2, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC



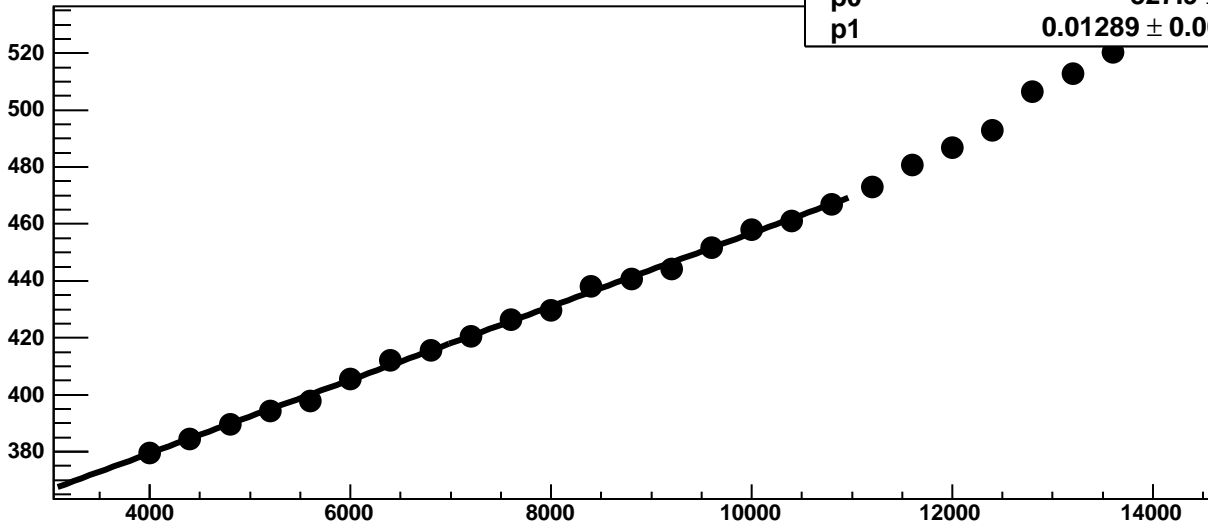
Chip 2, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC

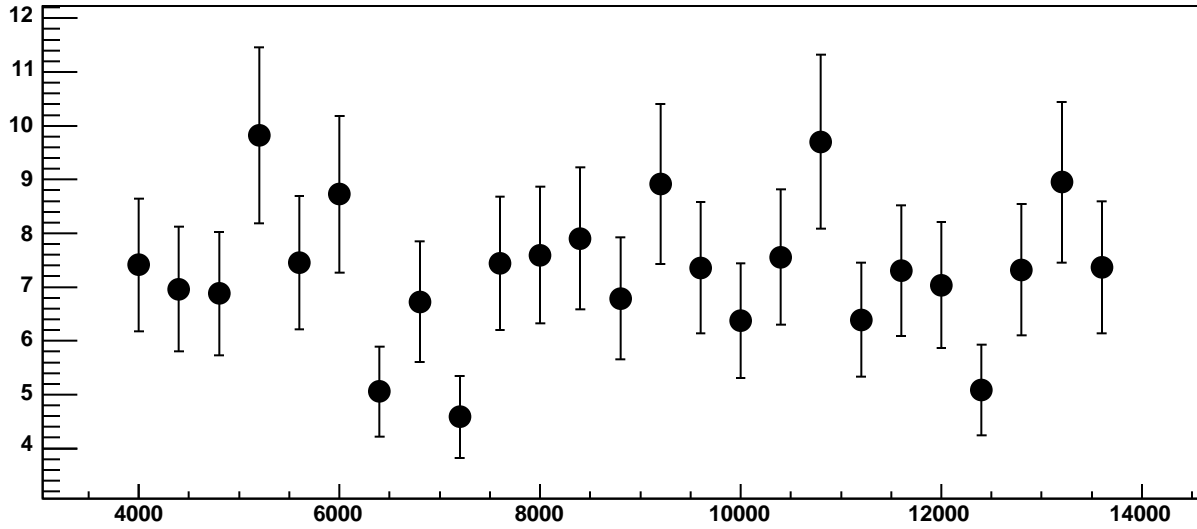


Chip 2, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

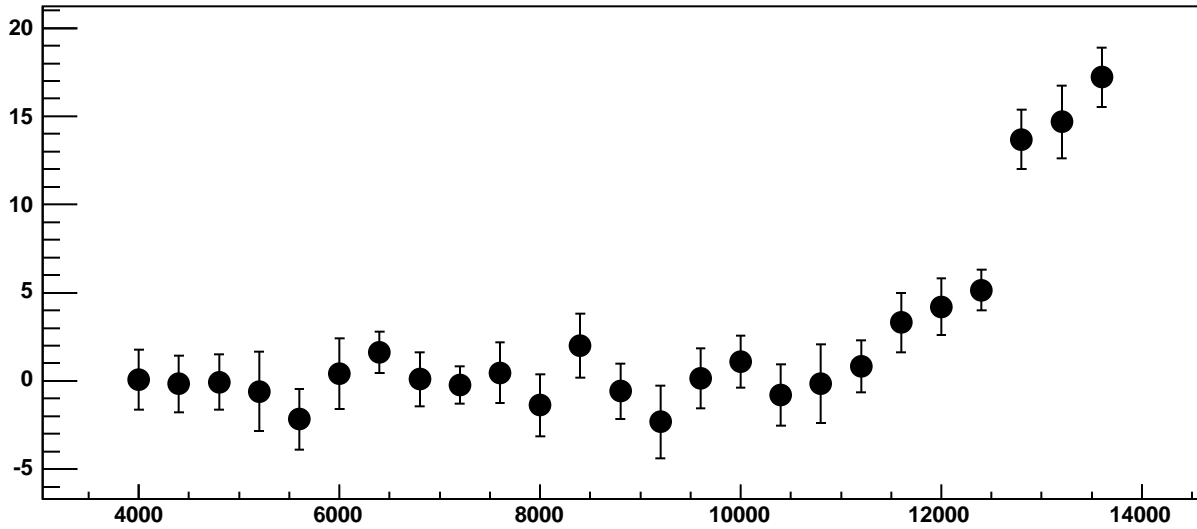


χ^2 / ndf 7.863 / 16
p0 327.9 ± 1.484
p1 0.01289 ± 0.0001967

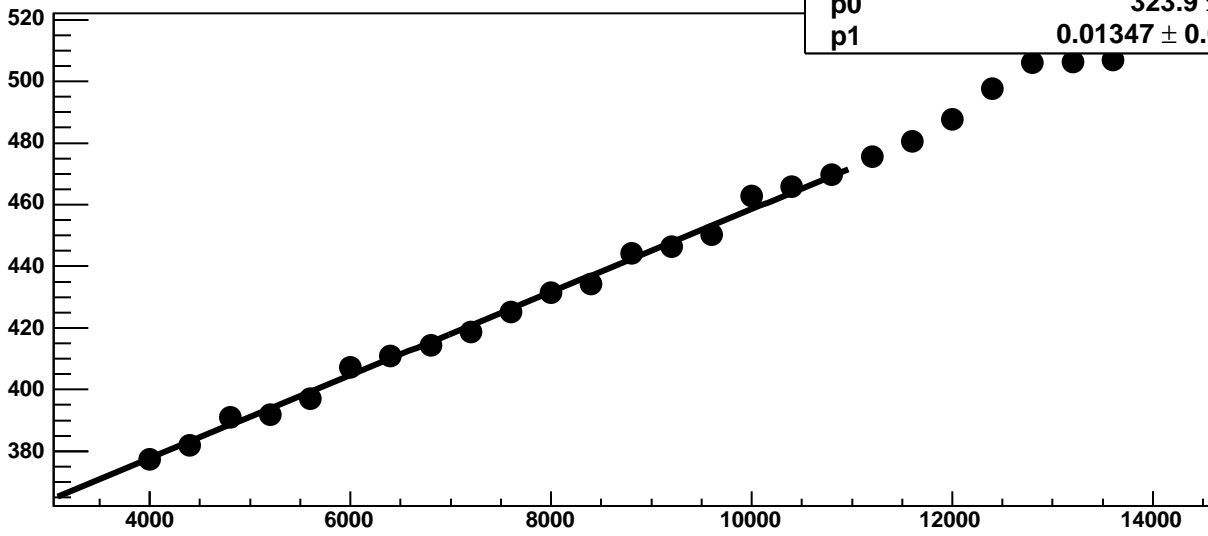
Chip 2, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

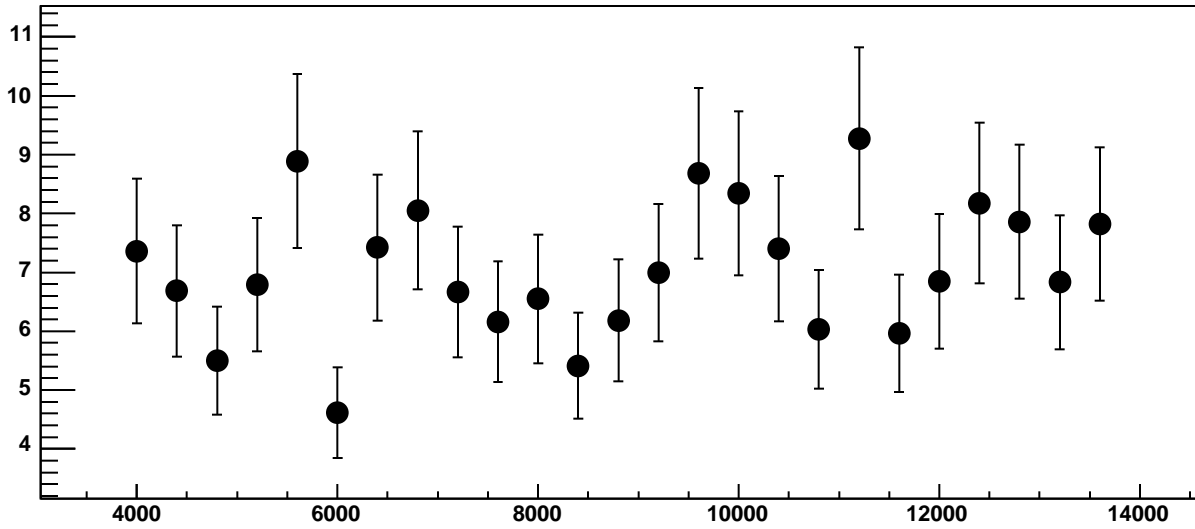


Chip 2, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

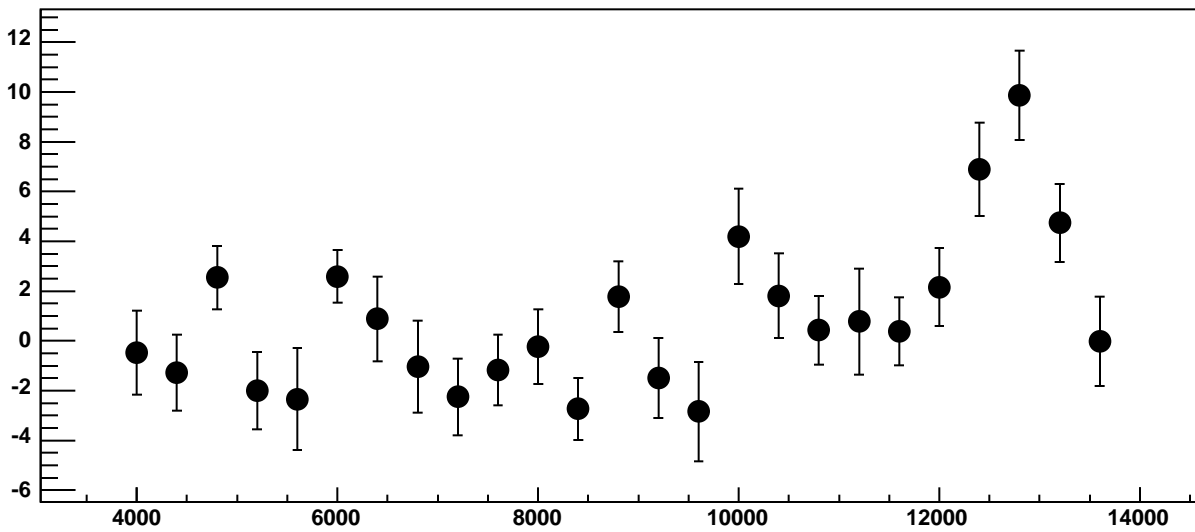


χ^2 / ndf 32.58 / 16
p0 323.9 ± 1.333
p1 0.01347 ± 0.000176

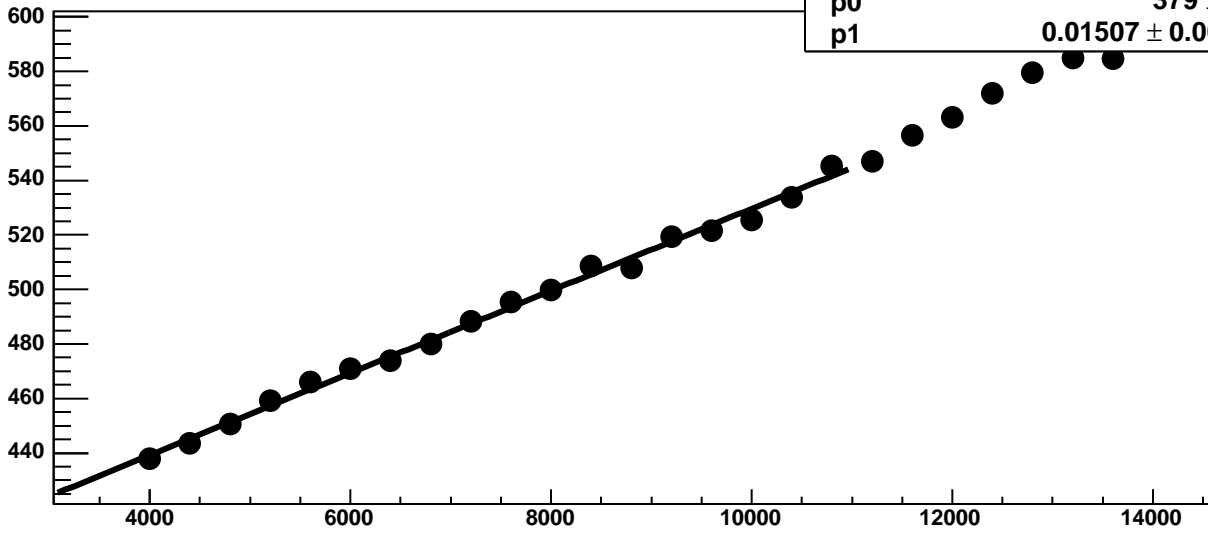
Chip 2, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

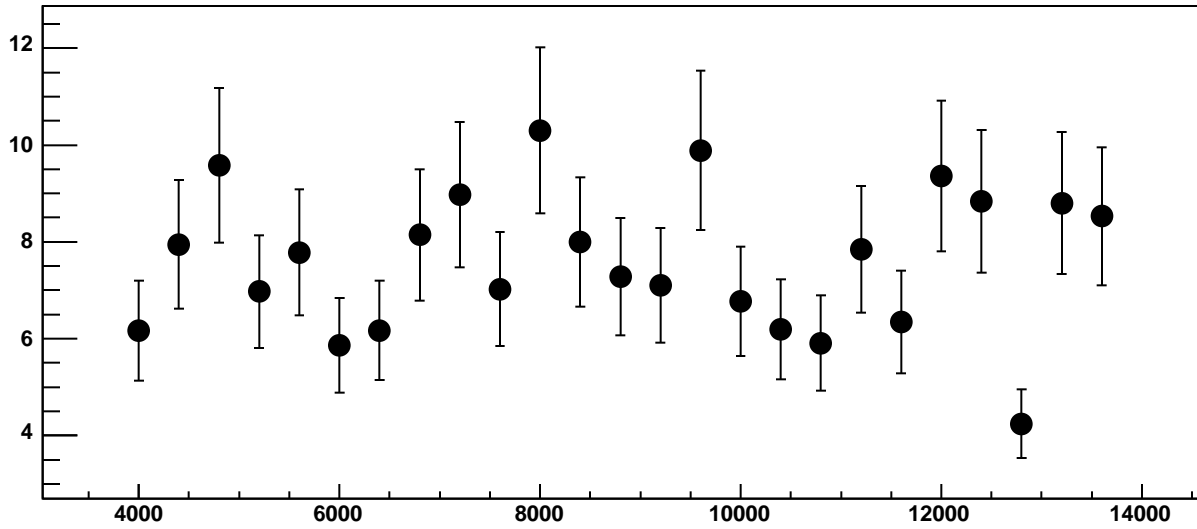


Chip 2, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

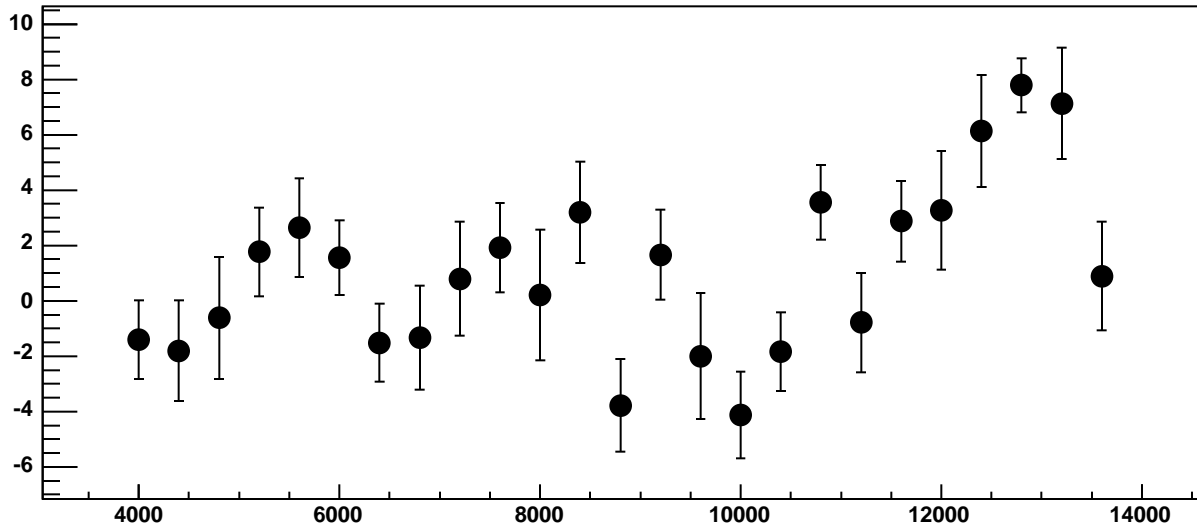


χ^2 / ndf 35.64 / 16
p0 379 ± 1.392
p1 0.01507 ± 0.0001789

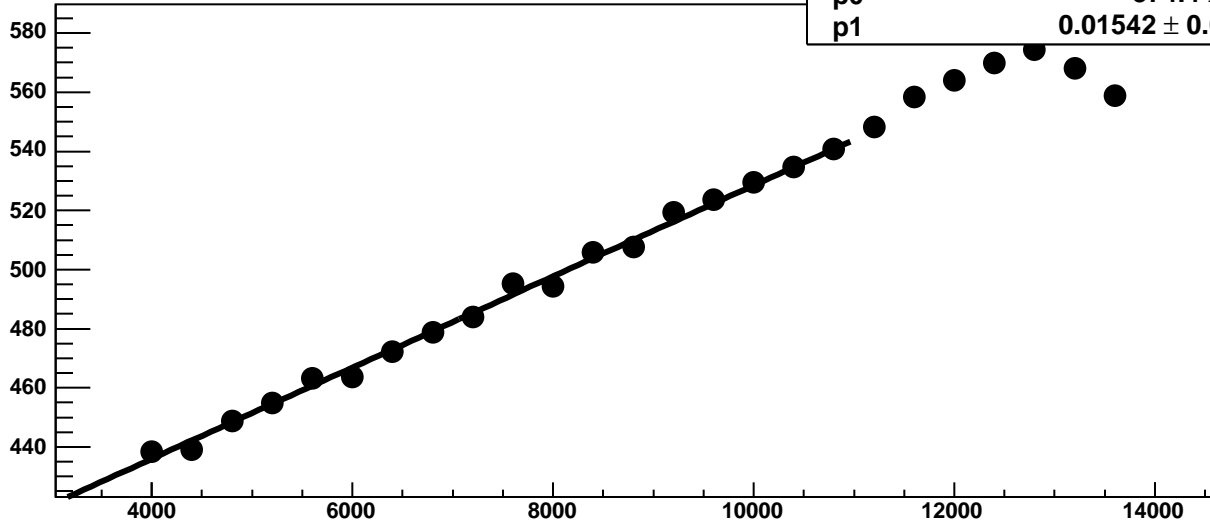
Chip 2, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



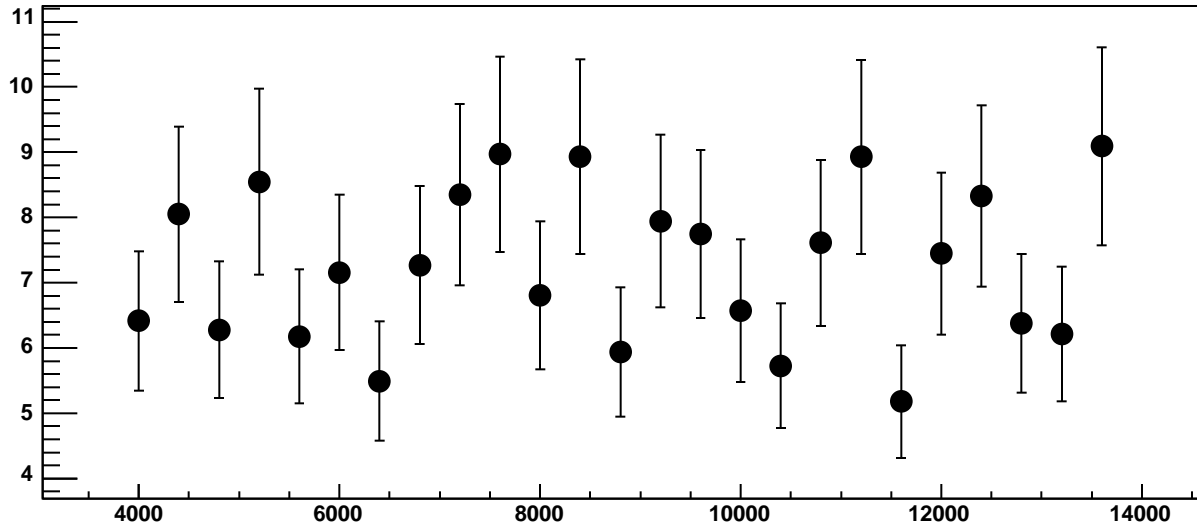
Chip 2, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



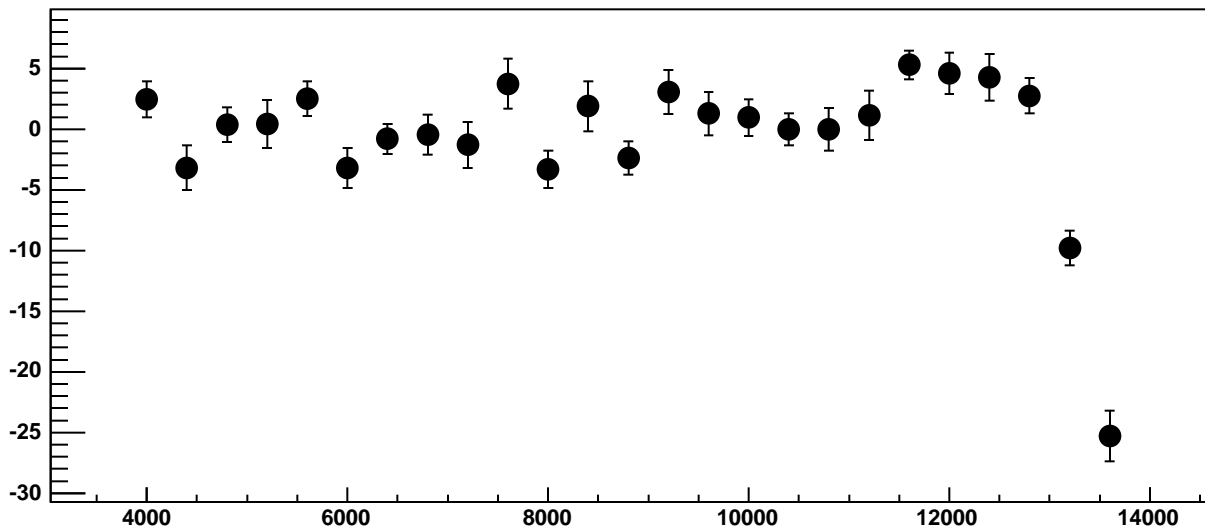
Chip 2, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



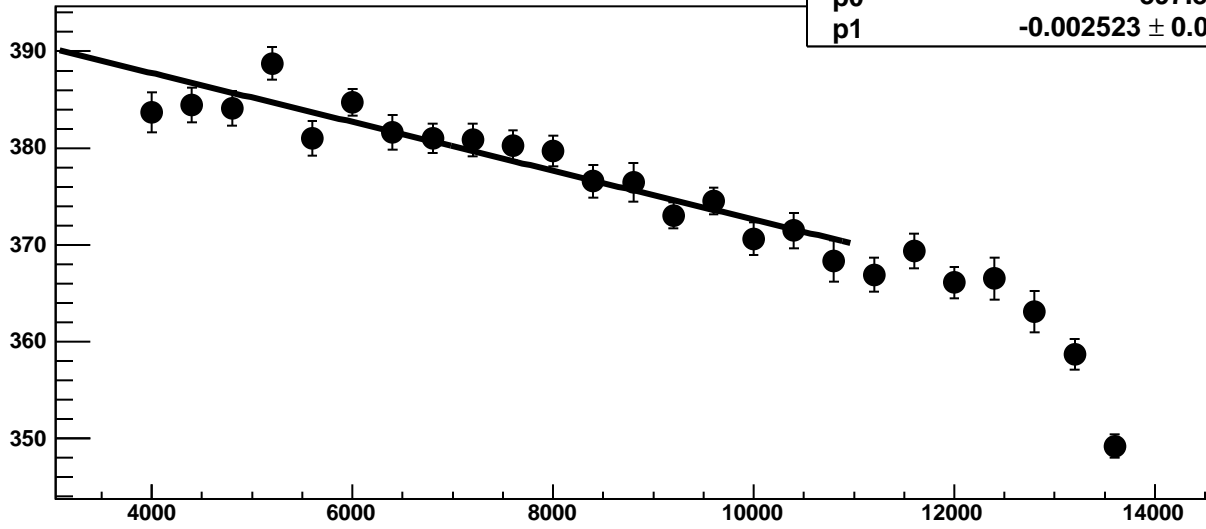
Chip 2, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



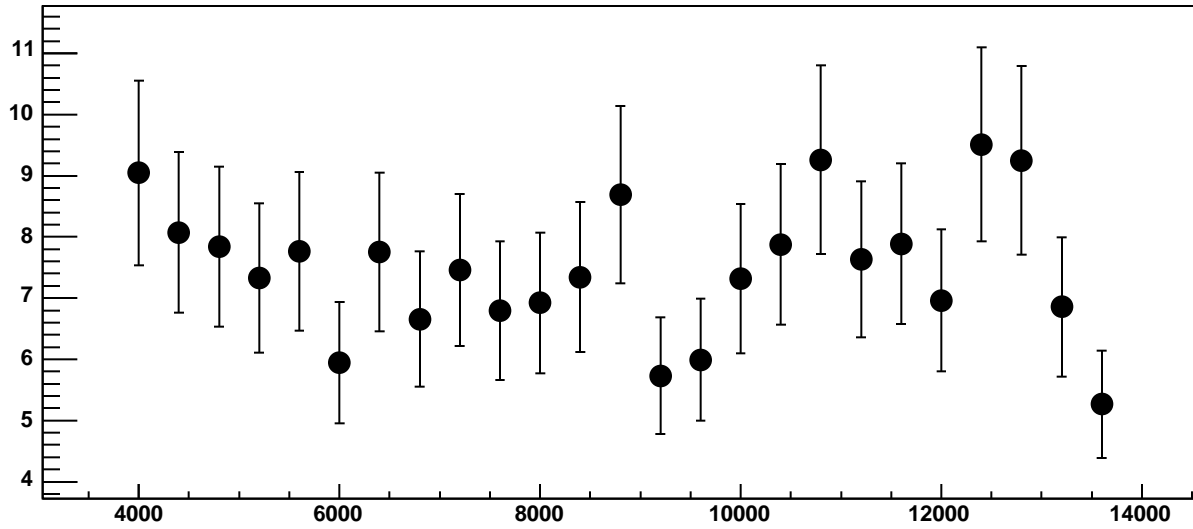
Chip 2, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



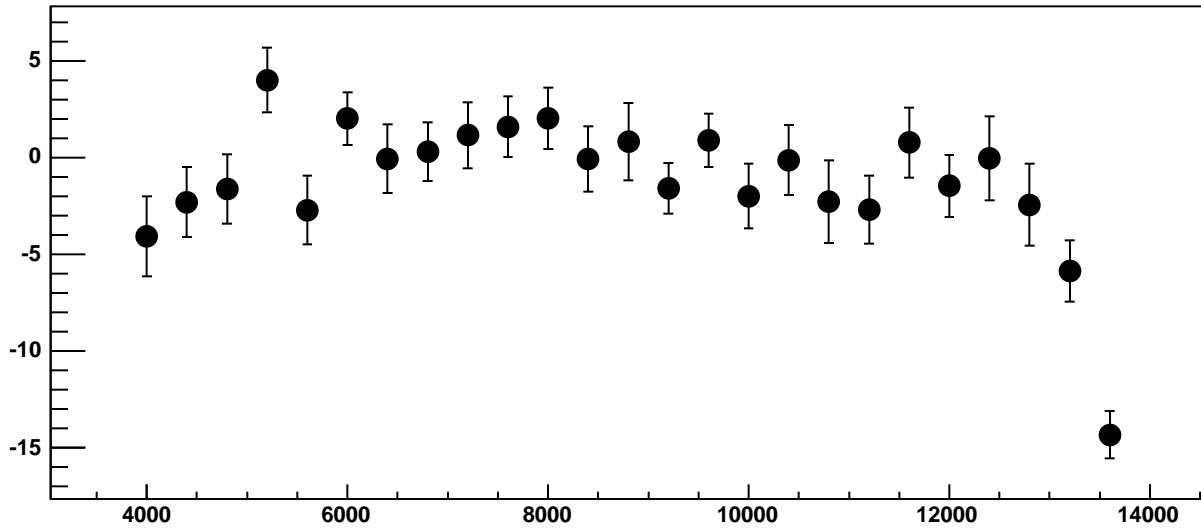
Chip 2, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC



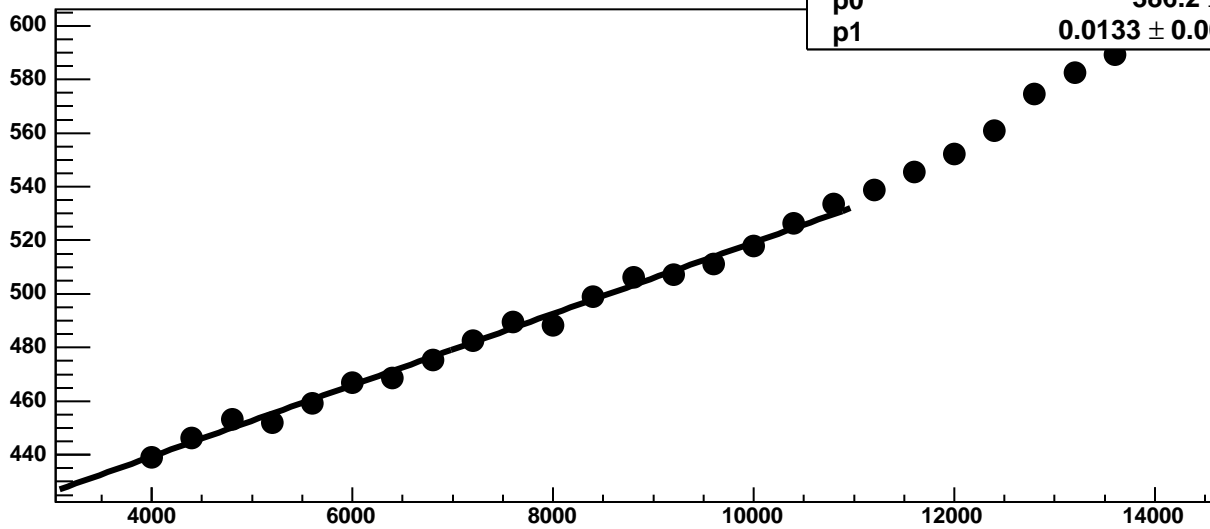
Chip 2, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

40.22 / 16

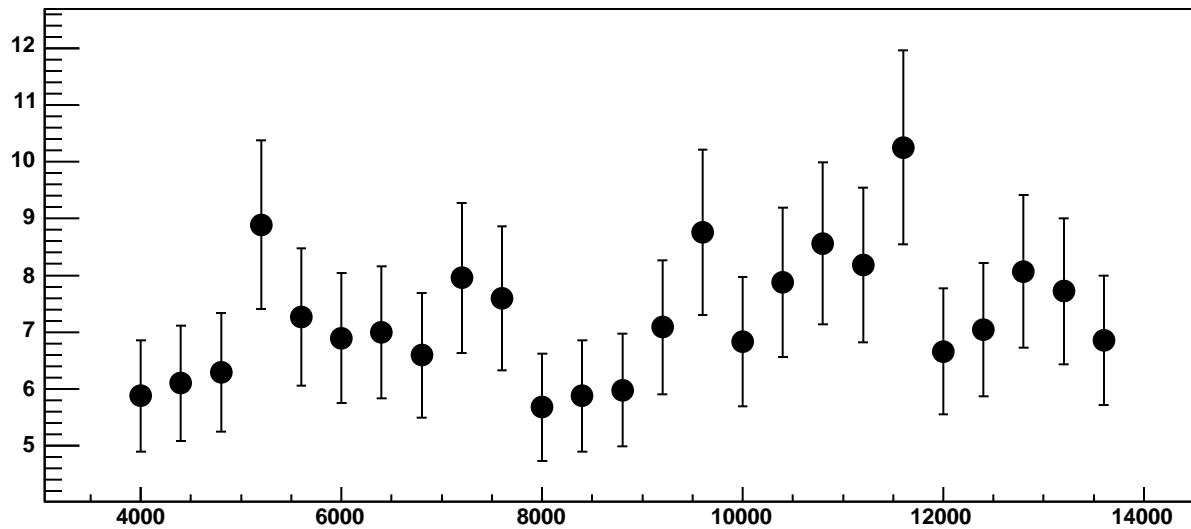
p0

386.2 ± 1.362

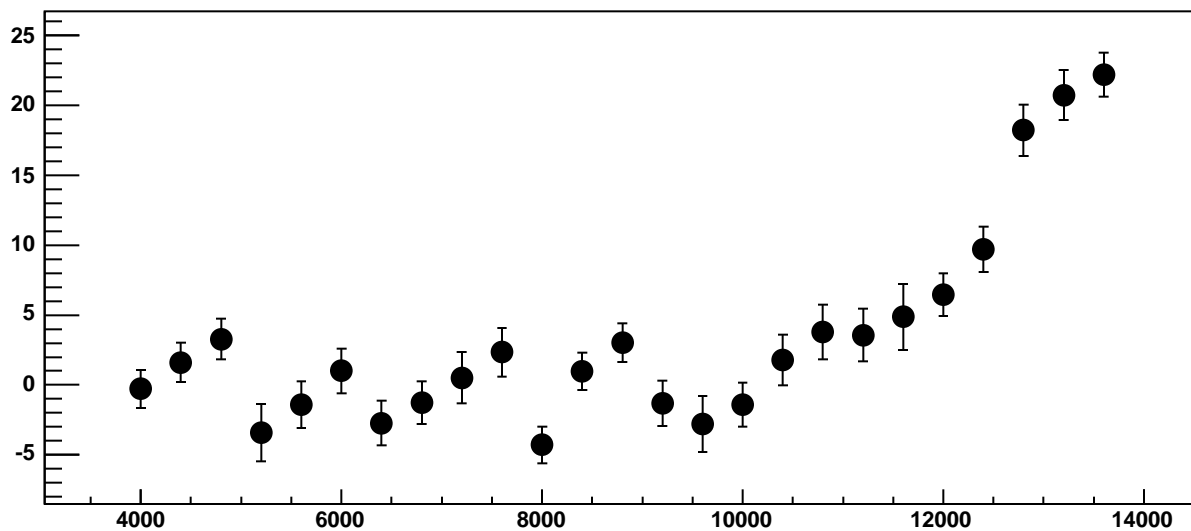
p1

0.0133 ± 0.0001814

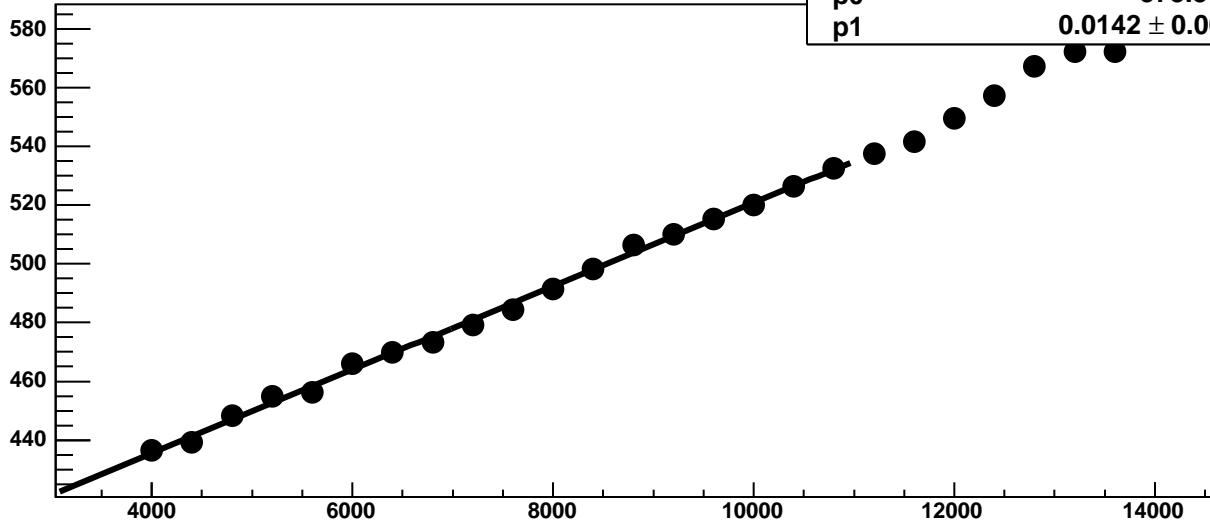
Chip 2, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

16.94 / 16

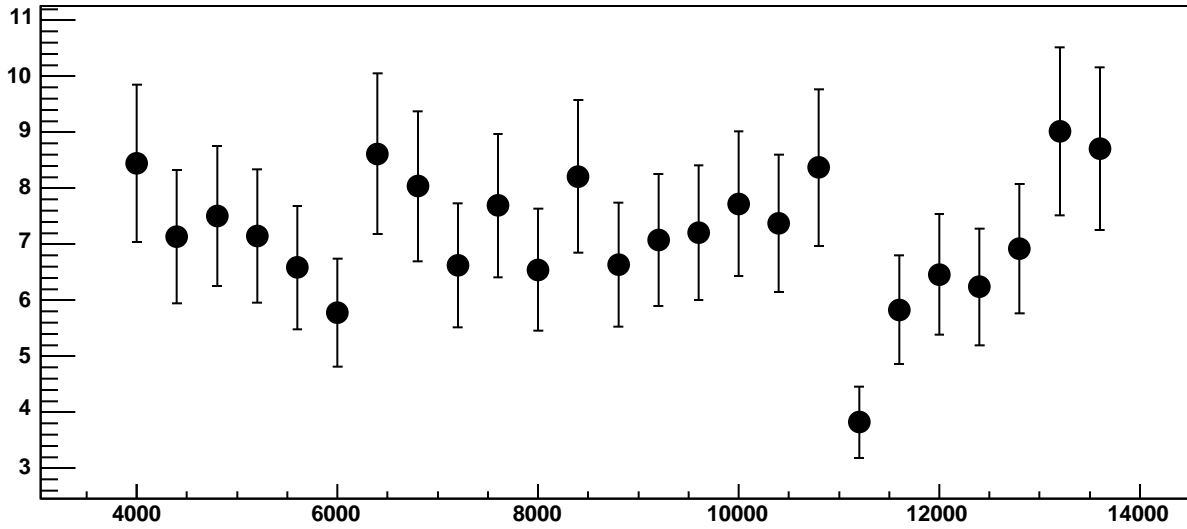
p0

378.8 ± 1.49

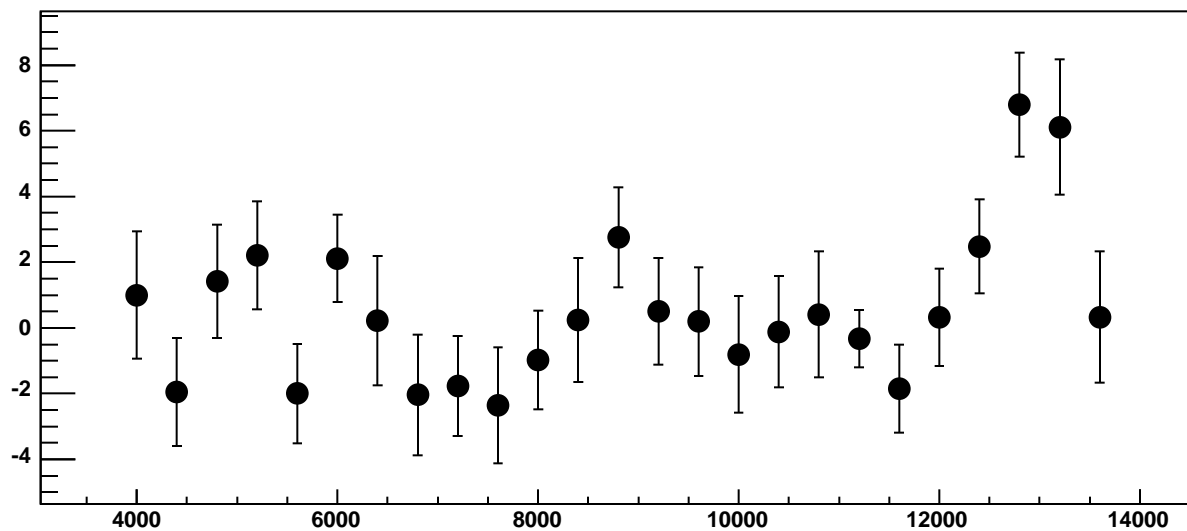
p1

0.0142 ± 0.0001956

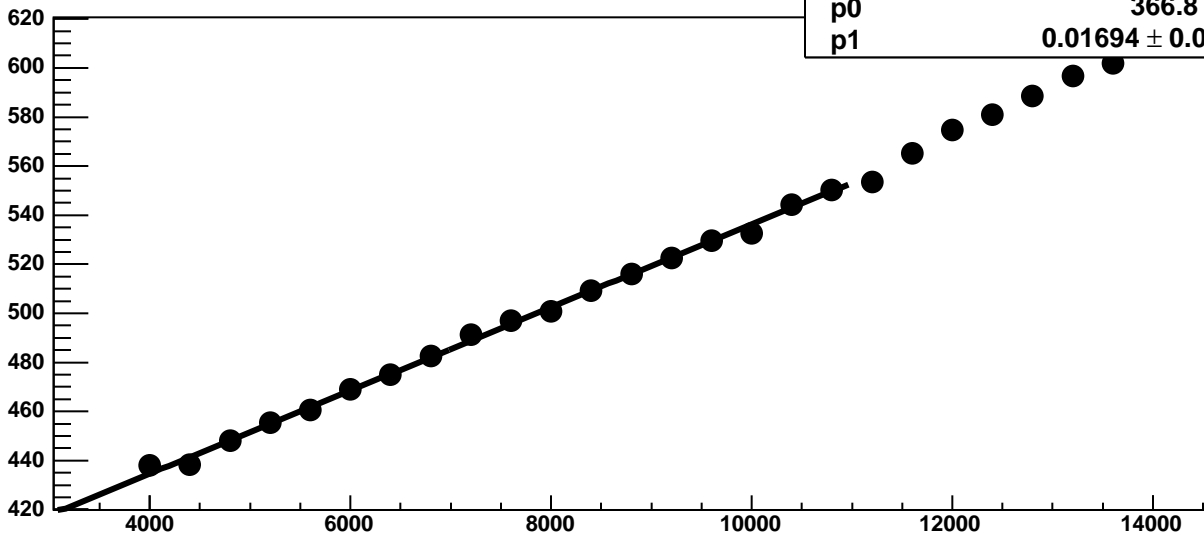
Chip 2, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

20.8 / 16

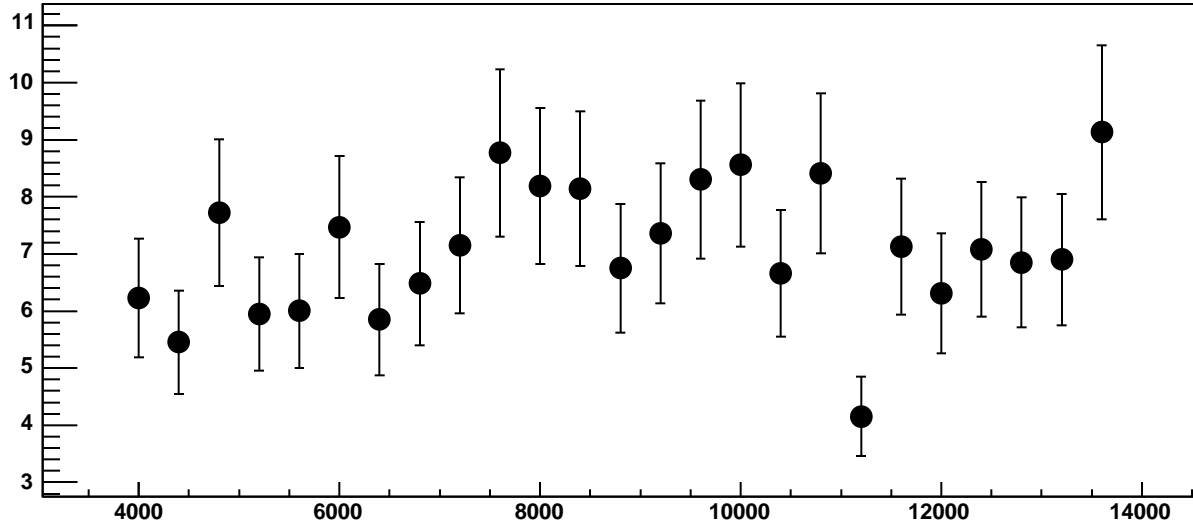
p0

366.8 ± 1.322

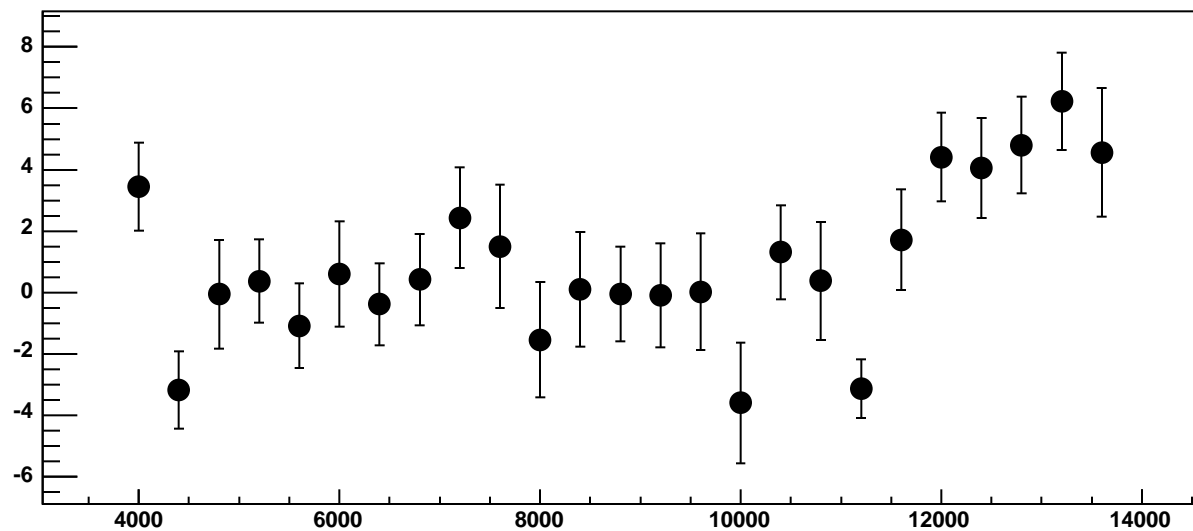
p1

0.01694 ± 0.0001807

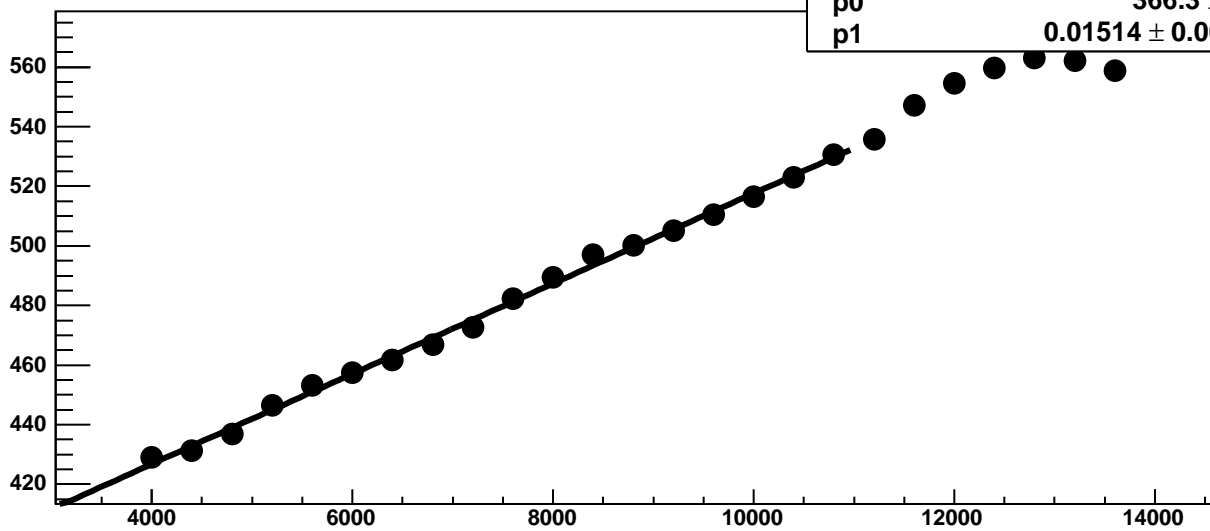
Chip 2, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



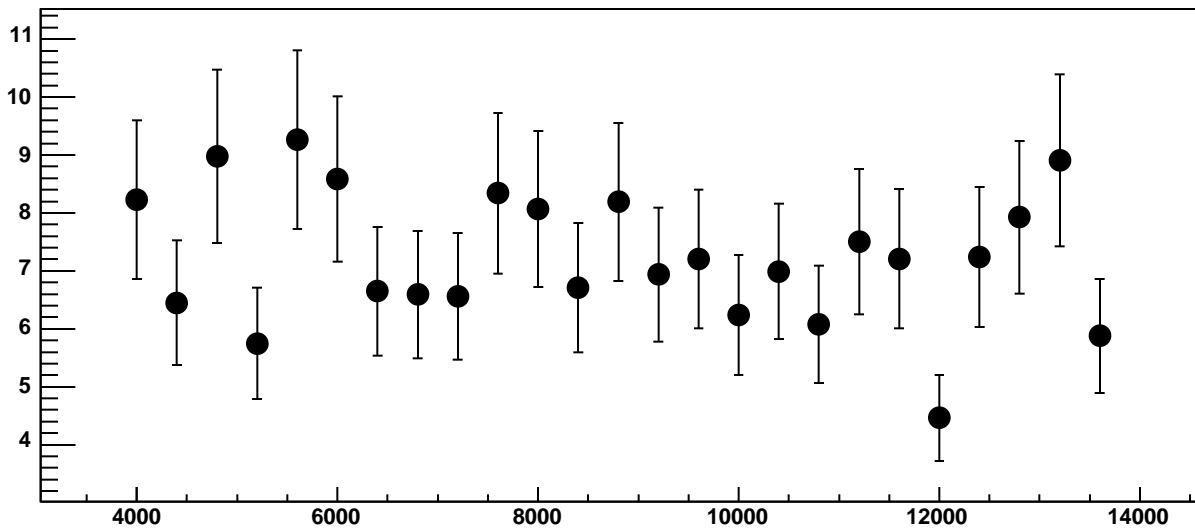
Chip 2, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



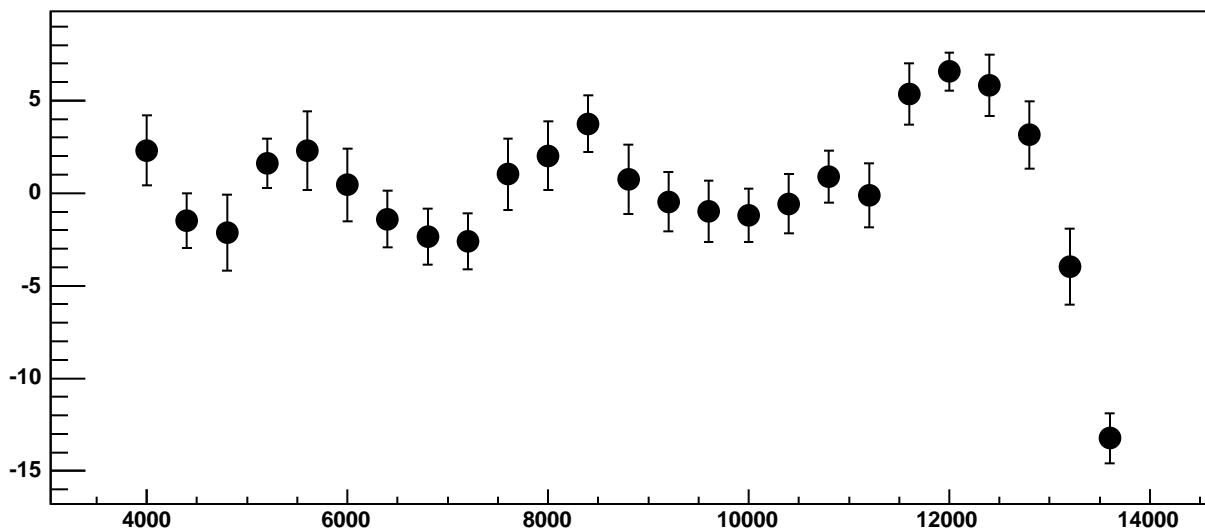
Chip 2, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



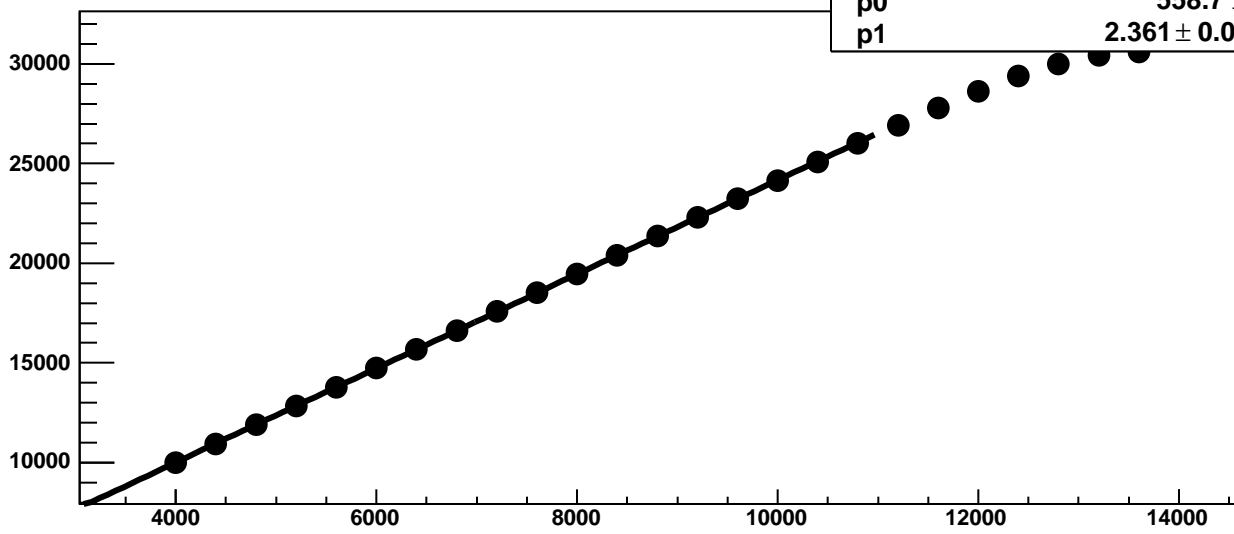
Chip 2, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



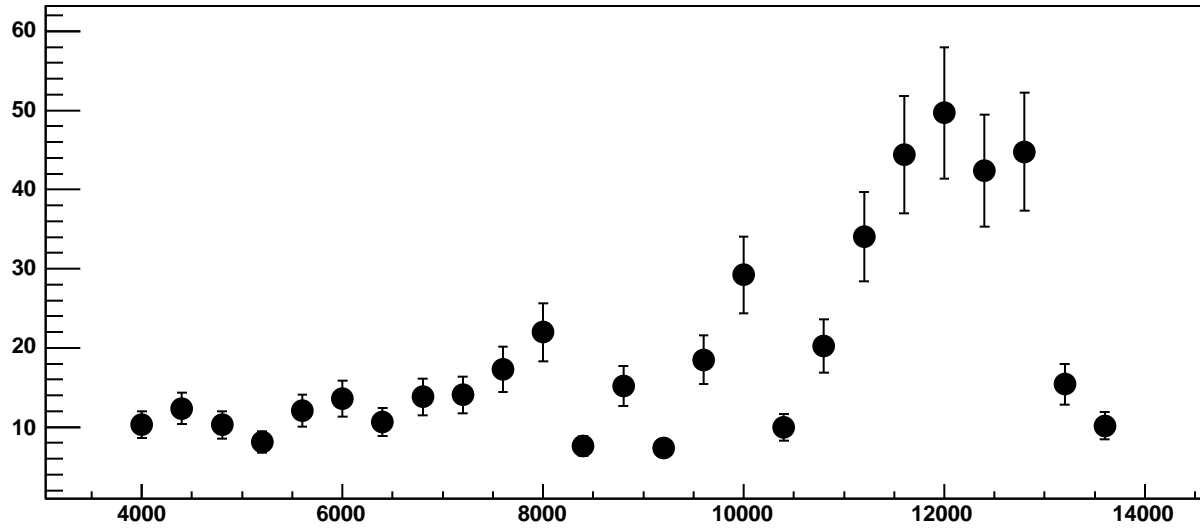
Chip 2, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



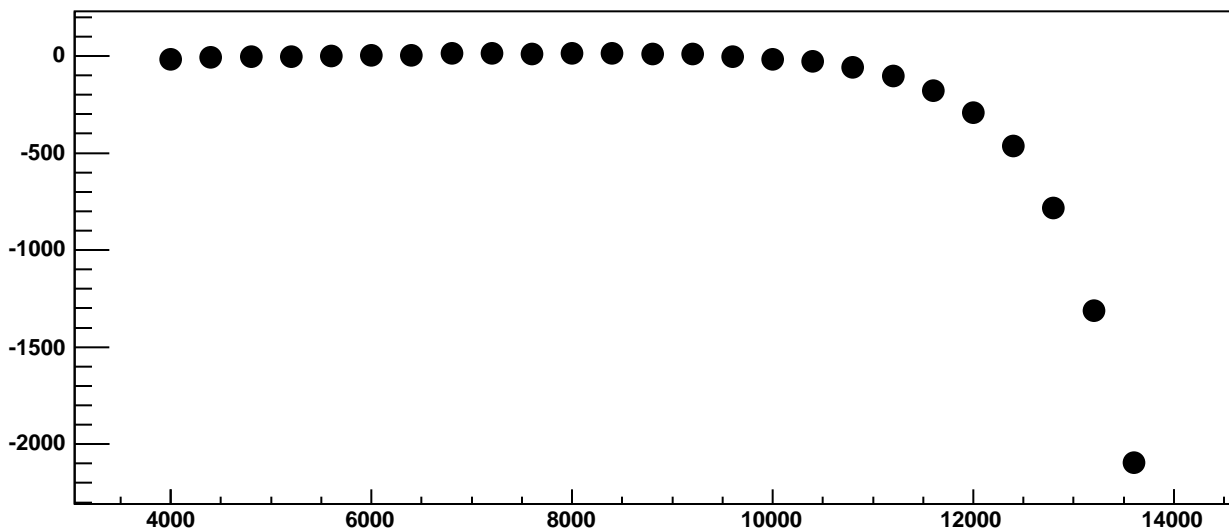
Chip 2, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC



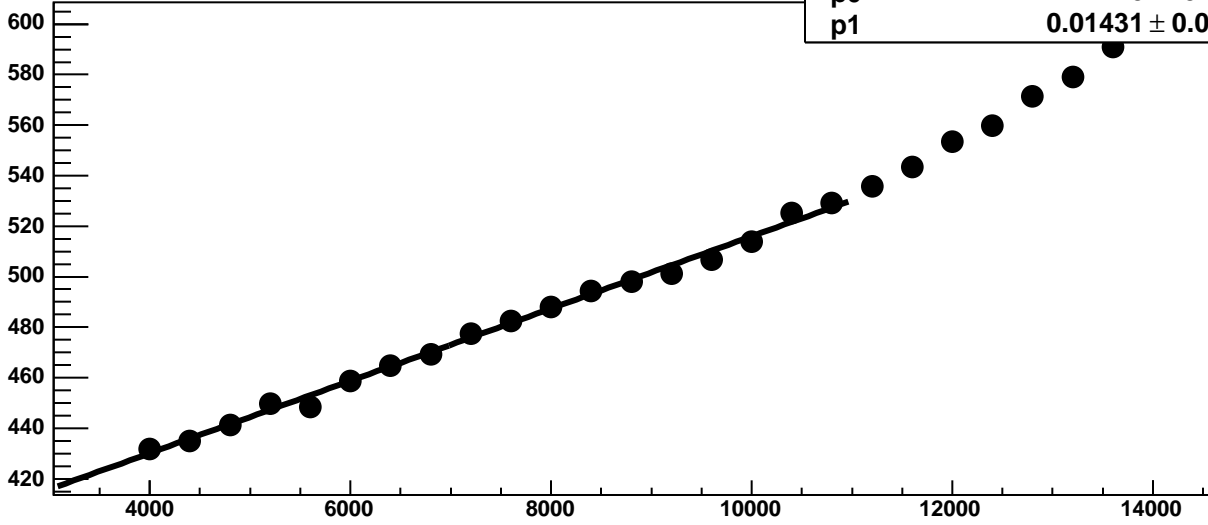
Chip 2, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

27.02 / 16

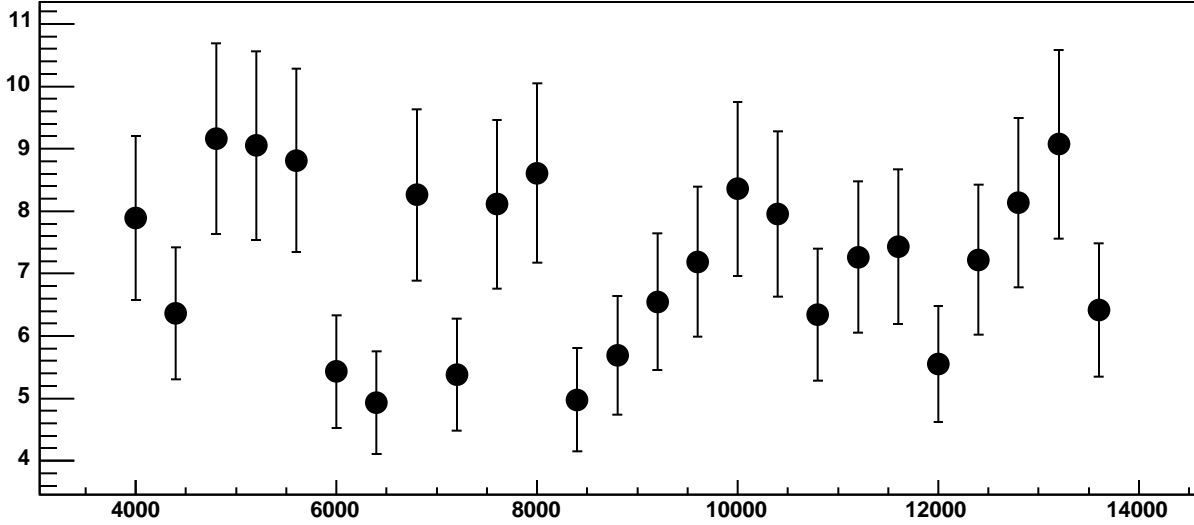
p0

372.9 ± 1.447

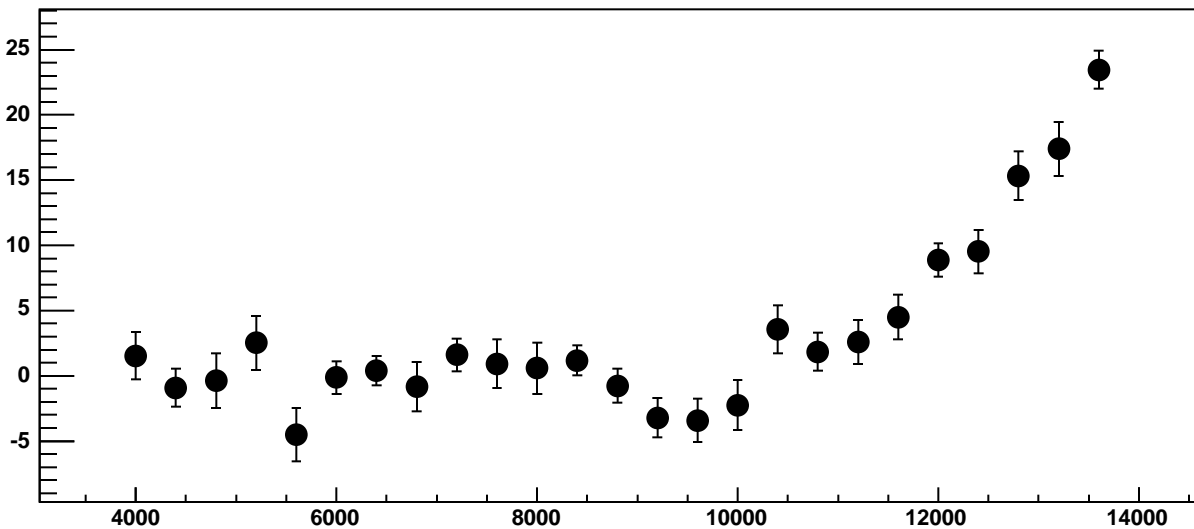
p1

0.01431 ± 0.0001871

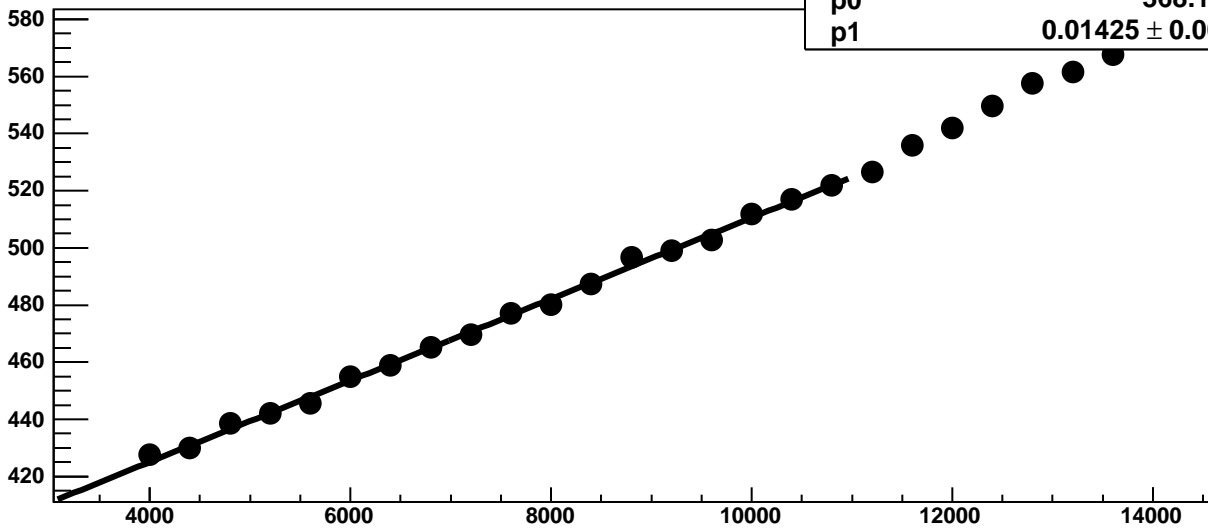
Chip 2, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

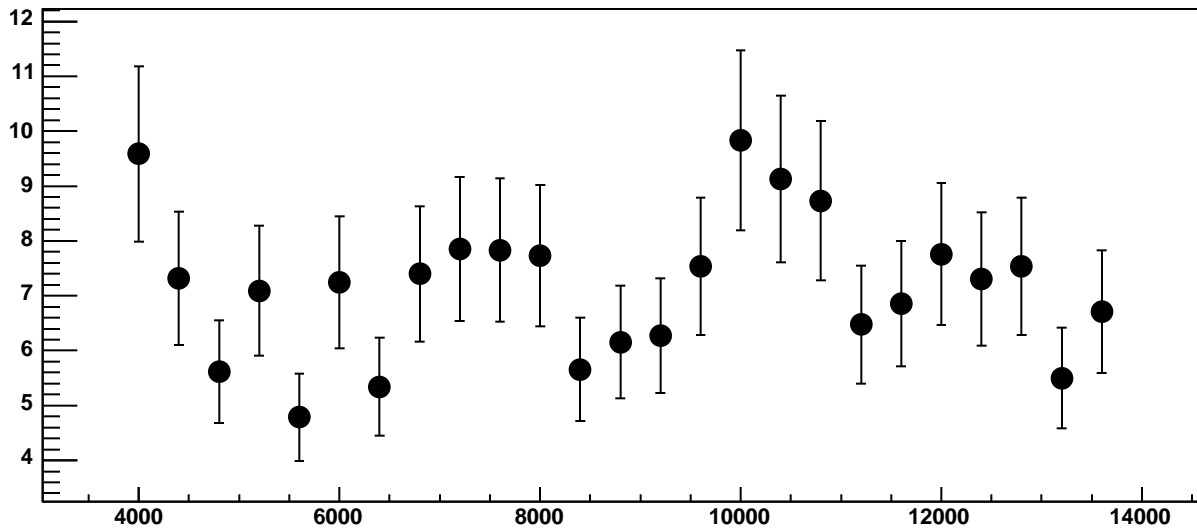


Chip 2, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

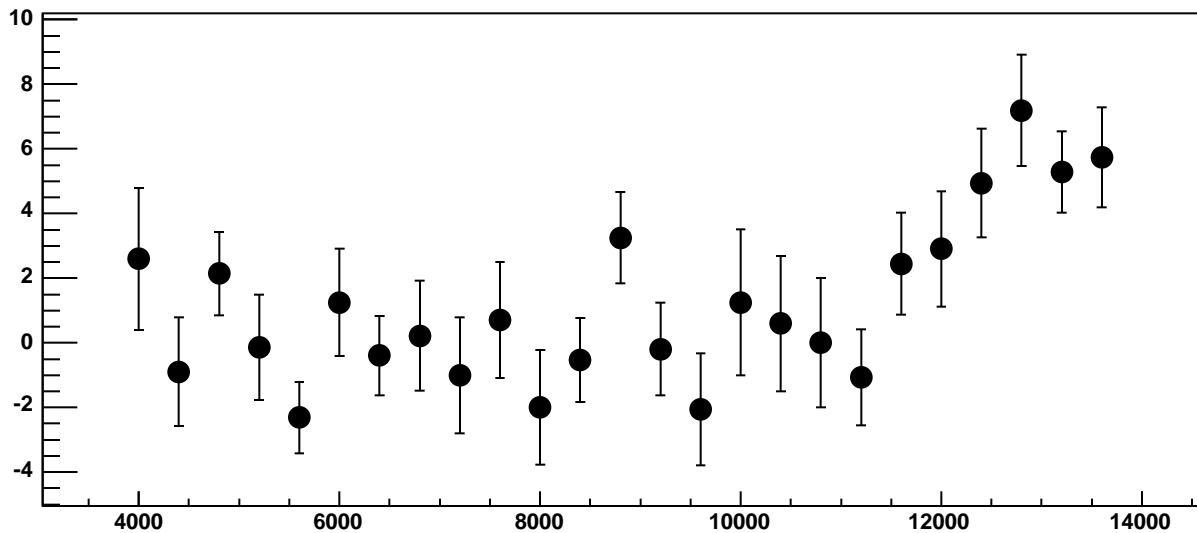


χ^2 / ndf 18.55 / 16
p0 368.1 ± 1.44
p1 0.01425 ± 0.0001949

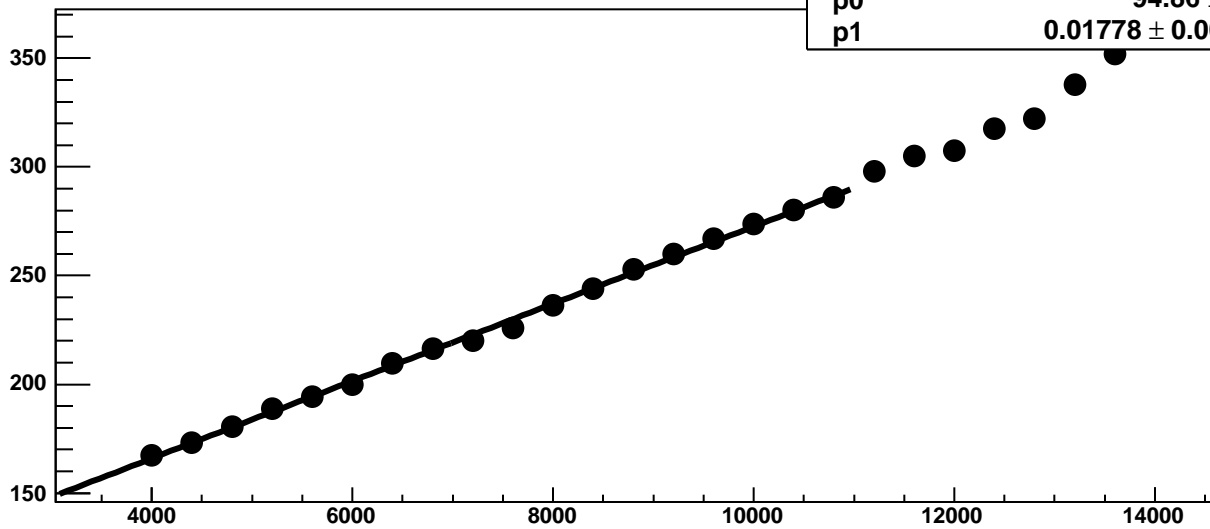
Chip 2, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

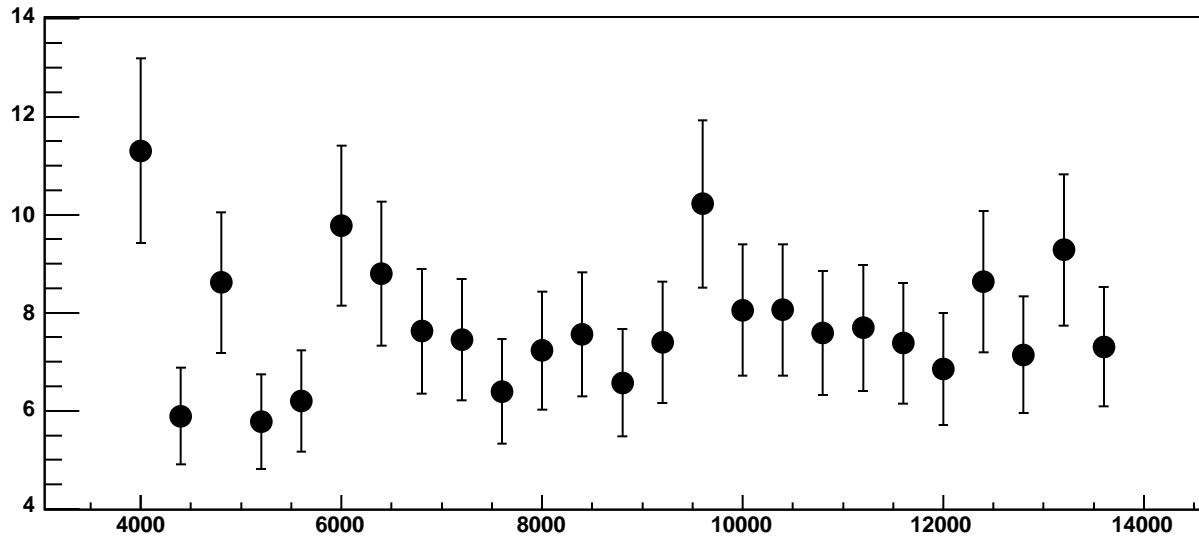


Chip 3, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

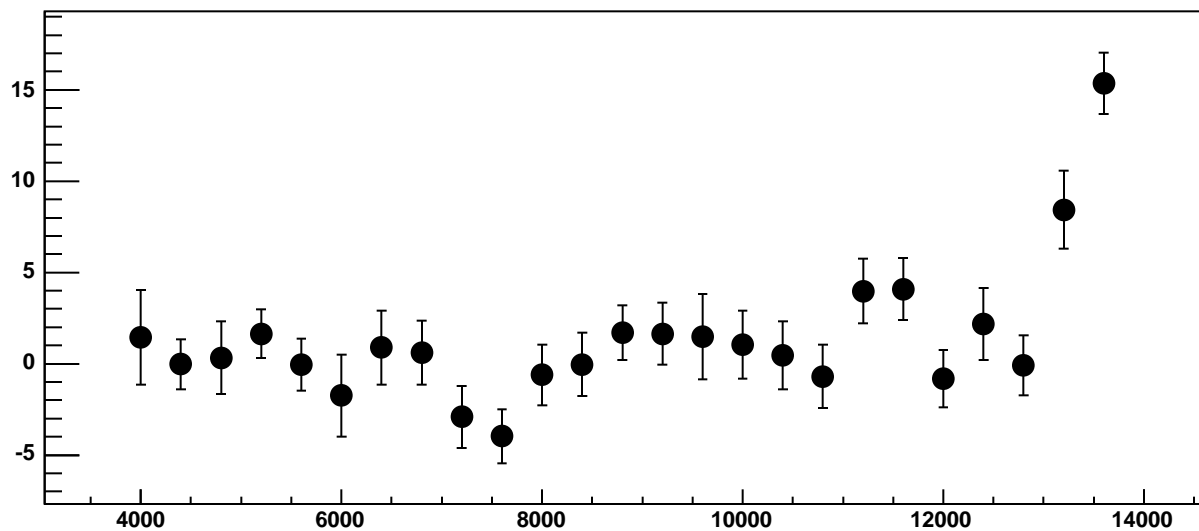


χ^2 / ndf 16.3 / 16
p0 94.86 ± 1.507
p1 0.01778 ± 0.0001992

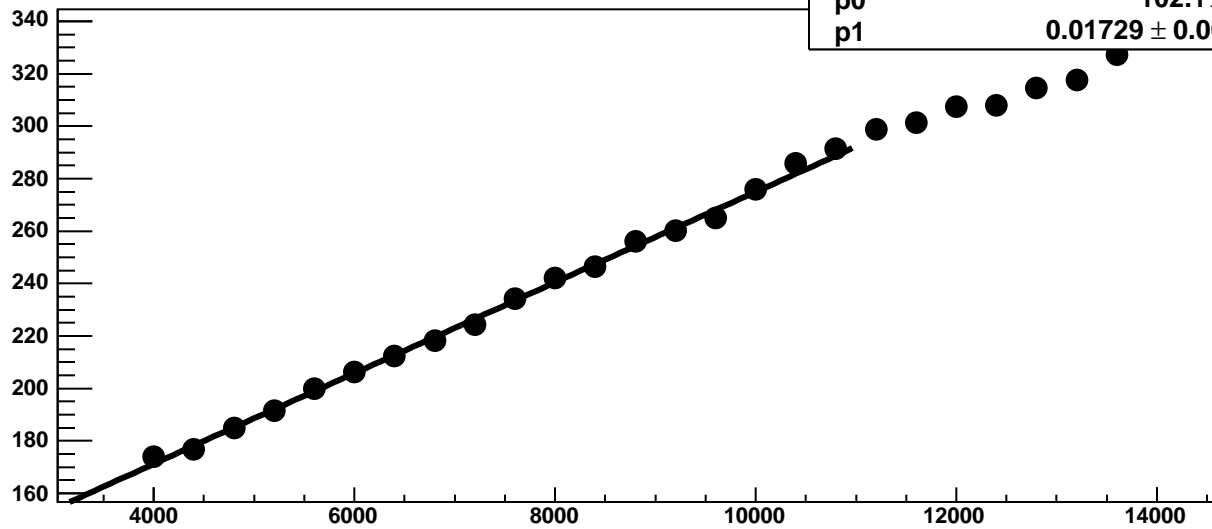
Chip 3, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

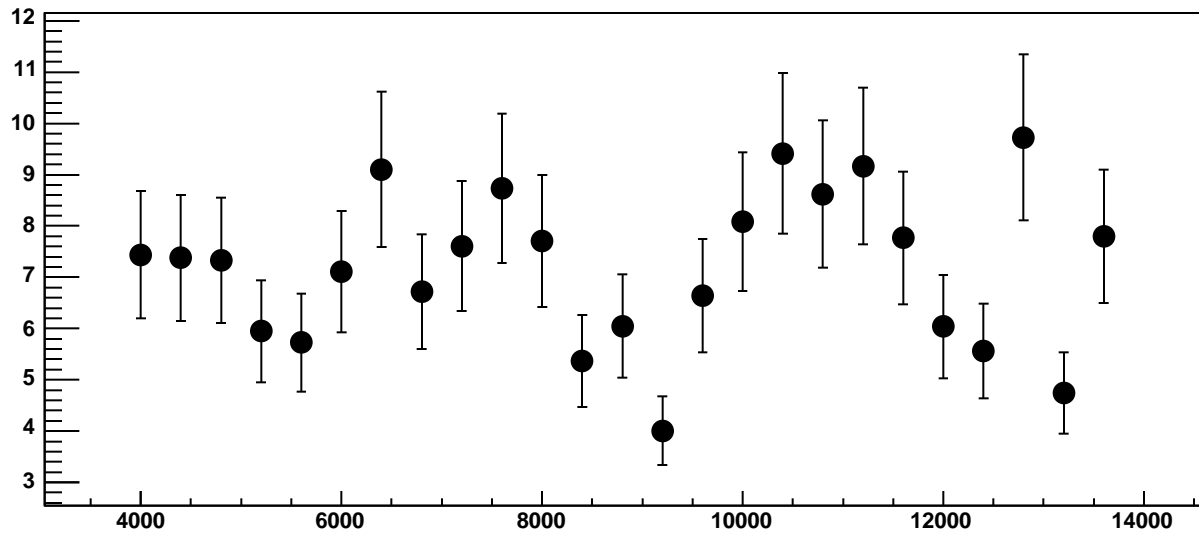


Chip 3, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

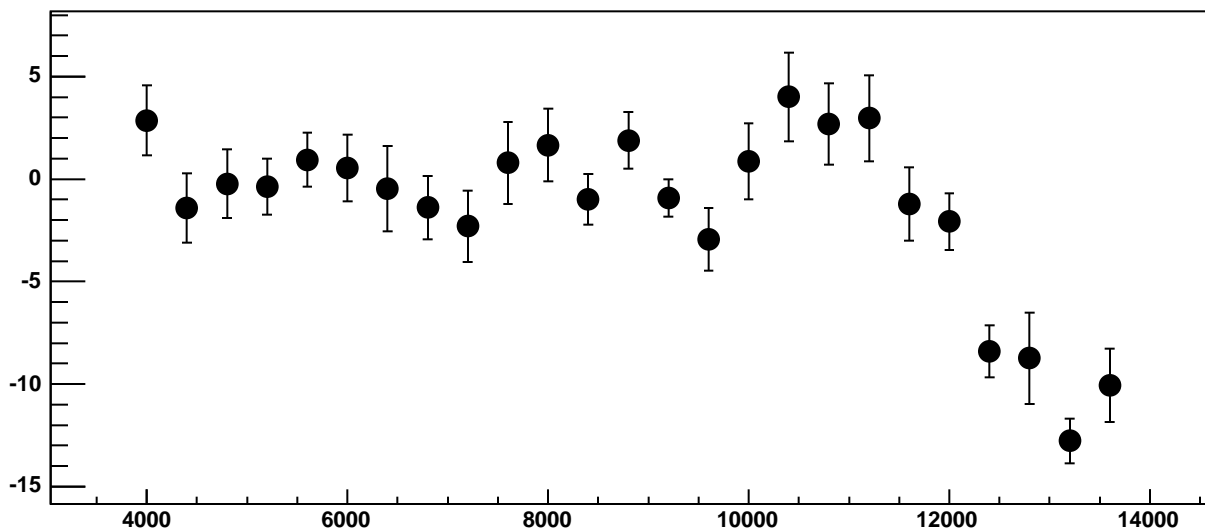


χ^2 / ndf 20.62 / 16
p0 102.1 ± 1.419
p1 0.01729 ± 0.0001837

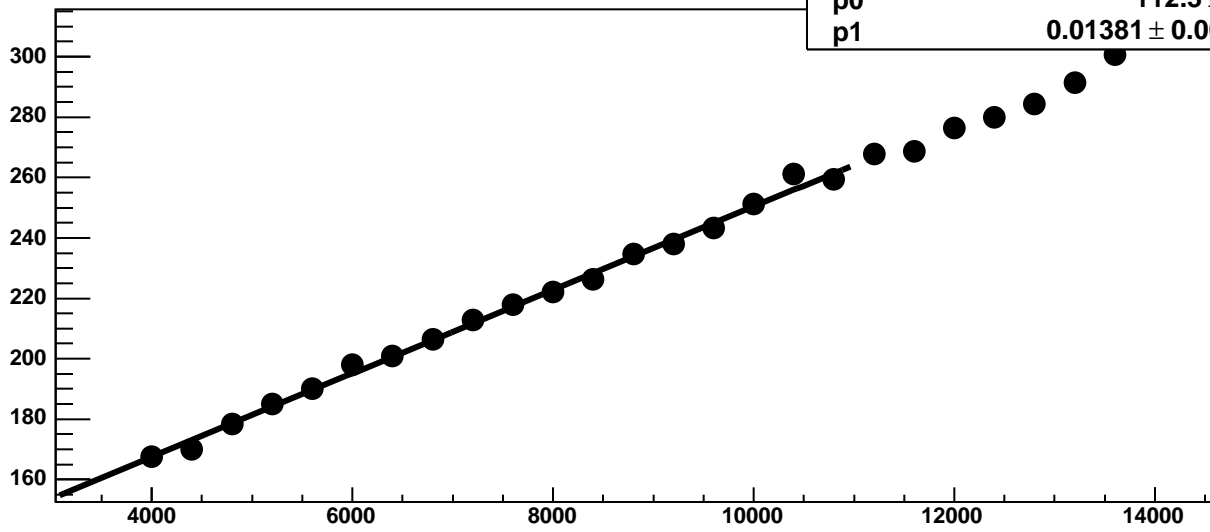
Chip 3, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

19.4 / 16

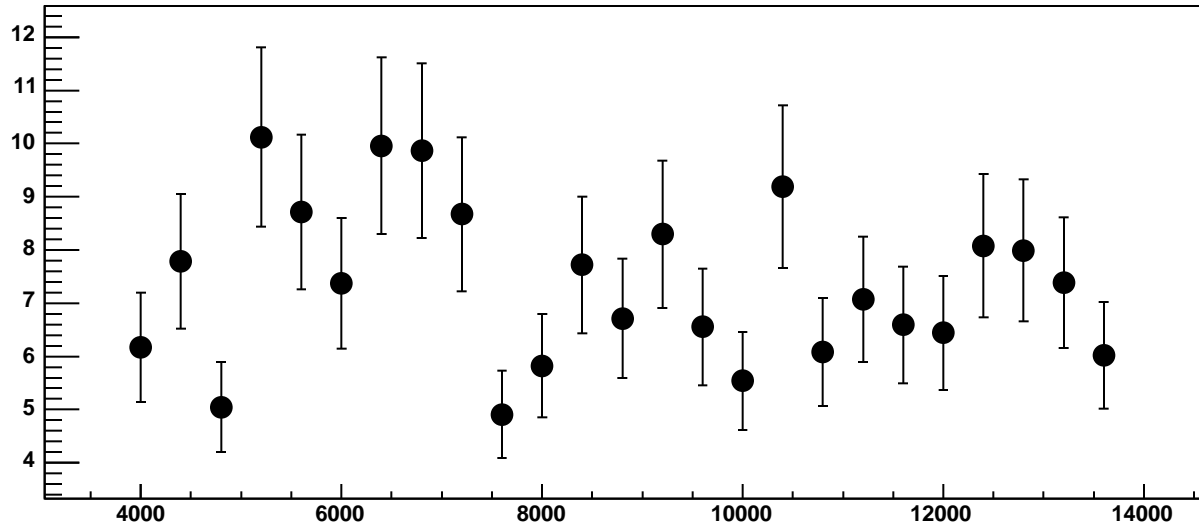
p0

112.3 ± 1.355

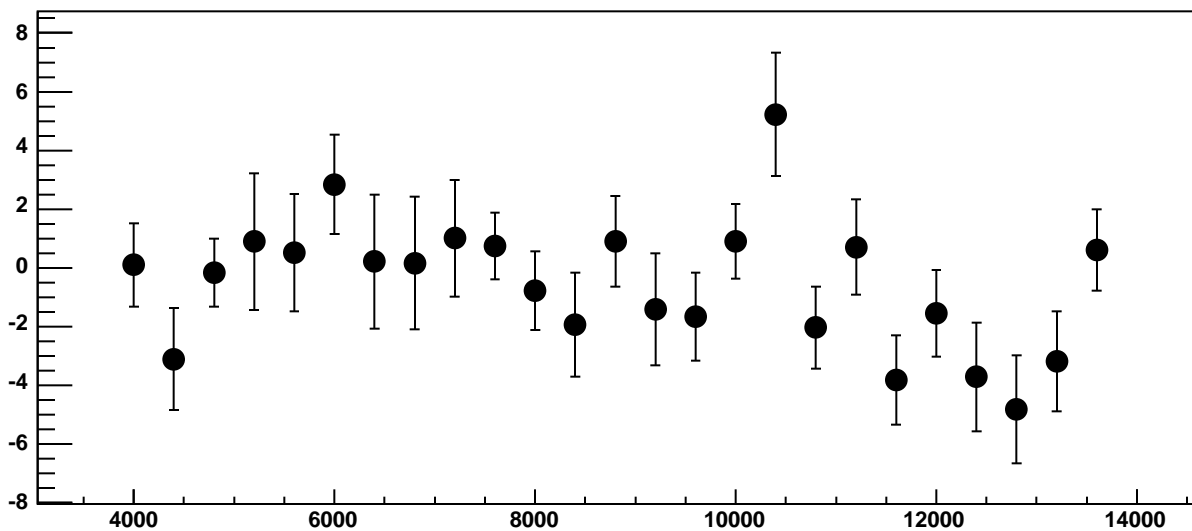
p1

0.01381 ± 0.0001742

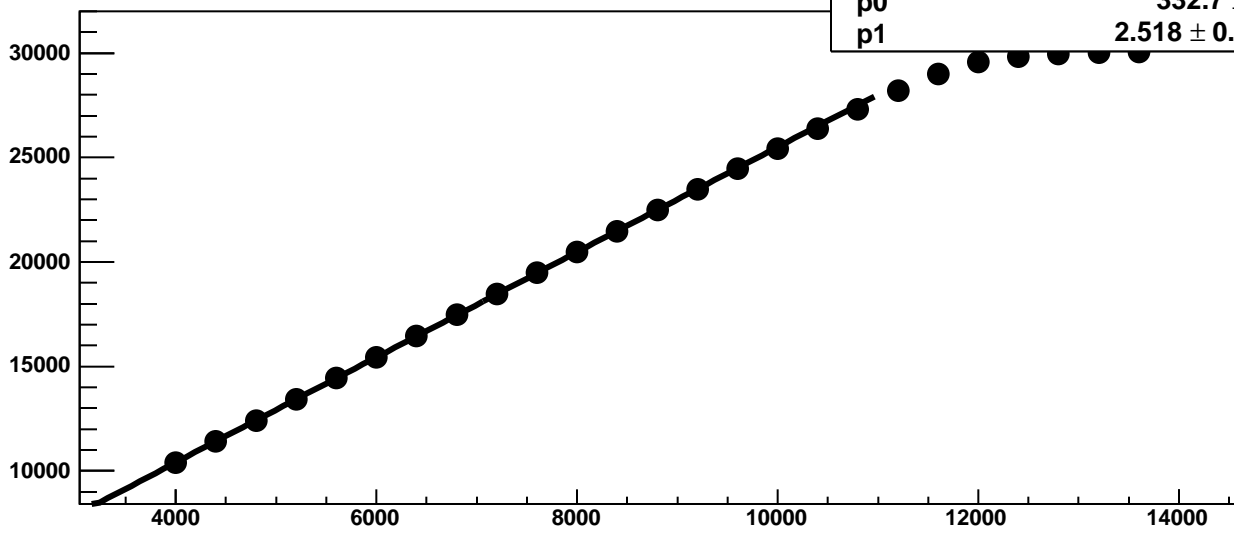
Chip 3, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



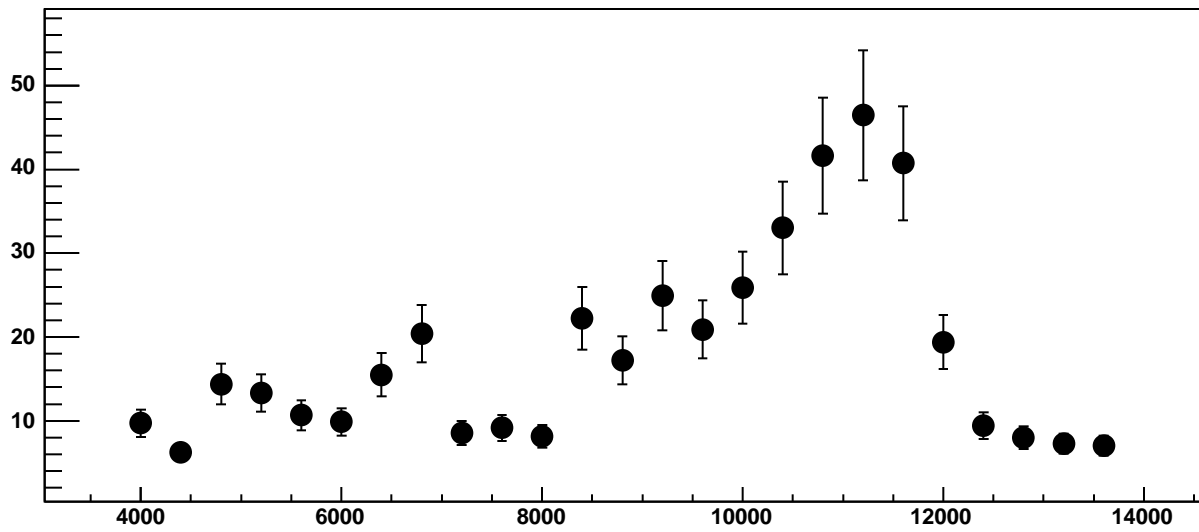
Chip 3, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



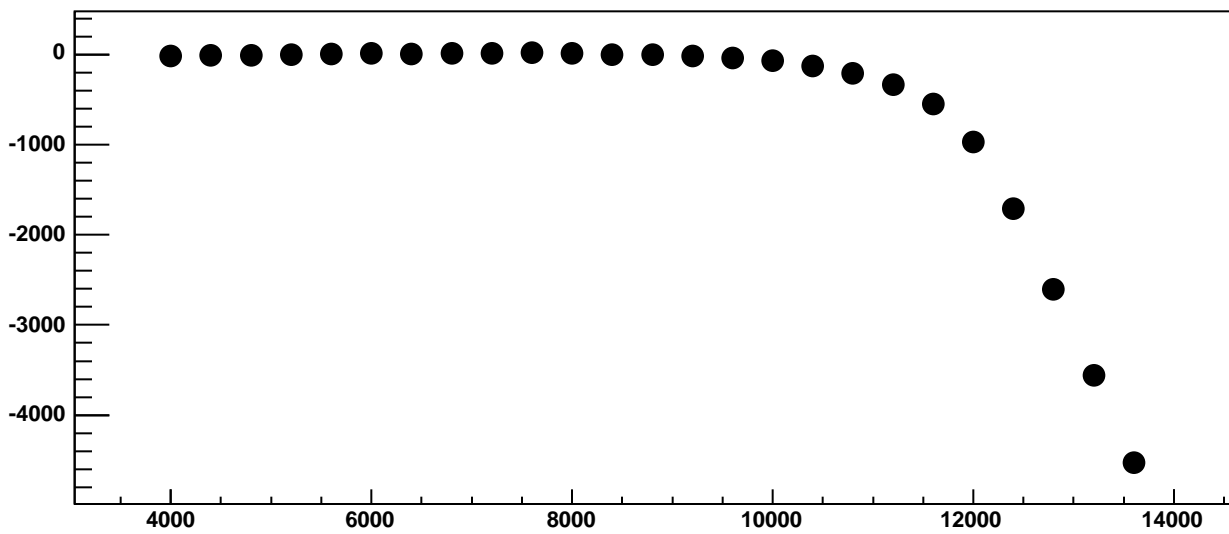
Chip 3, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC



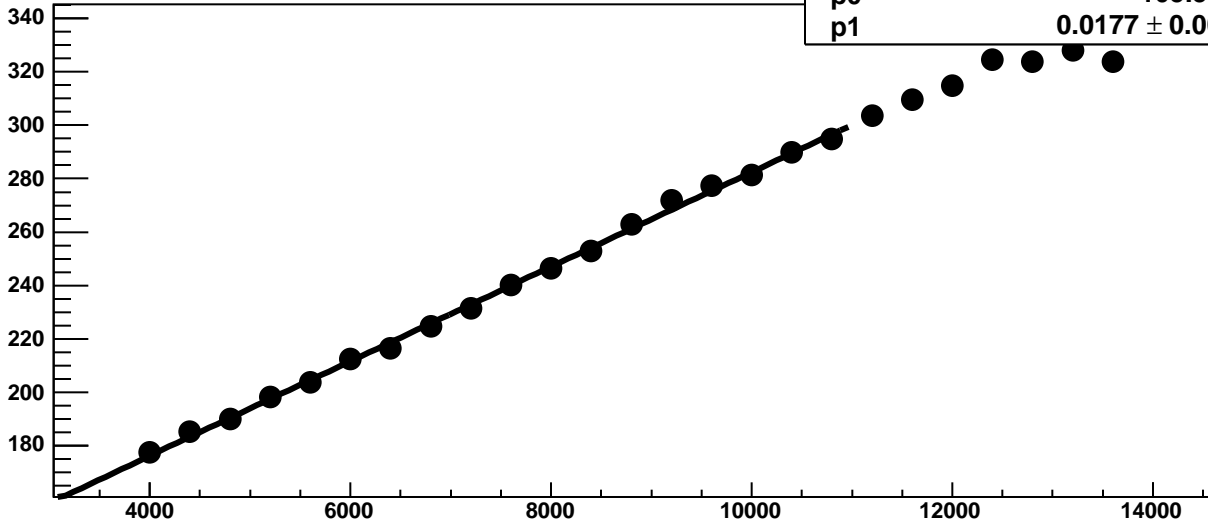
Chip 3, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC

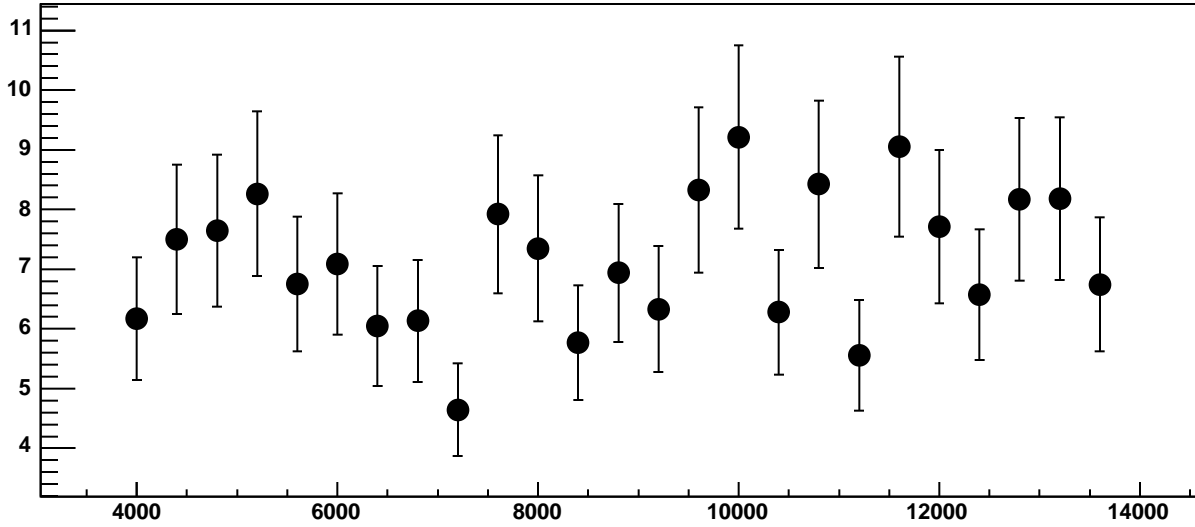


Chip 3, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

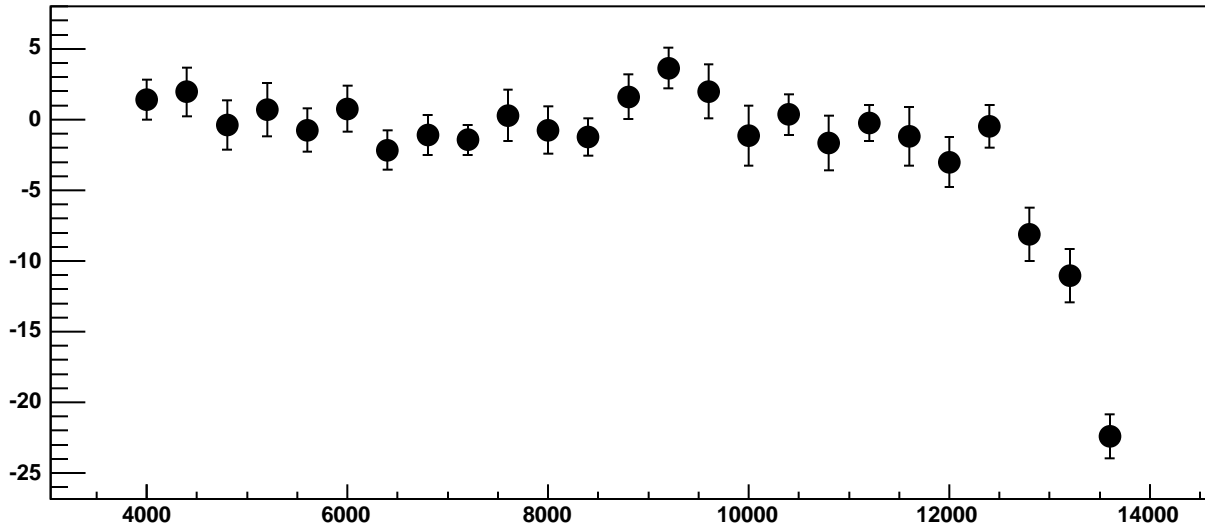


χ^2 / ndf 18.41 / 16
p0 105.5 ± 1.42
p1 0.0177 ± 0.0001876

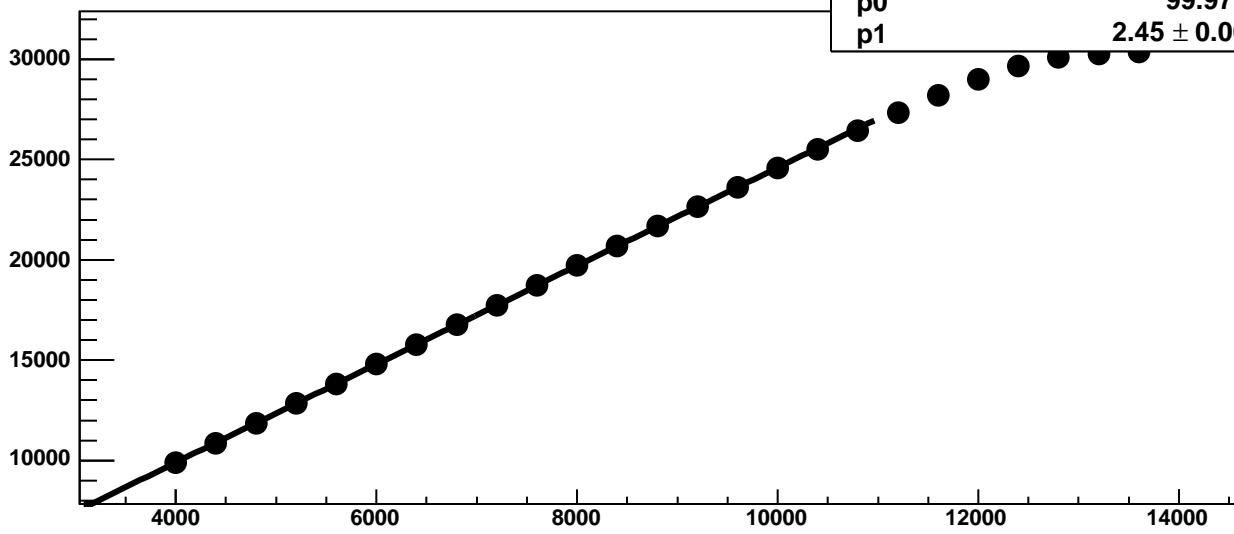
Chip 3, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



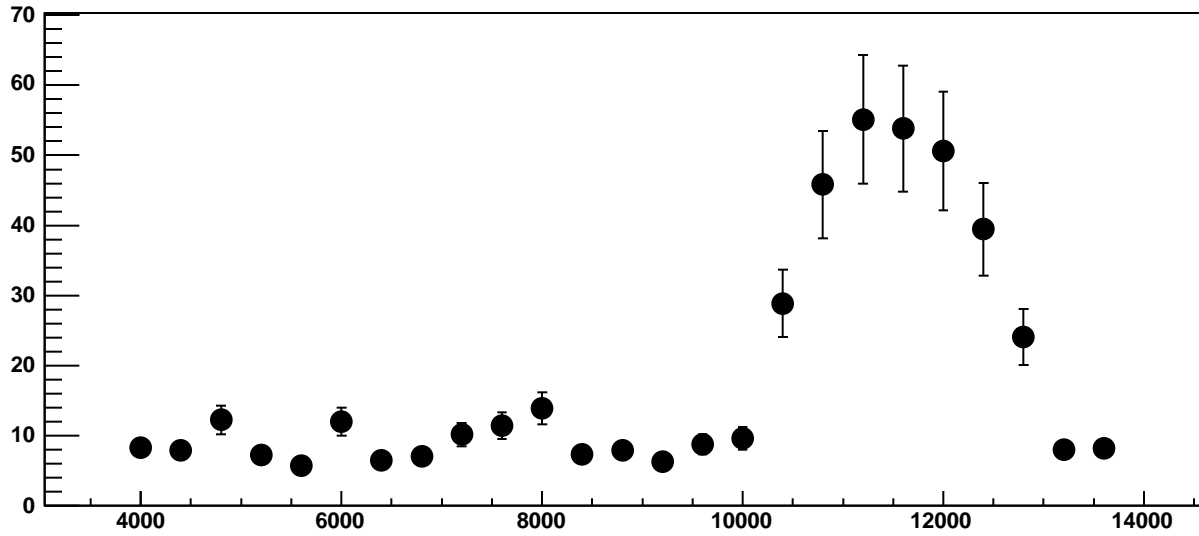
Chip 3, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



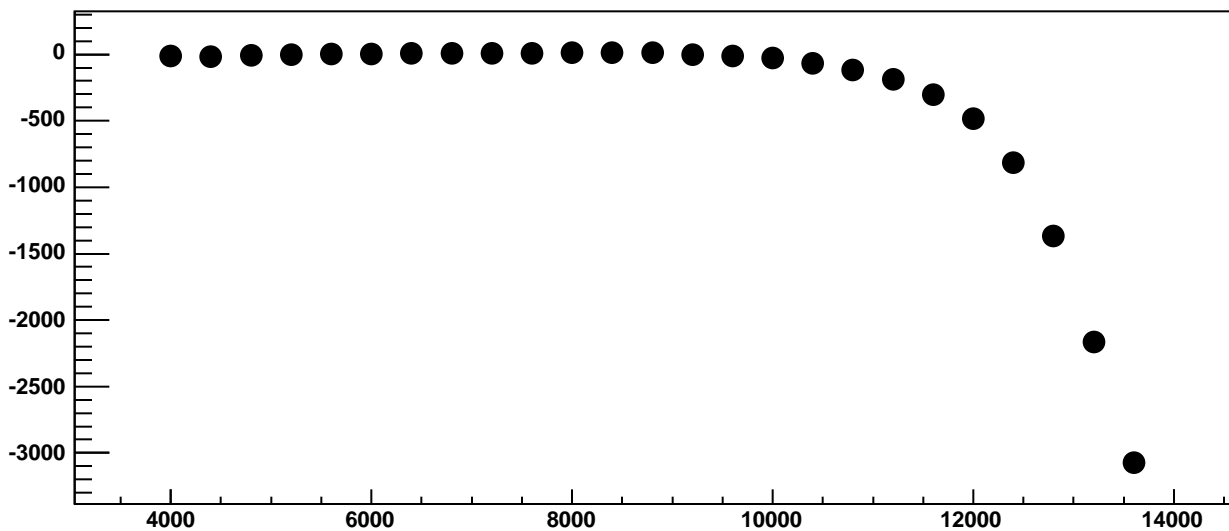
Chip 3, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



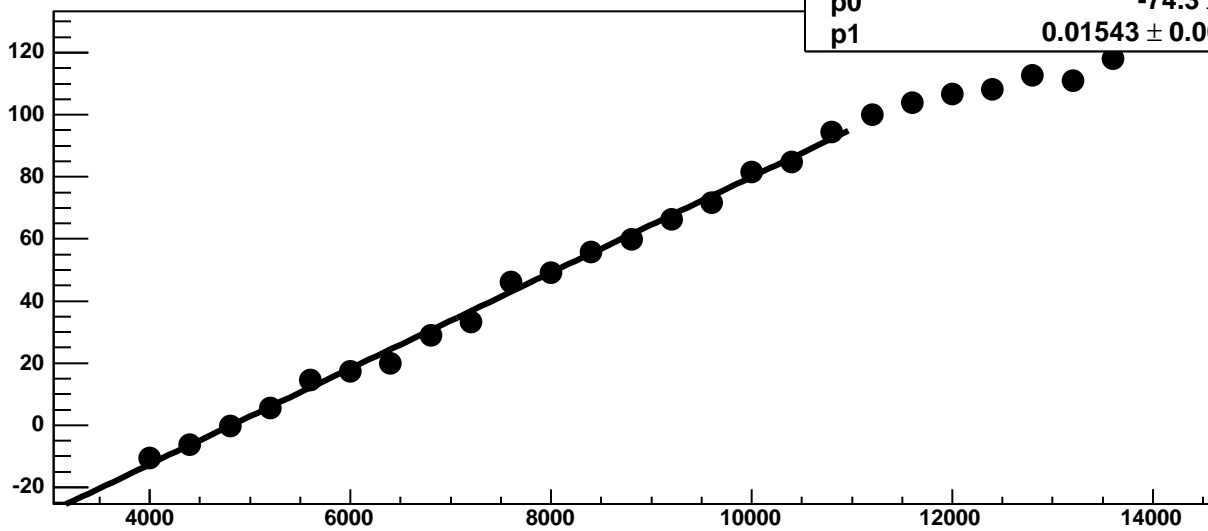
Chip 3, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



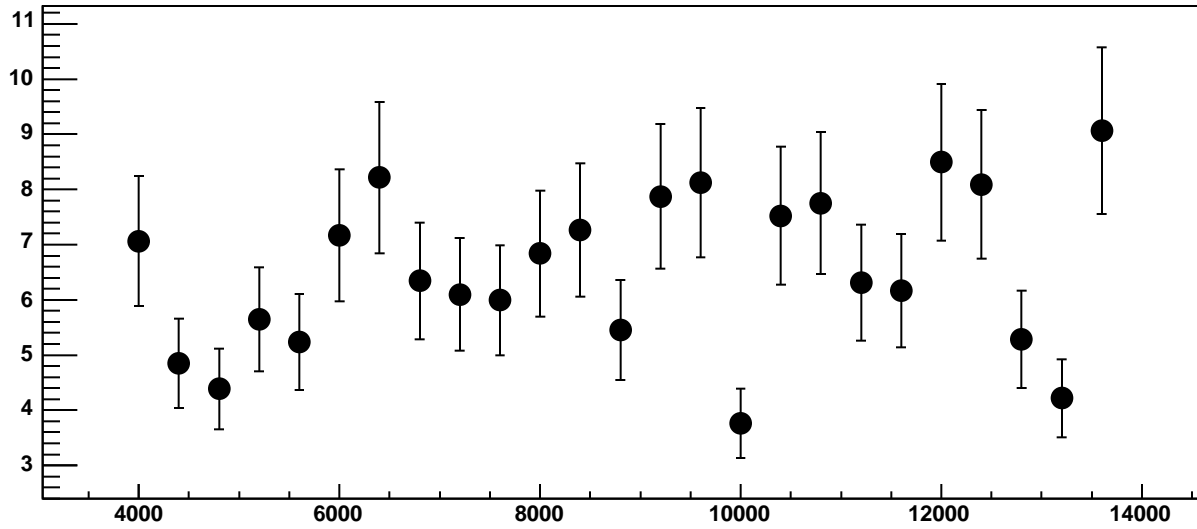
Chip 3, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC



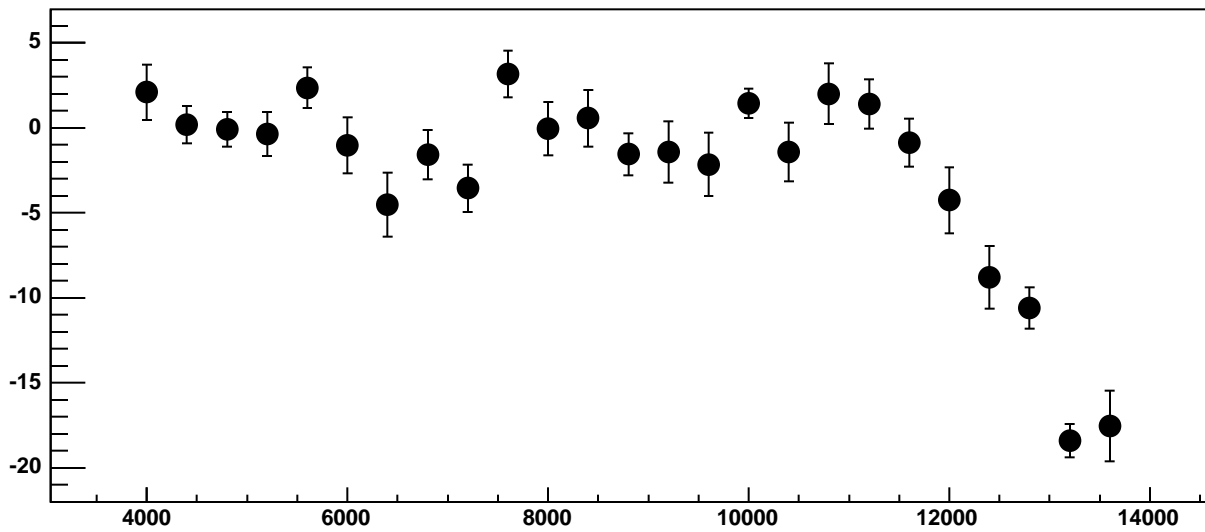
Chip 3, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



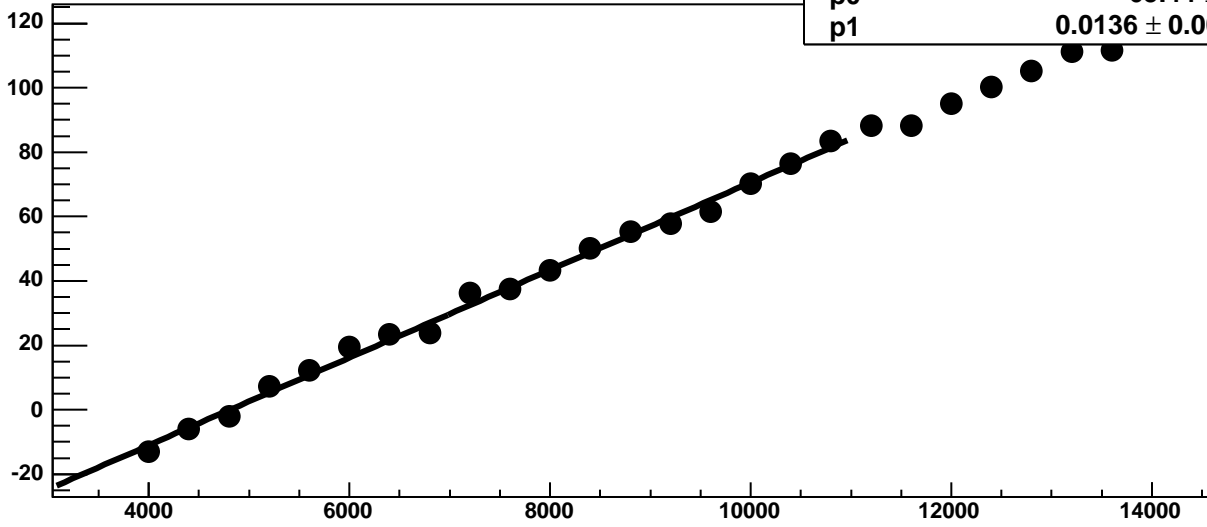
Chip 3, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

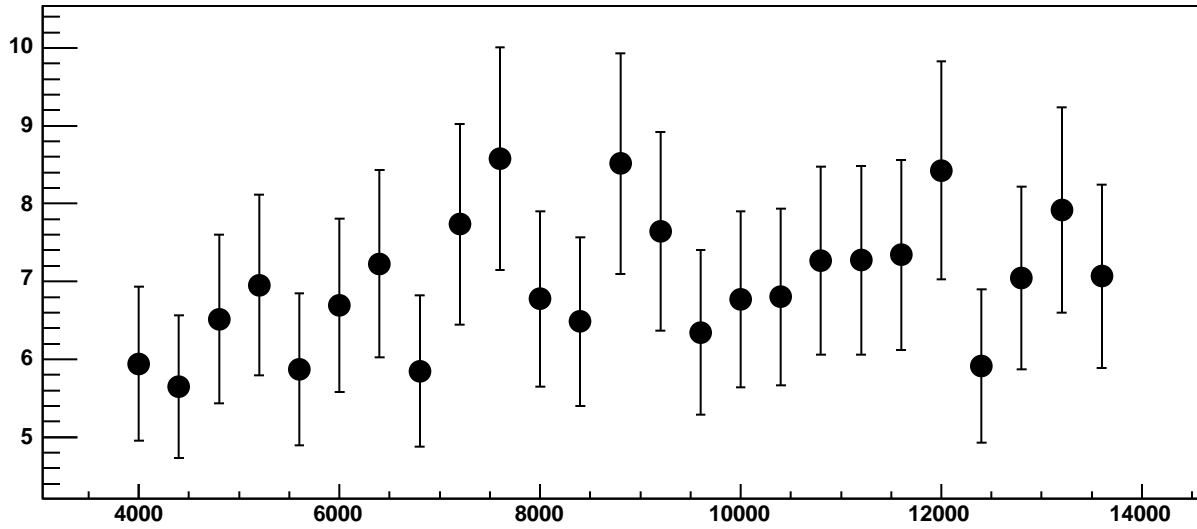


Chip 3, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

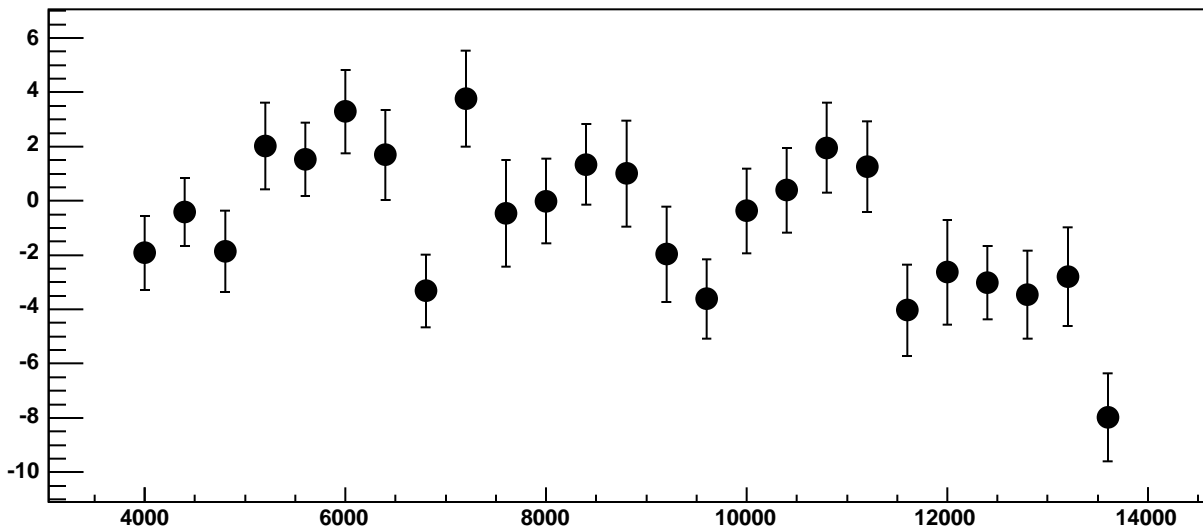


χ^2 / ndf 32.86 / 16
p0 -65.44 ± 1.269
p1 0.0136 ± 0.0001698

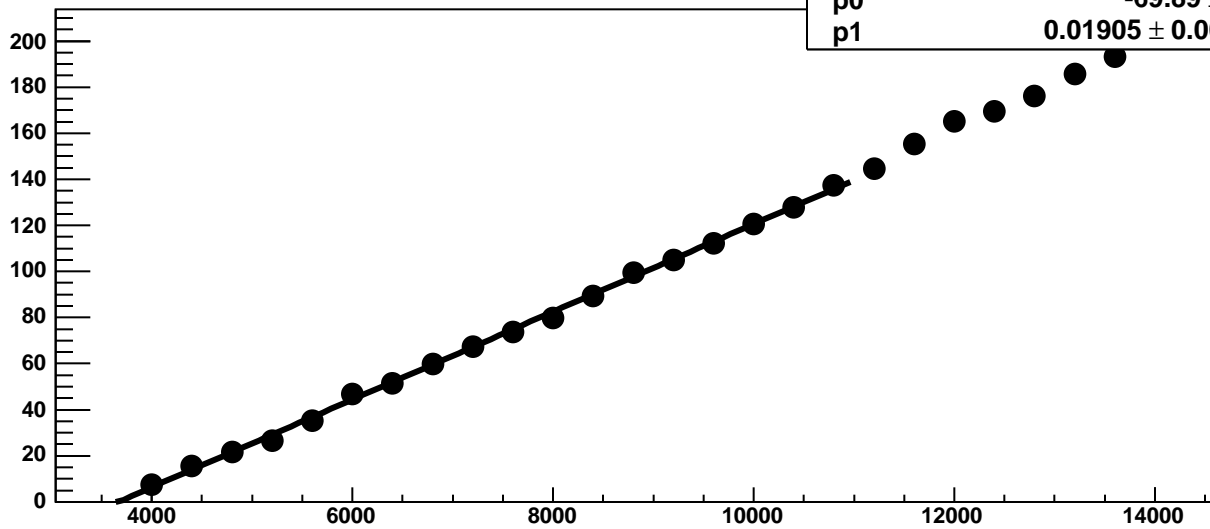
Chip 3, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



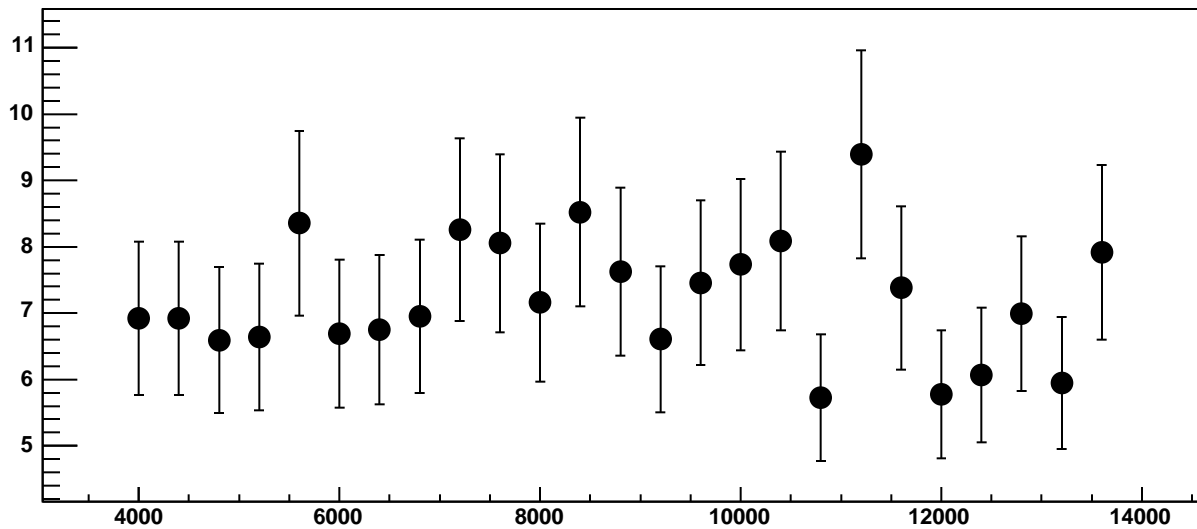
Chip 3, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



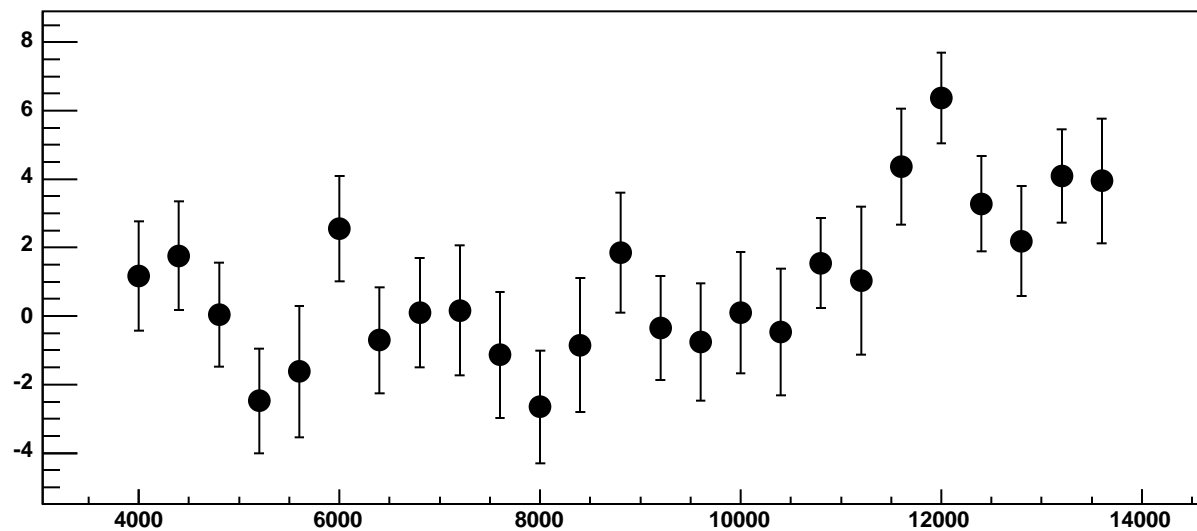
Chip 3, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC



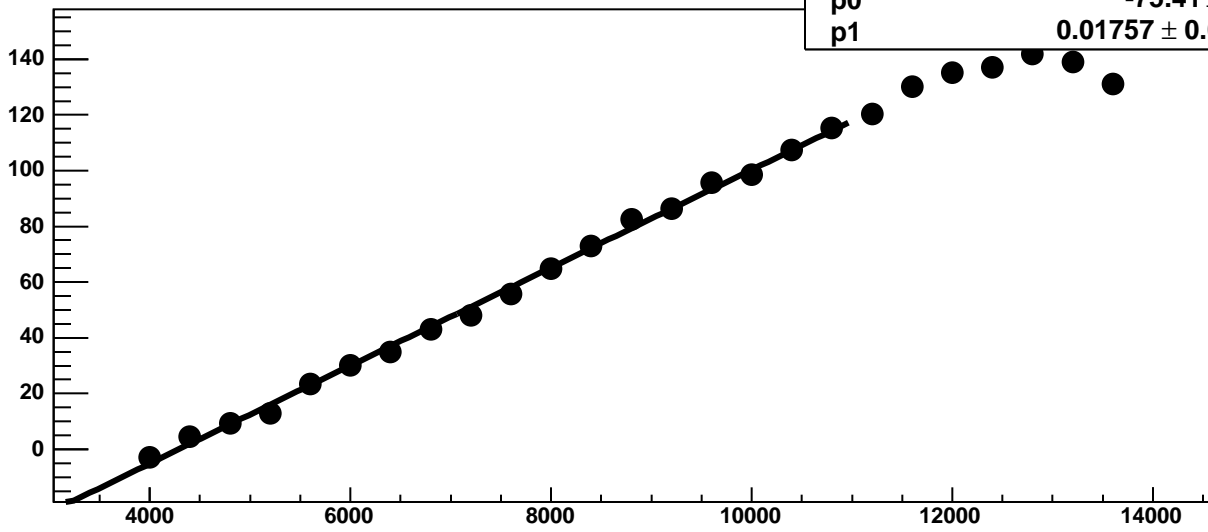
Chip 3, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



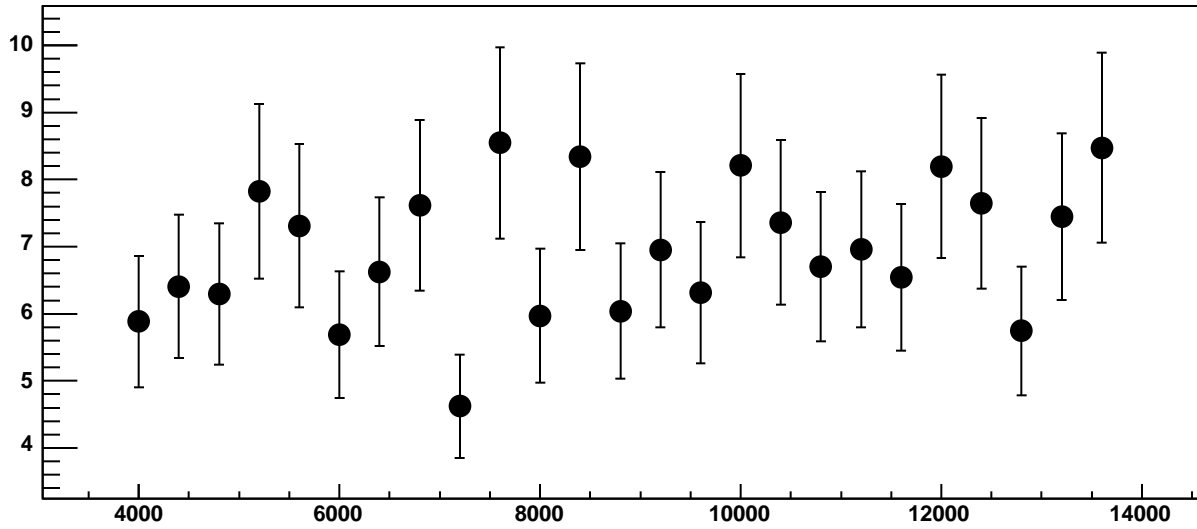
Chip 3, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC



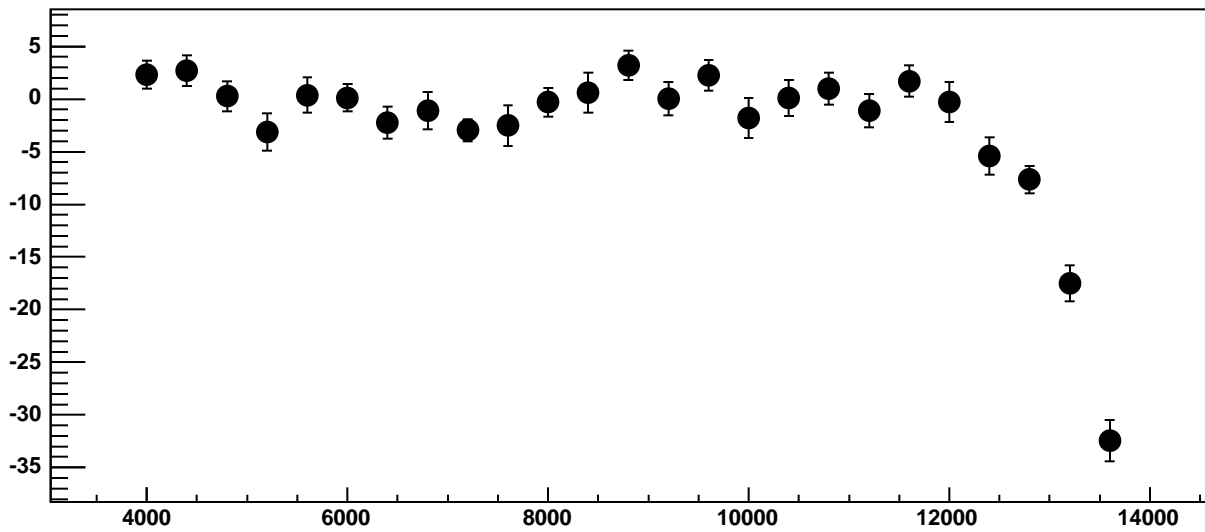
Chip 3, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



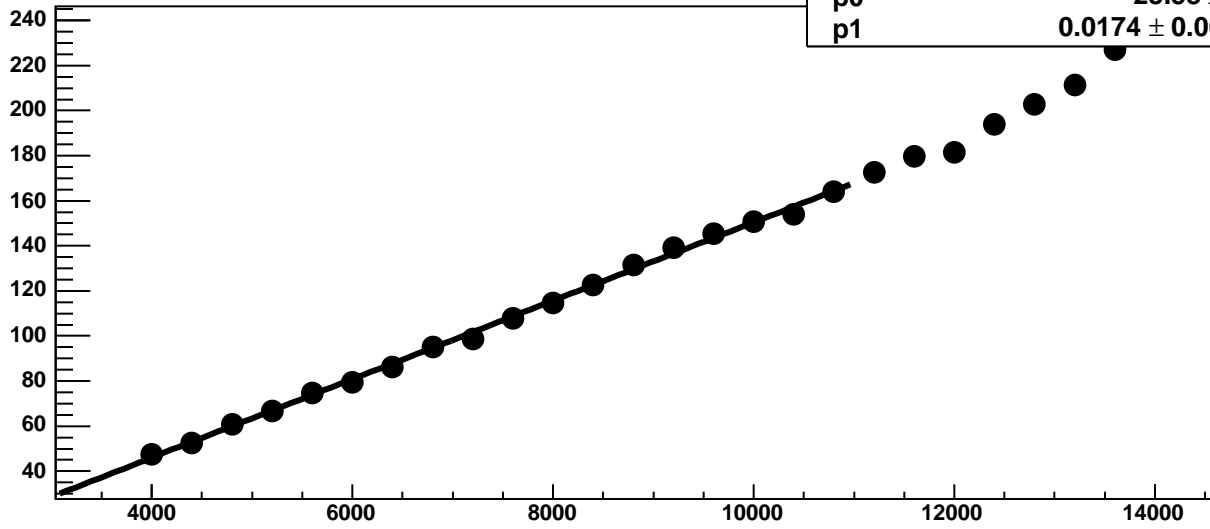
Chip 3, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

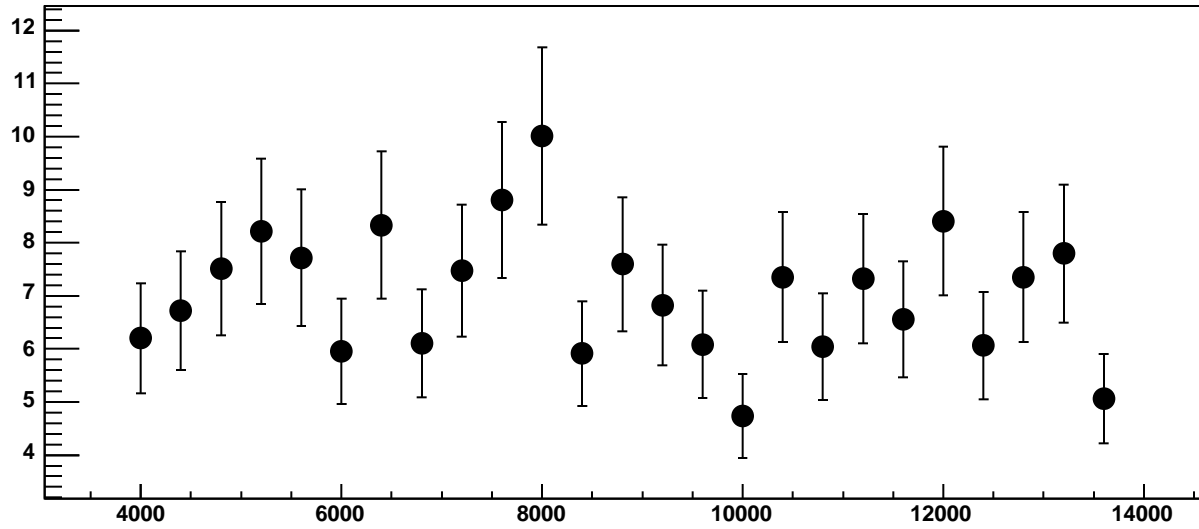


Chip 3, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC

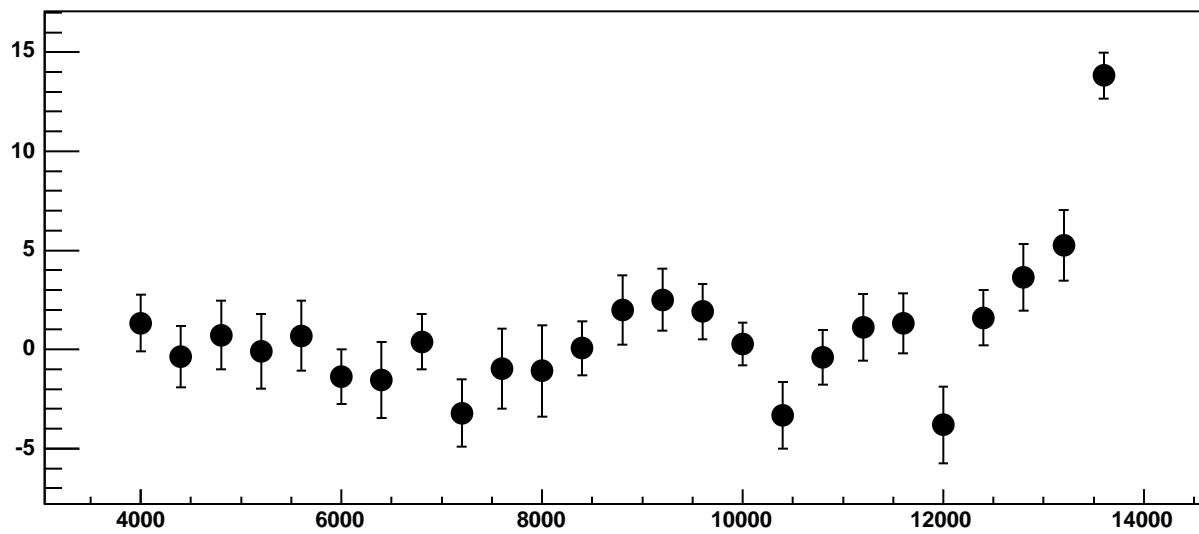


χ^2 / ndf 16.74 / 16
p0 -23.58 ± 1.334
p1 0.0174 ± 0.0001683

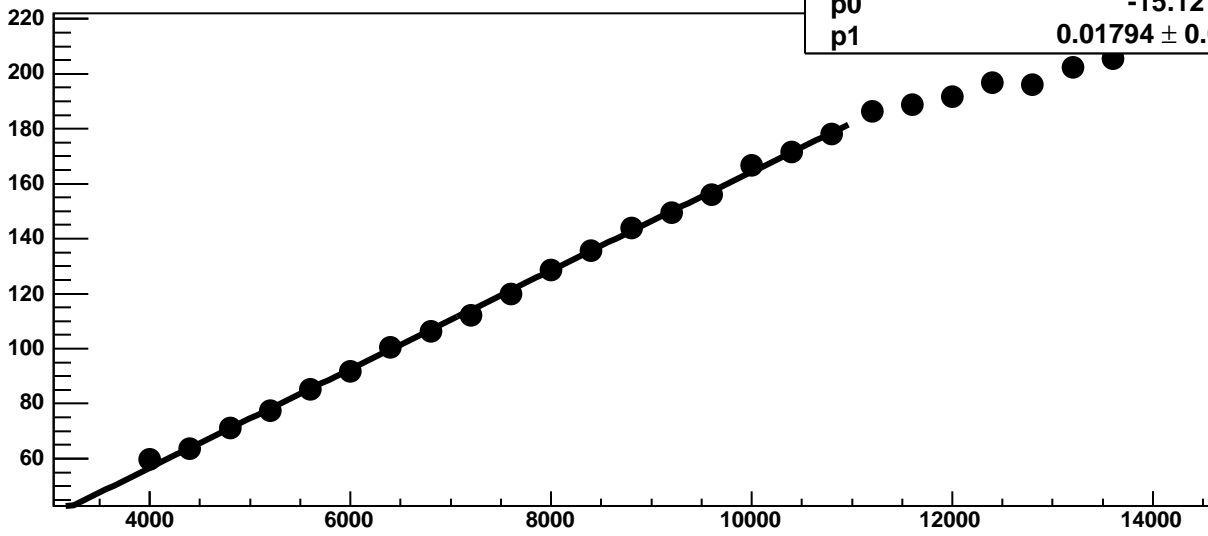
Chip 3, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



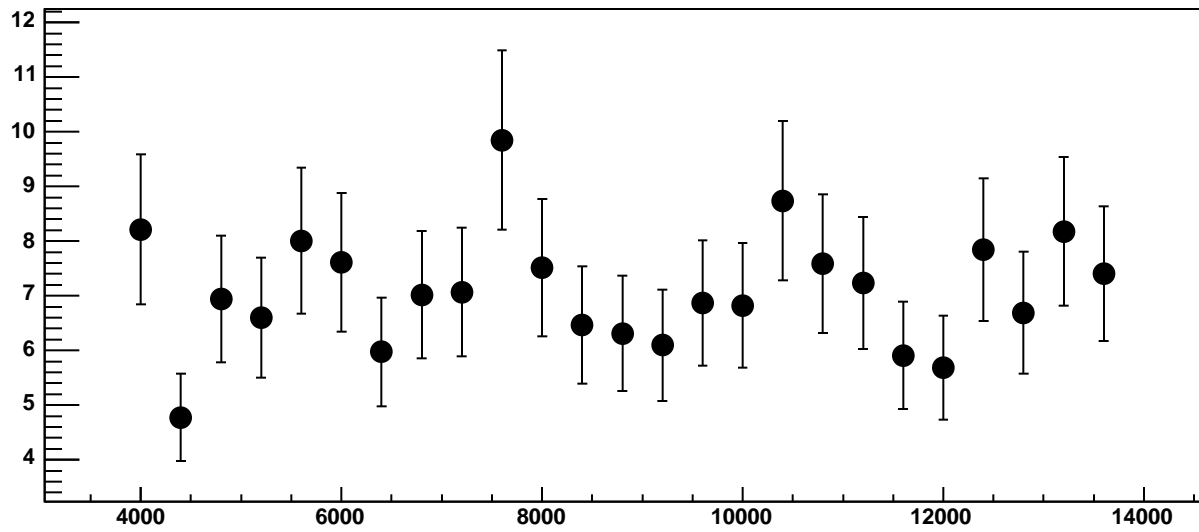
Chip 3, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC



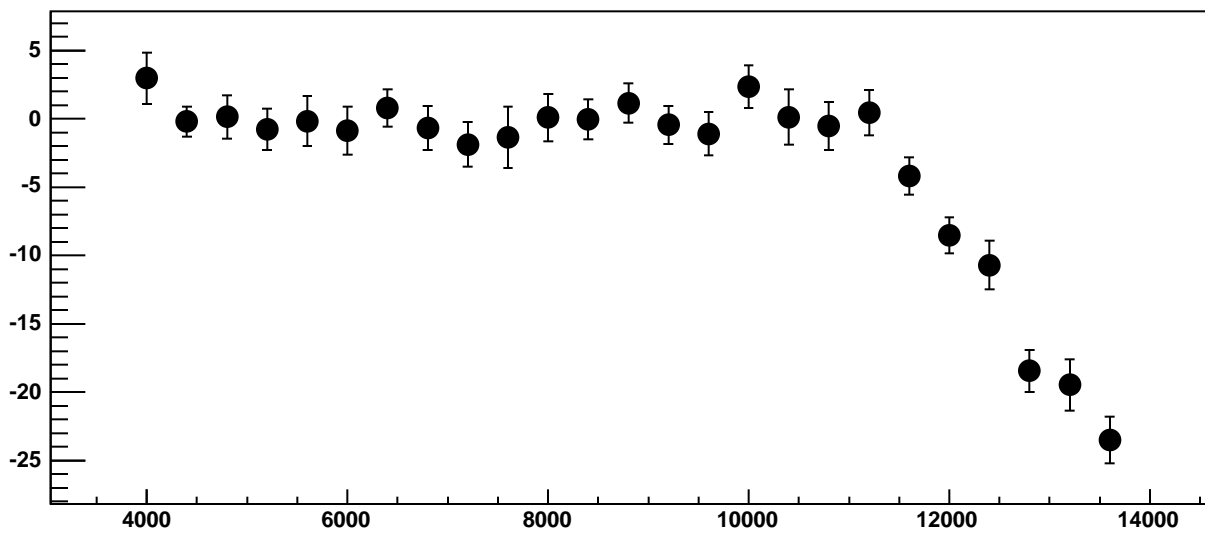
Chip 3, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



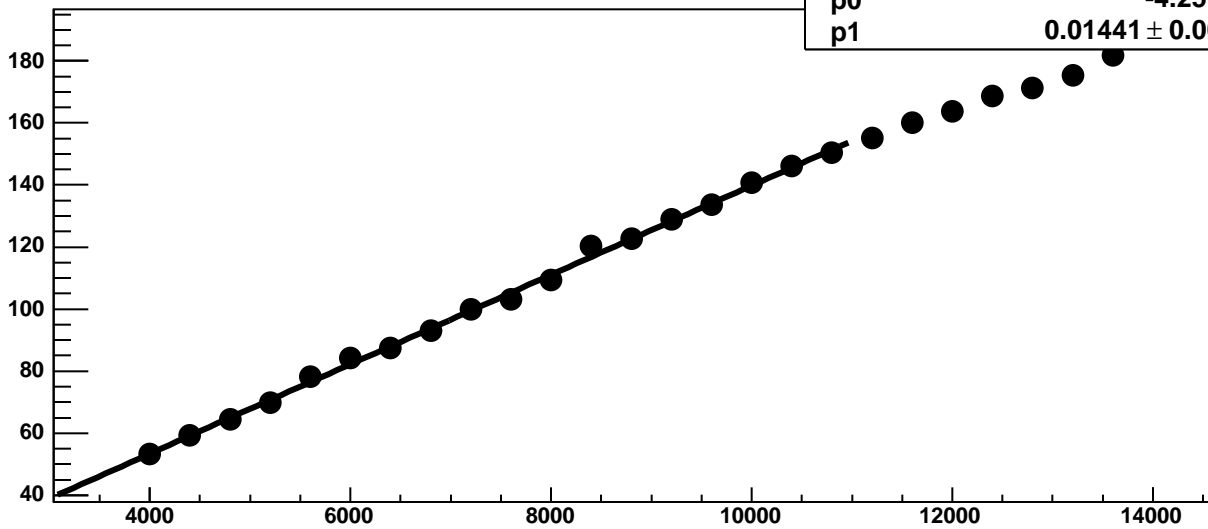
Chip 3, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

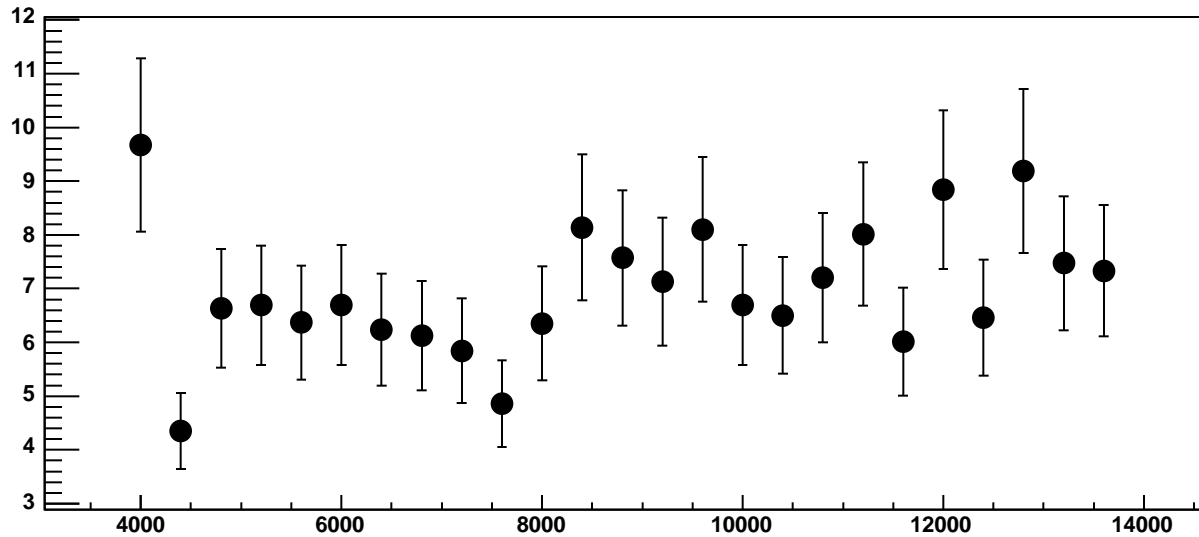


Chip 3, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

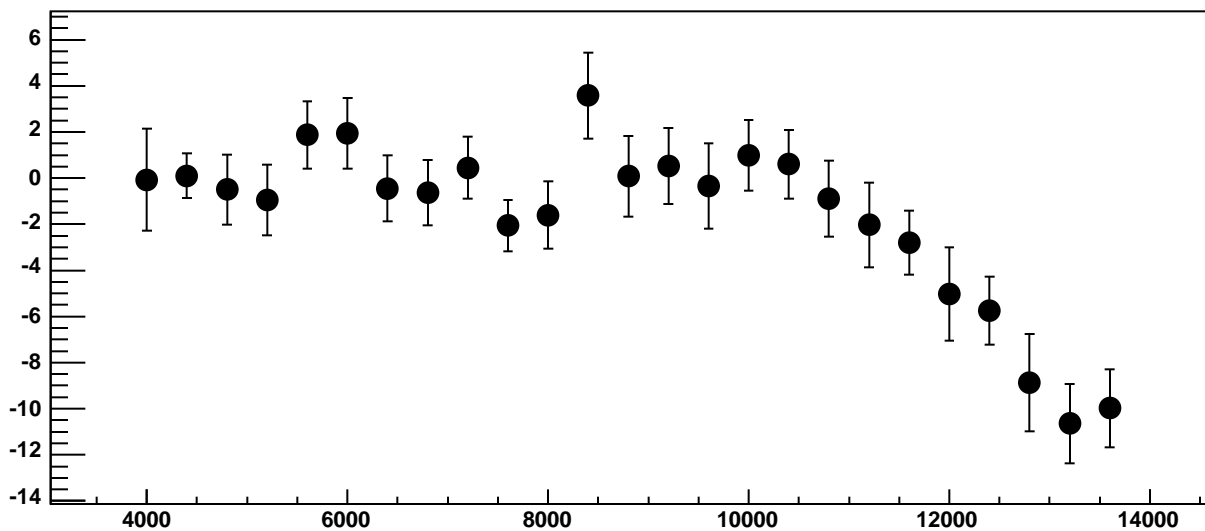


χ^2 / ndf 13.45 / 16
p0 -4.25 ± 1.27
p1 0.01441 ± 0.0001709

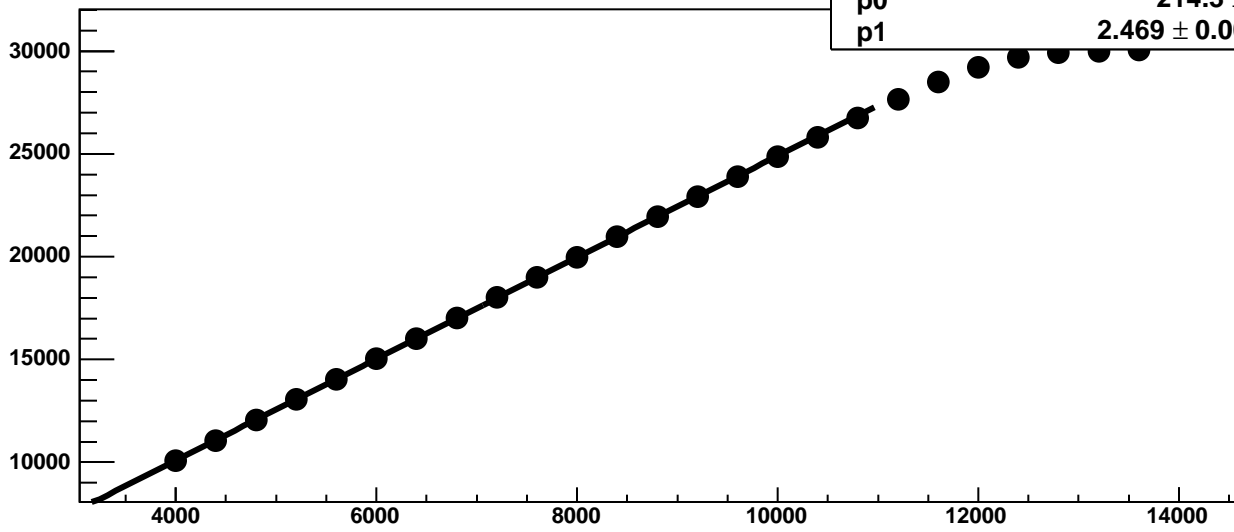
Chip 3, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

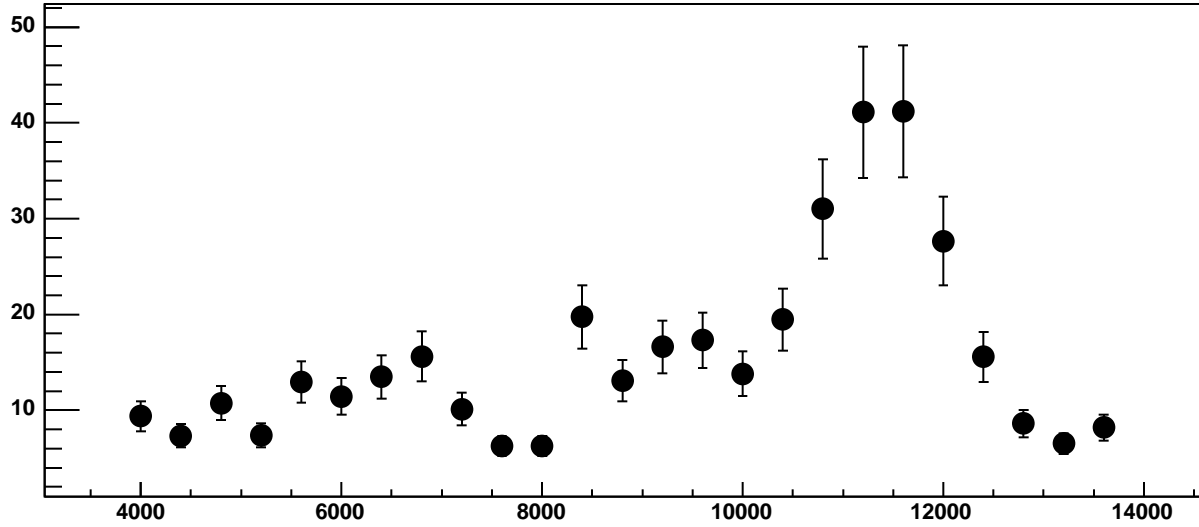


Chip 3, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC

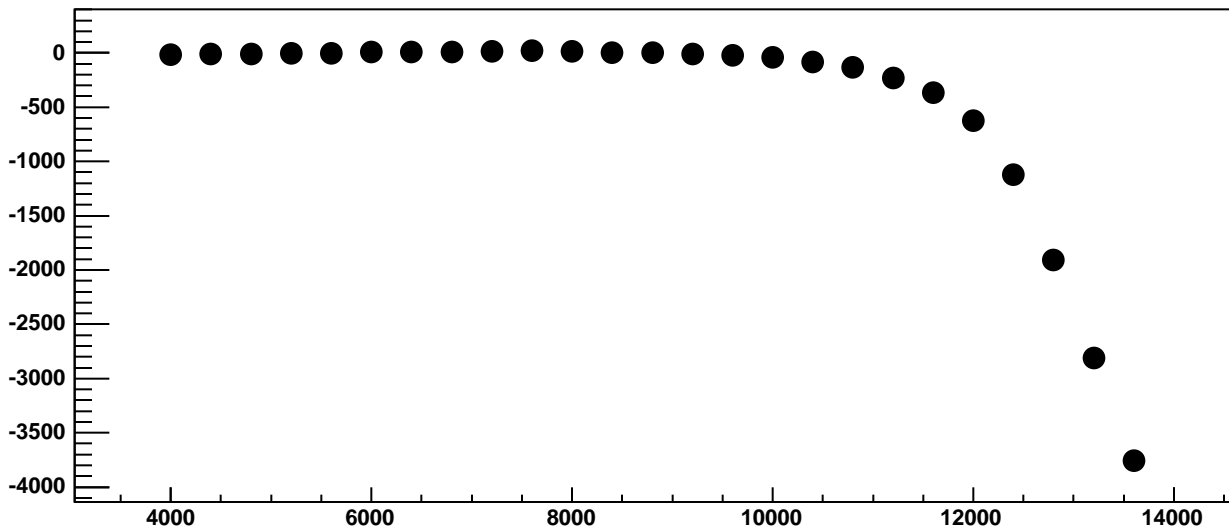


χ^2 / ndf 1446 / 16
p0 214.3 ± 2.192
p1 2.469 ± 0.0003169

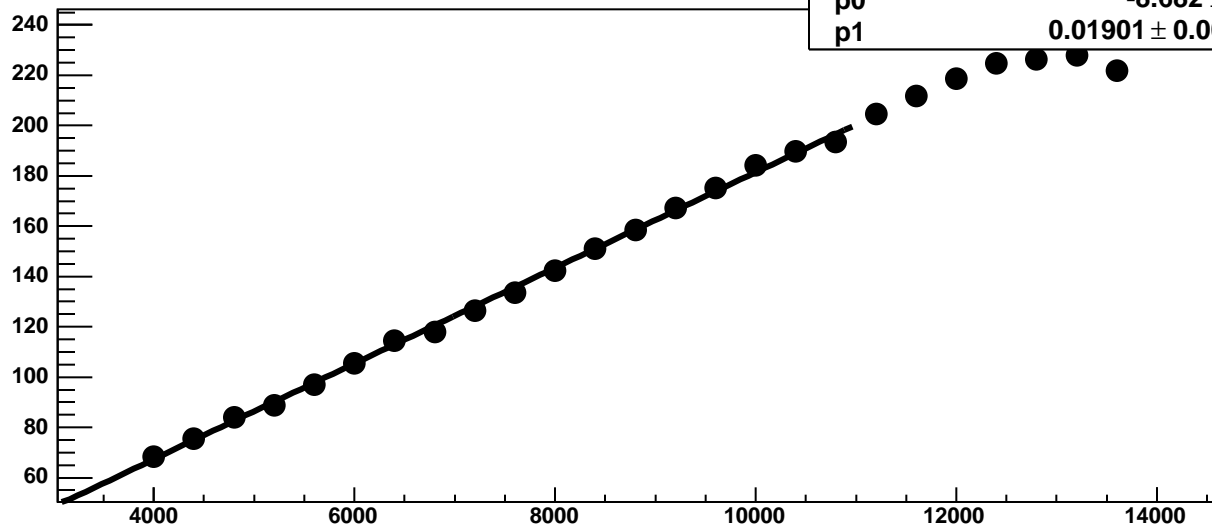
Chip 3, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



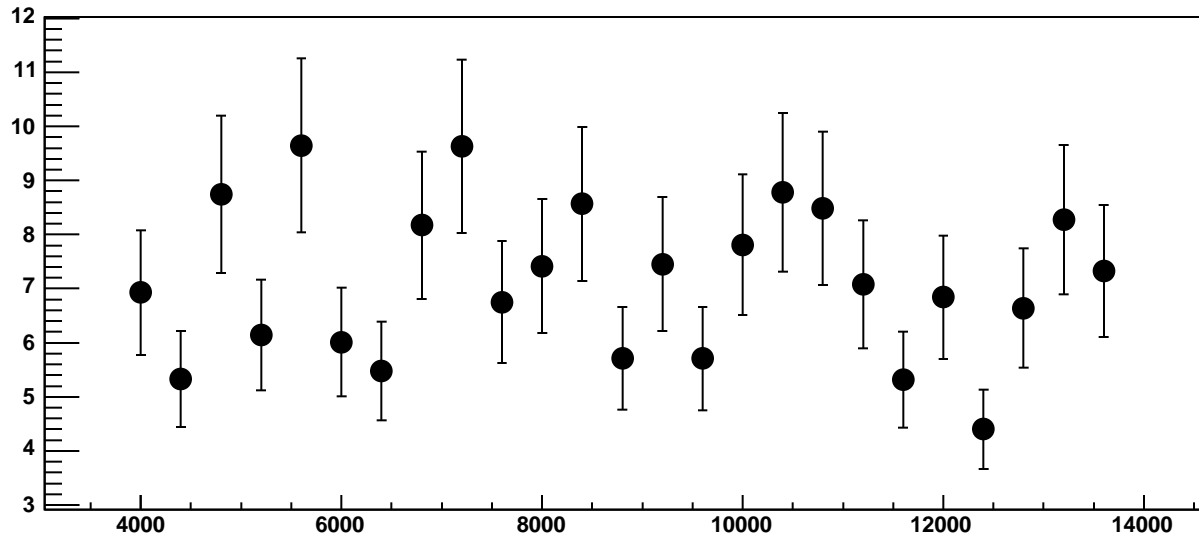
Chip 3, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC



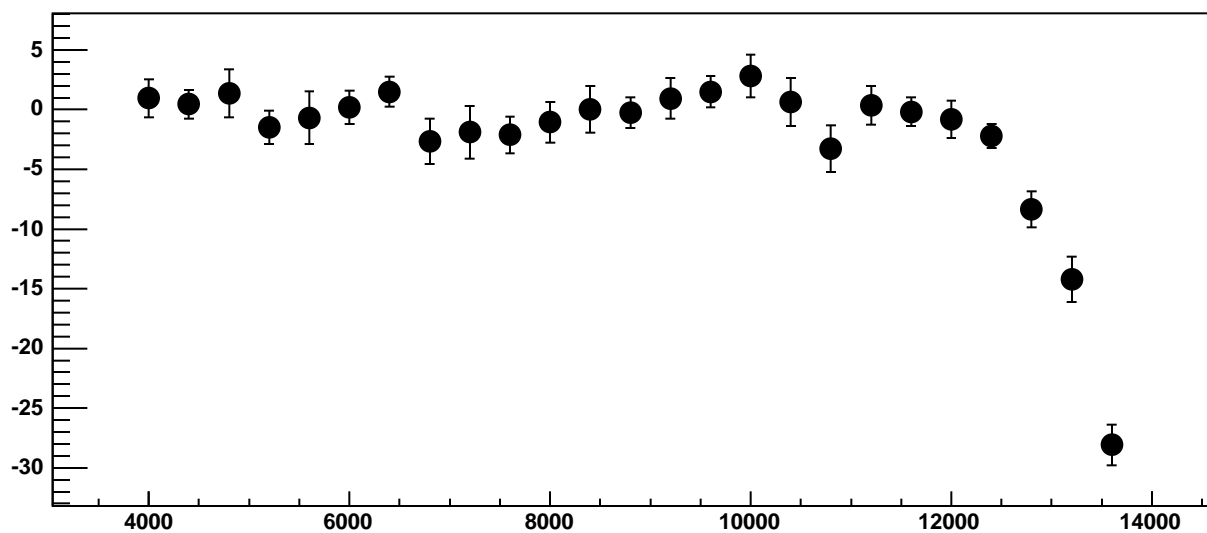
Chip 3, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC



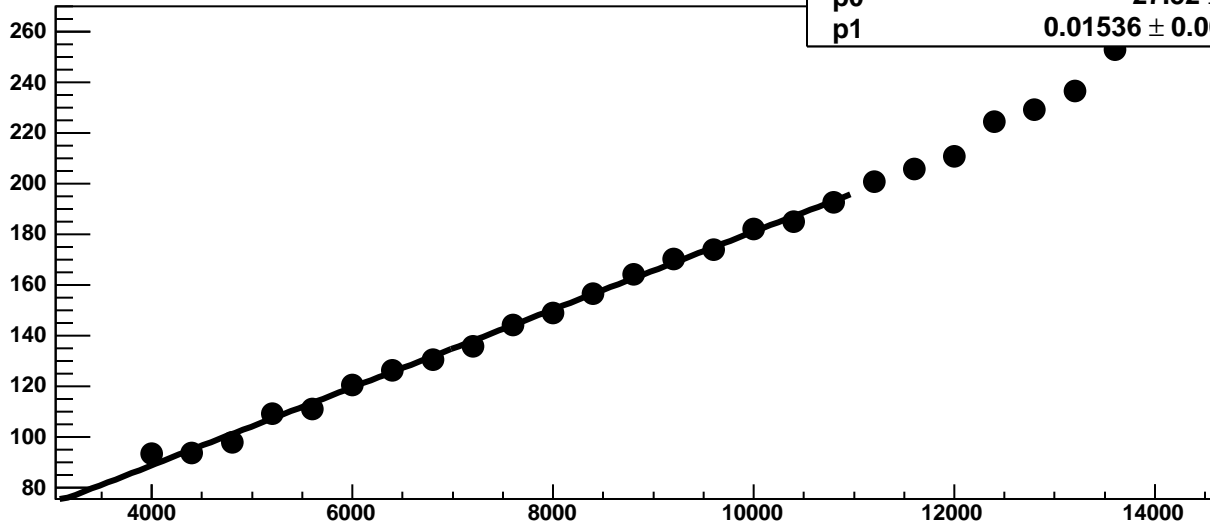
Chip 3, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



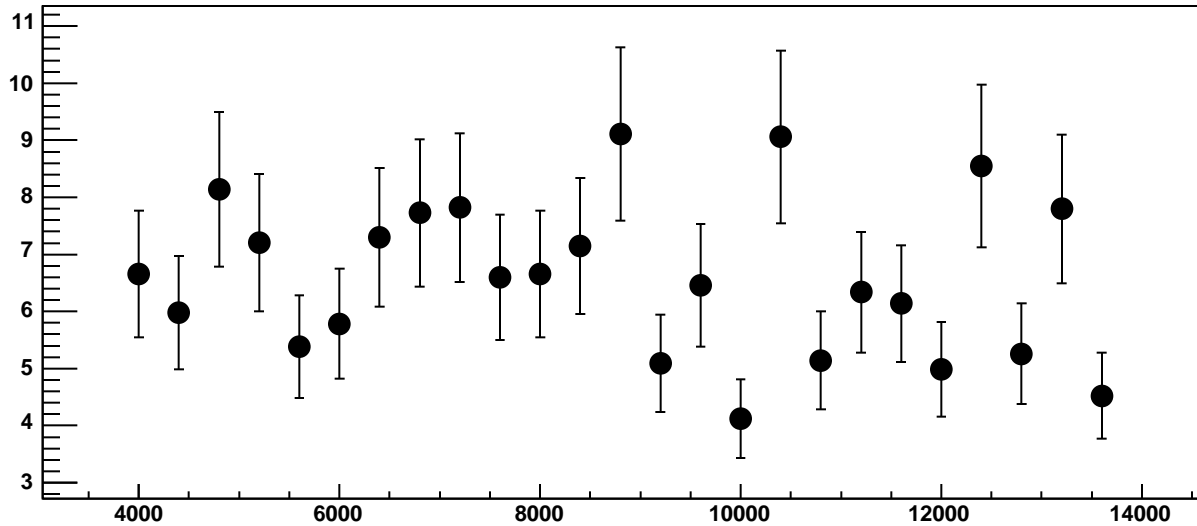
Chip 3, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



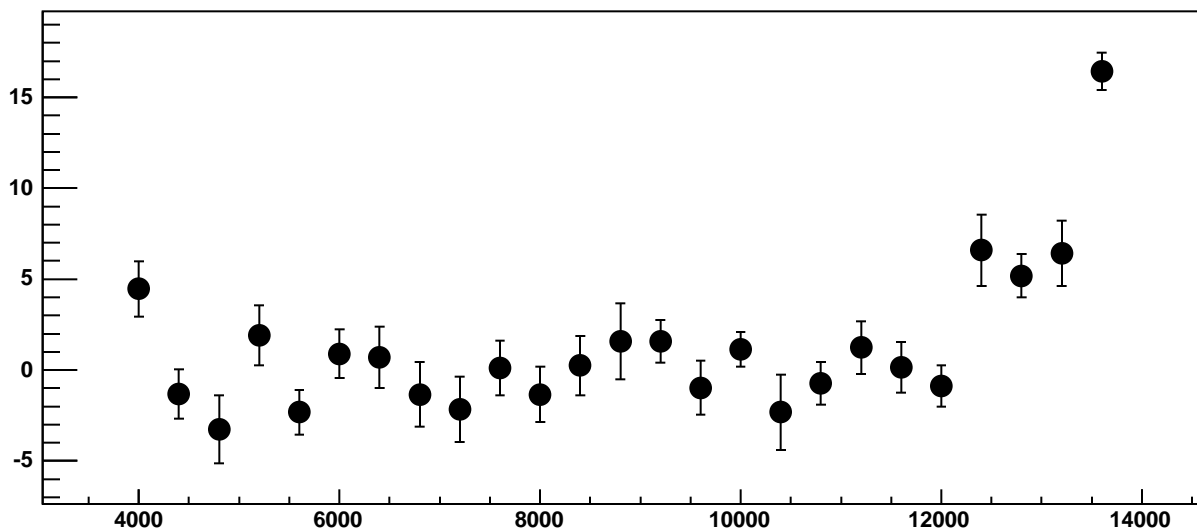
Chip 3, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



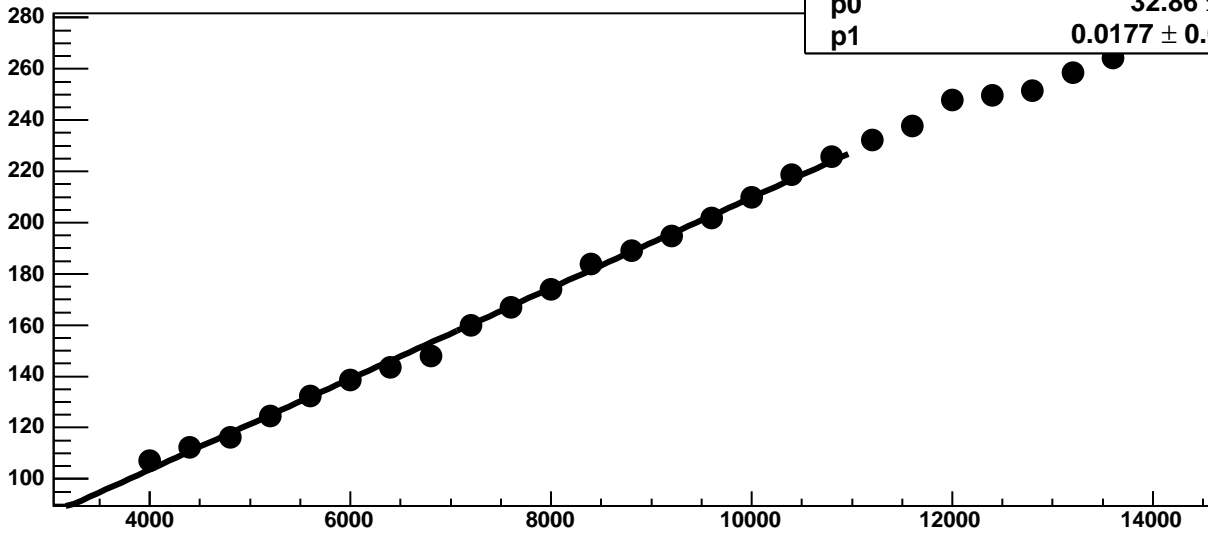
Chip 3, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



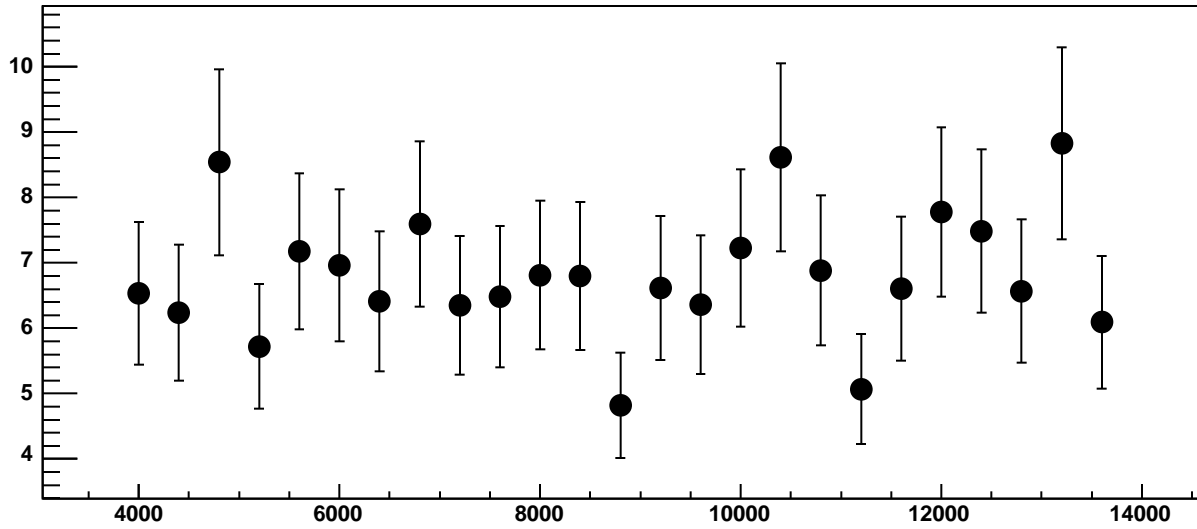
Chip 3, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



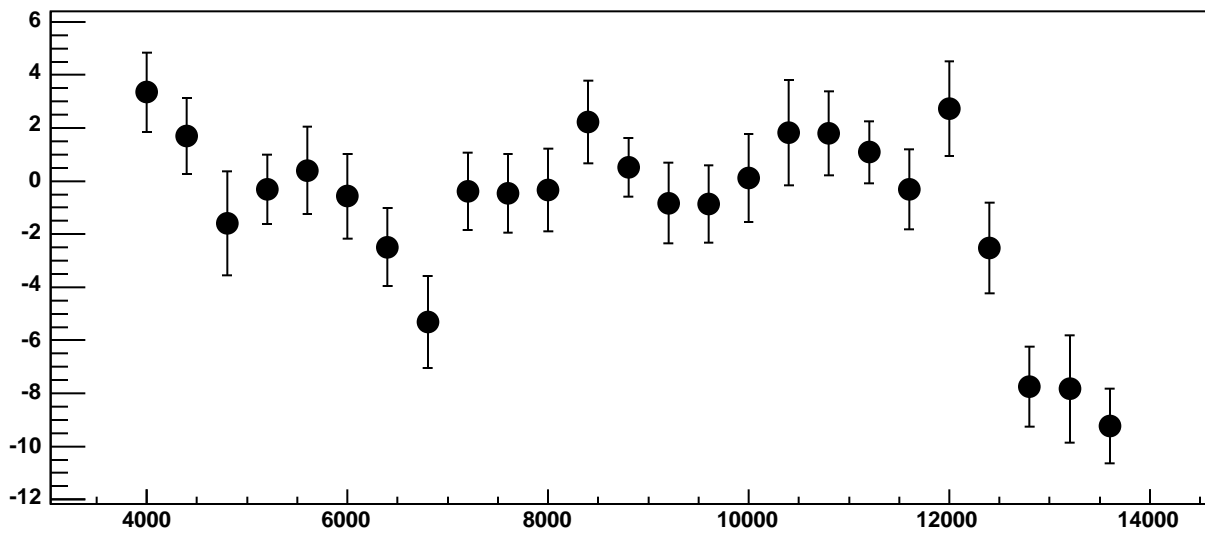
Chip 3, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC



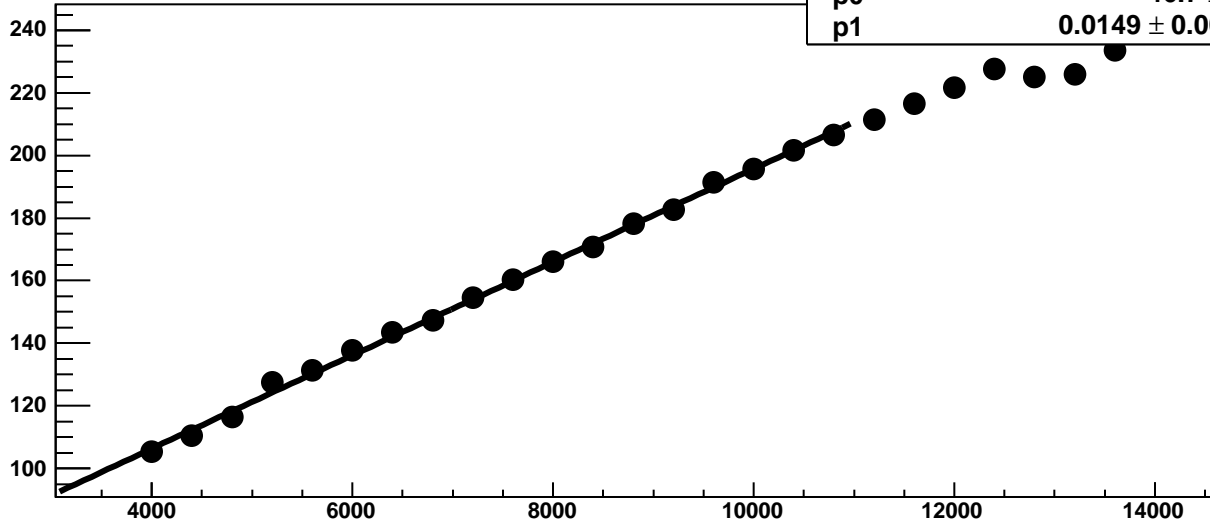
Chip 3, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

13.64 / 16

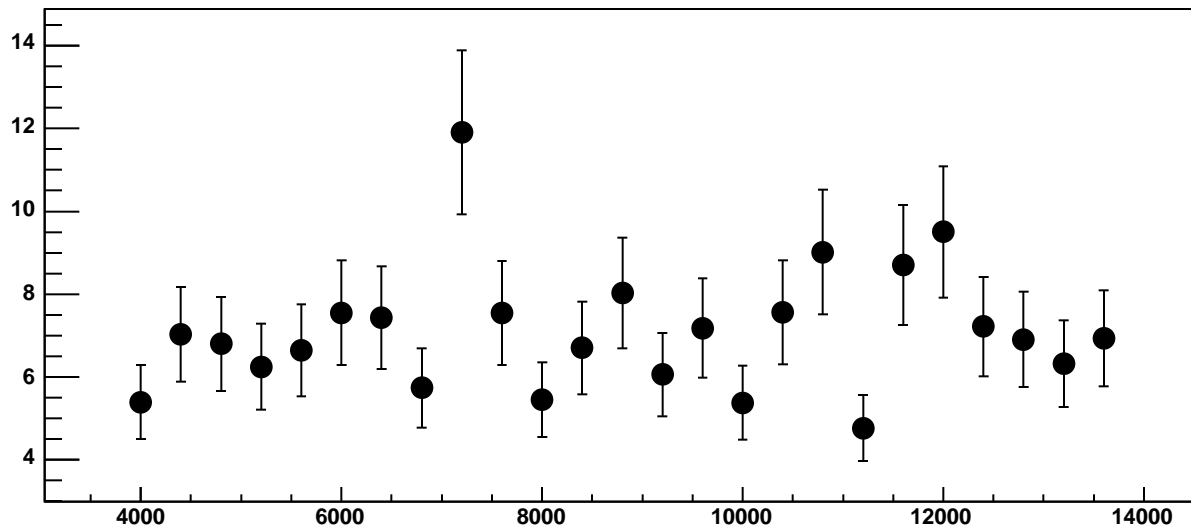
p0

46.7 ± 1.305

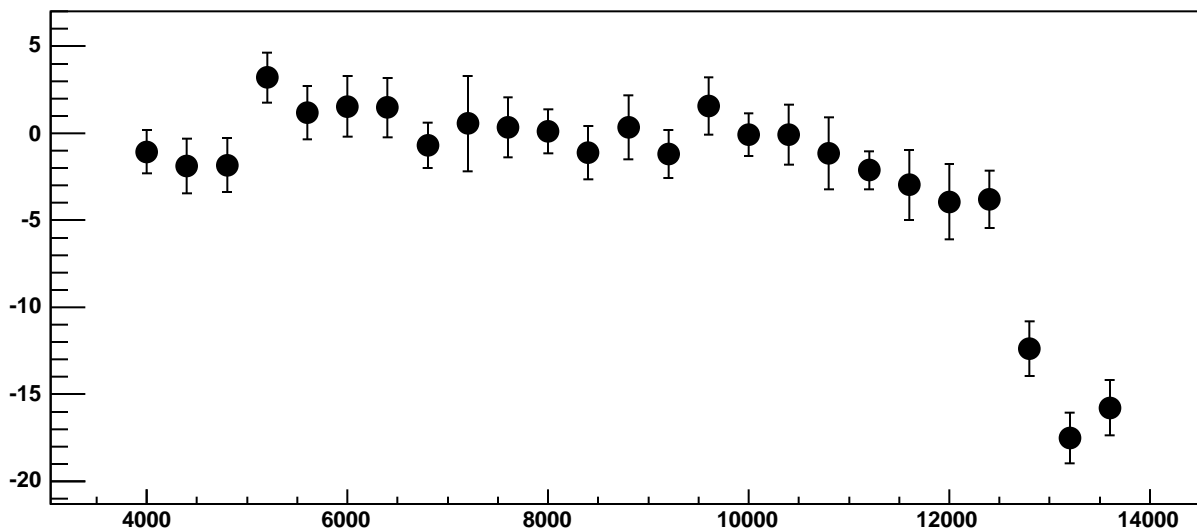
p1

0.0149 ± 0.0001724

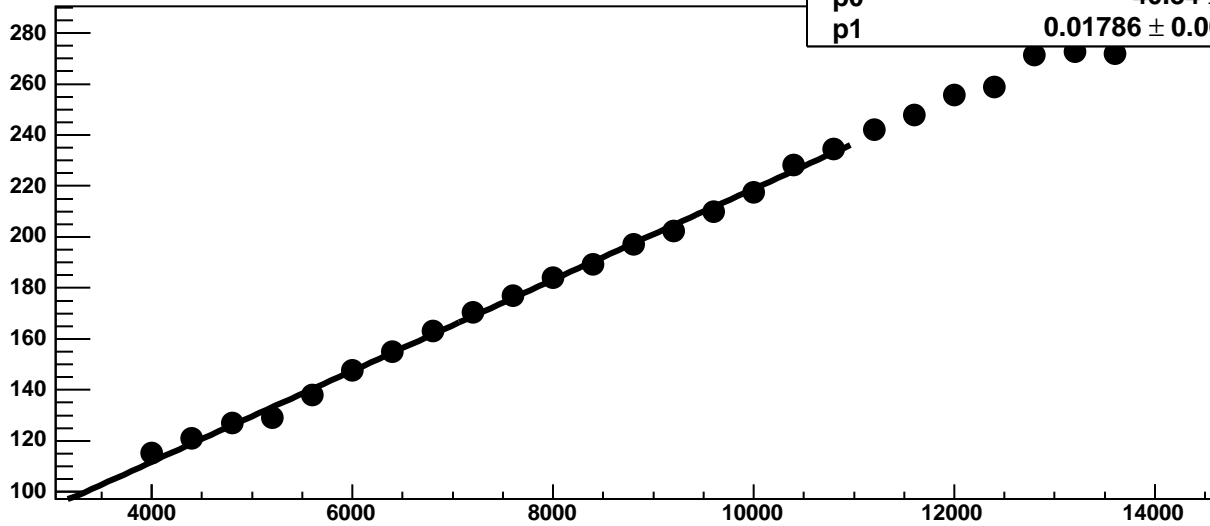
Chip 3, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

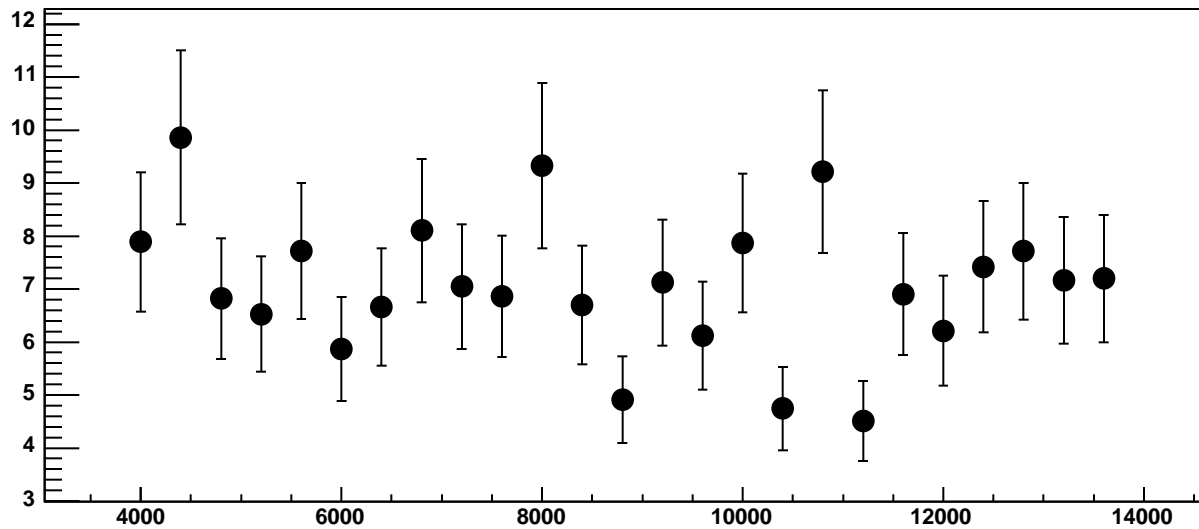


Chip 3, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

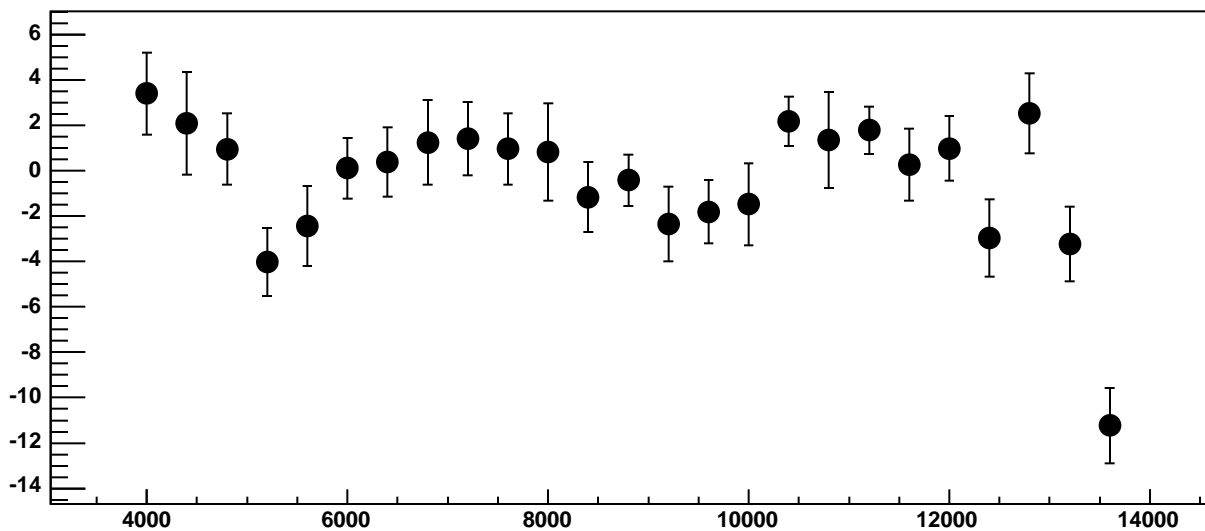


χ^2 / ndf 25.18 / 16
p0 40.34 ± 1.438
p1 0.01786 ± 0.0001815

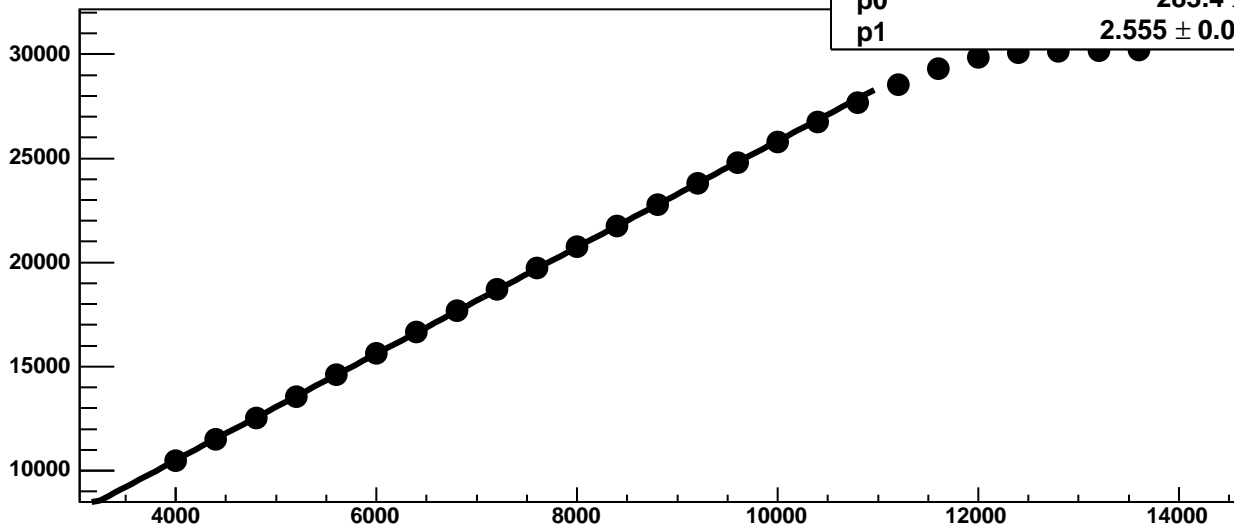
Chip 3, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

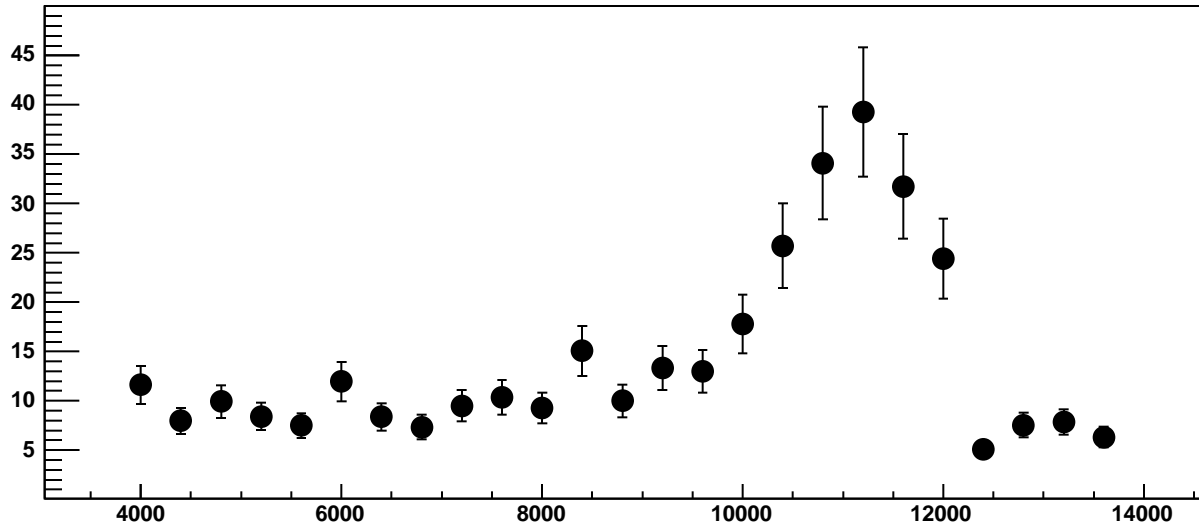


Chip 3, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC

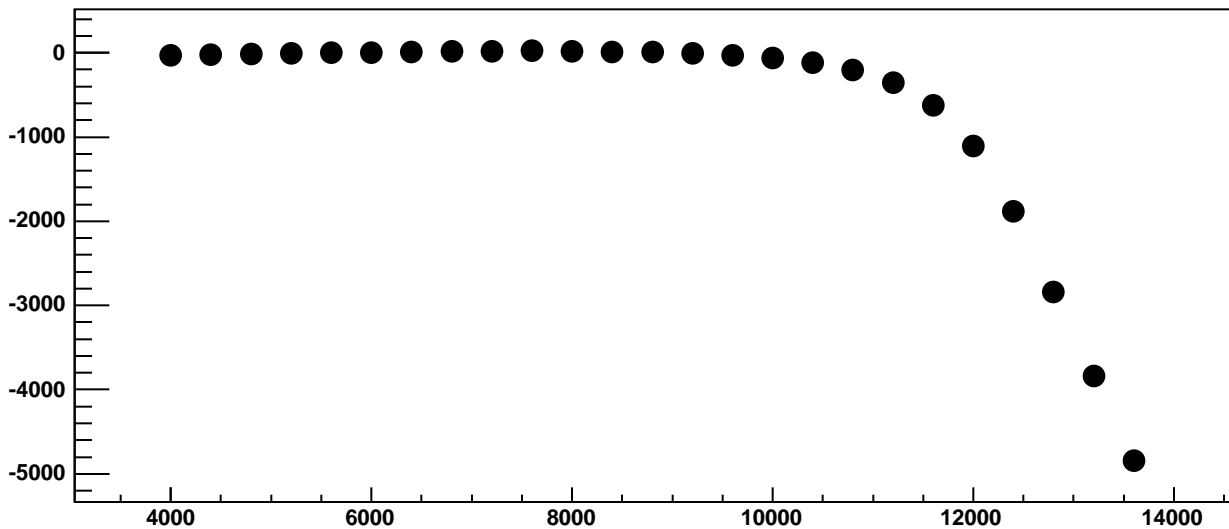


χ^2 / ndf 2145 / 16
p0 283.4 ± 2.278
p1 2.555 ± 0.0003341

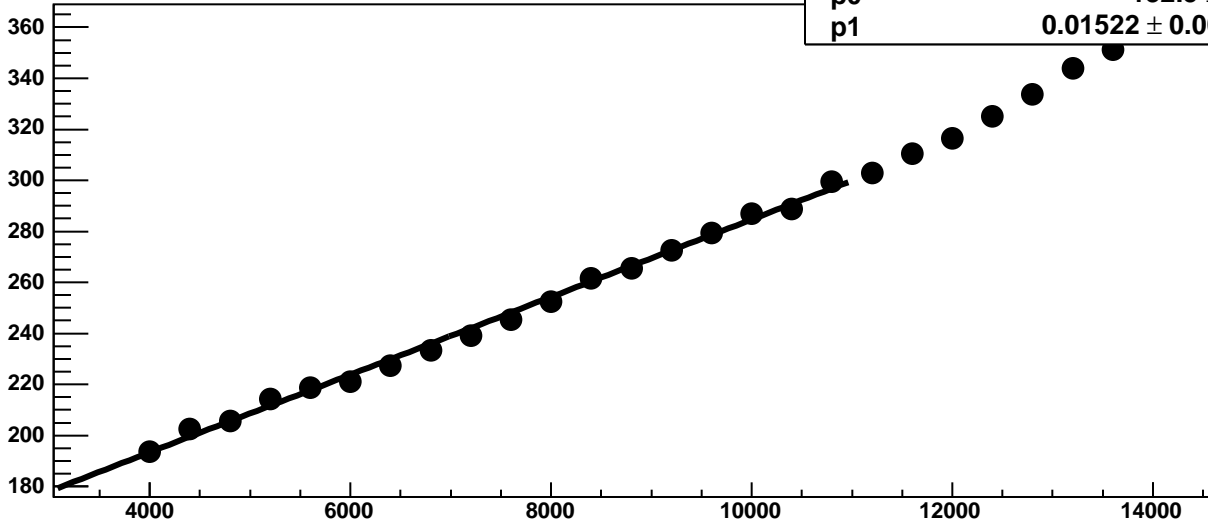
Chip 3, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

31.96 / 16

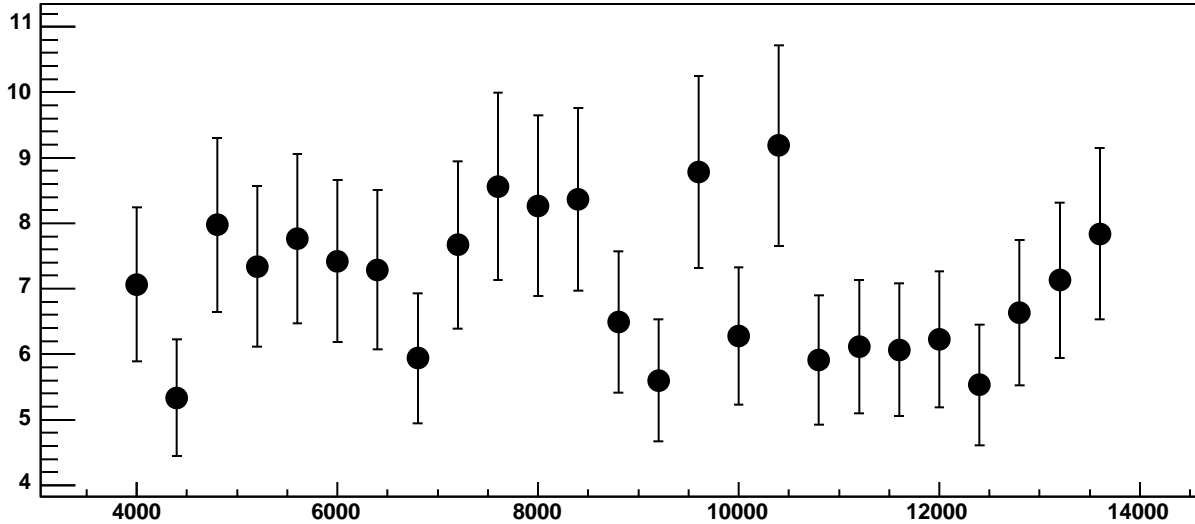
p0

132.5 ± 1.352

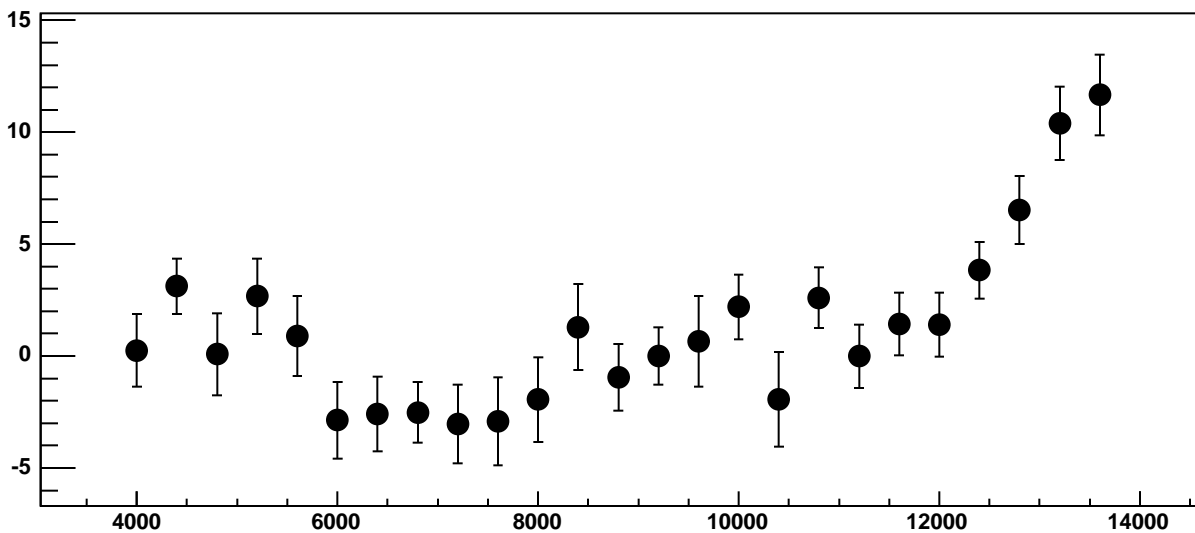
p1

0.01522 ± 0.0001757

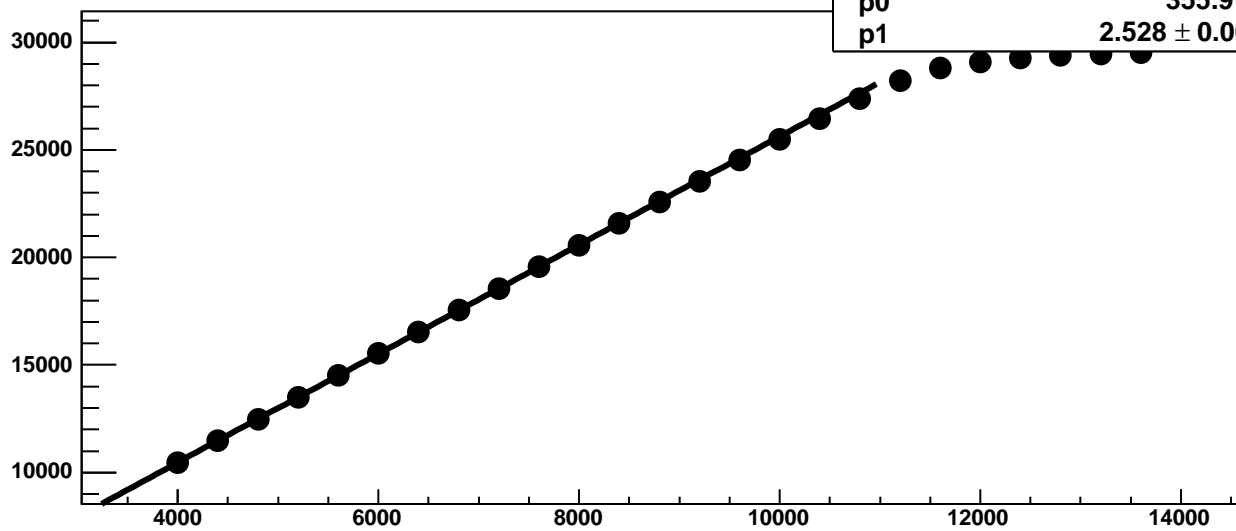
Chip 3, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



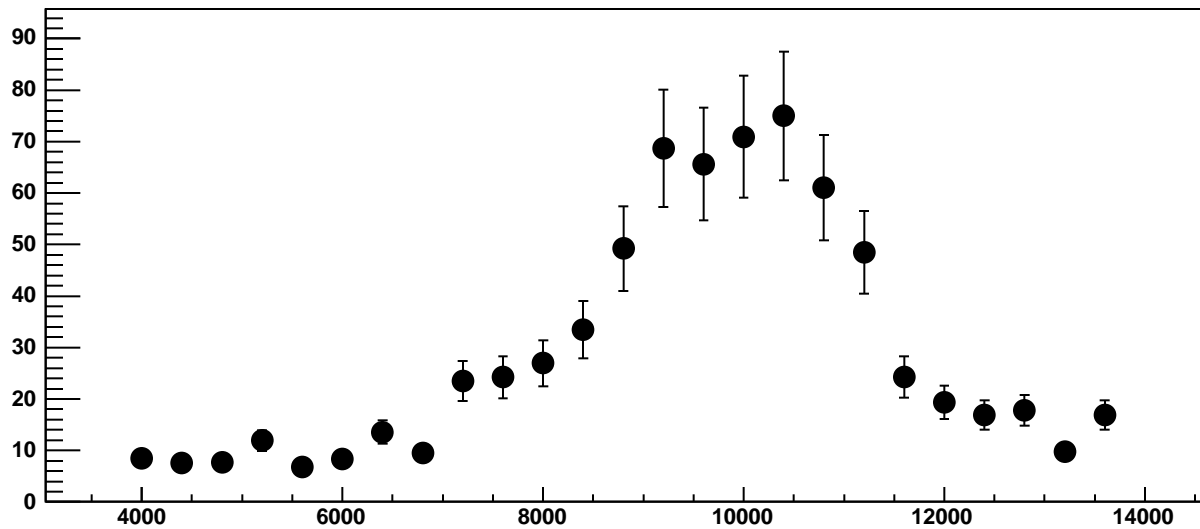
Chip 3, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



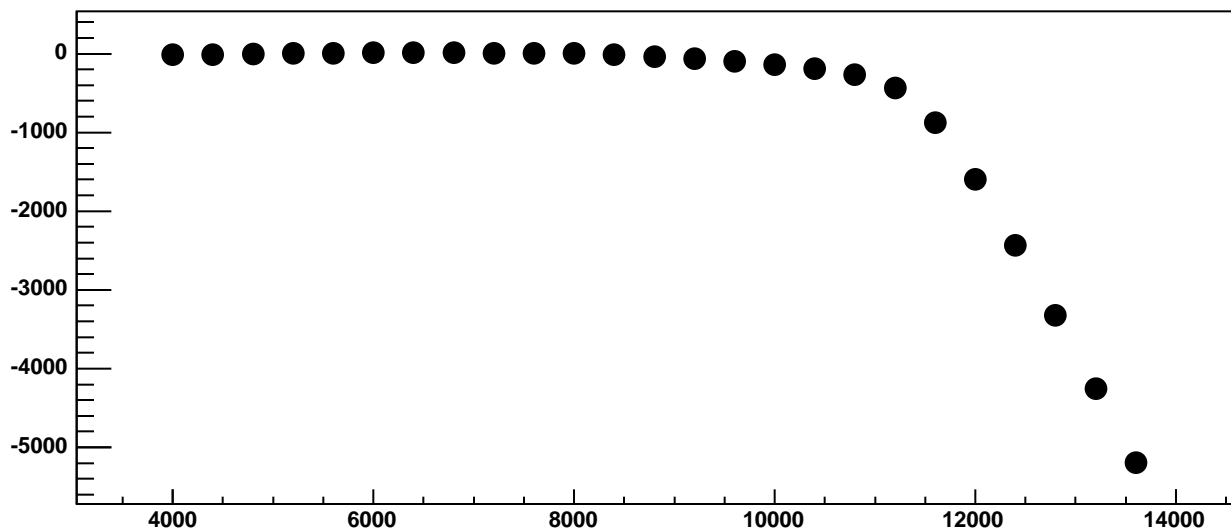
Chip 3, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC



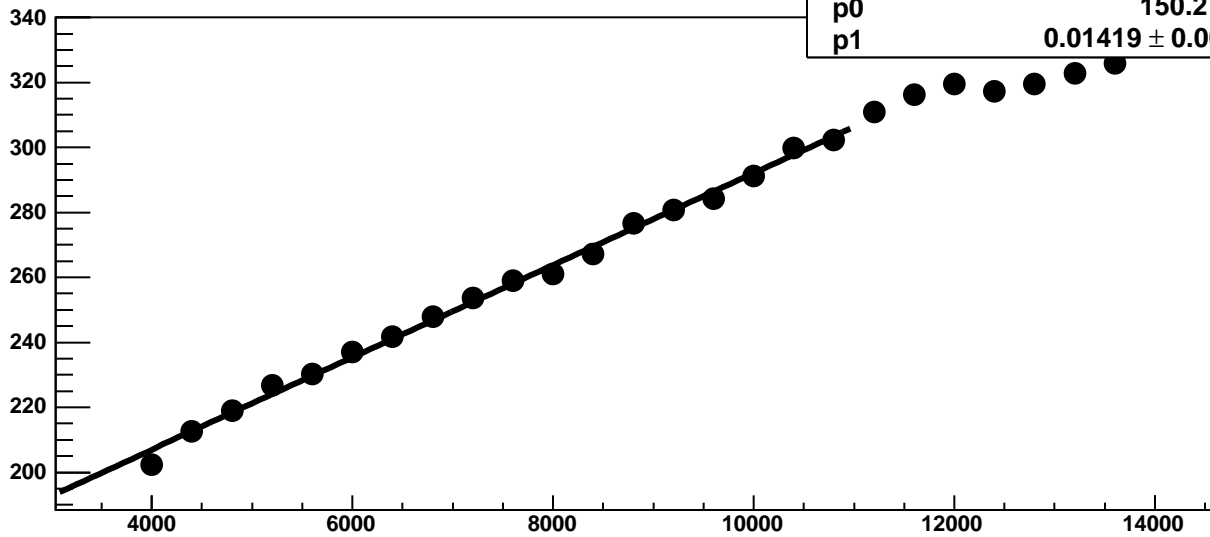
Chip 3, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



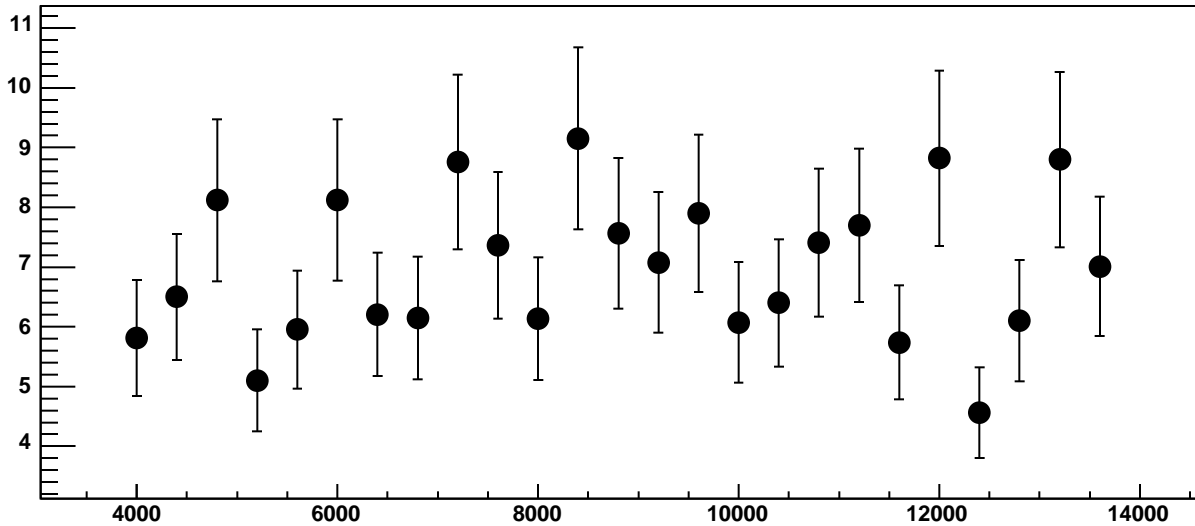
Chip 3, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC



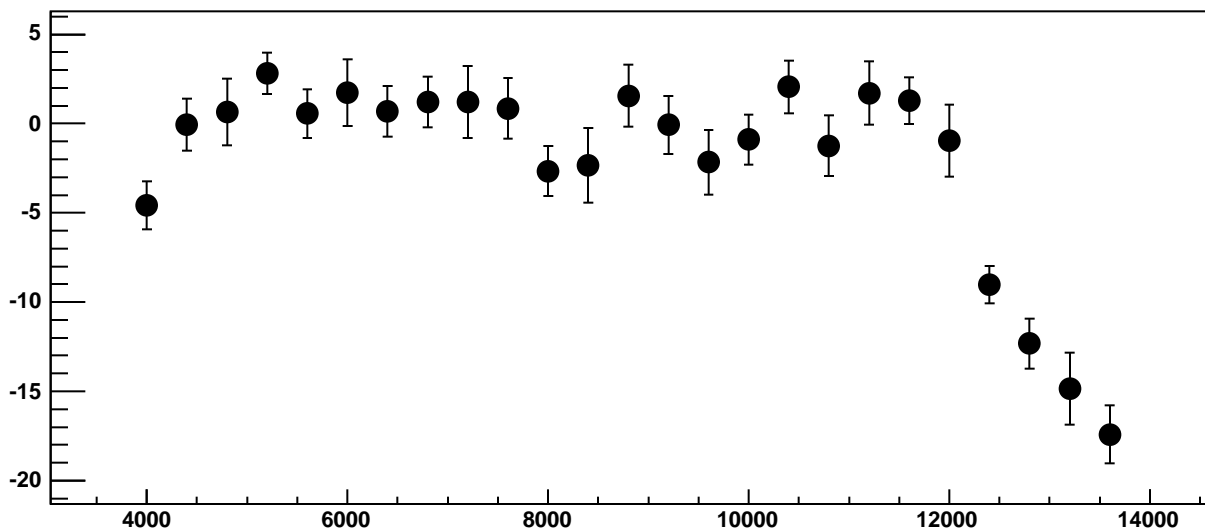
Chip 3, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



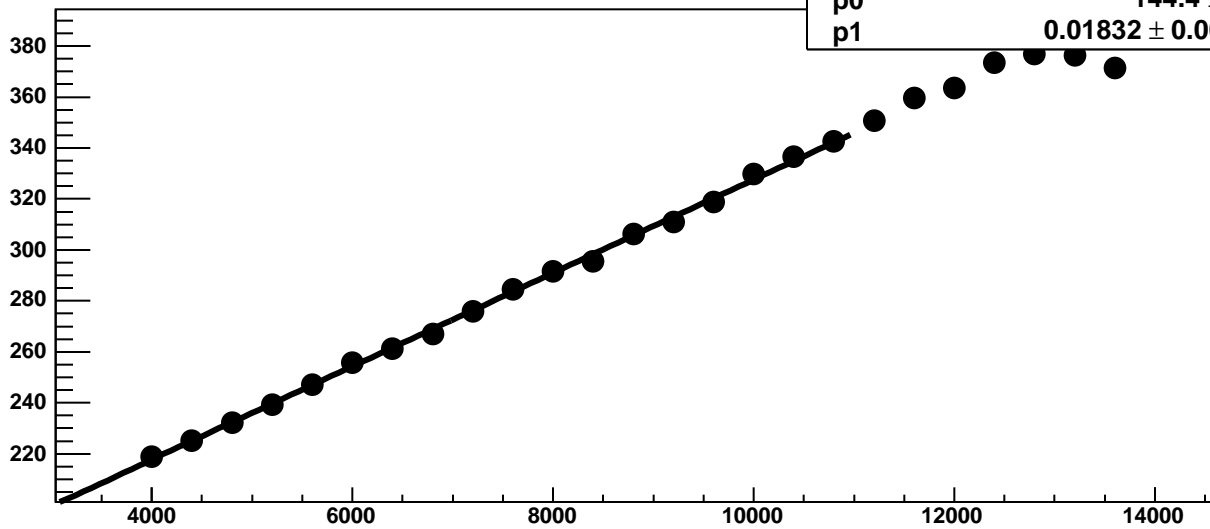
Chip 3, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

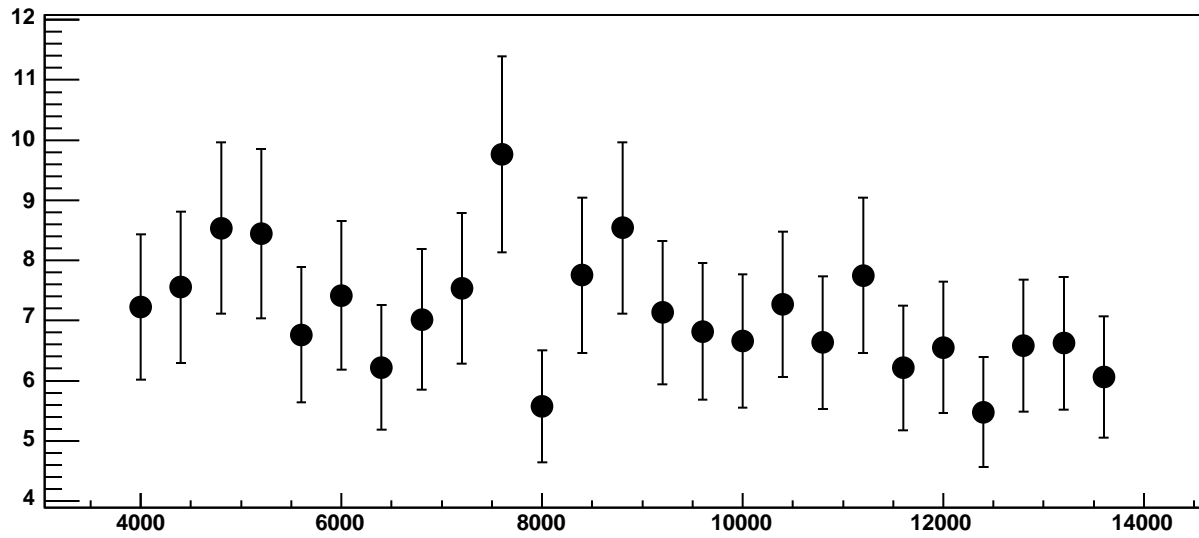


Chip 3, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

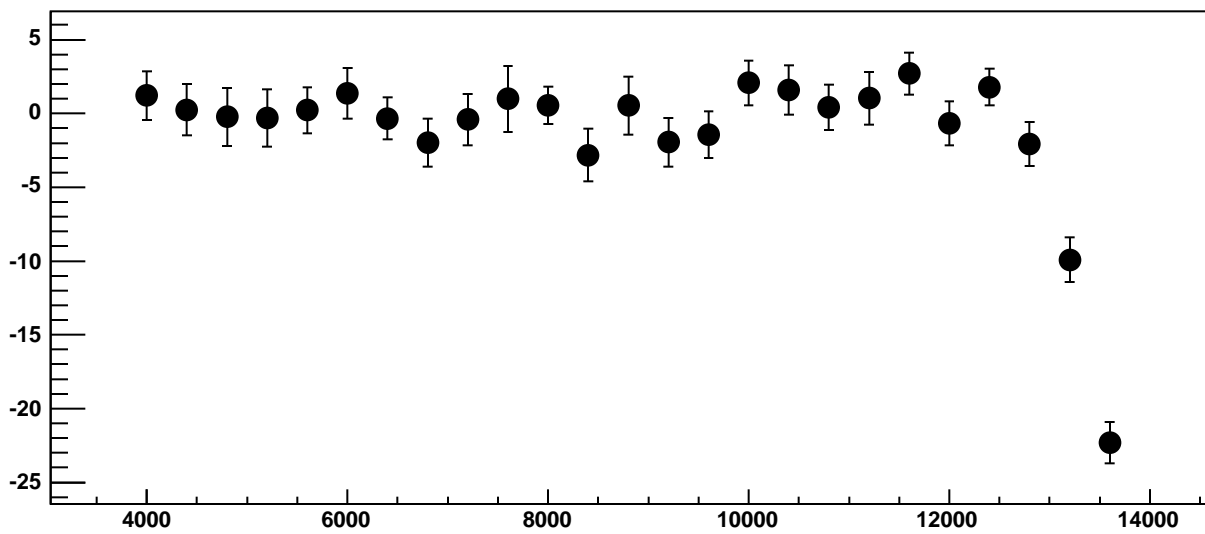


χ^2 / ndf 10.96 / 16
p0 144.4 ± 1.469
p1 0.01832 ± 0.0001883

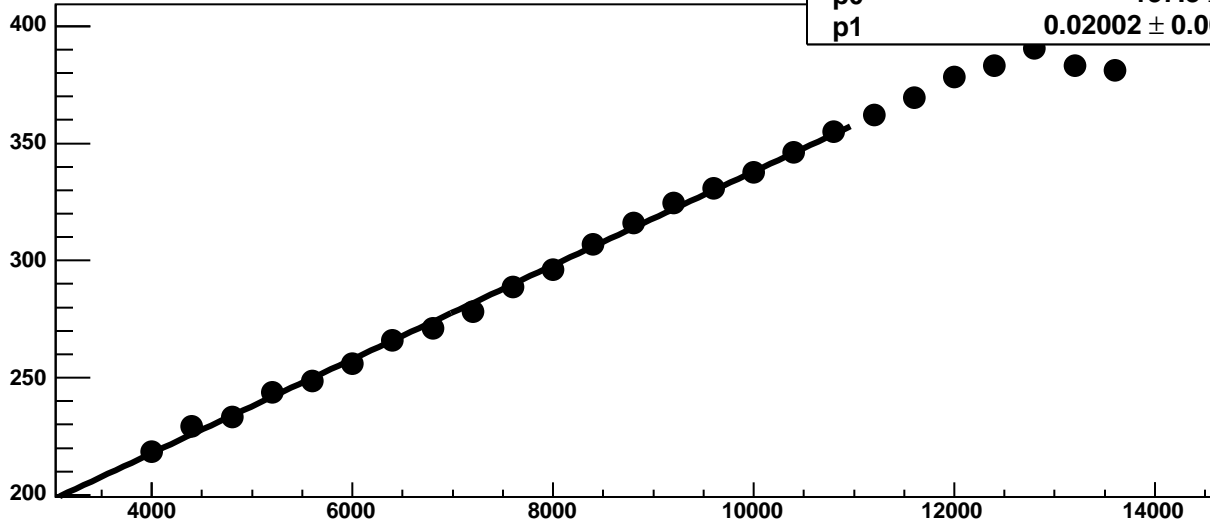
Chip 3, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

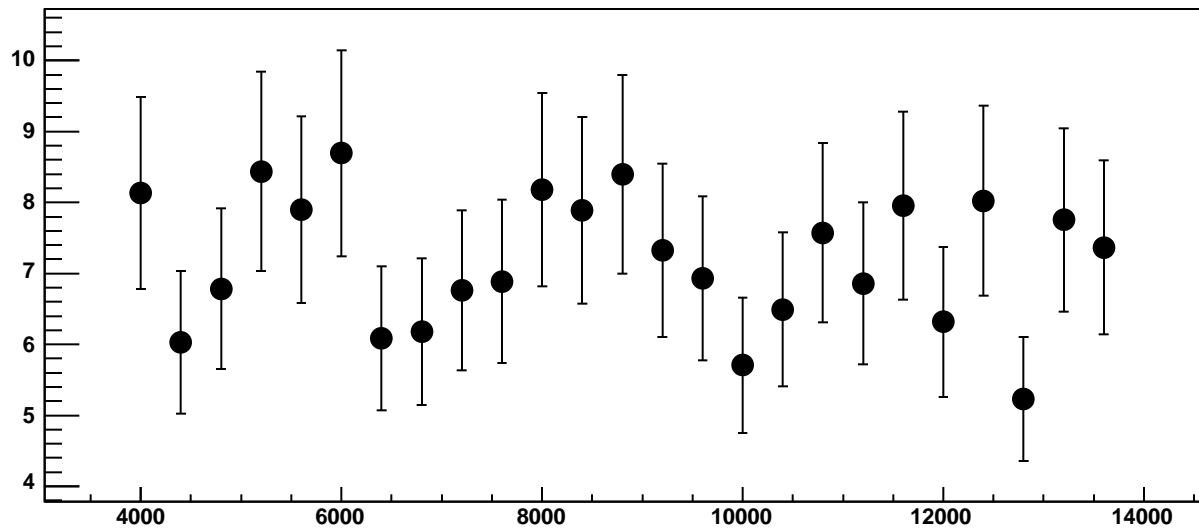


Chip 3, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

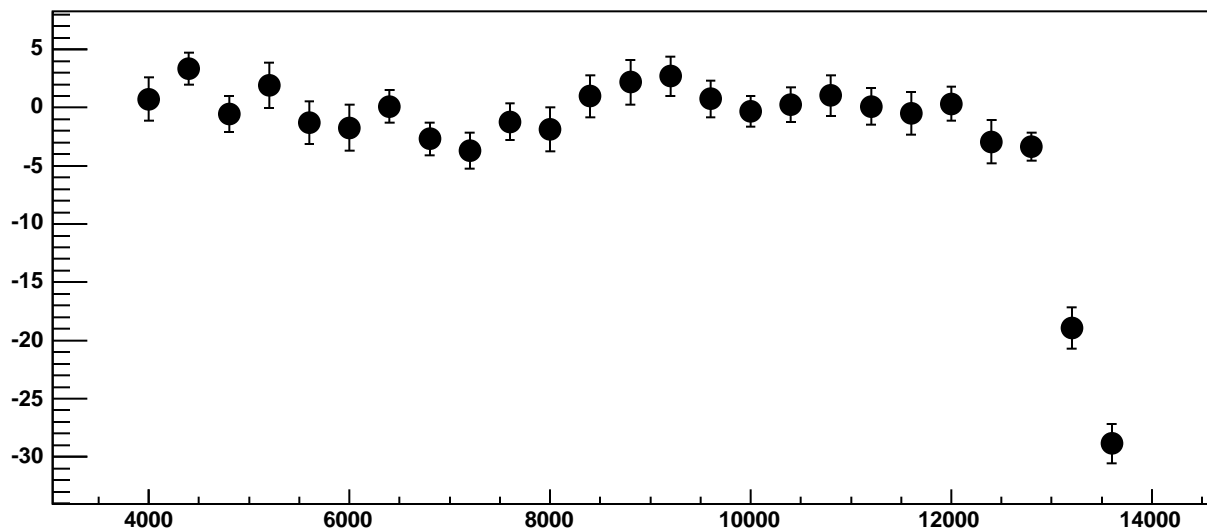


χ^2 / ndf 24.04 / 16
p0 137.8 ± 1.413
p1 0.02002 ± 0.0001823

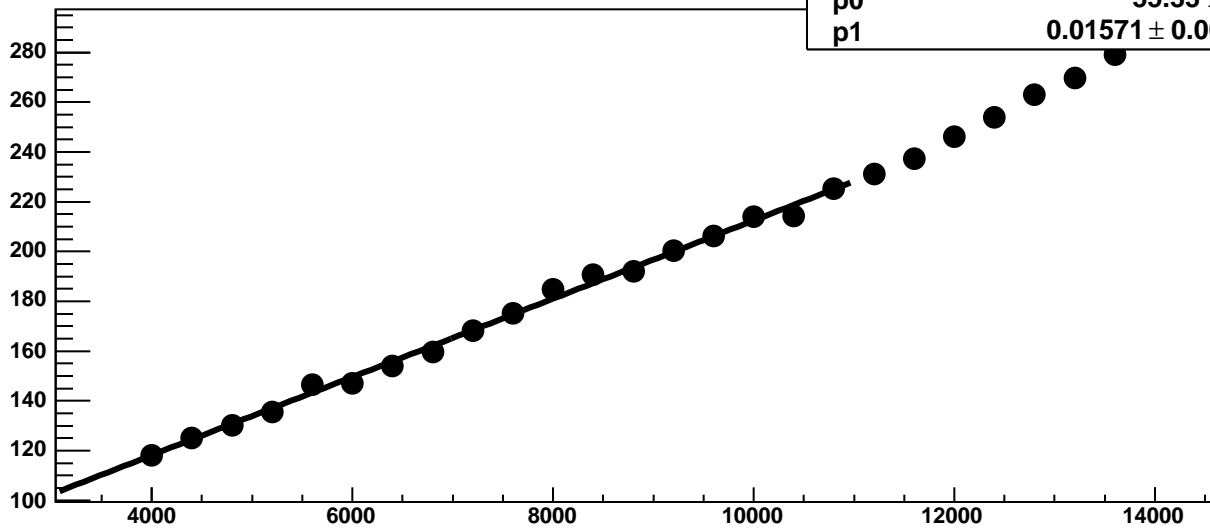
Chip 3, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

38.07 / 16

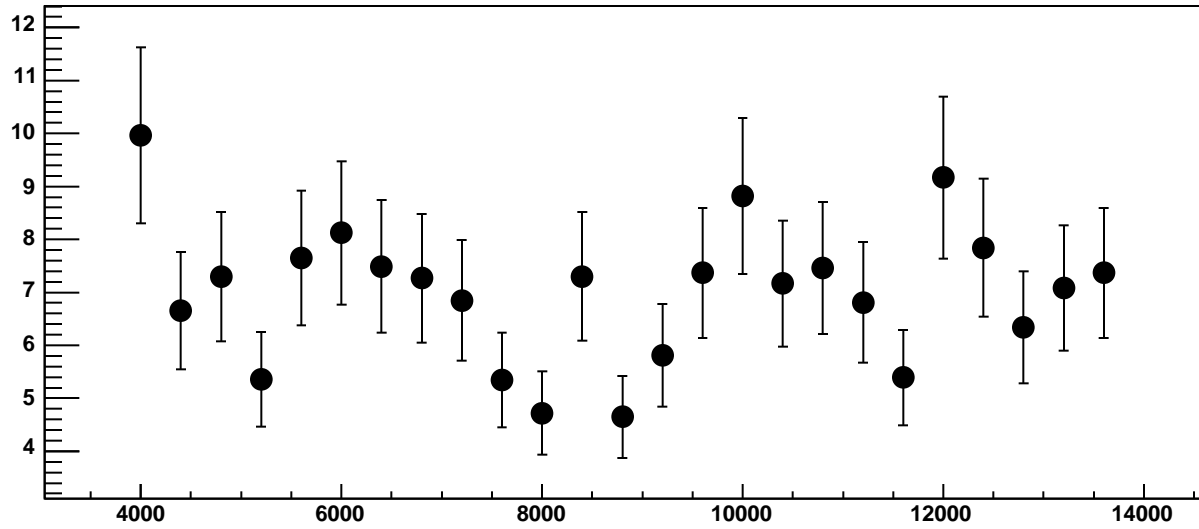
p0

55.33 ± 1.452

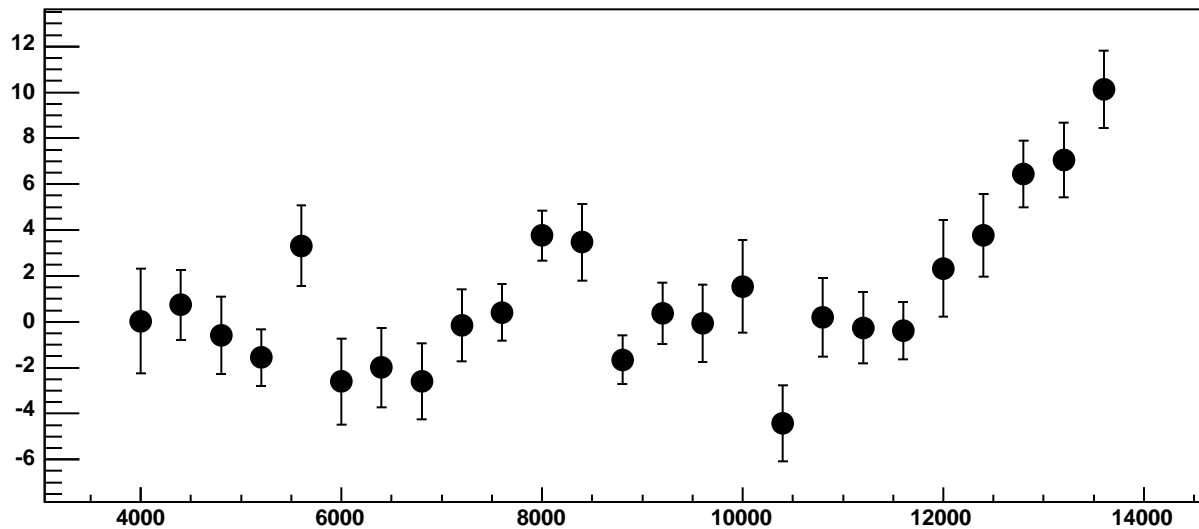
p1

0.01571 ± 0.0001872

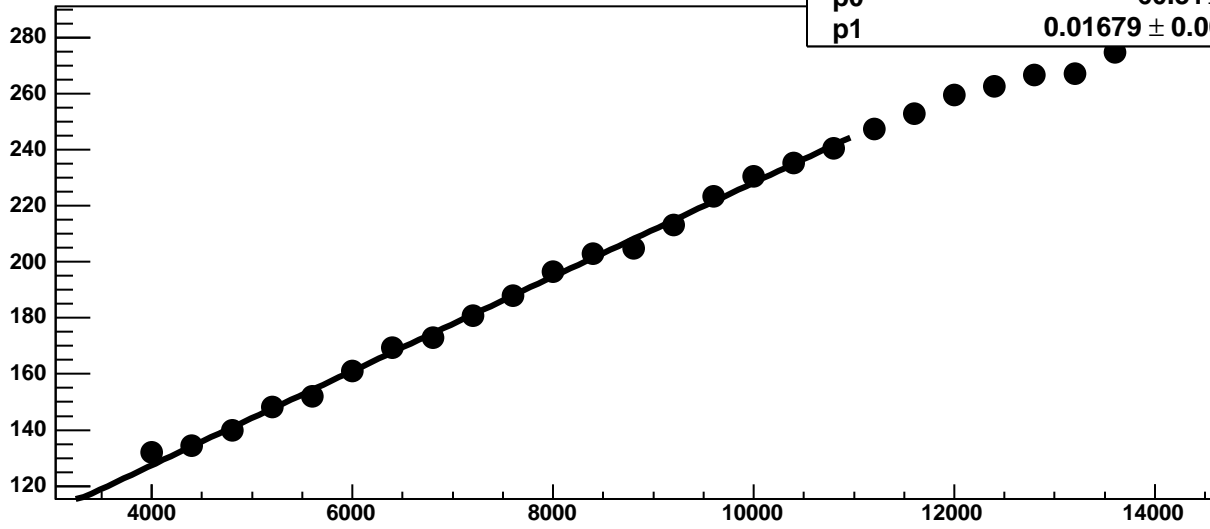
Chip 3, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

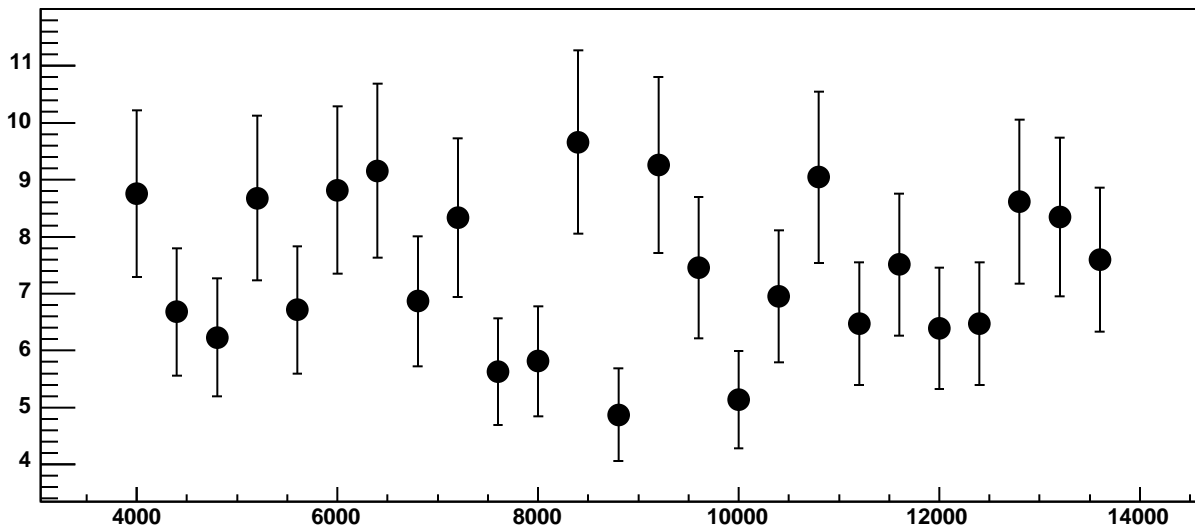


Chip 3, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

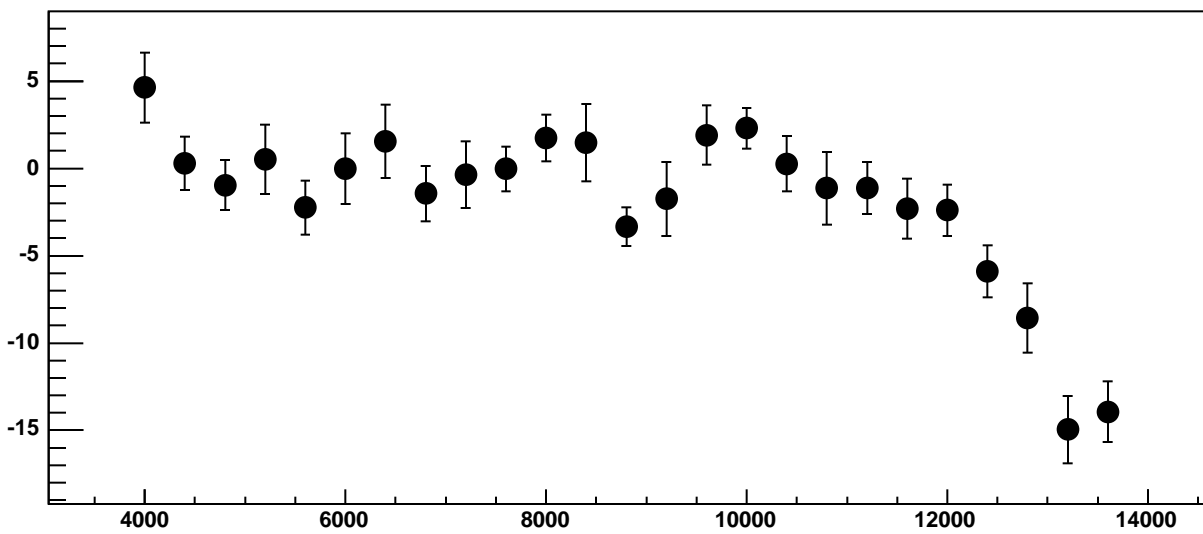


χ^2 / ndf 26.48 / 16
p0 60.31 ± 1.462
p1 0.01679 ± 0.0001866

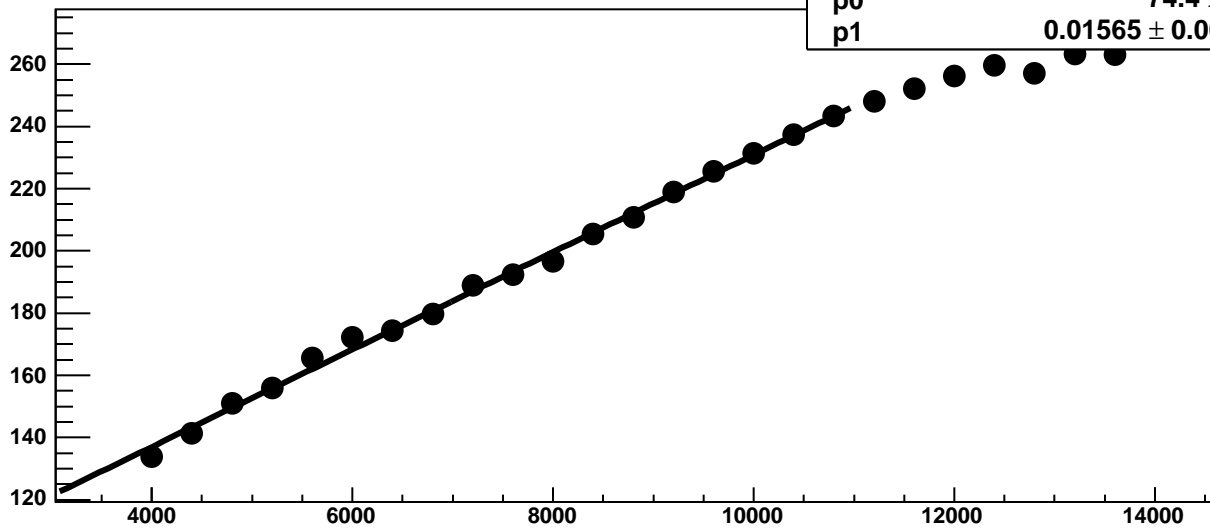
Chip 3, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



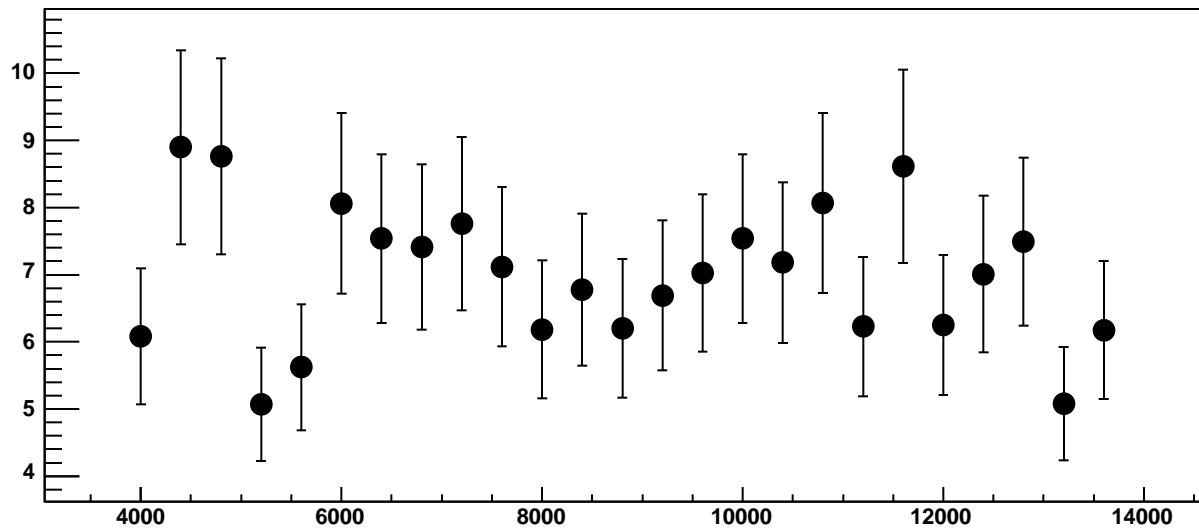
Chip 3, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC



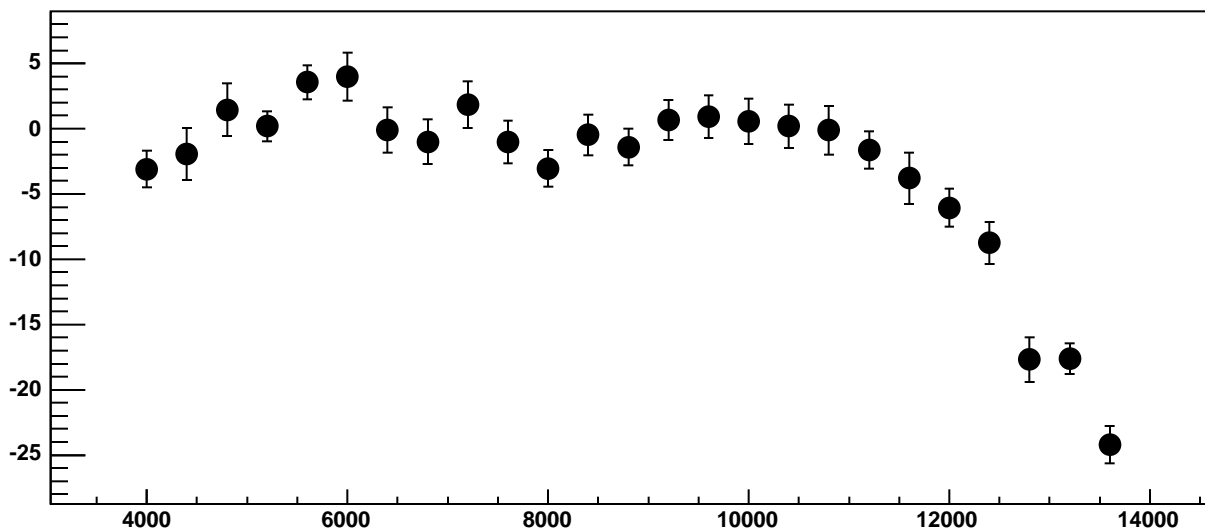
Chip 3, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



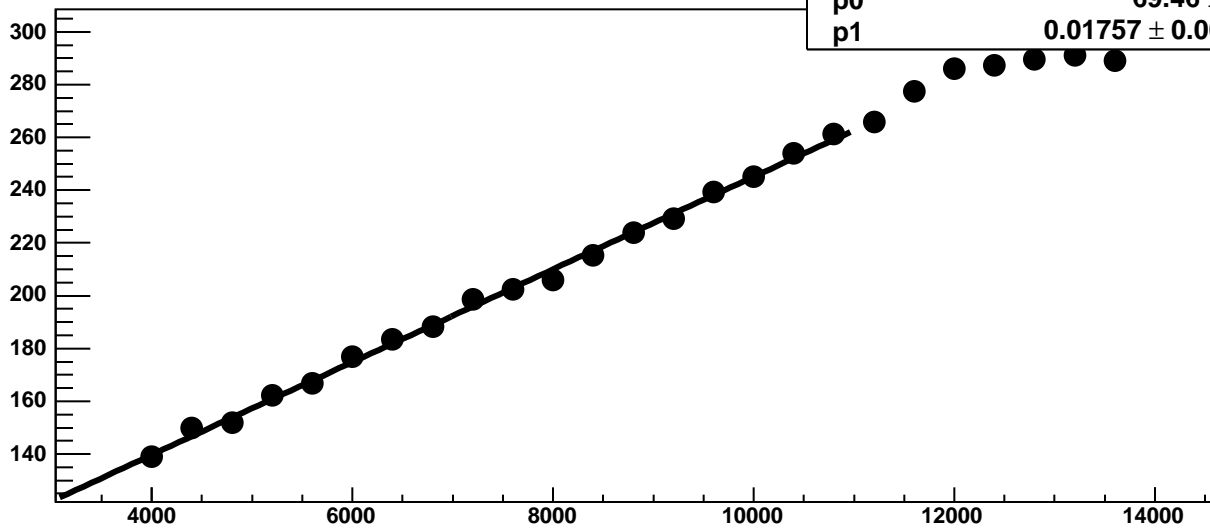
Chip 3, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



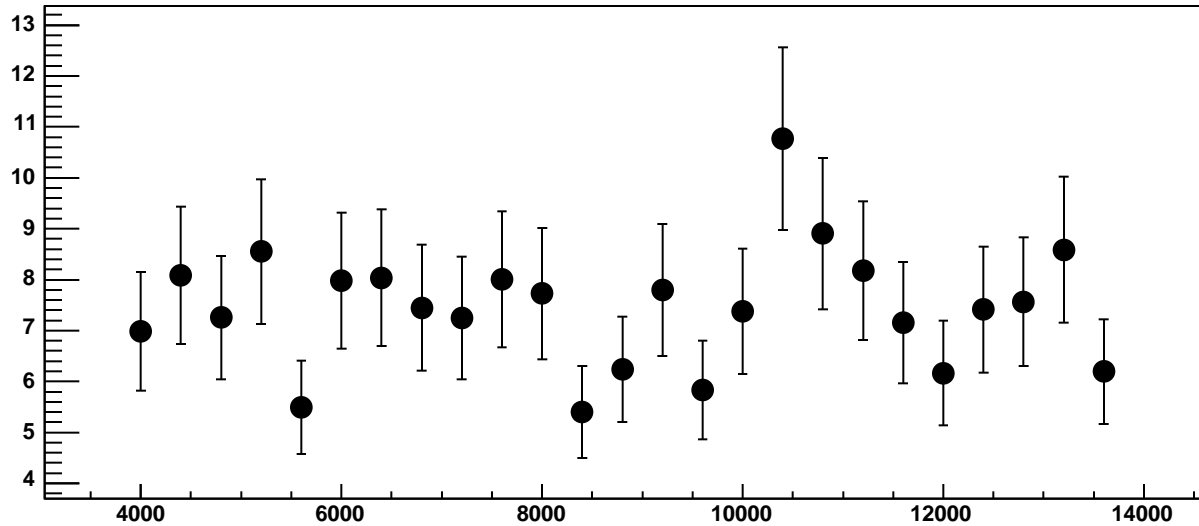
Chip 3, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



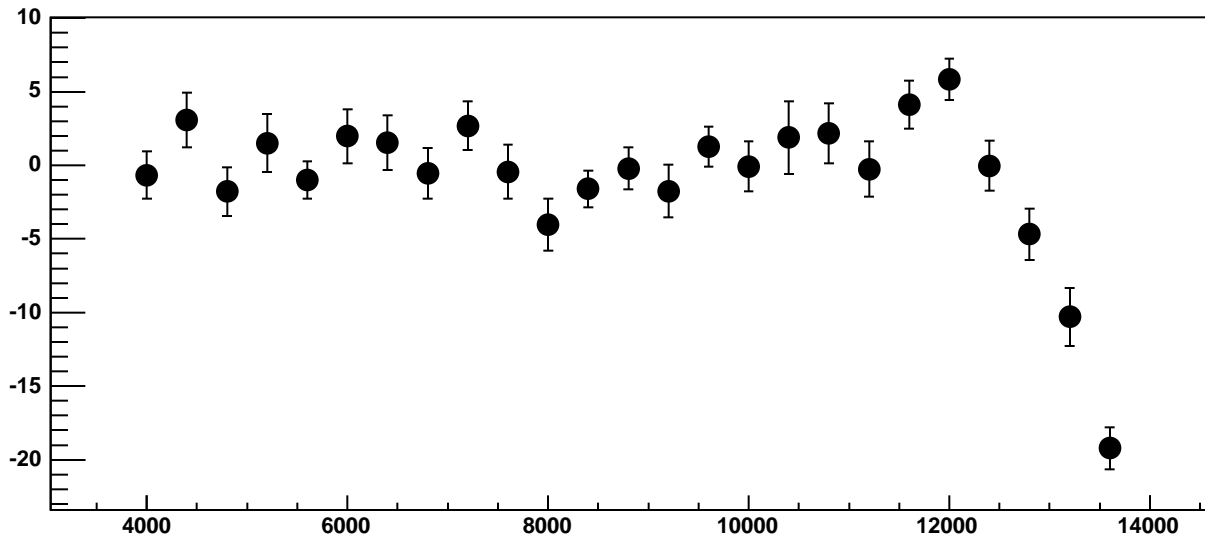
Chip 3, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



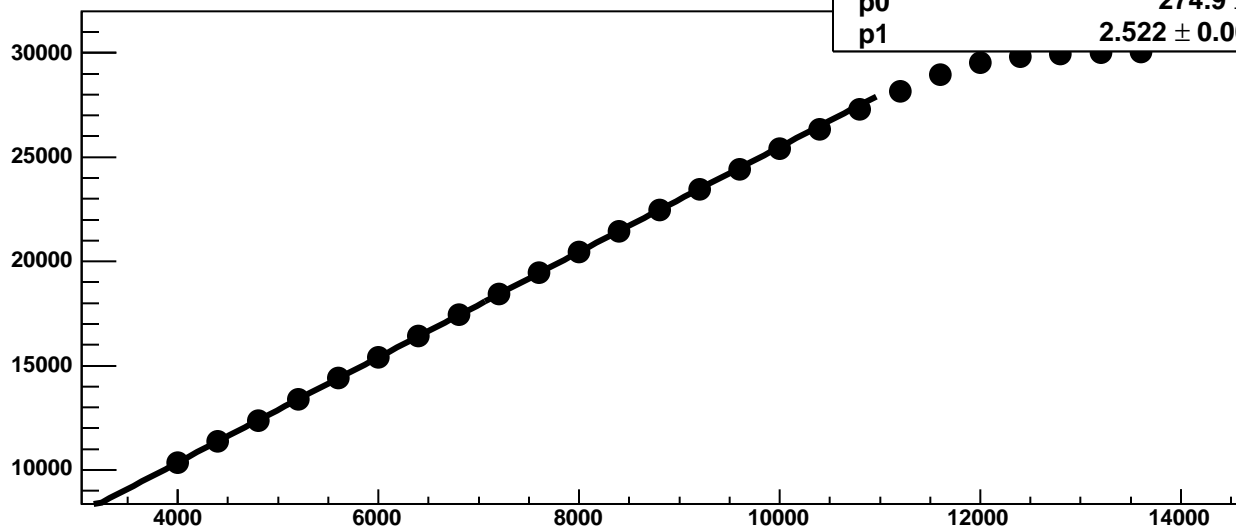
Chip 3, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



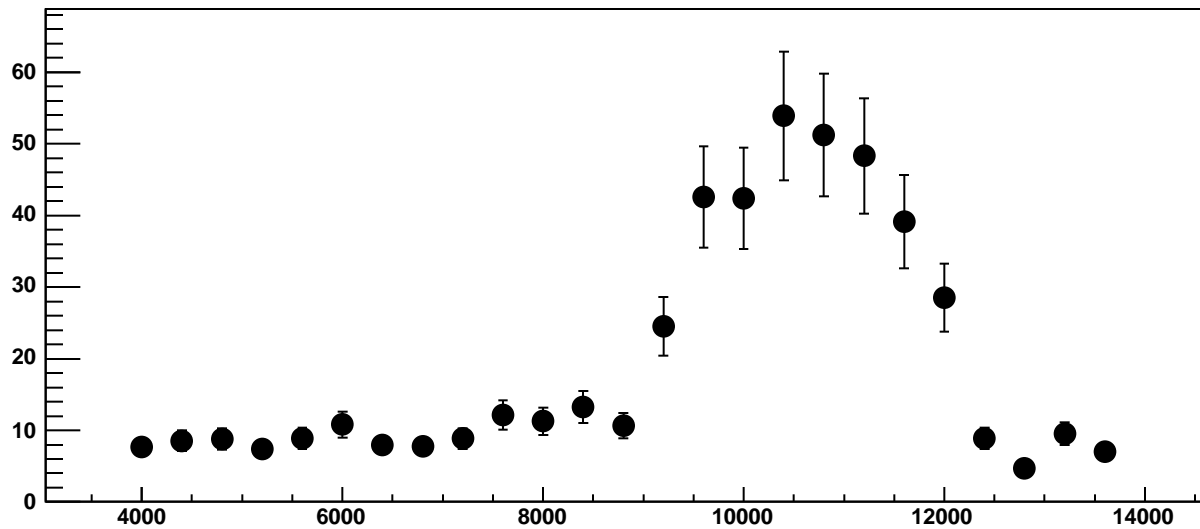
Chip 3, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



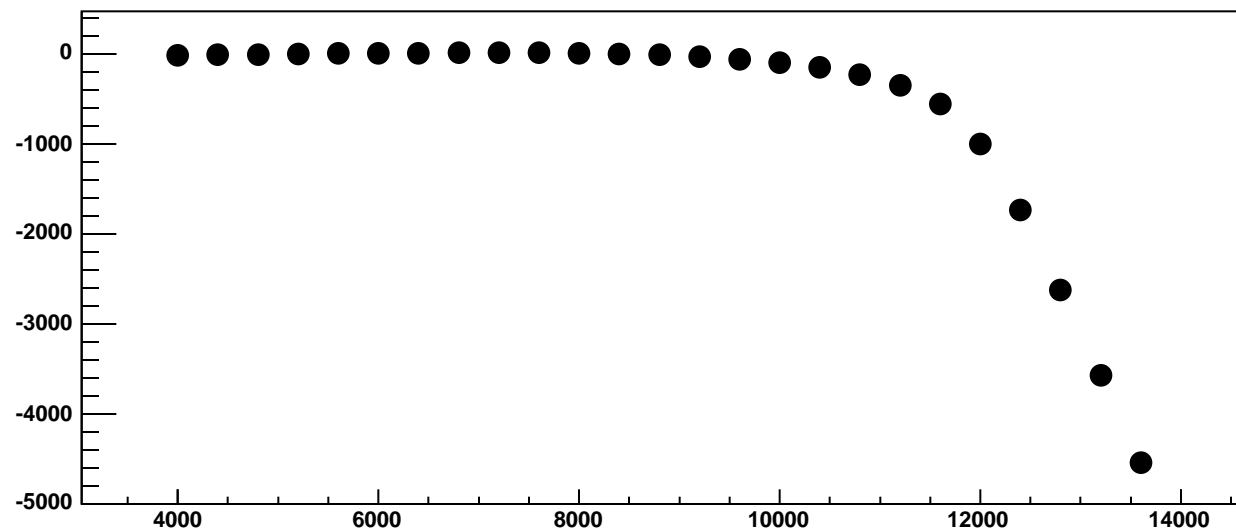
Chip 3, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC



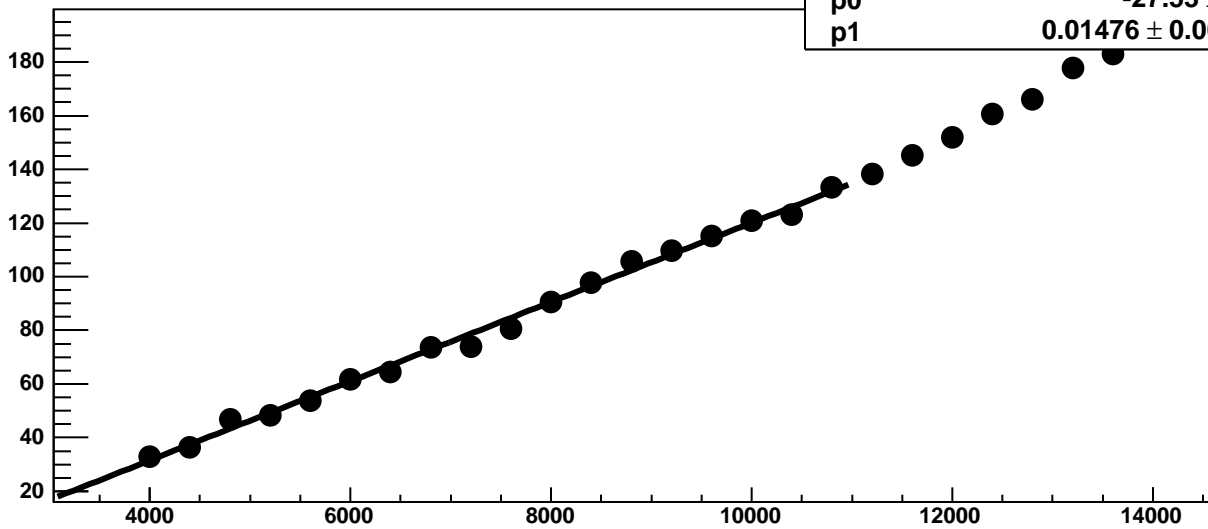
Chip 3, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

32.61 / 16

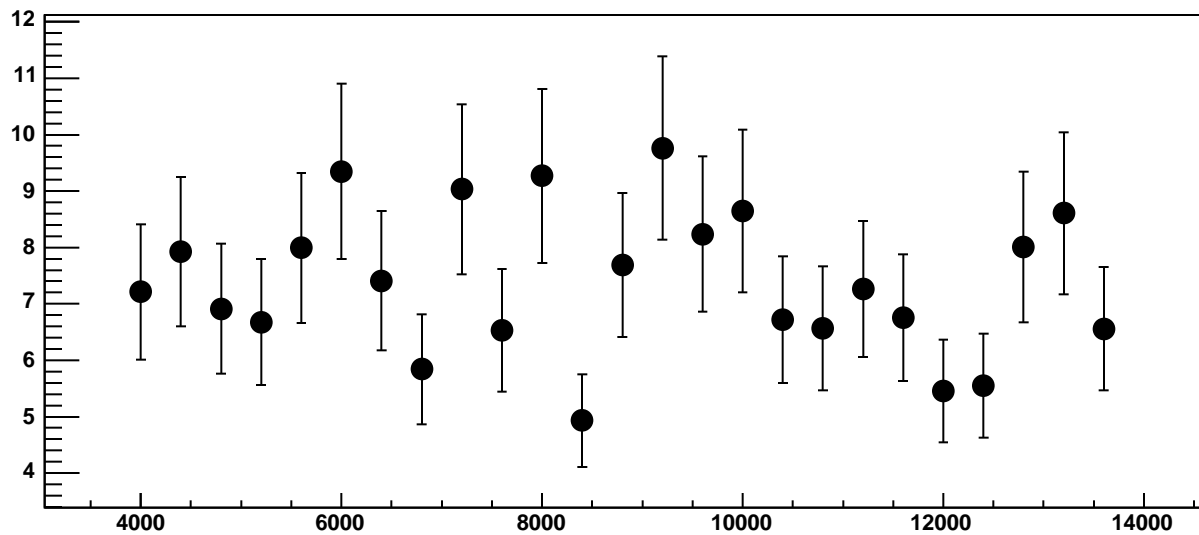
p0

-27.53 ± 1.463

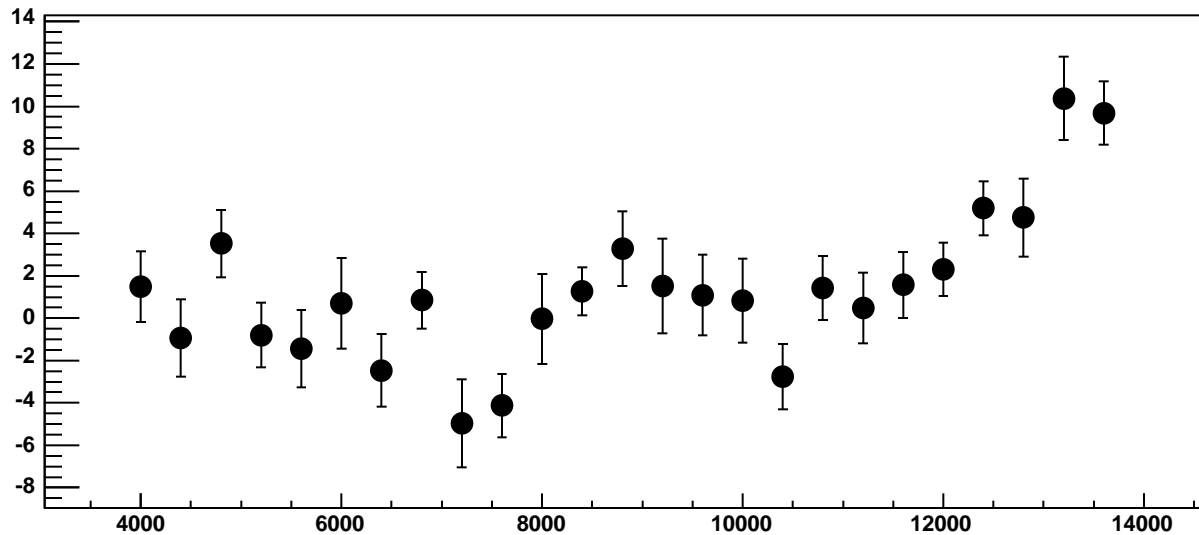
p1

0.01476 ± 0.0001899

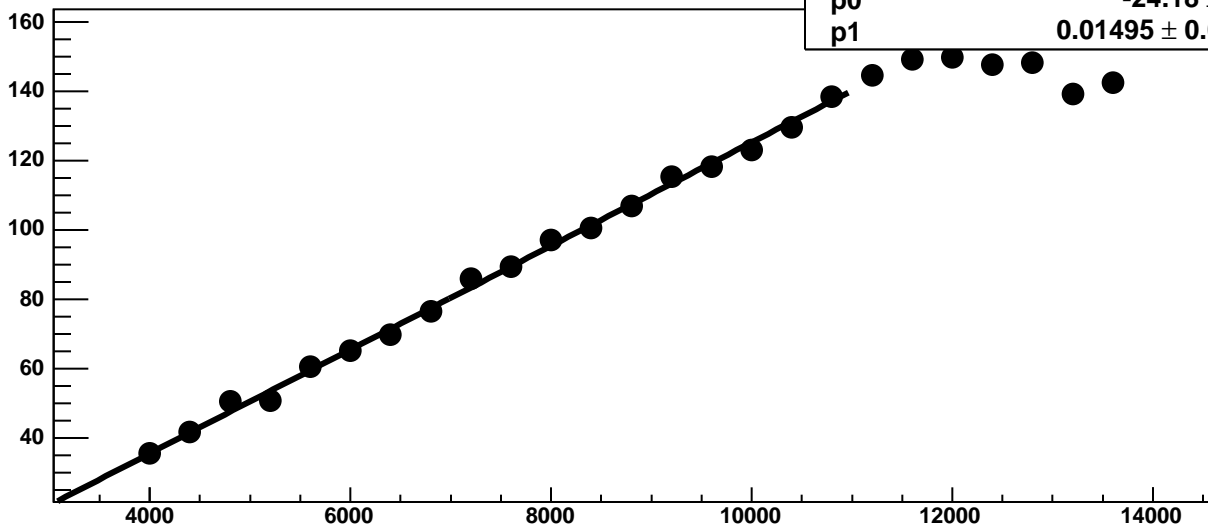
Chip 3, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



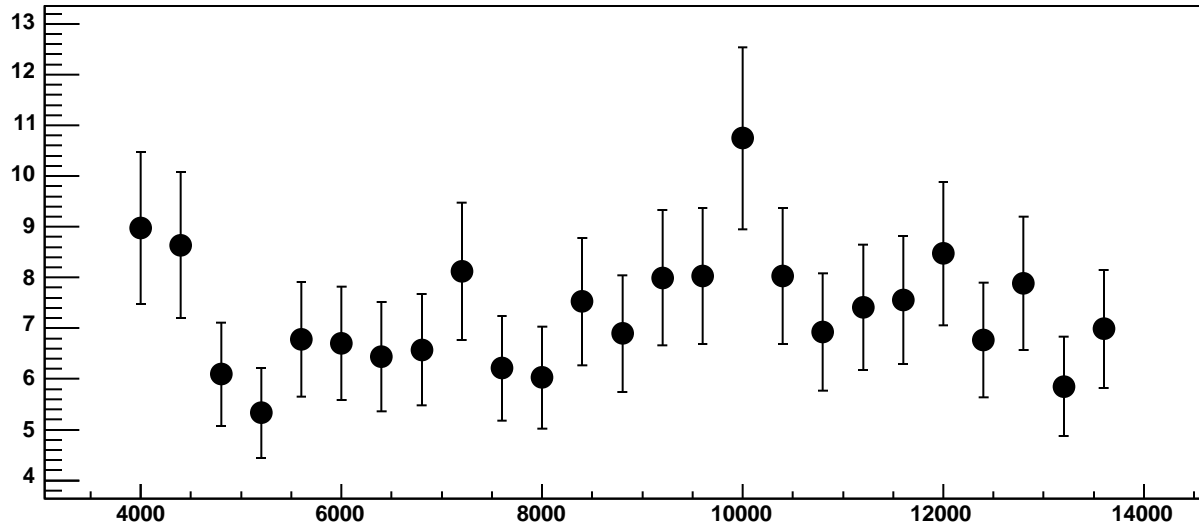
Chip 3, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



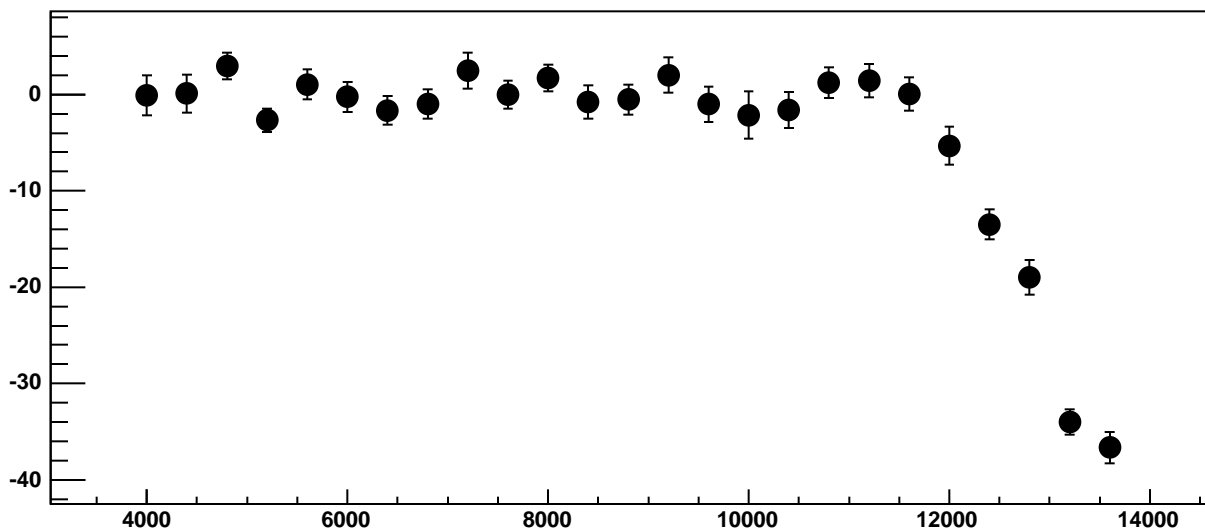
Chip 3, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



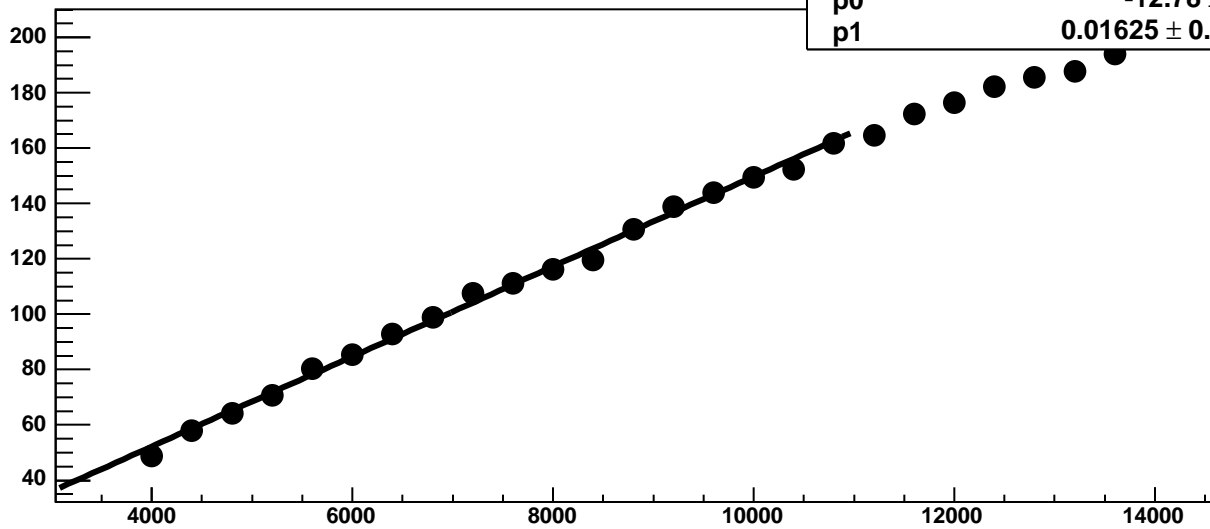
Chip 3, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

23.27 / 16

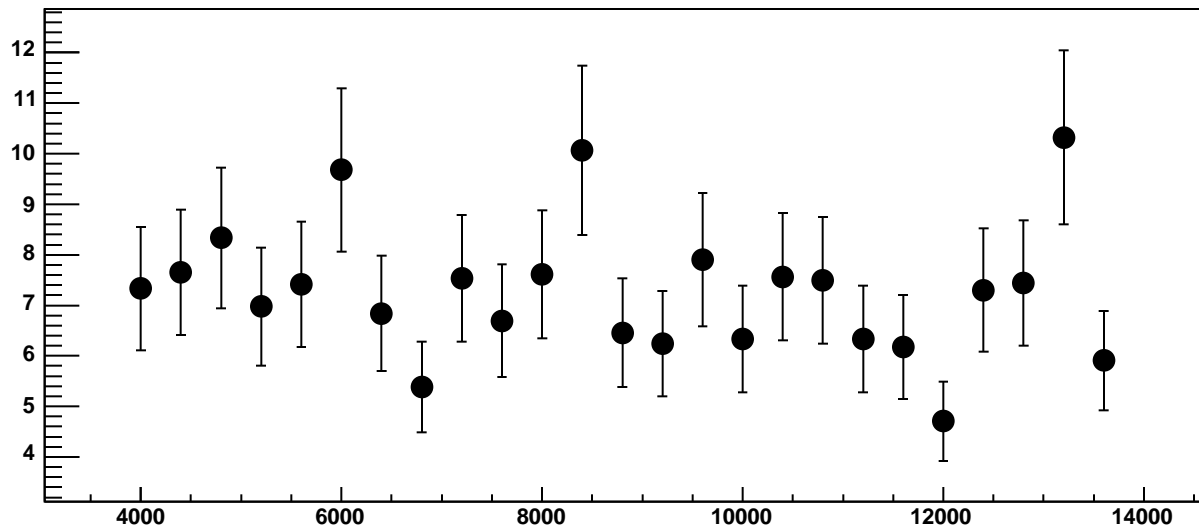
p0

-12.78 ± 1.479

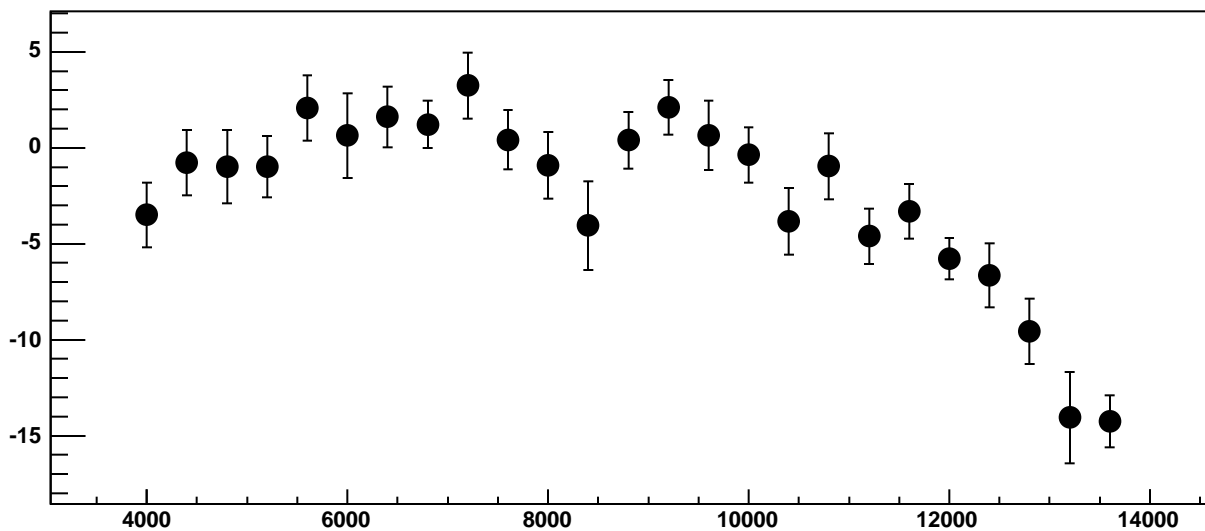
p1

0.01625 ± 0.000191

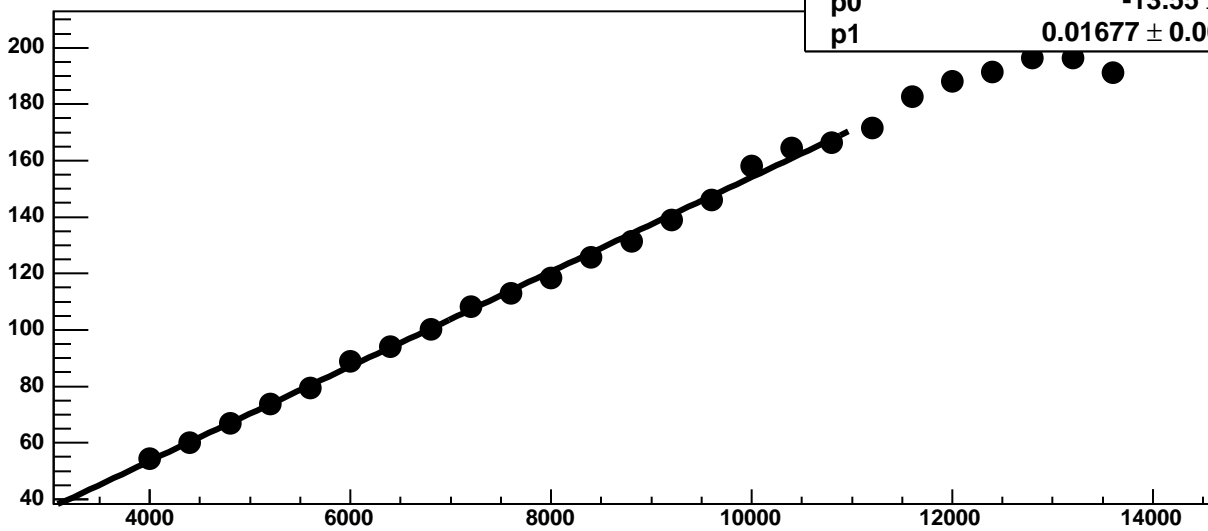
Chip 3, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



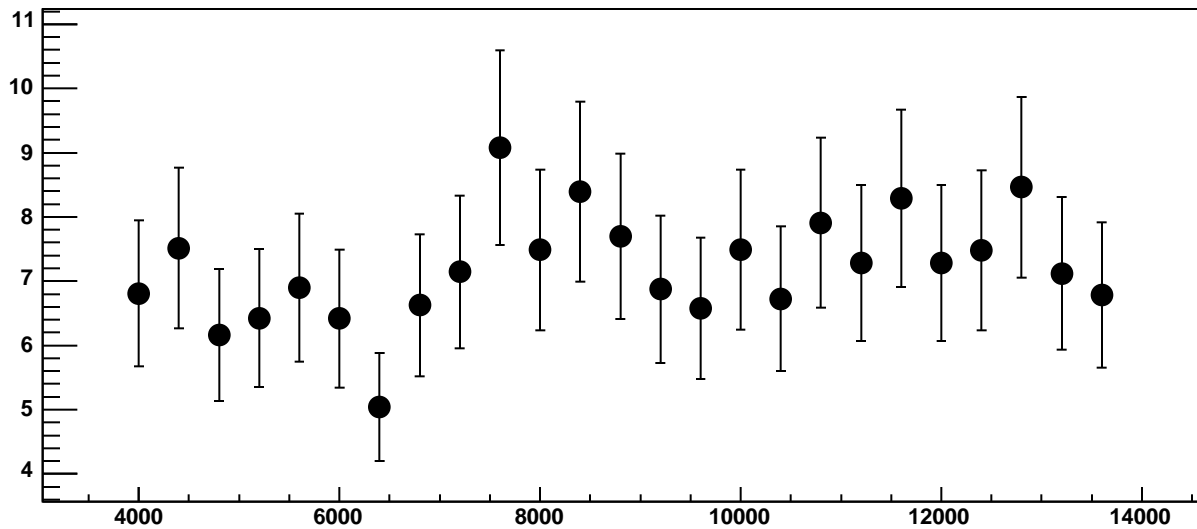
Chip 3, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC



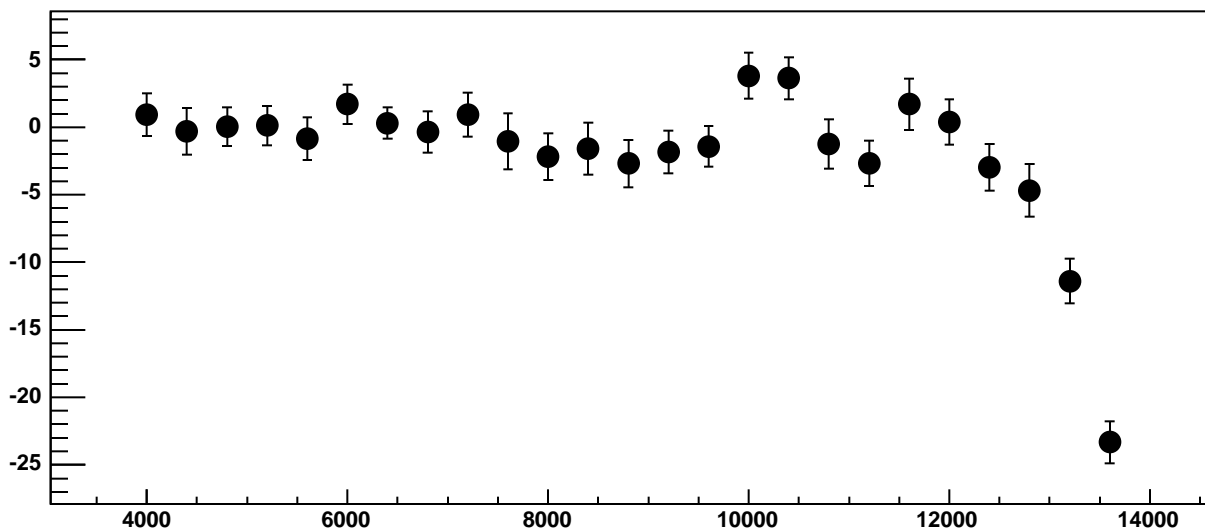
Chip 3, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



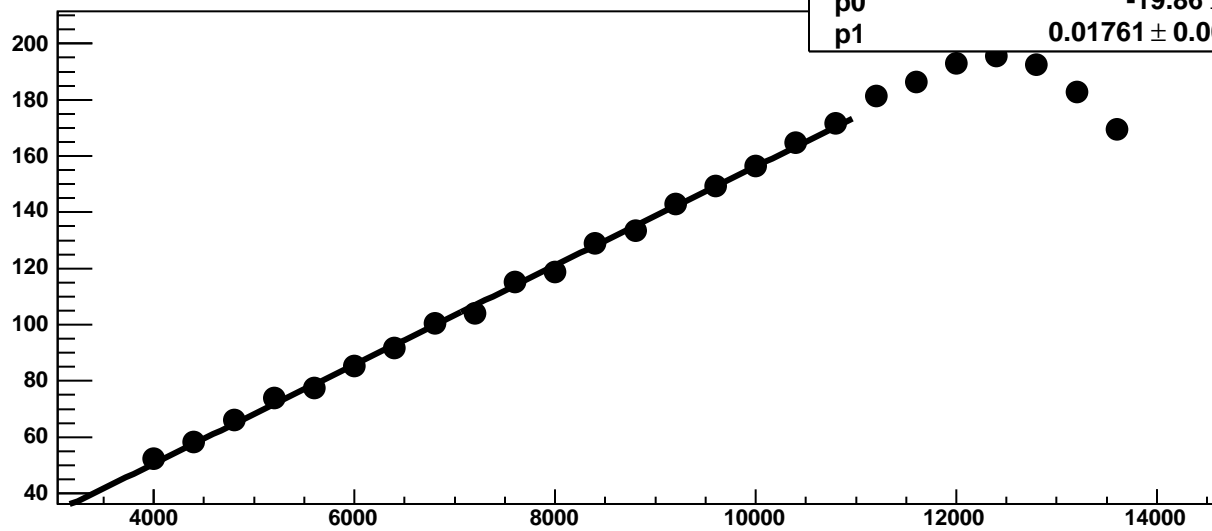
Chip 3, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



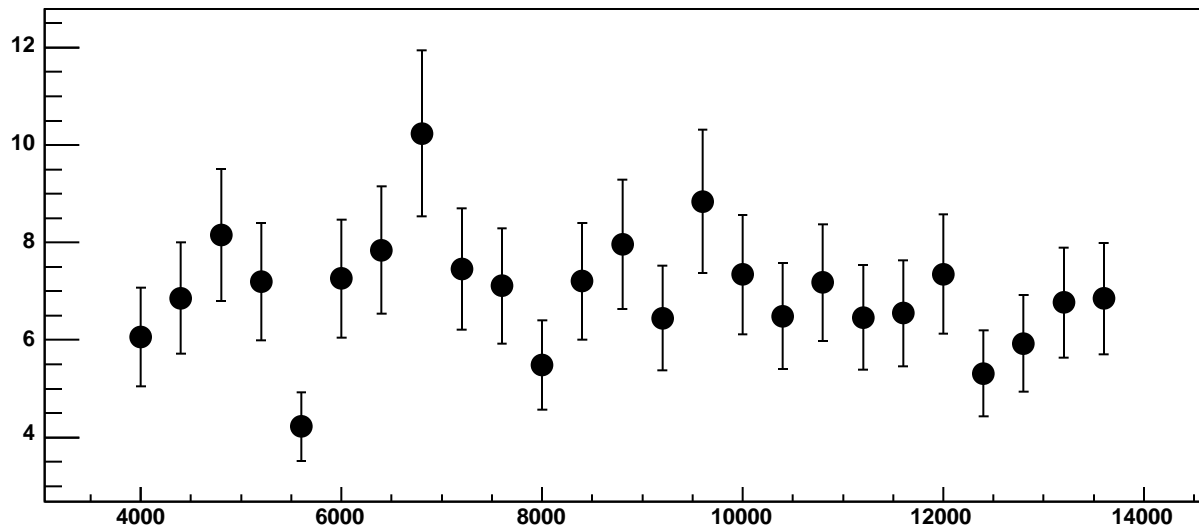
Chip 3, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



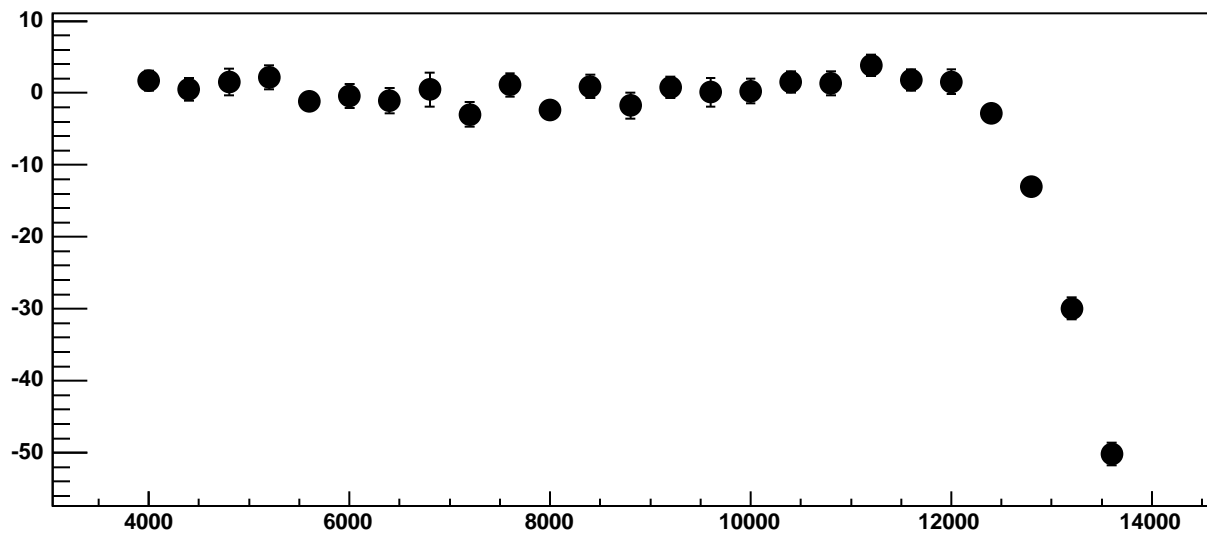
Chip 3, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



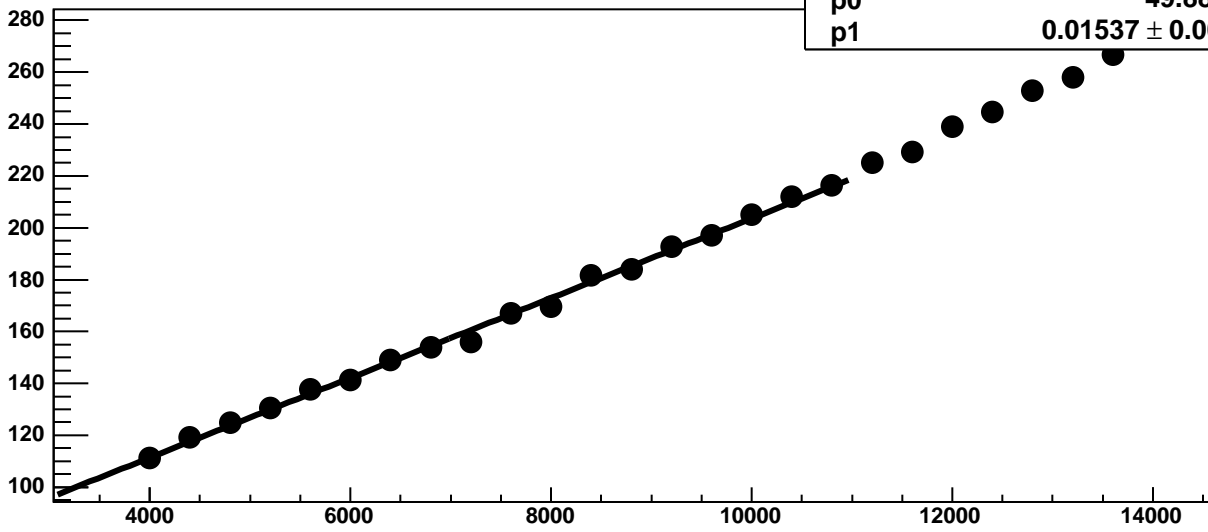
Chip 3, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

21.42 / 16

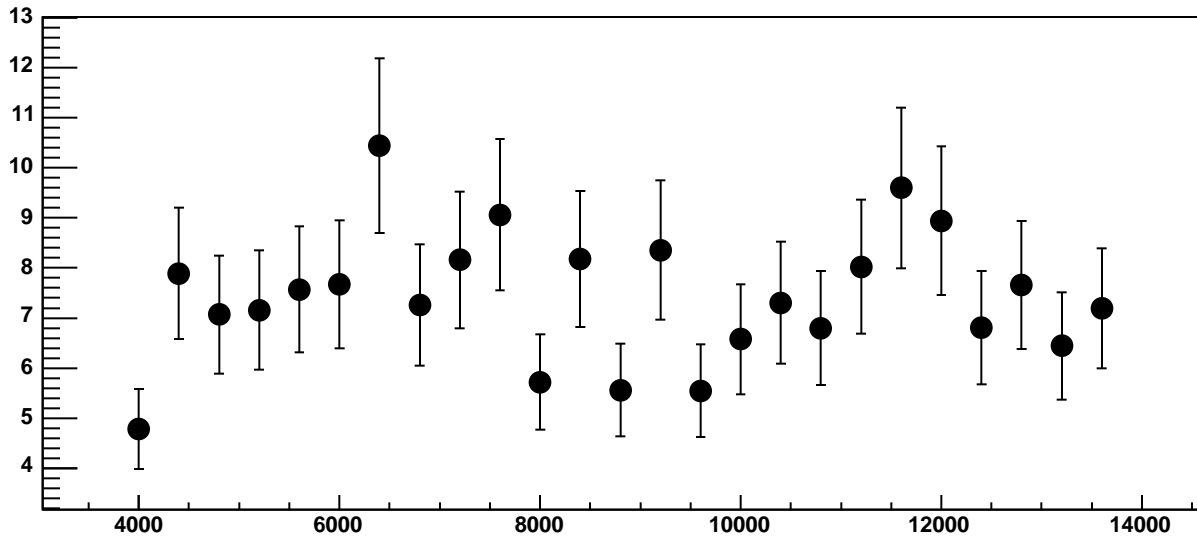
p0

49.88 ± 1.3

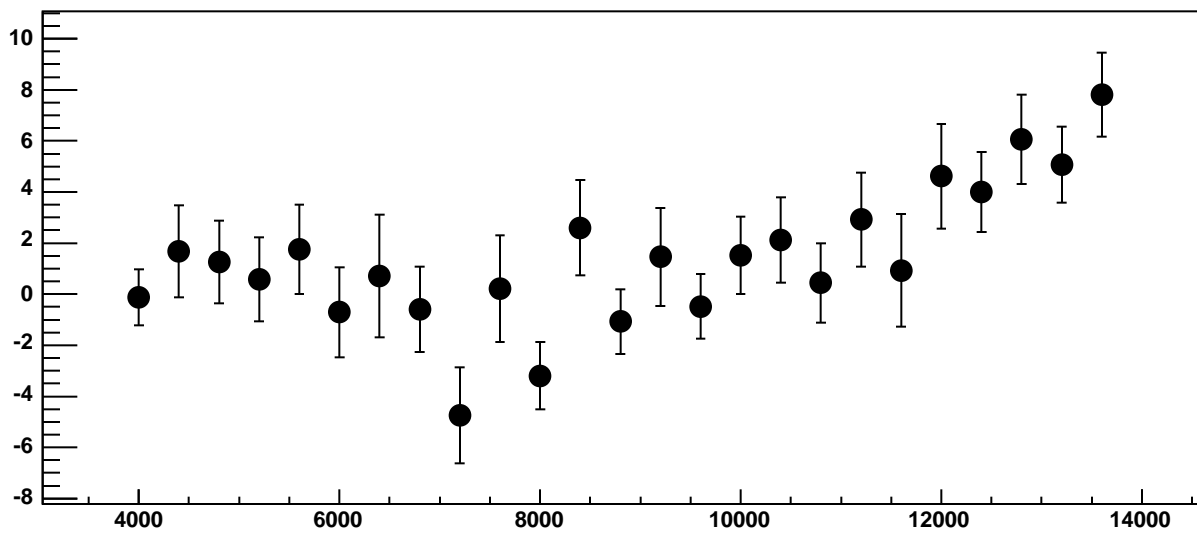
p1

0.01537 ± 0.0001685

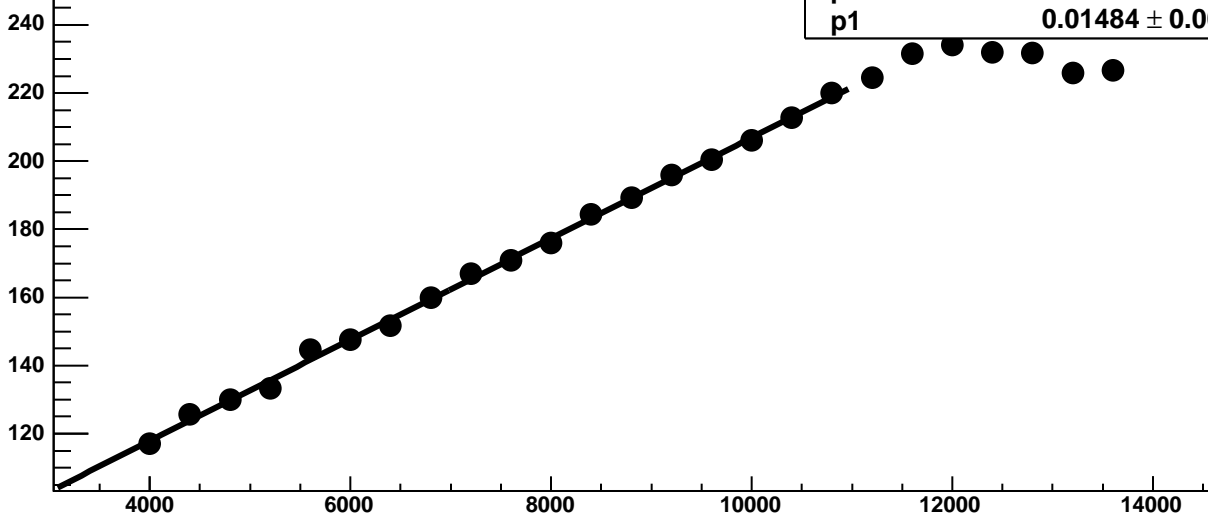
Chip 3, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



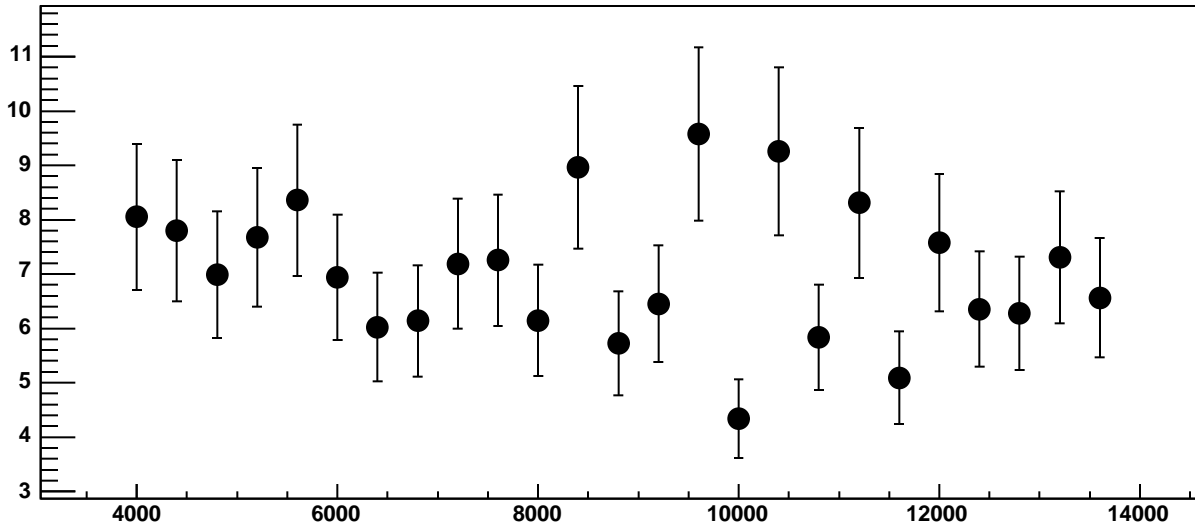
Chip 3, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



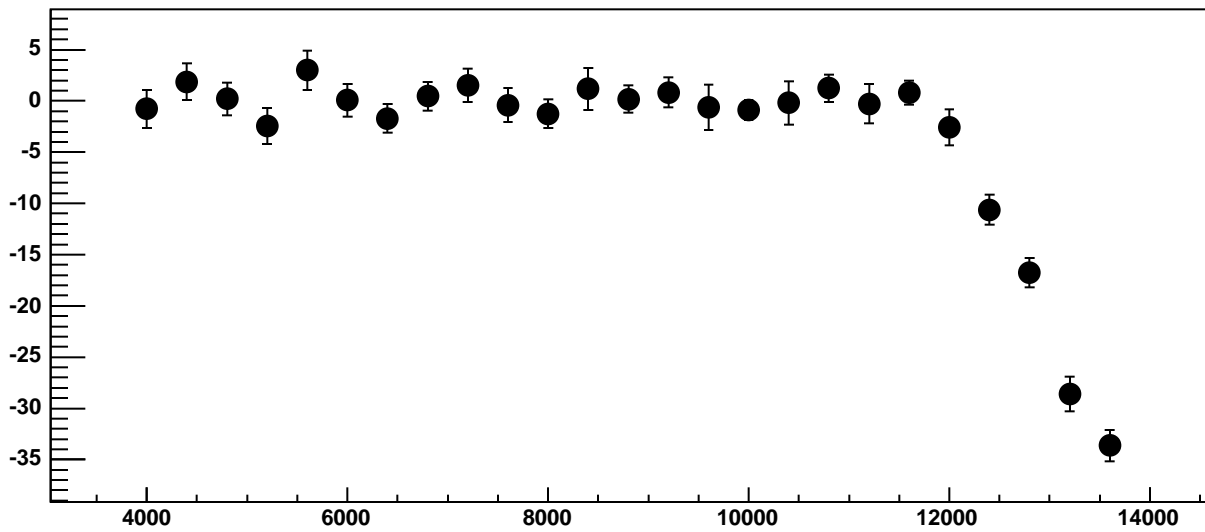
Chip 3, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



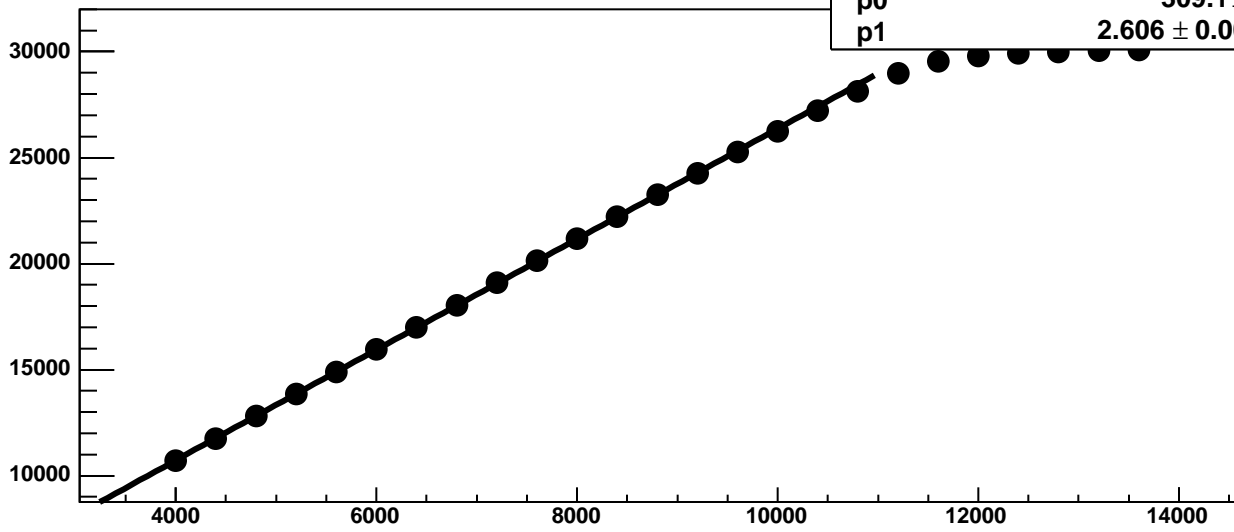
Chip 3, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



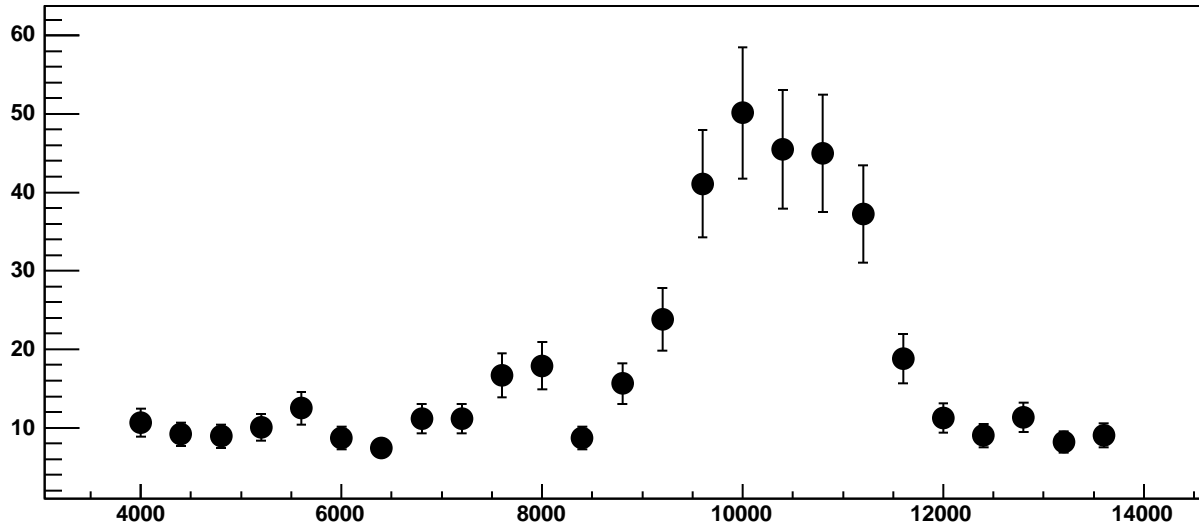
Chip 3, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



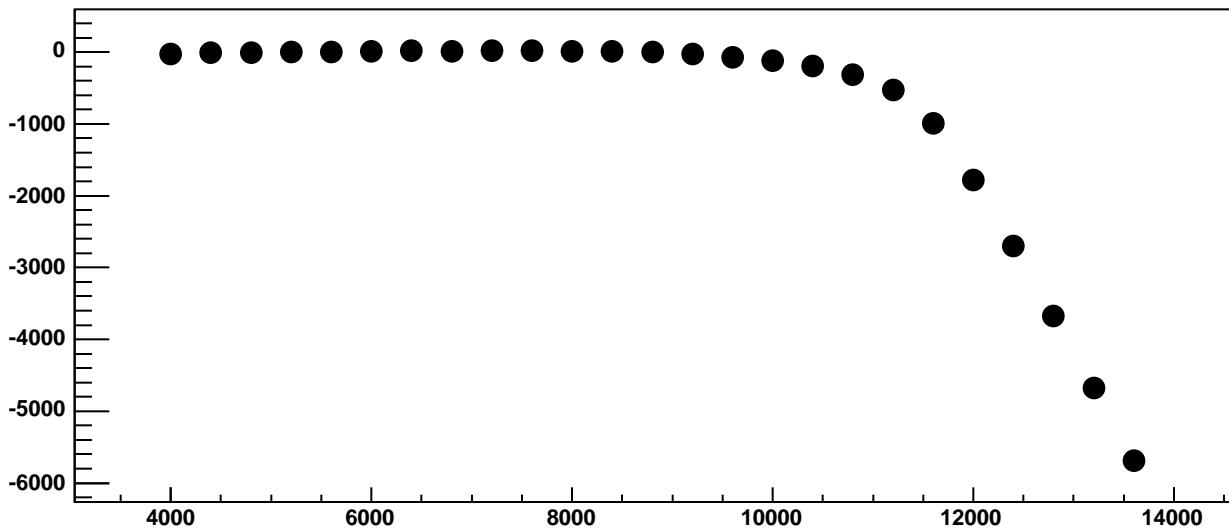
Chip 3, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC



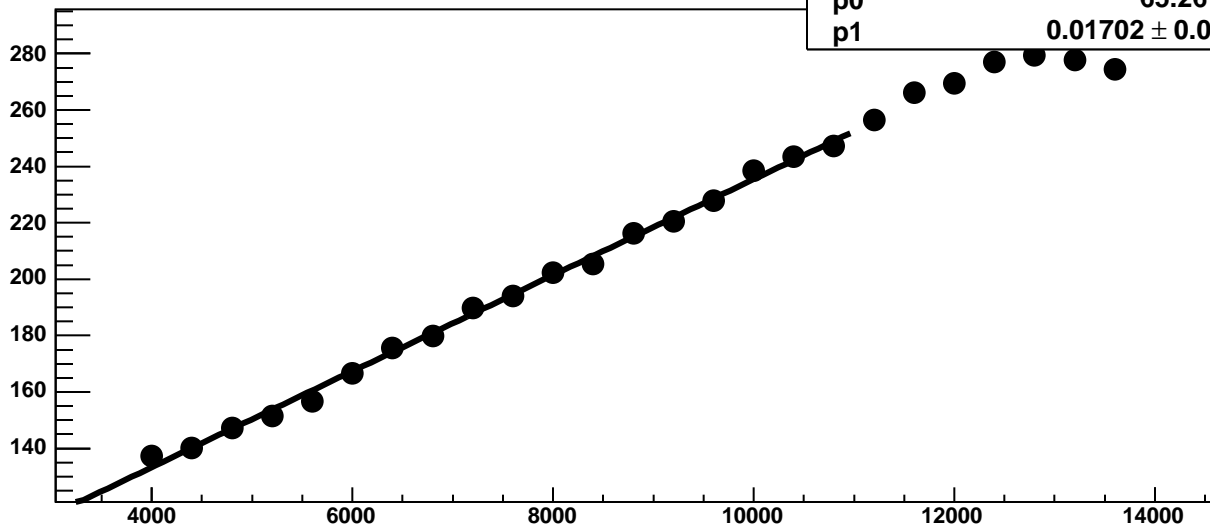
Chip 3, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



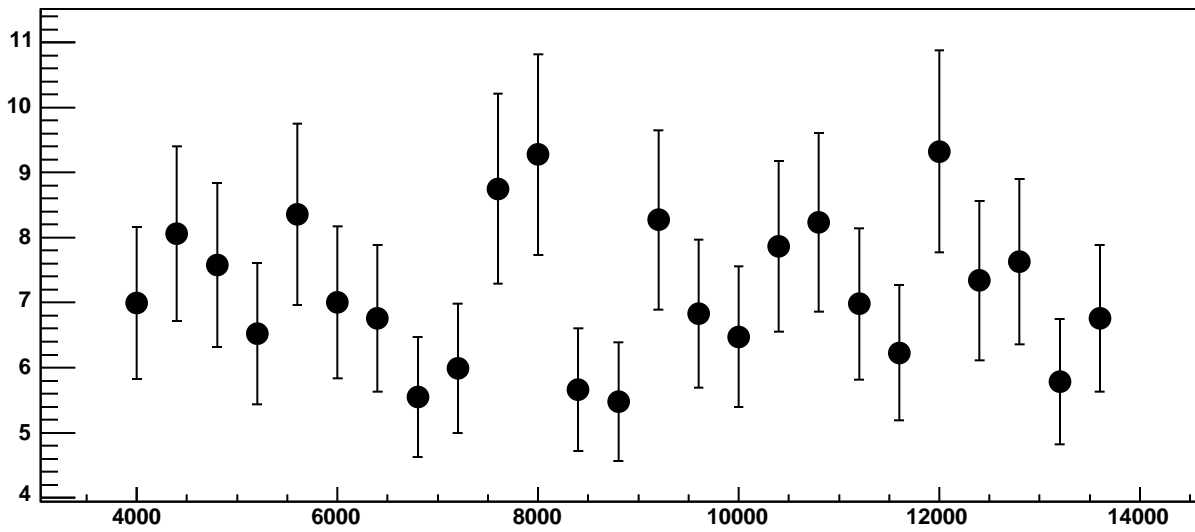
Chip 3, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC



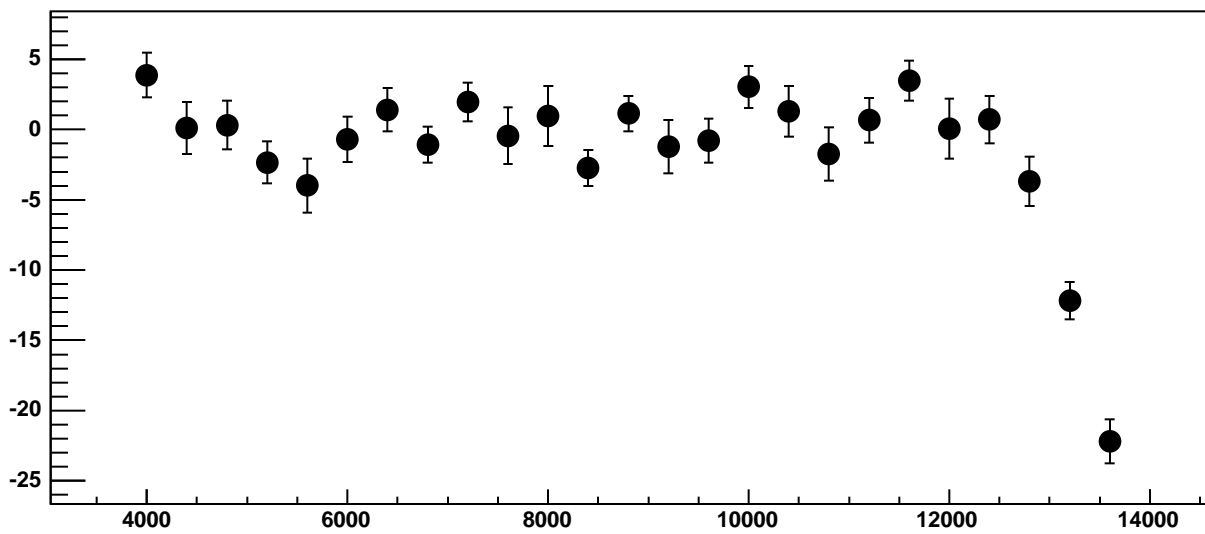
Chip 3, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



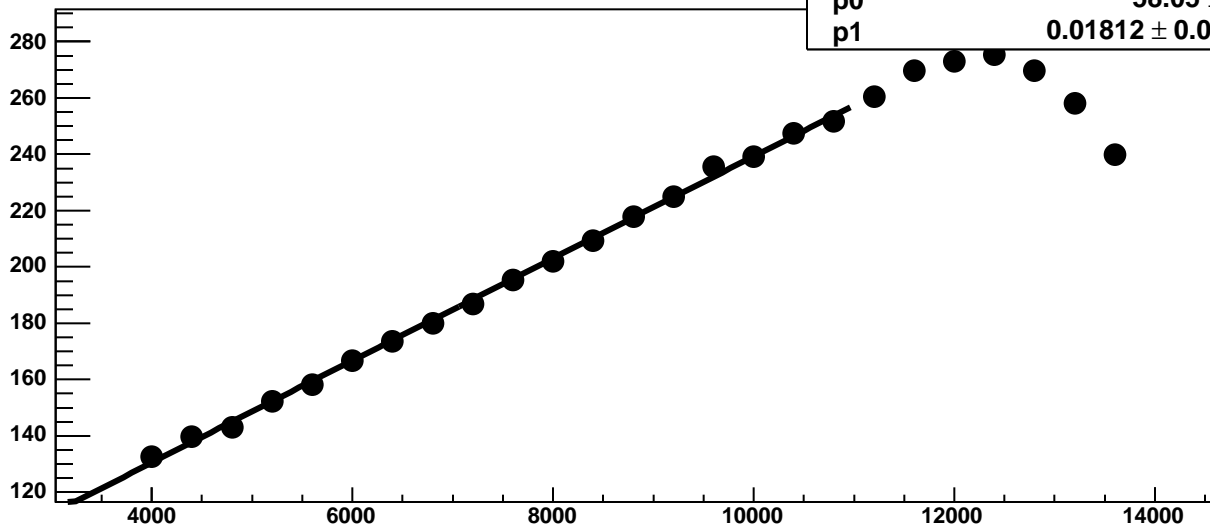
Chip 3, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



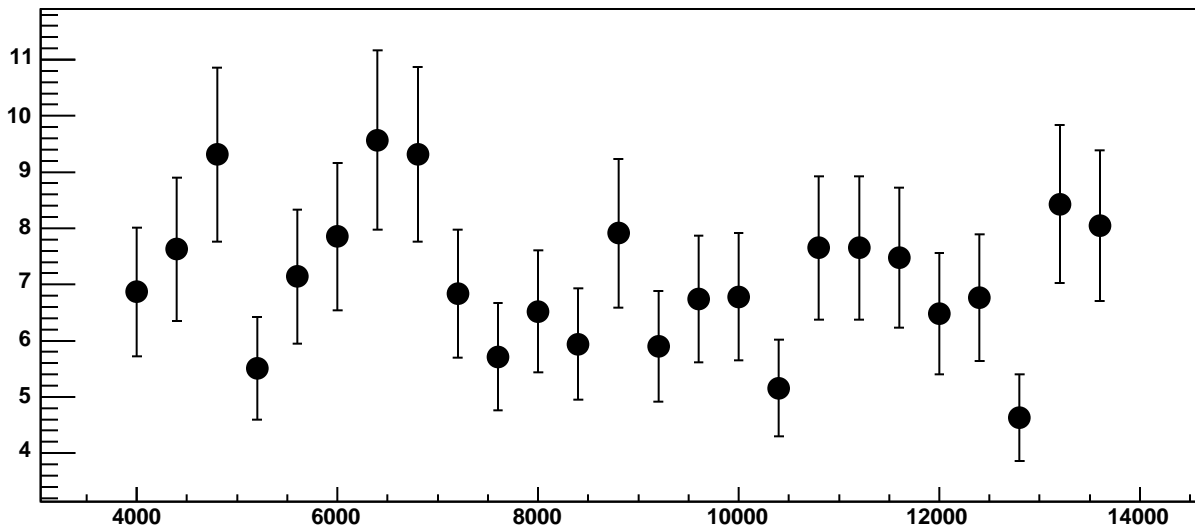
Chip 3, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



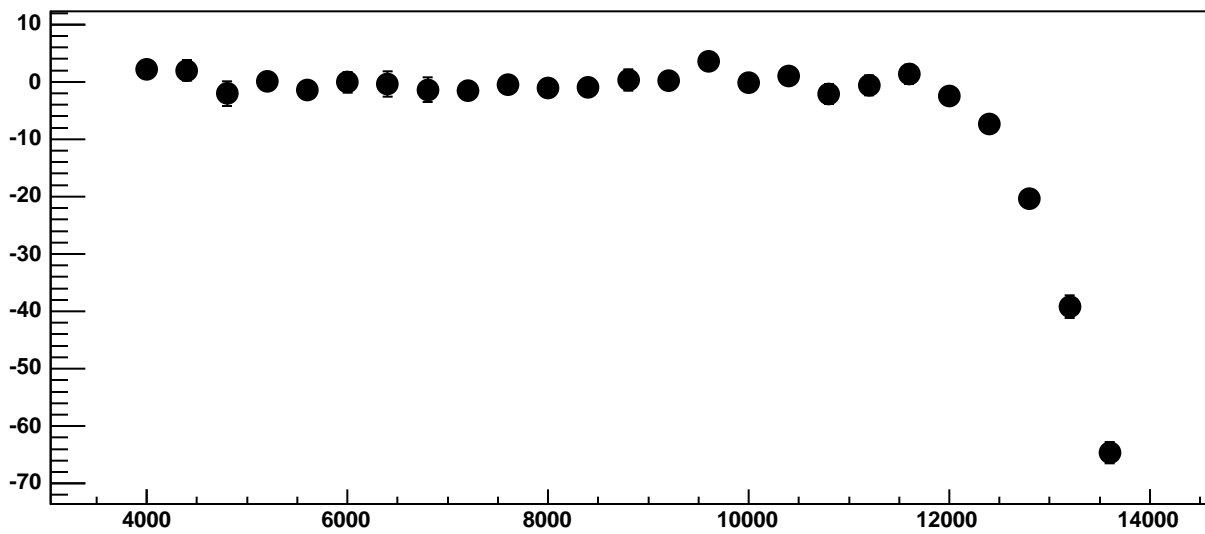
Chip 3, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



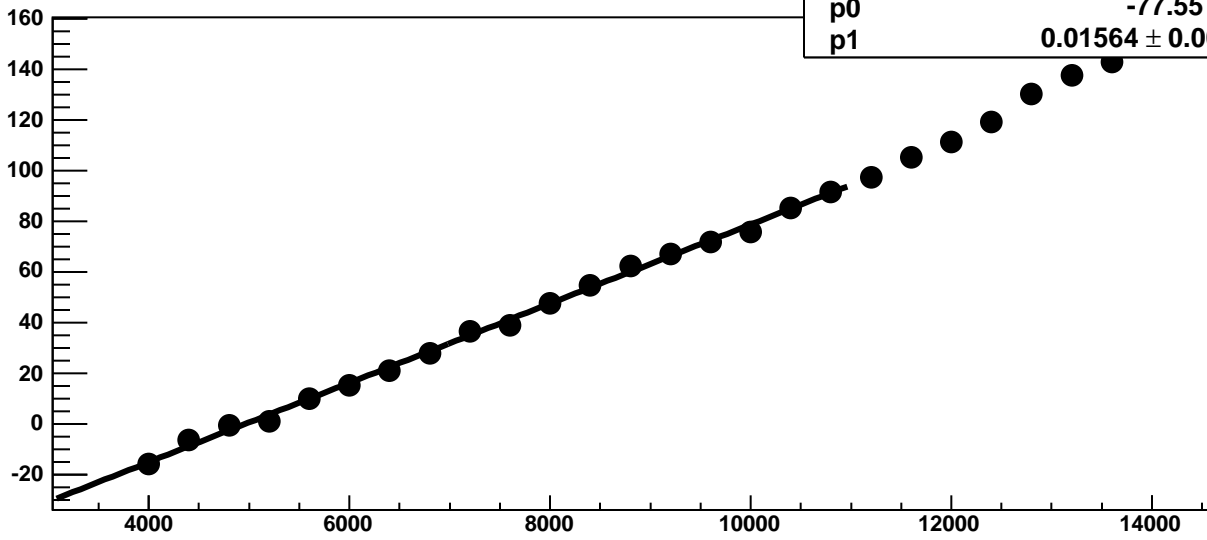
Chip 3, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

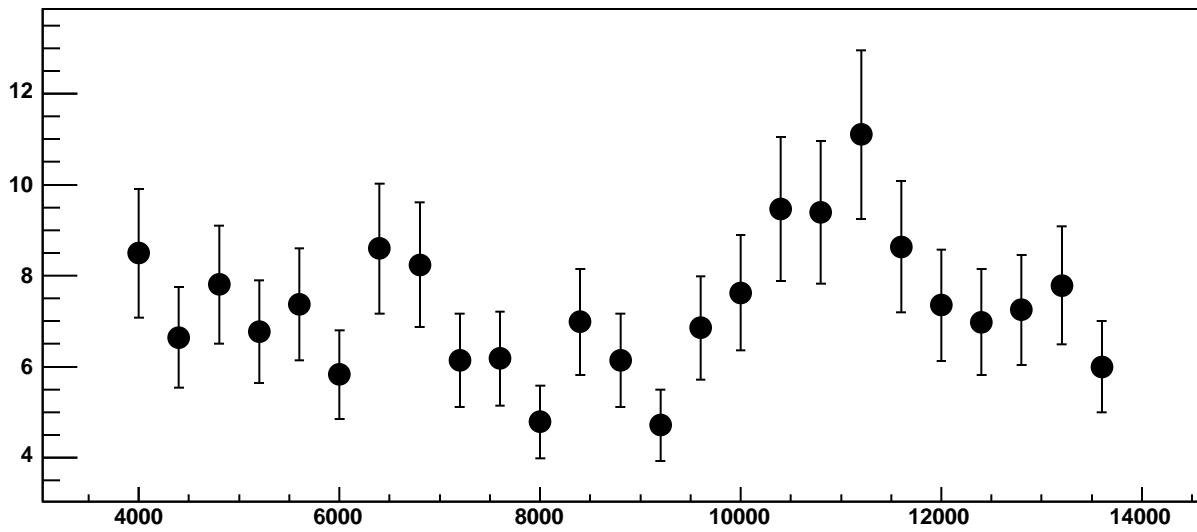


Chip 3, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

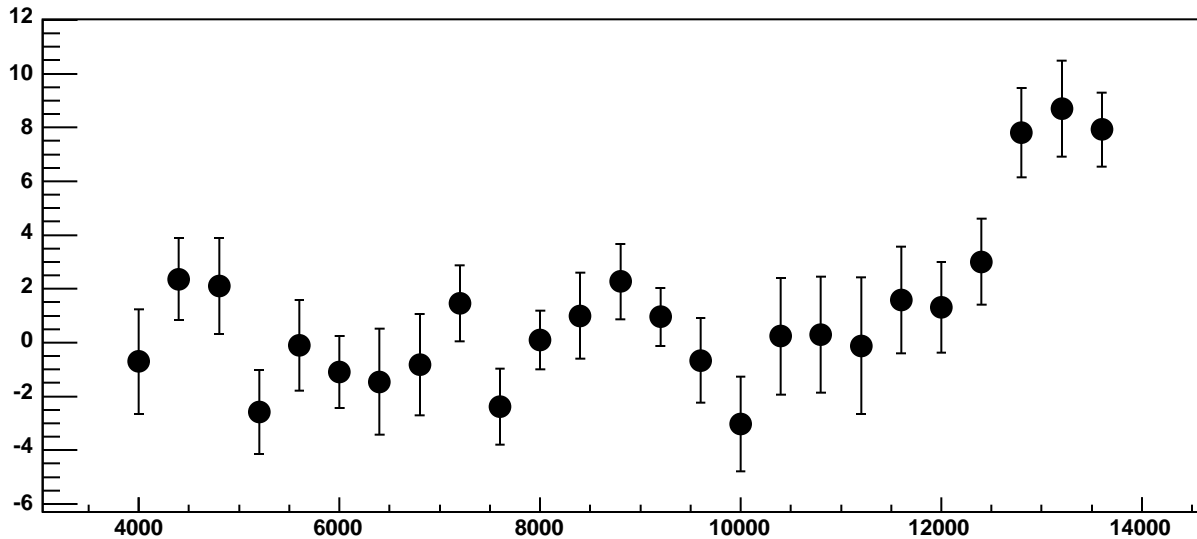


χ^2 / ndf 18.93 / 16
p0 -77.55 ± 1.491
p1 0.01564 ± 0.0001937

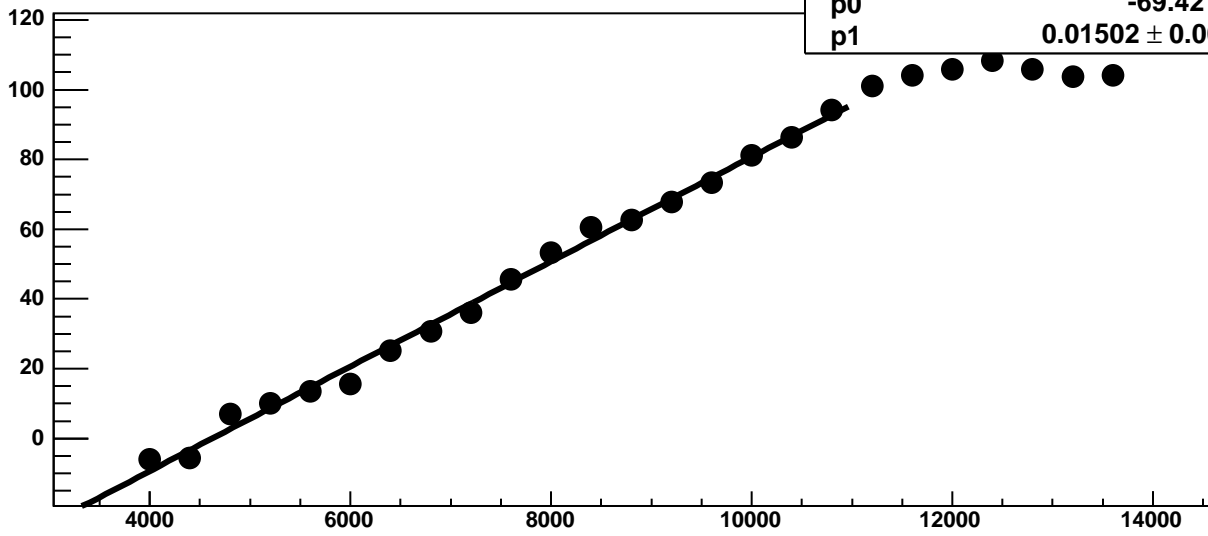
Chip 3, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



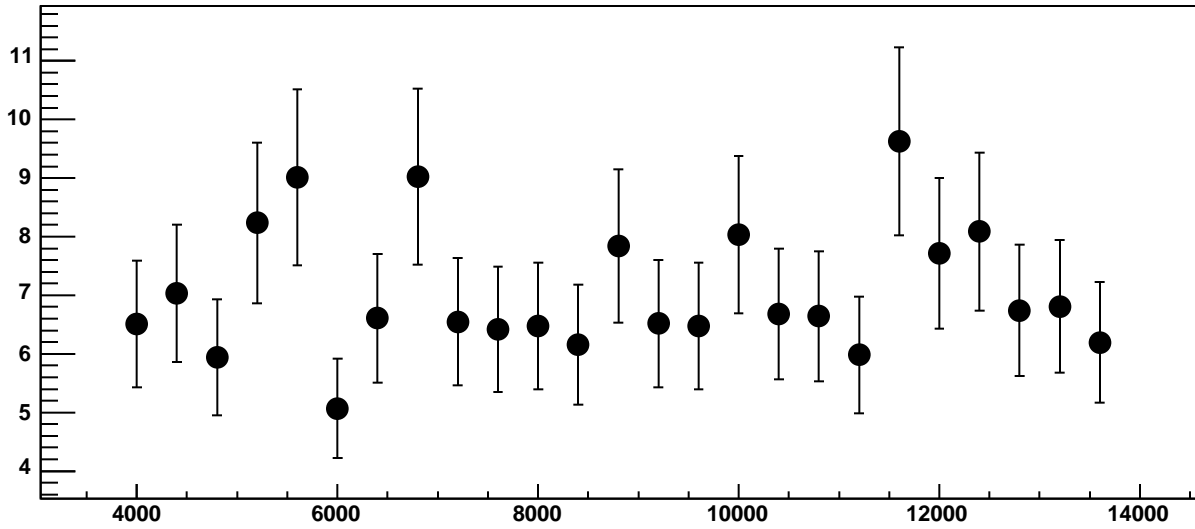
Chip 3, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



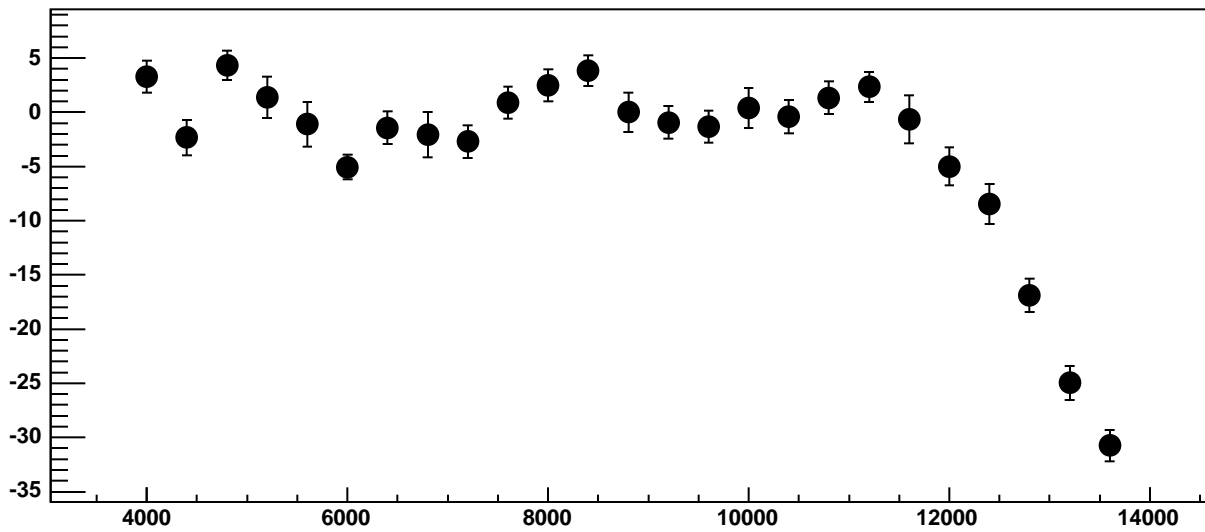
Chip 3, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



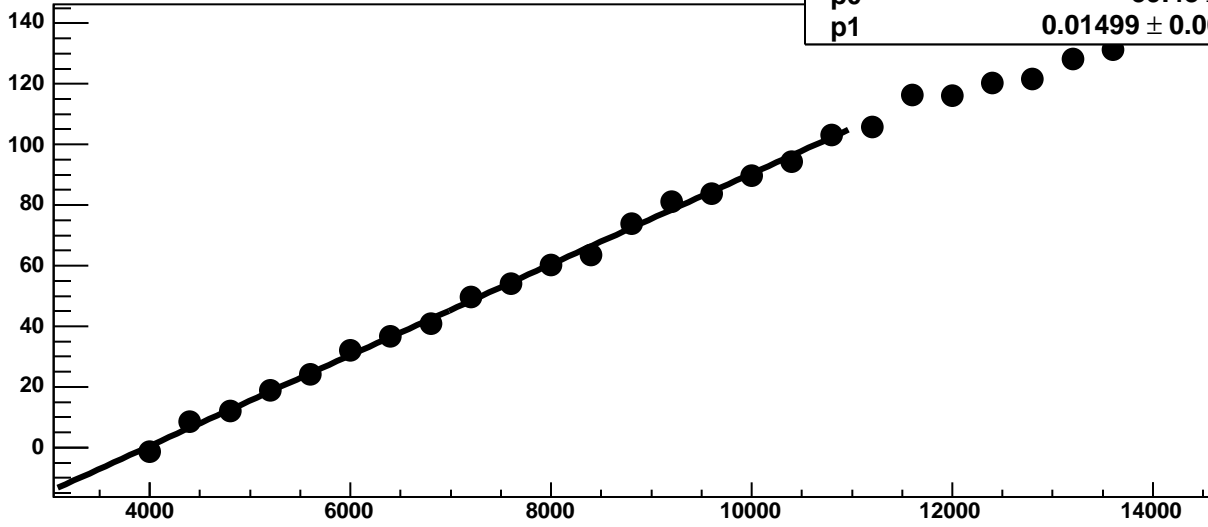
Chip 3, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



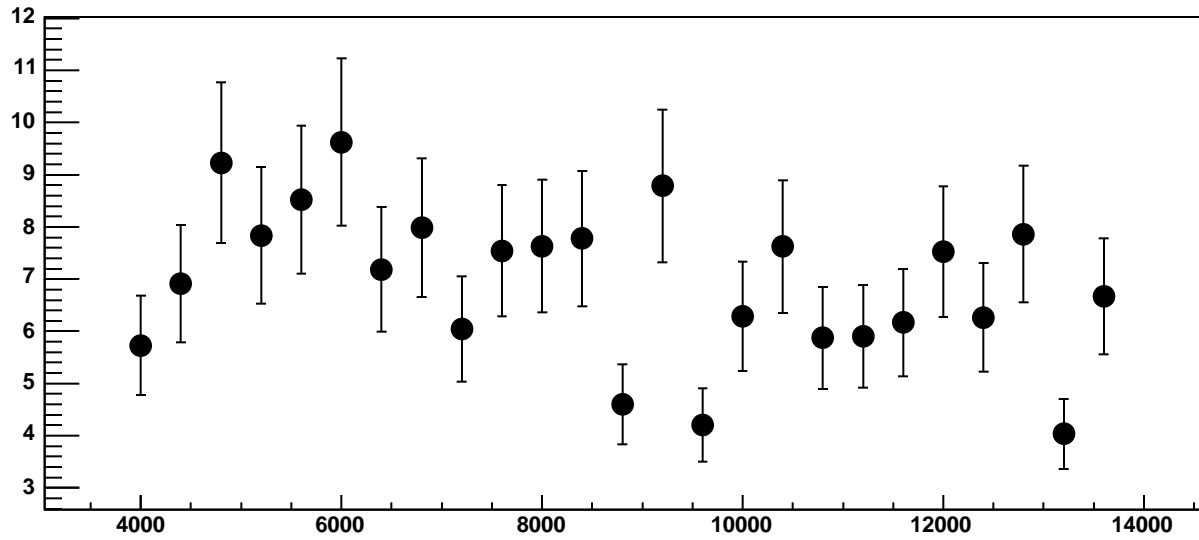
Chip 3, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



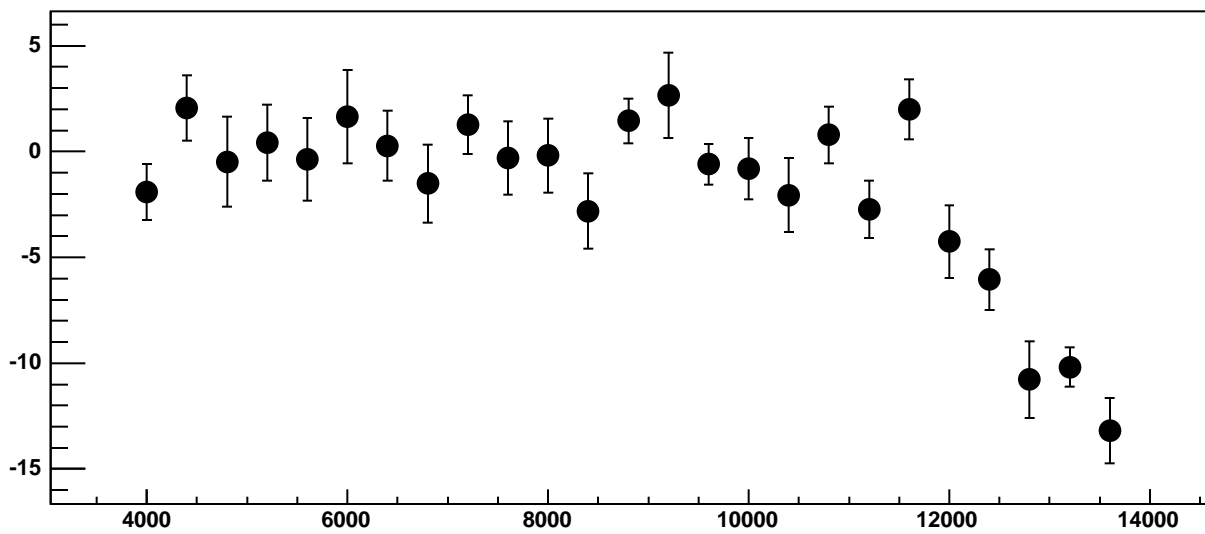
Chip 3, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC



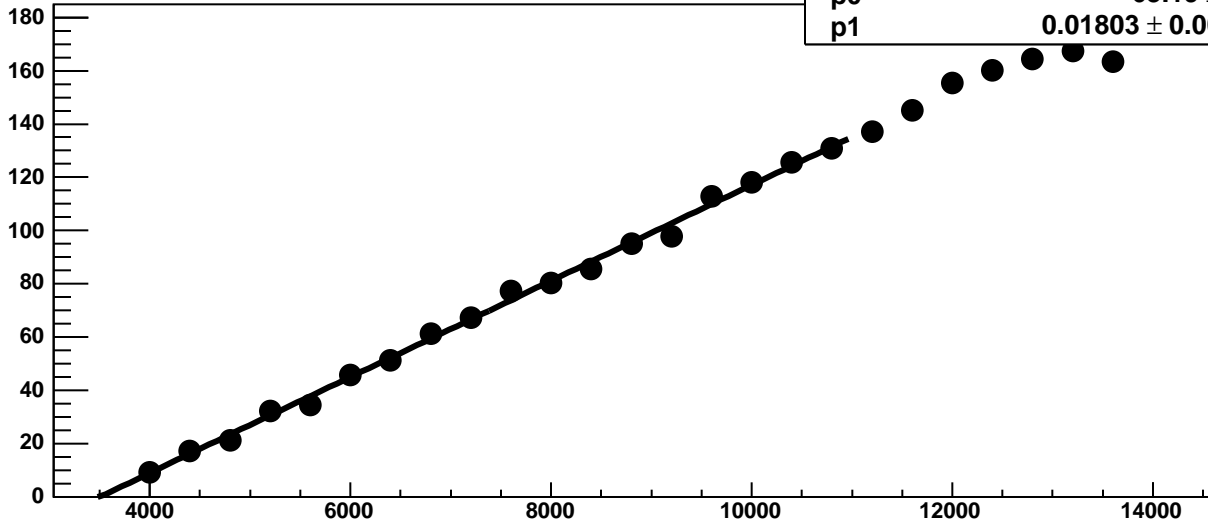
Chip 3, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



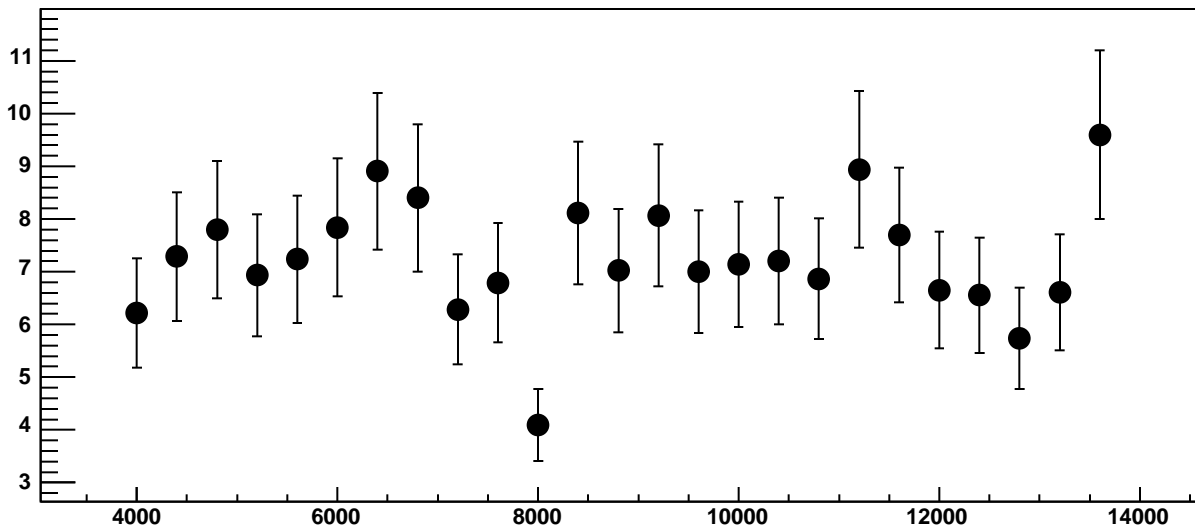
Chip 3, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC



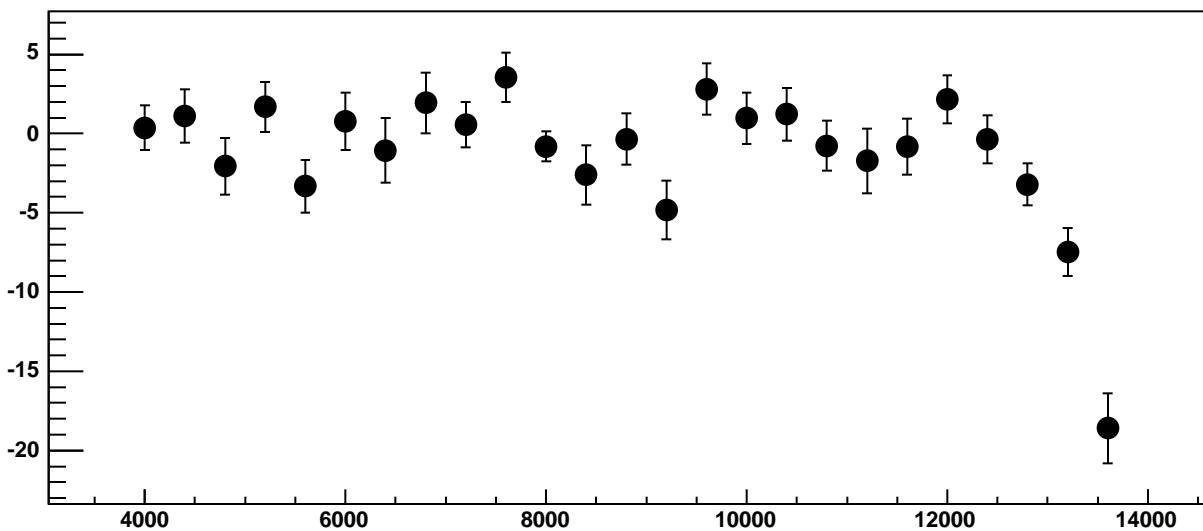
Chip 3, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



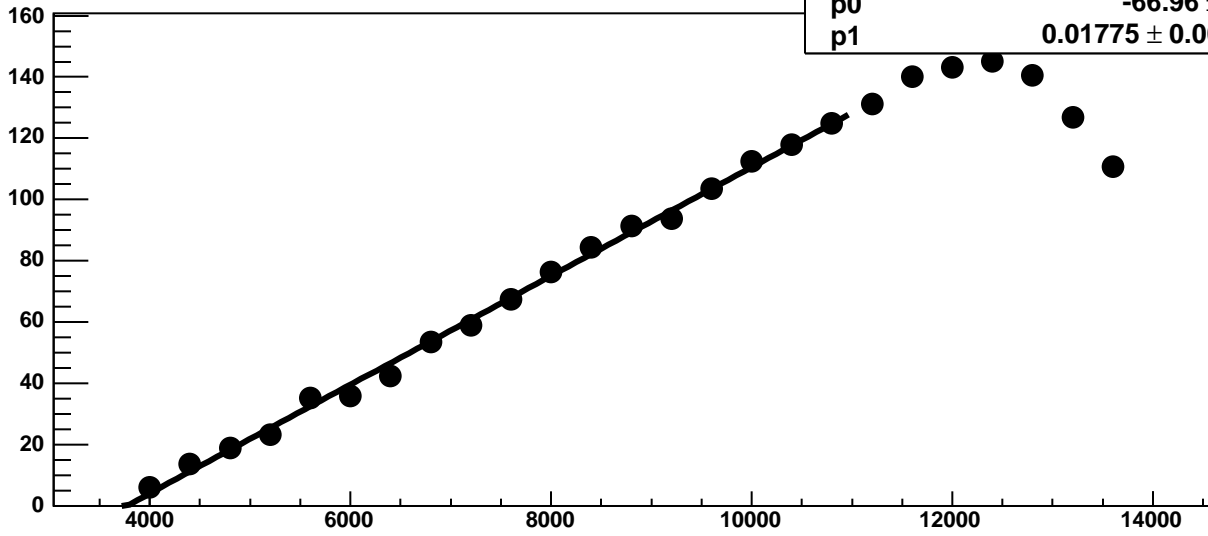
Chip 3, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



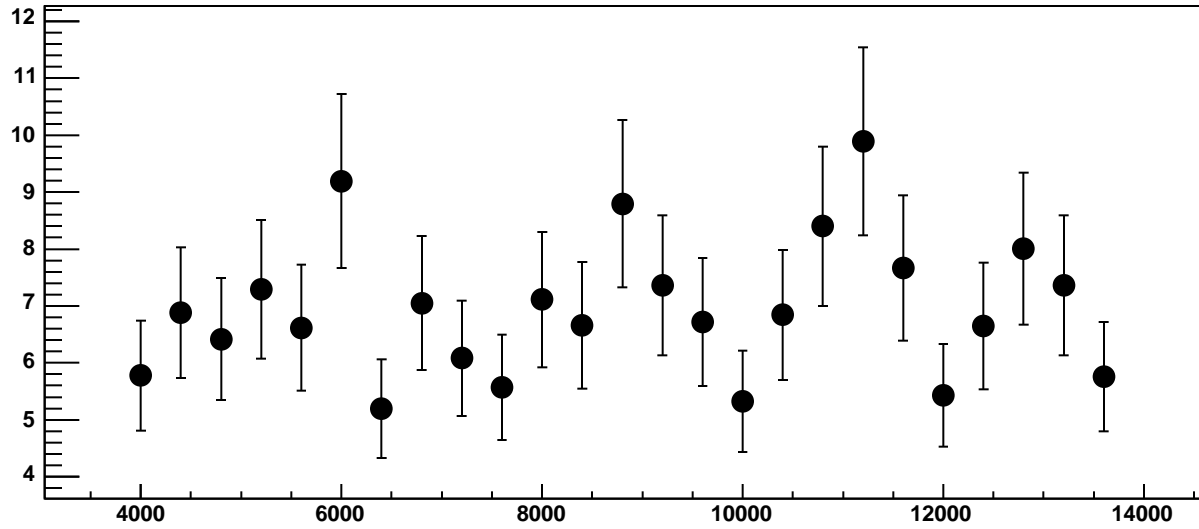
Chip 3, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



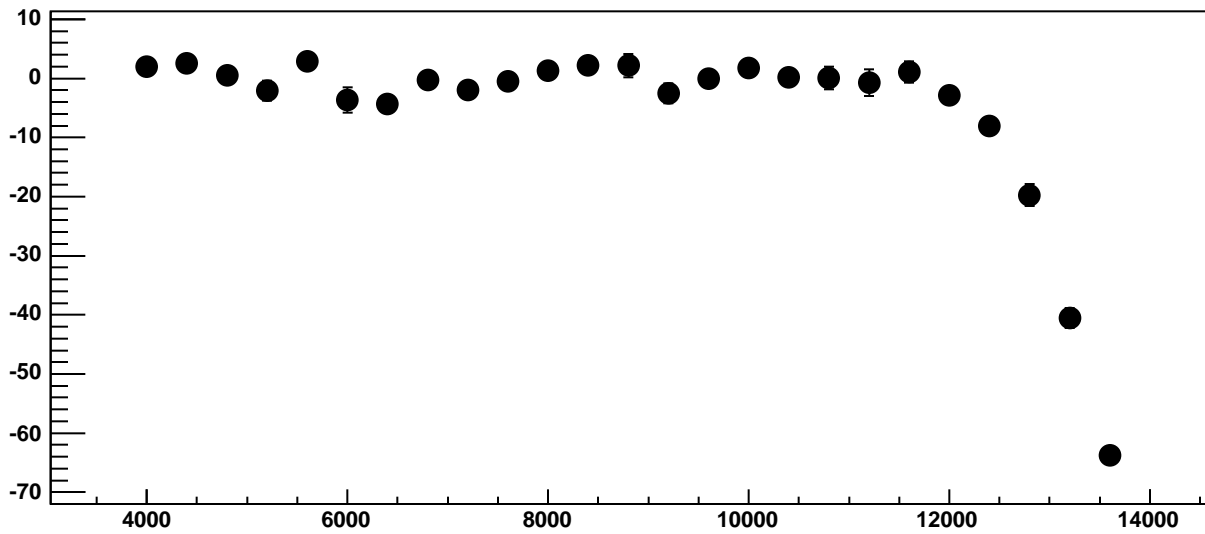
Chip 3, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



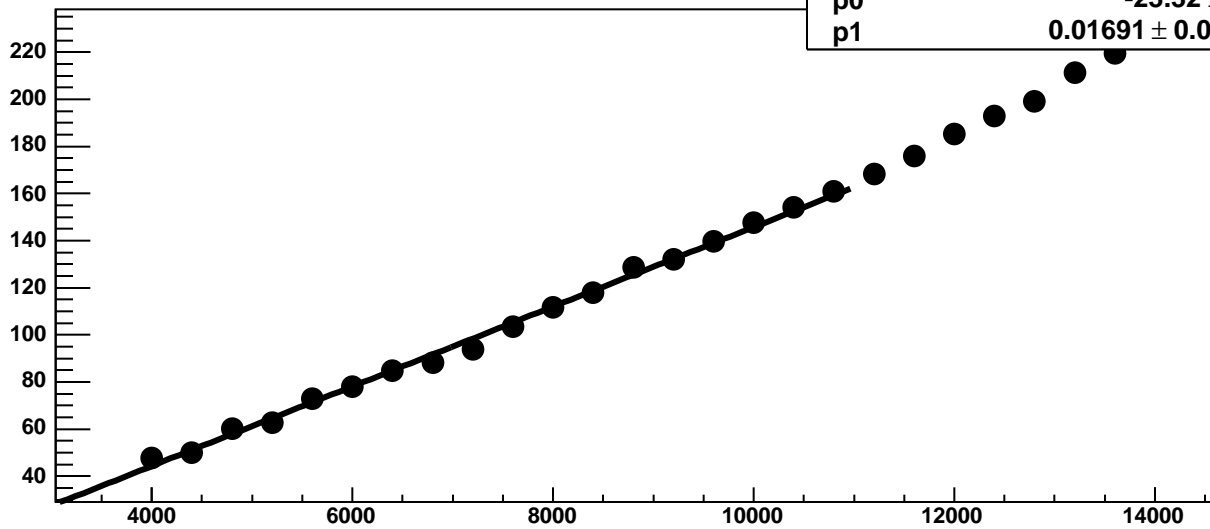
Chip 3, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

37.86 / 16

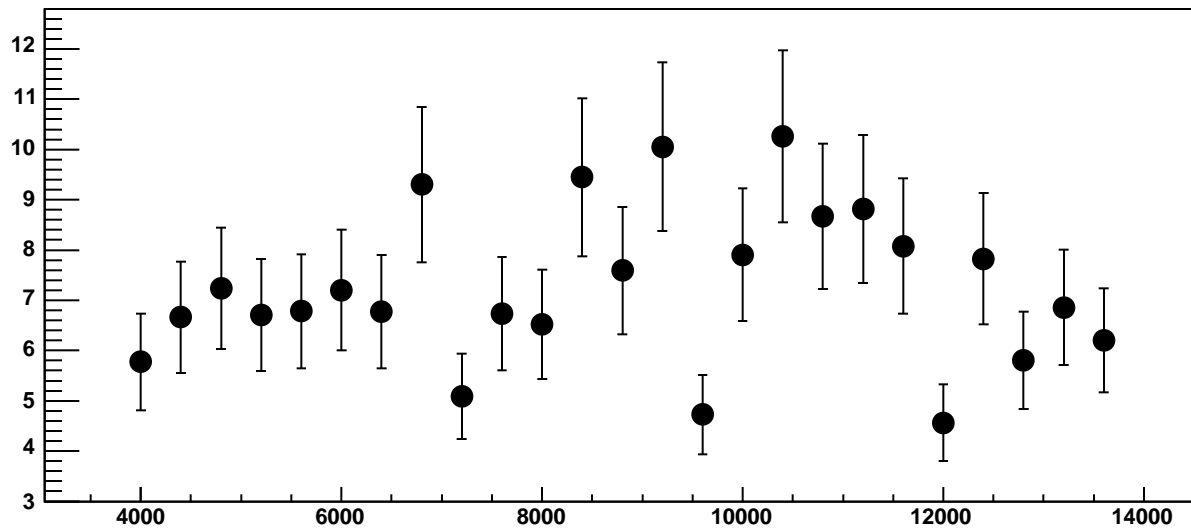
p0

-23.32 ± 1.373

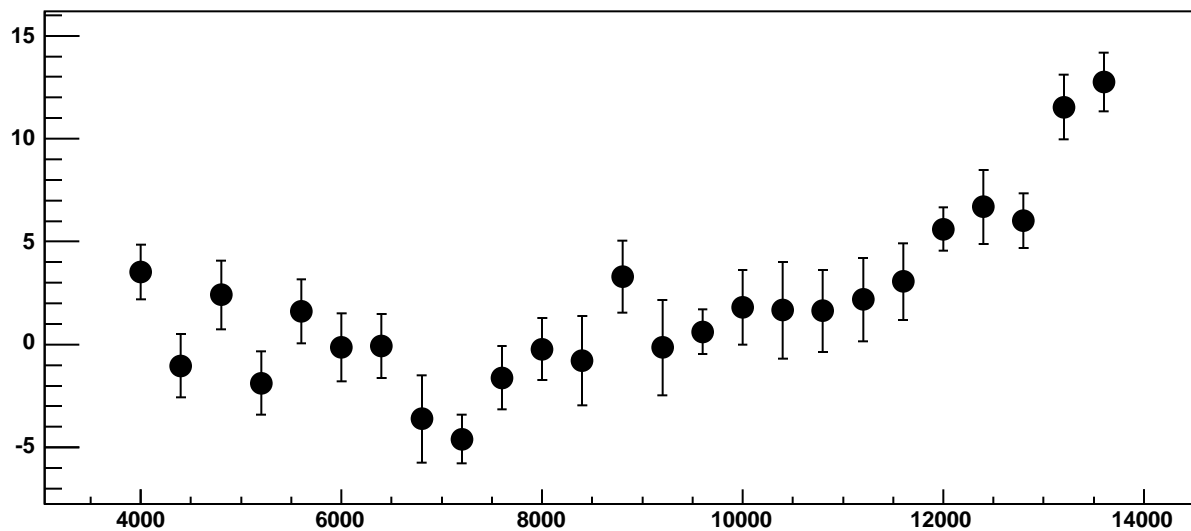
p1

0.01691 ± 0.0001841

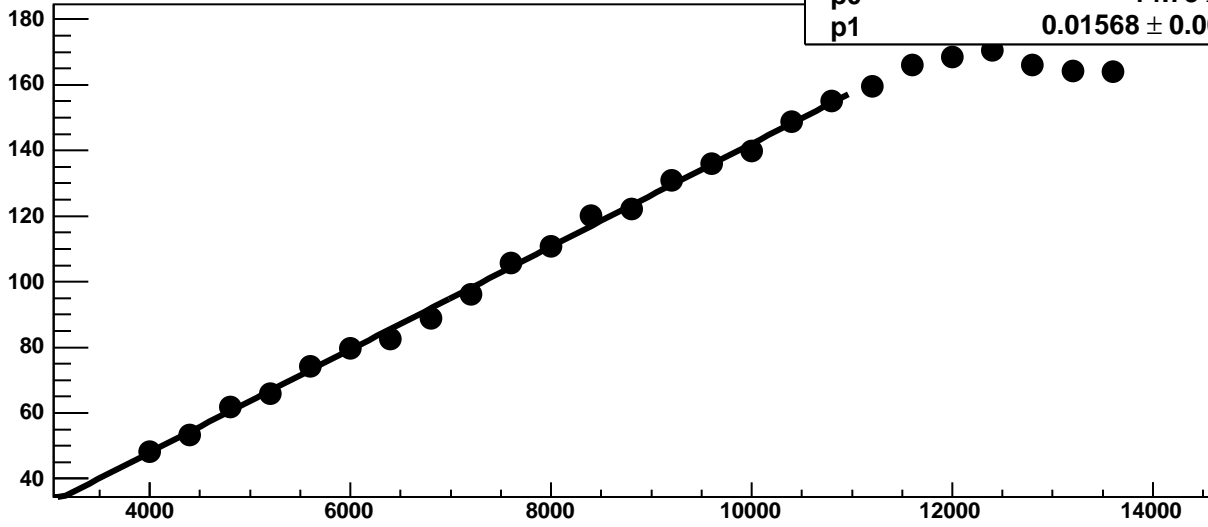
Chip 3, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



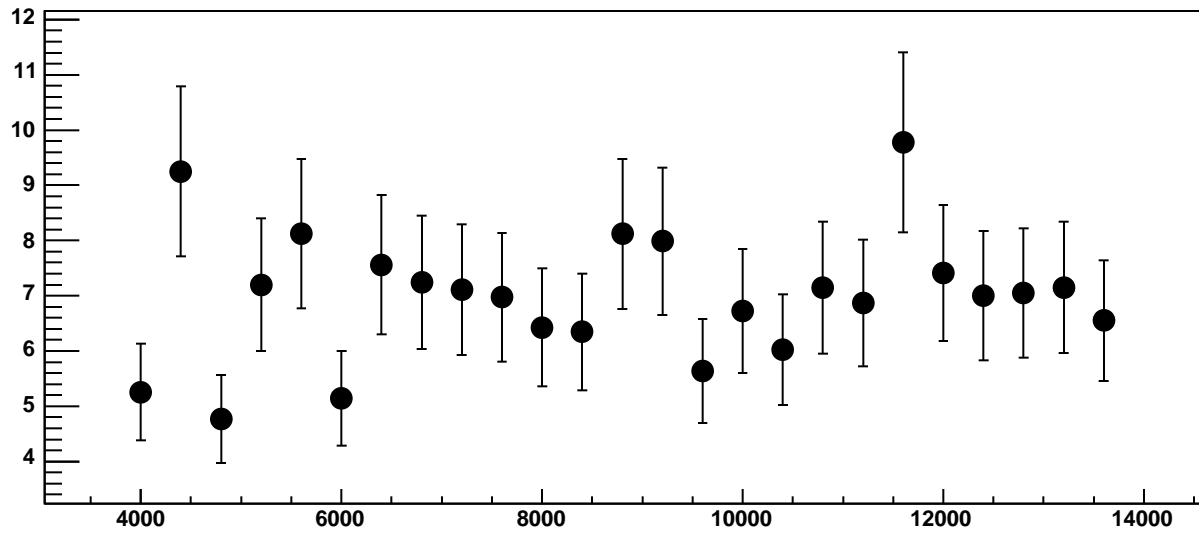
Chip 3, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC



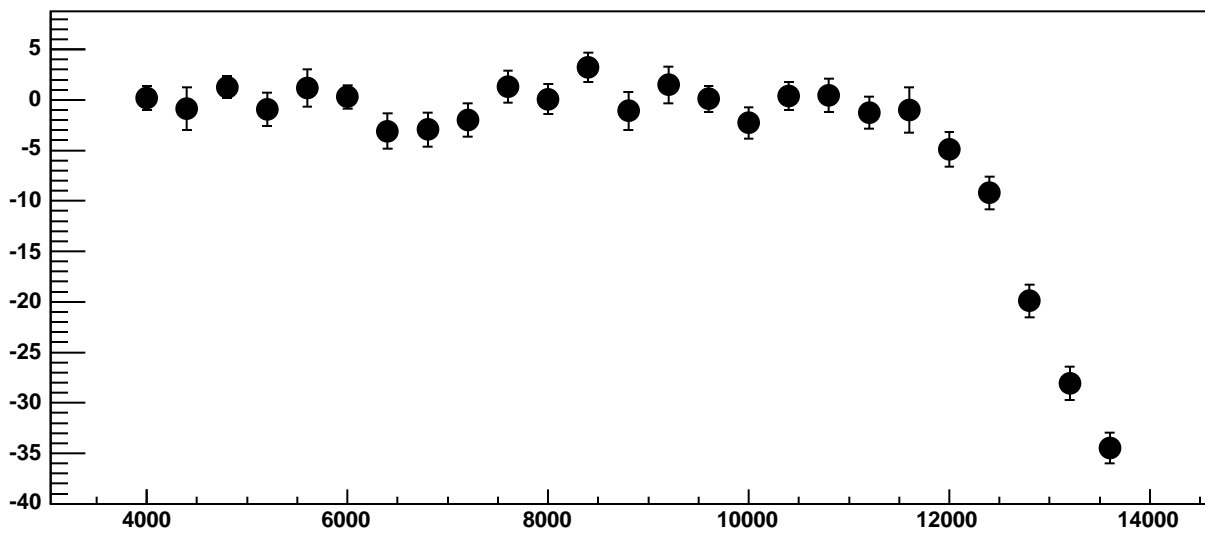
Chip 3, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



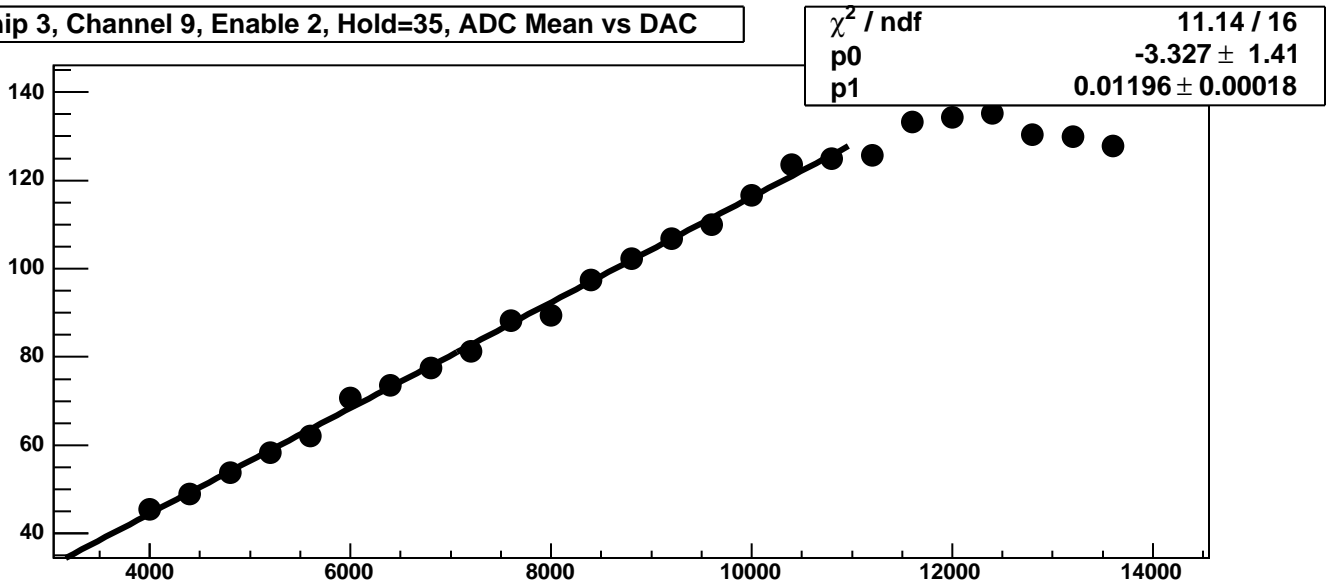
Chip 3, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



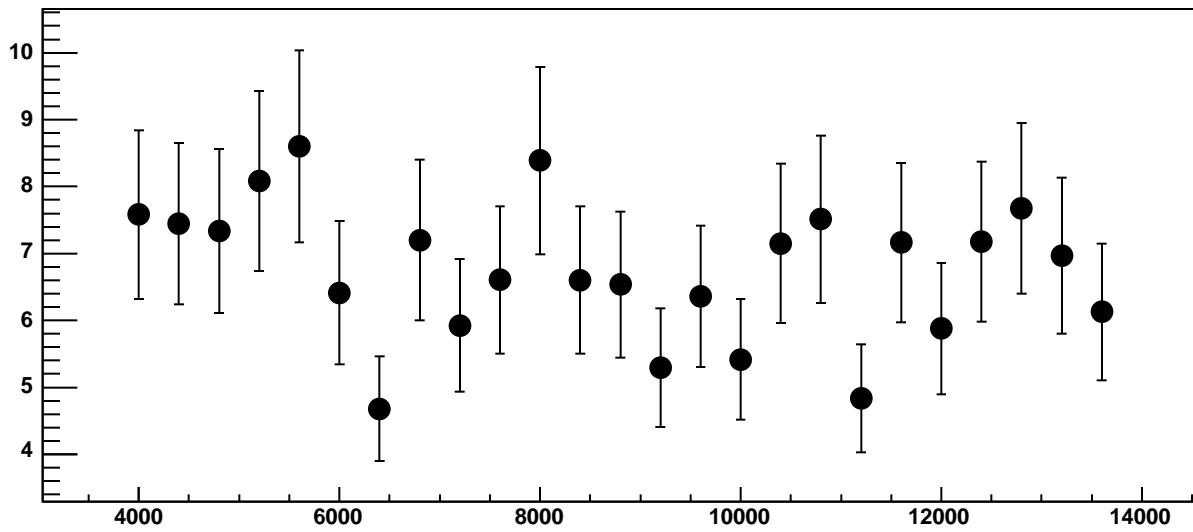
Chip 3, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



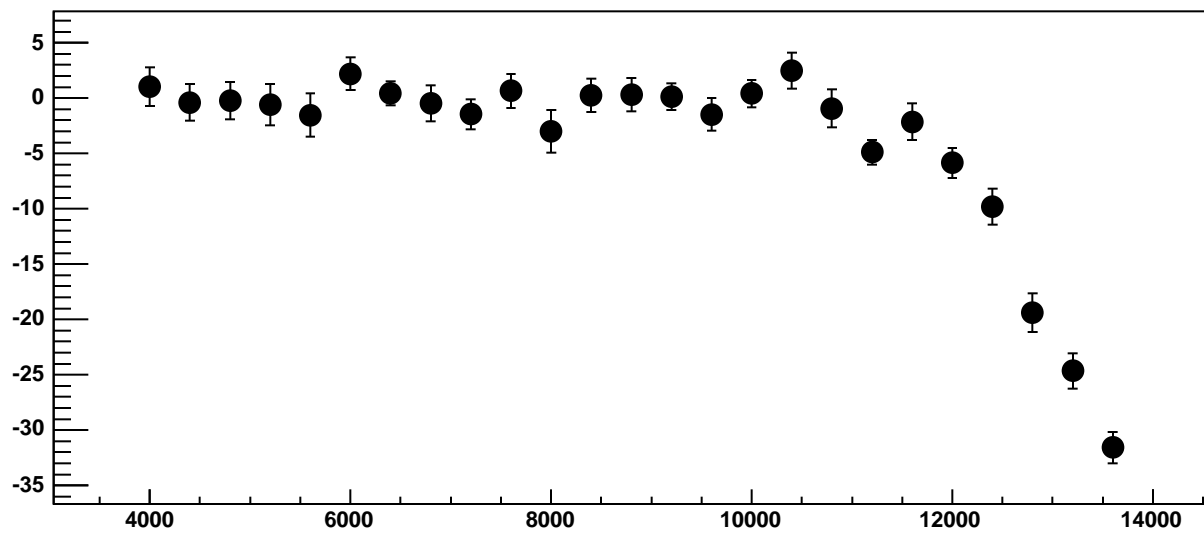
Chip 3, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



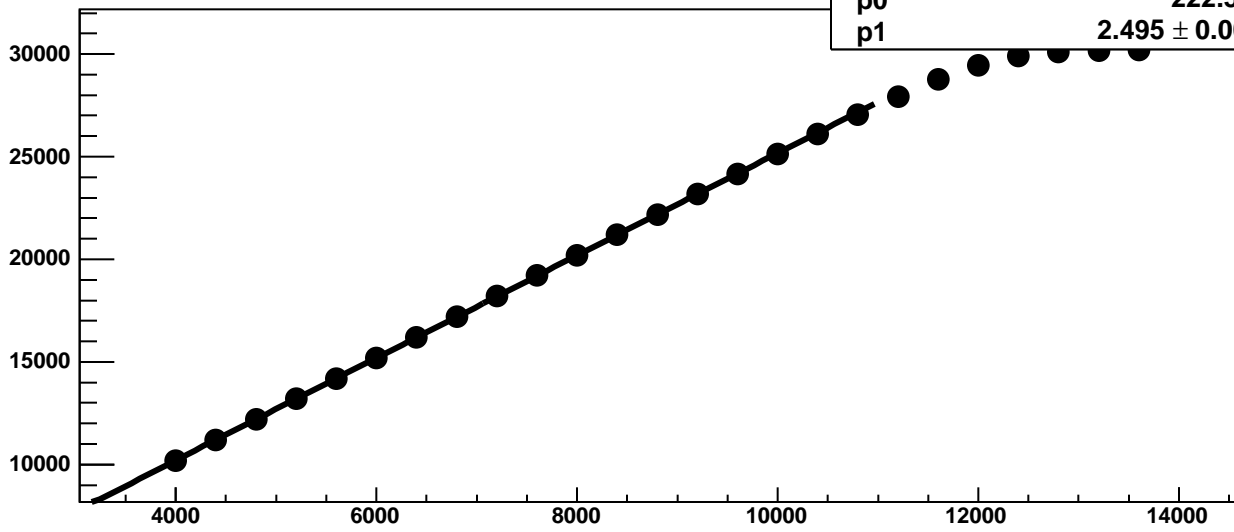
Chip 3, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

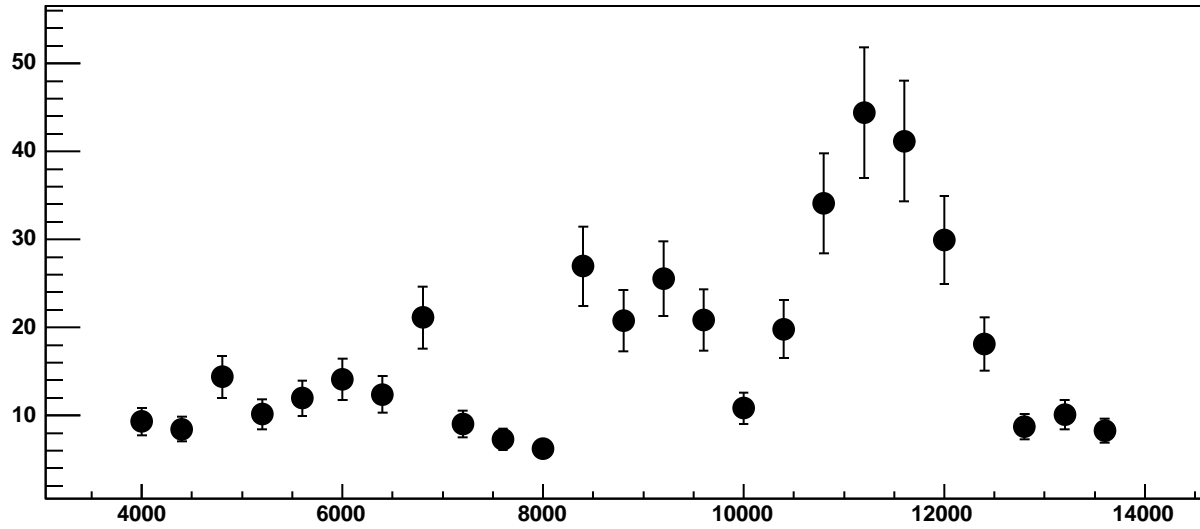


Chip 3, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC

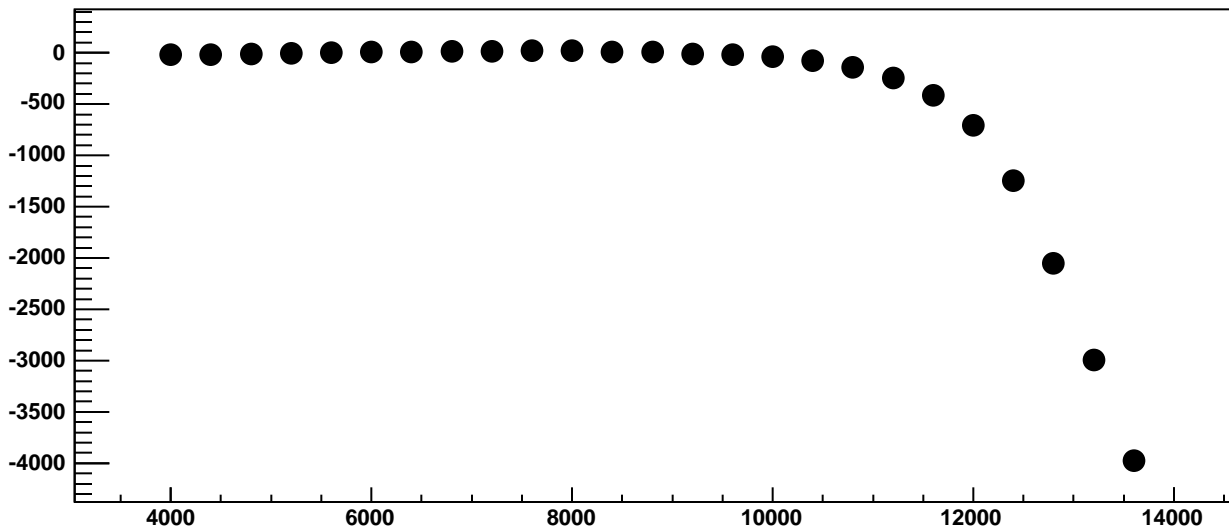


χ^2 / ndf 1482 / 16
p0 222.5 ± 2.4
p1 2.495 ± 0.0003403

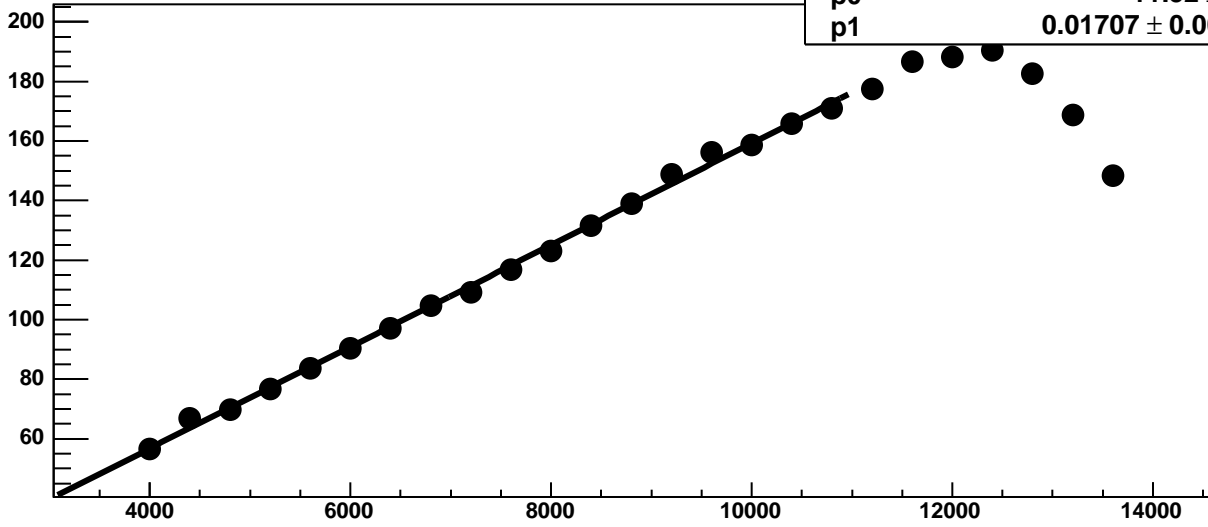
Chip 3, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



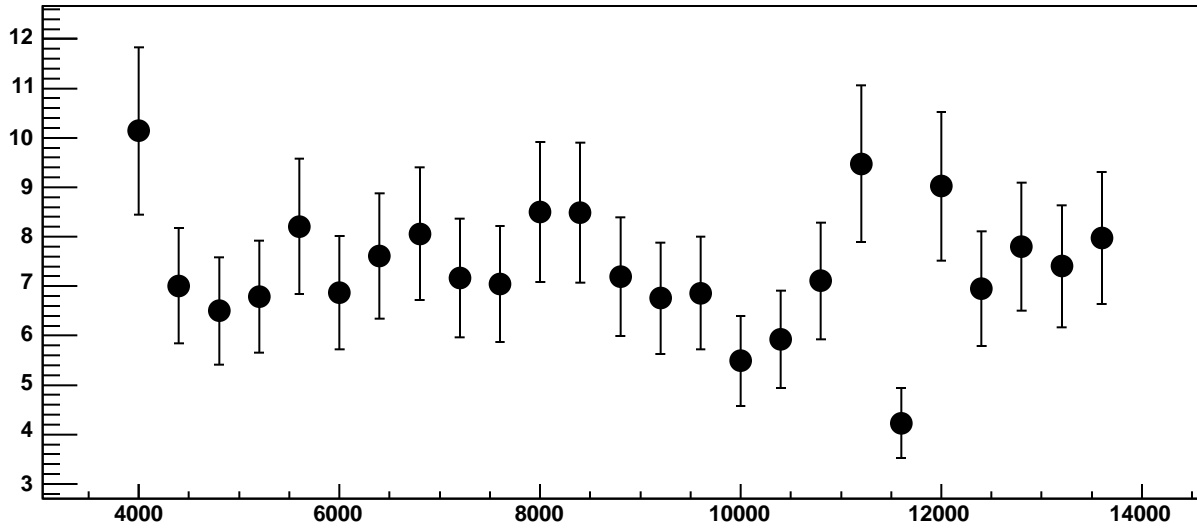
Chip 3, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC



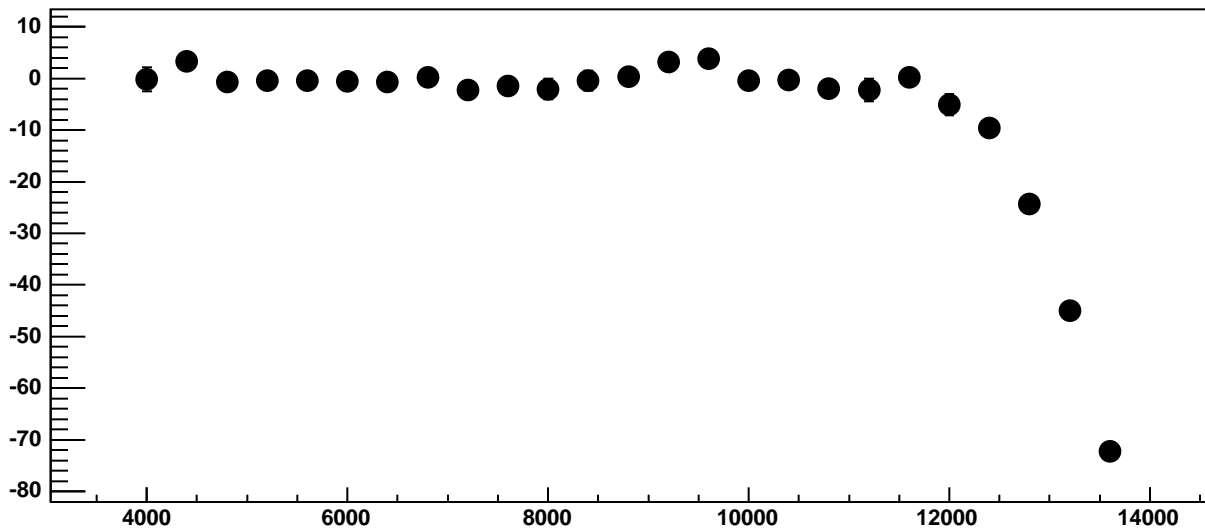
Chip 3, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



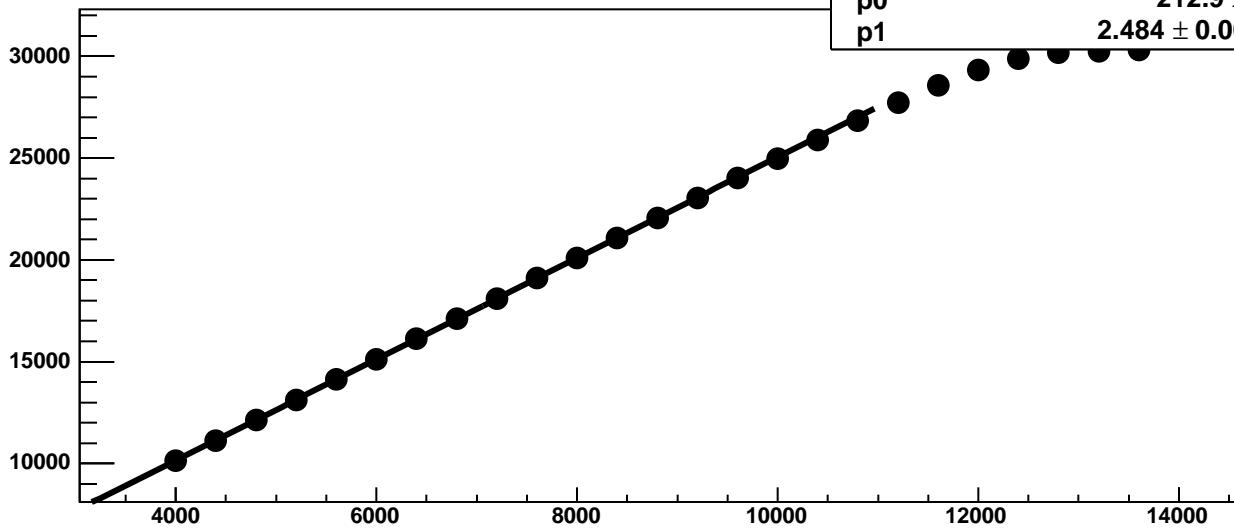
Chip 3, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



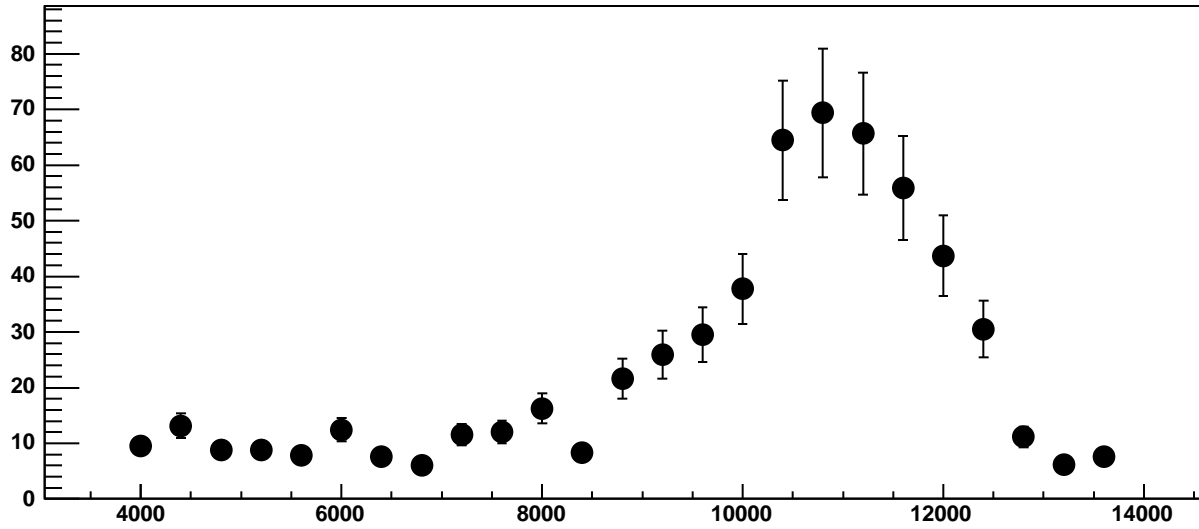
Chip 3, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



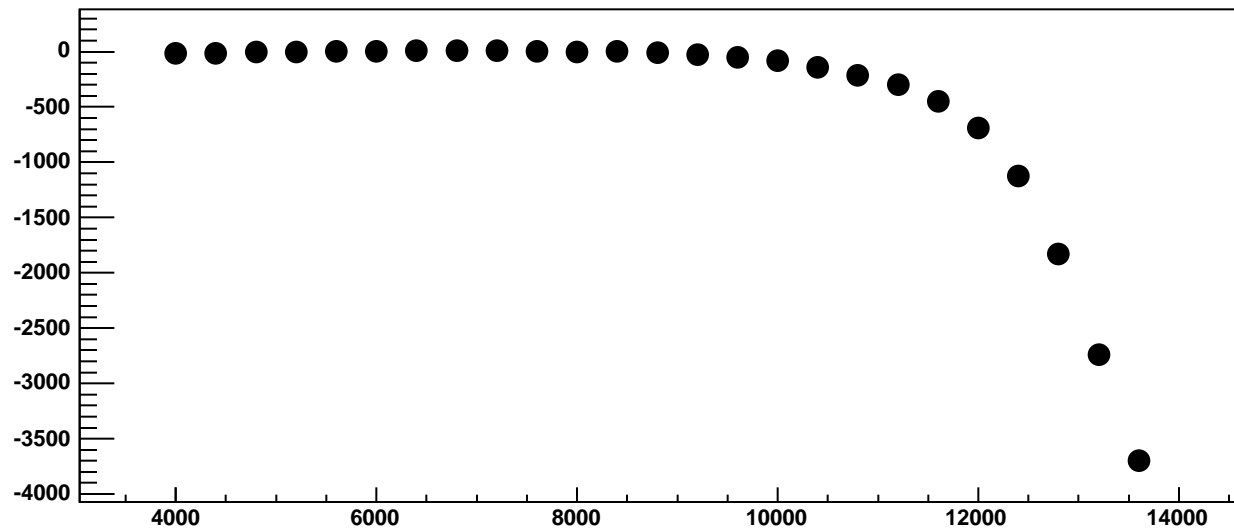
Chip 3, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC



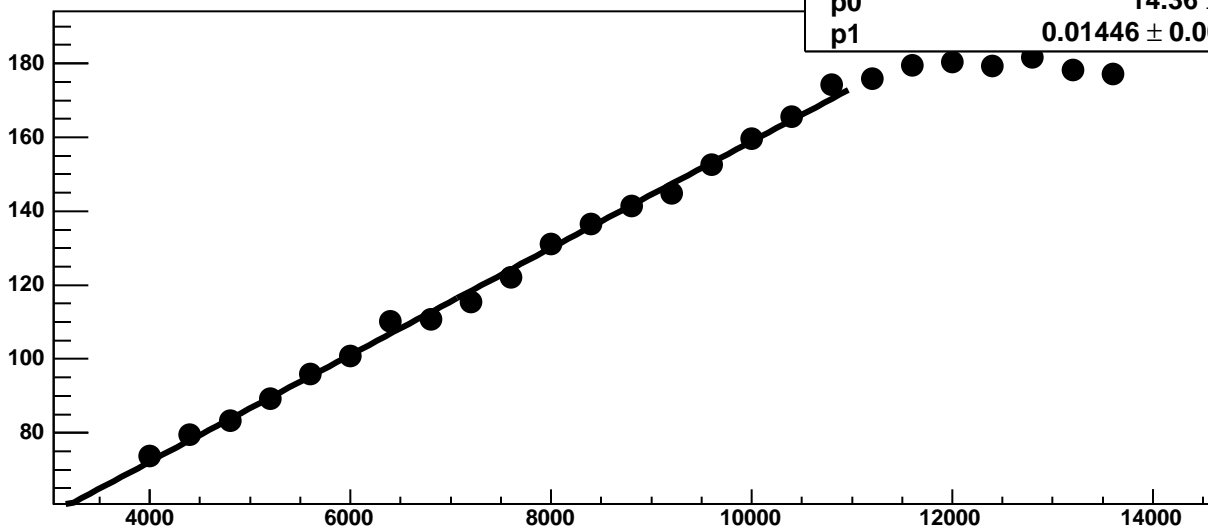
Chip 3, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



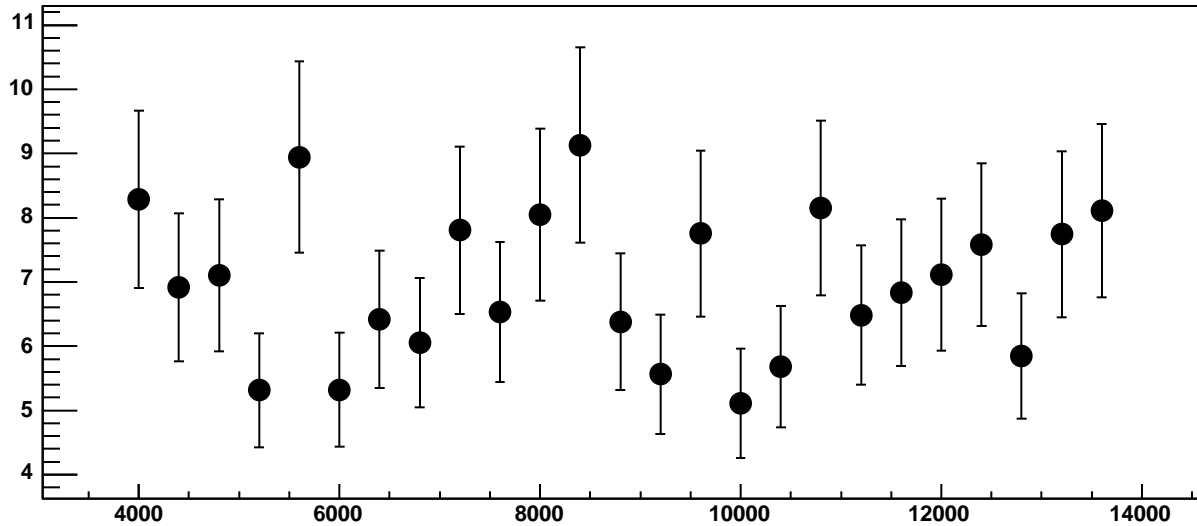
Chip 3, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC



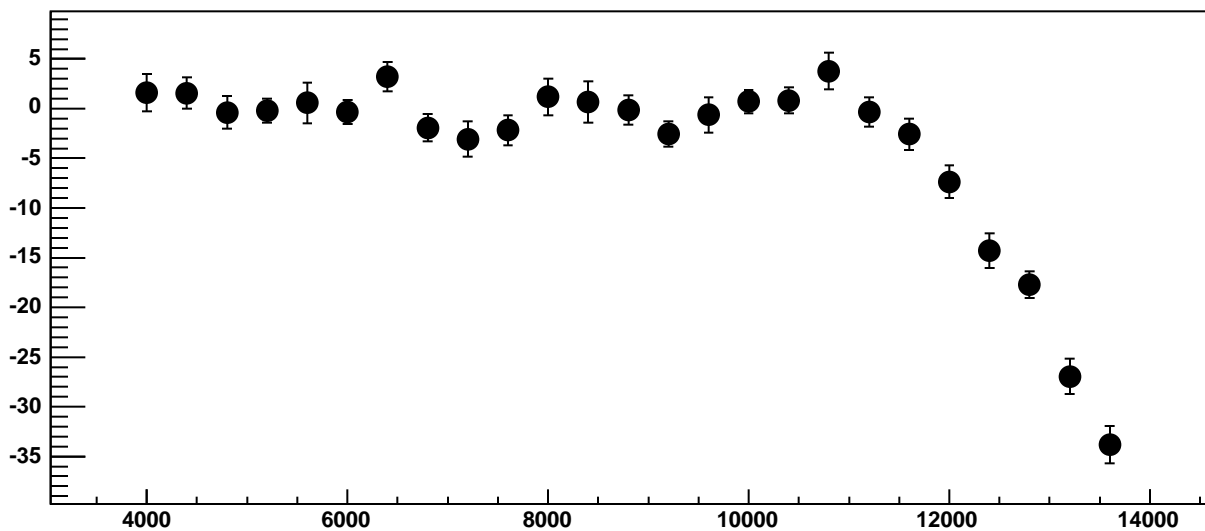
Chip 3, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



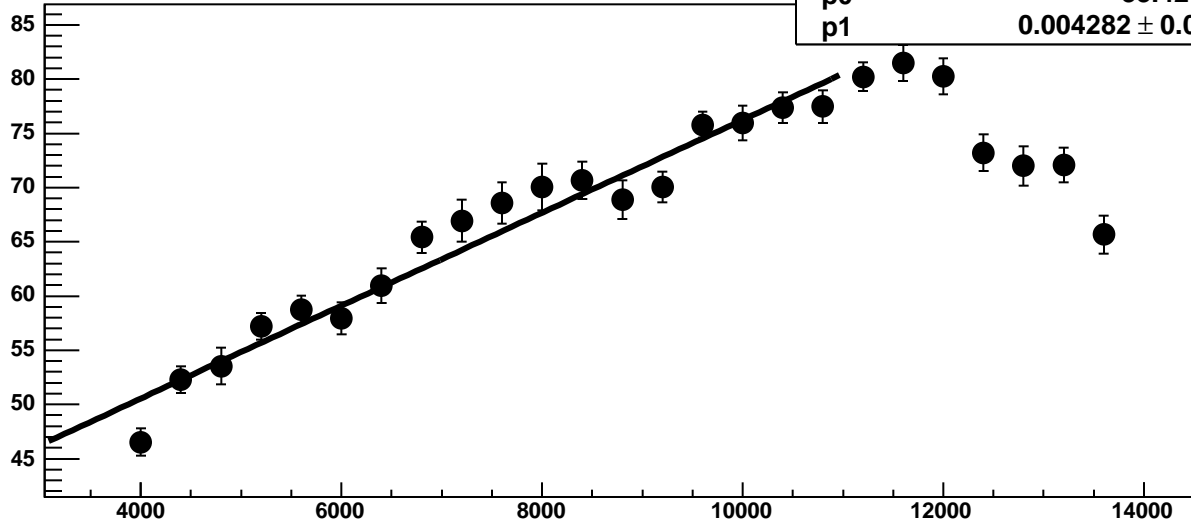
Chip 3, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



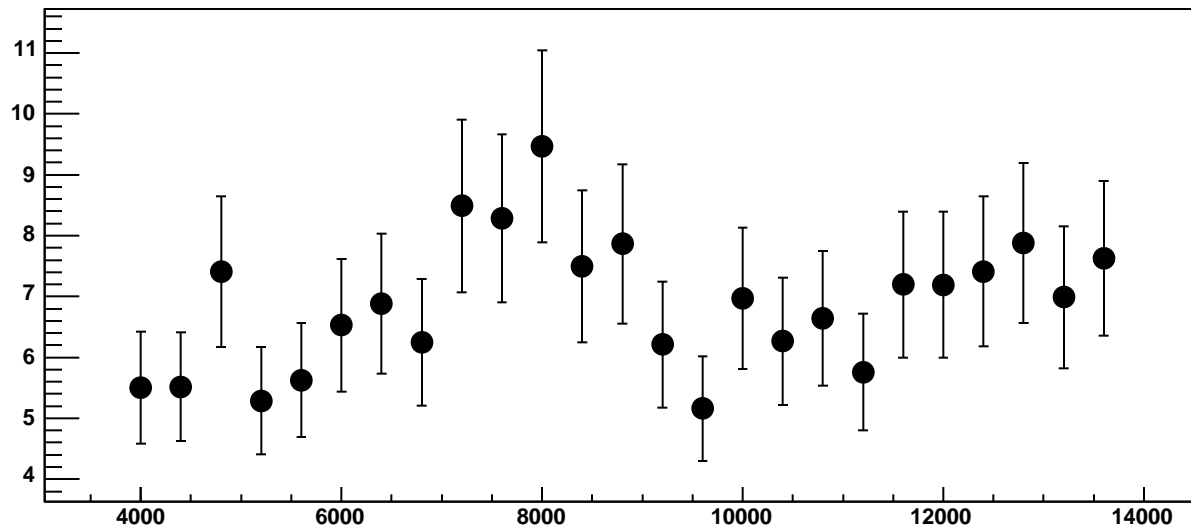
Chip 3, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



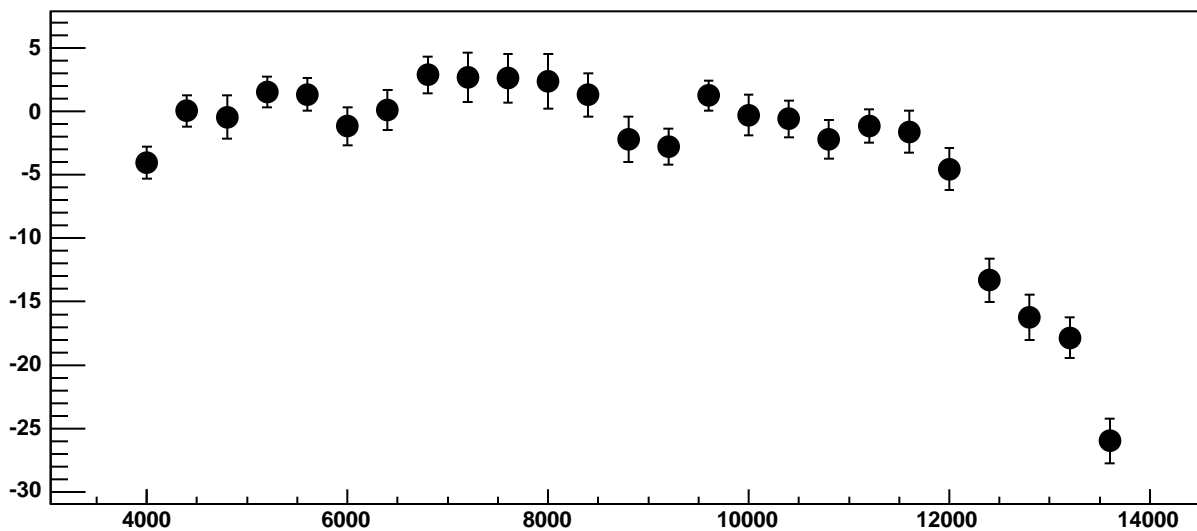
Chip 3, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



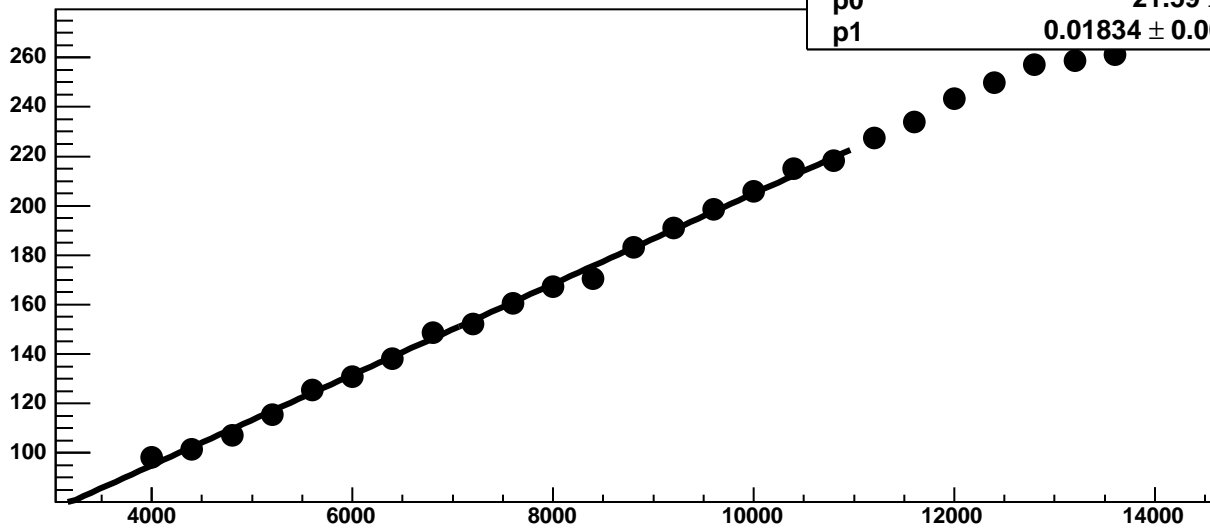
Chip 3, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

22.25 / 16

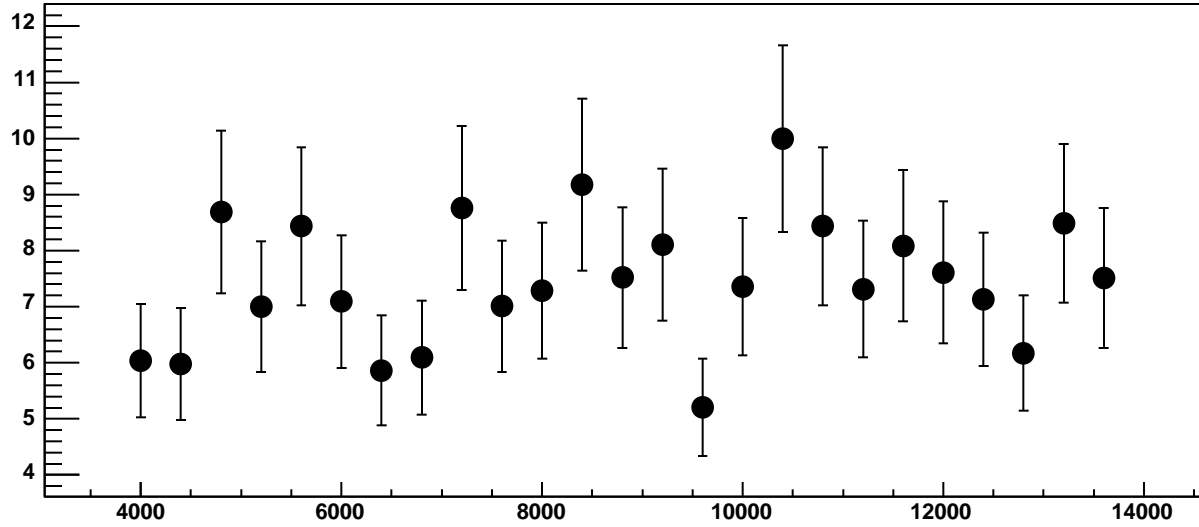
p0

21.59 ± 1.392

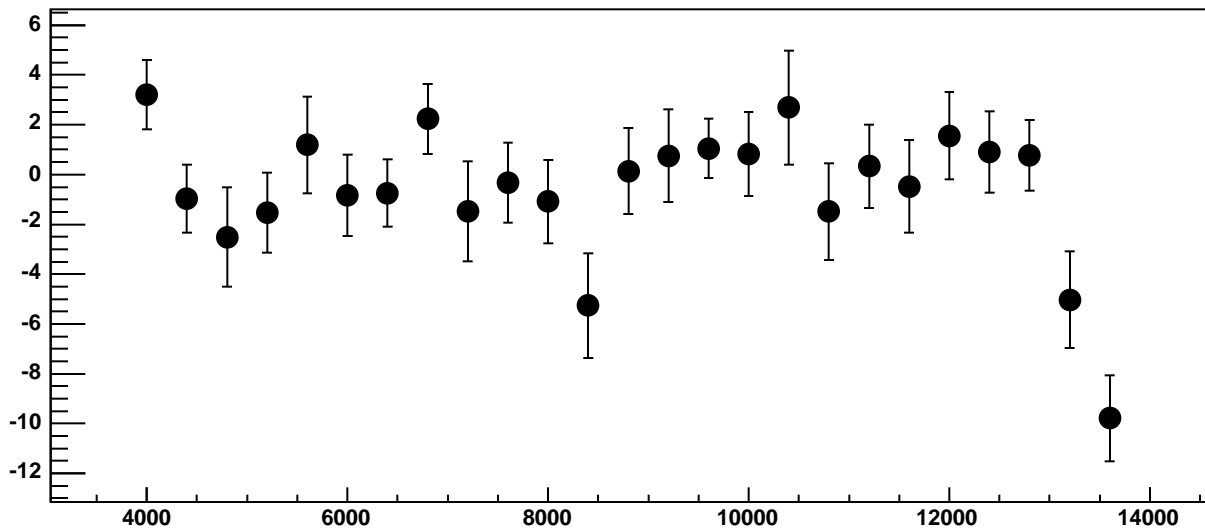
p1

0.01834 ± 0.0001857

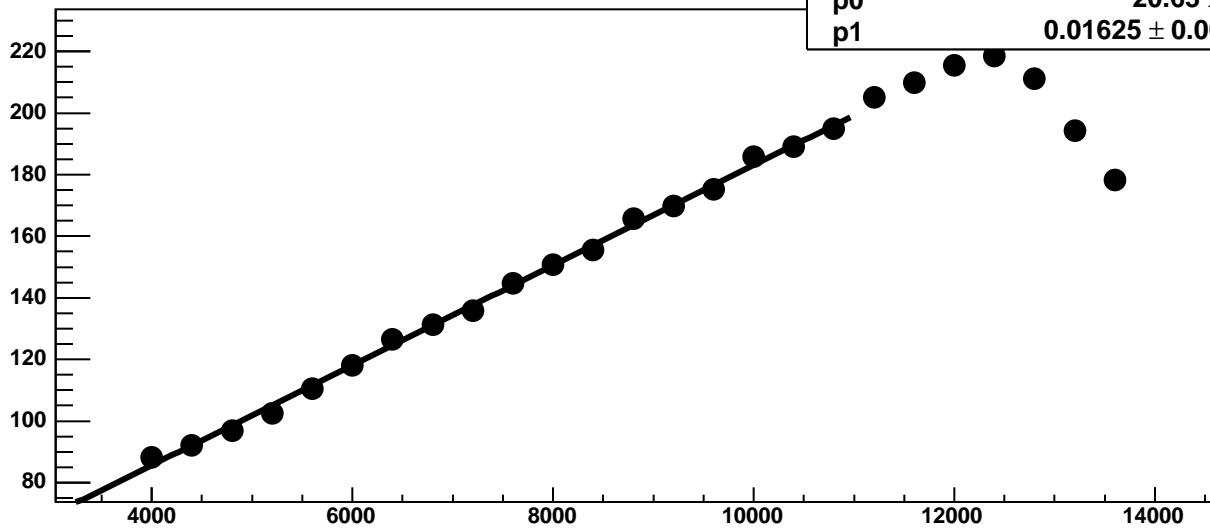
Chip 3, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



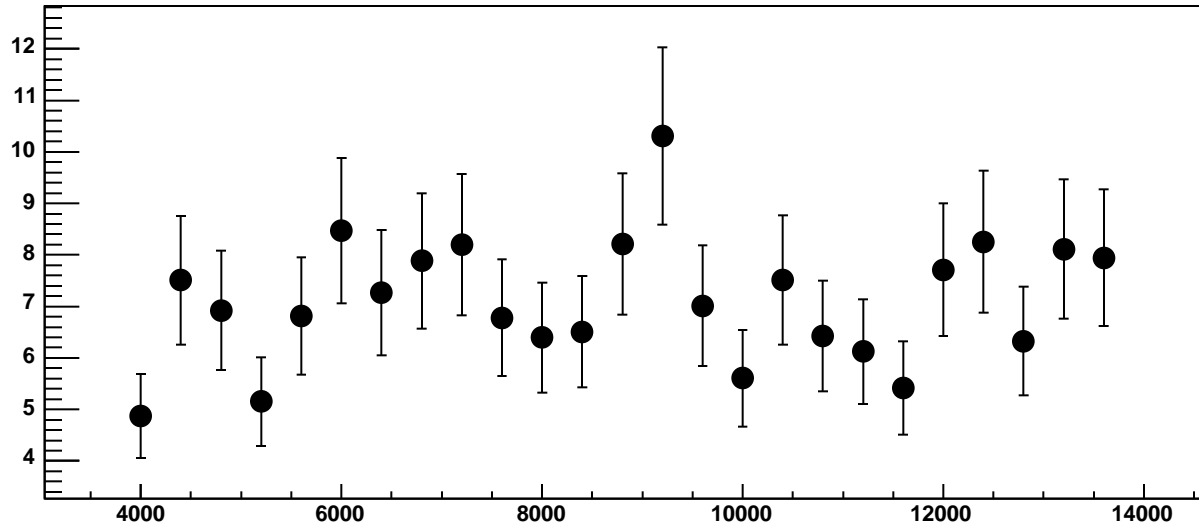
Chip 3, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC



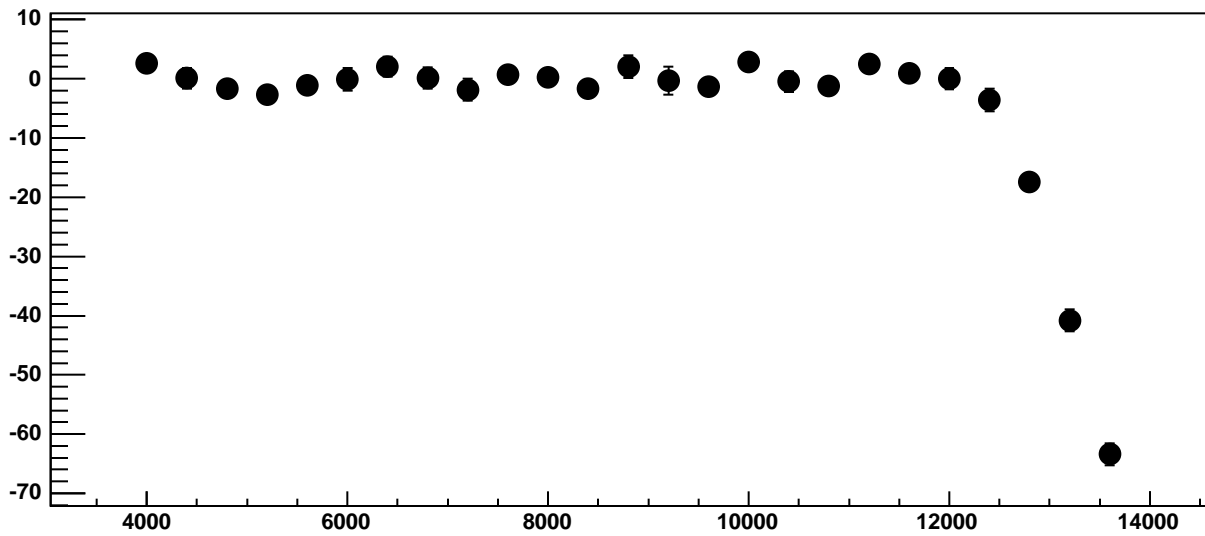
Chip 3, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



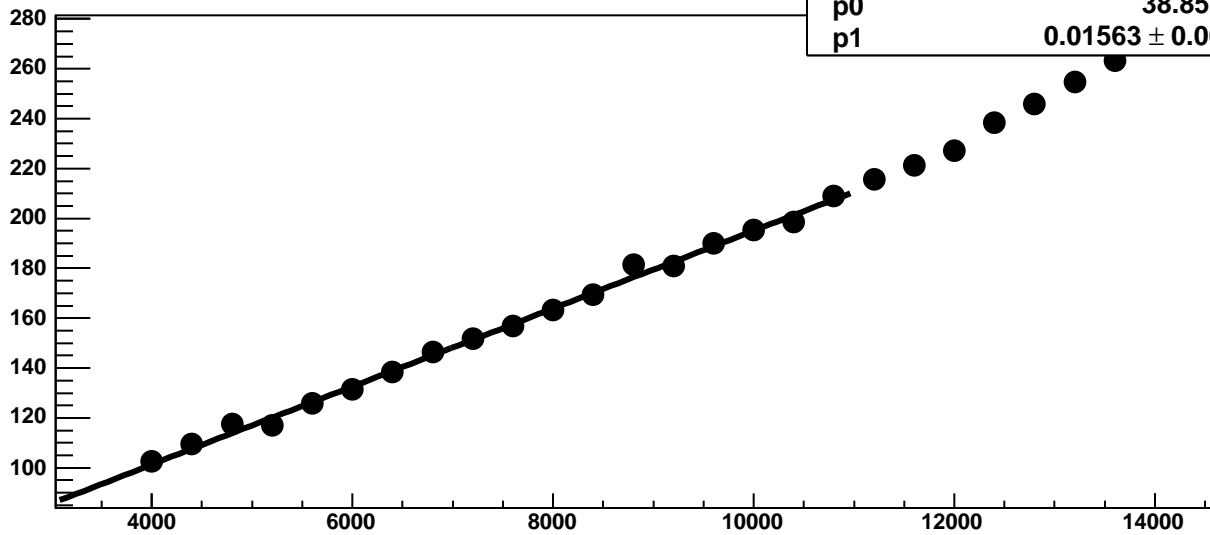
Chip 3, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

27.37 / 16

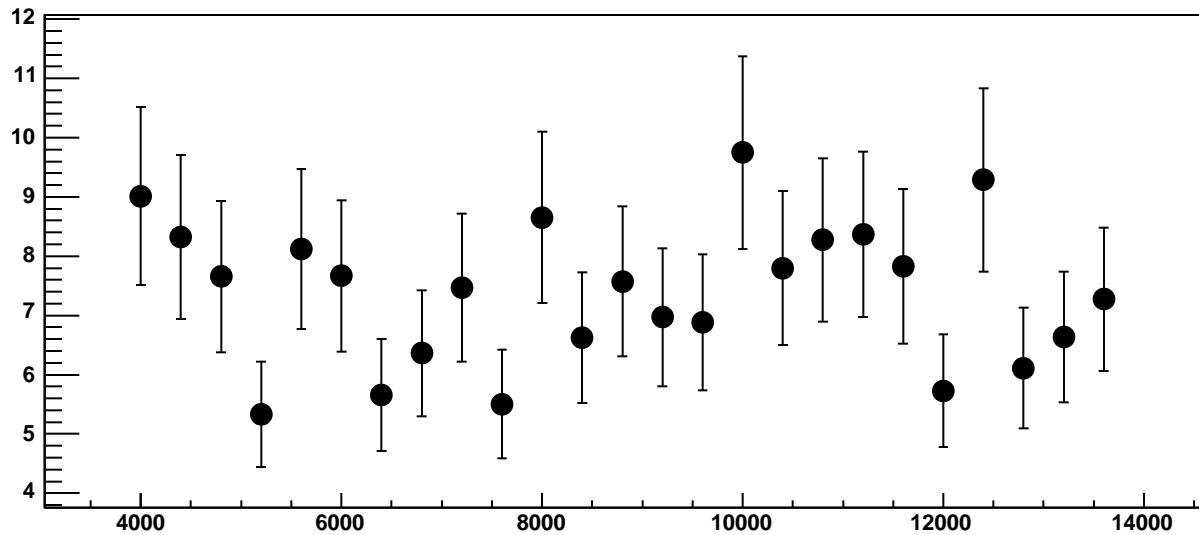
p0

38.85 ± 1.52

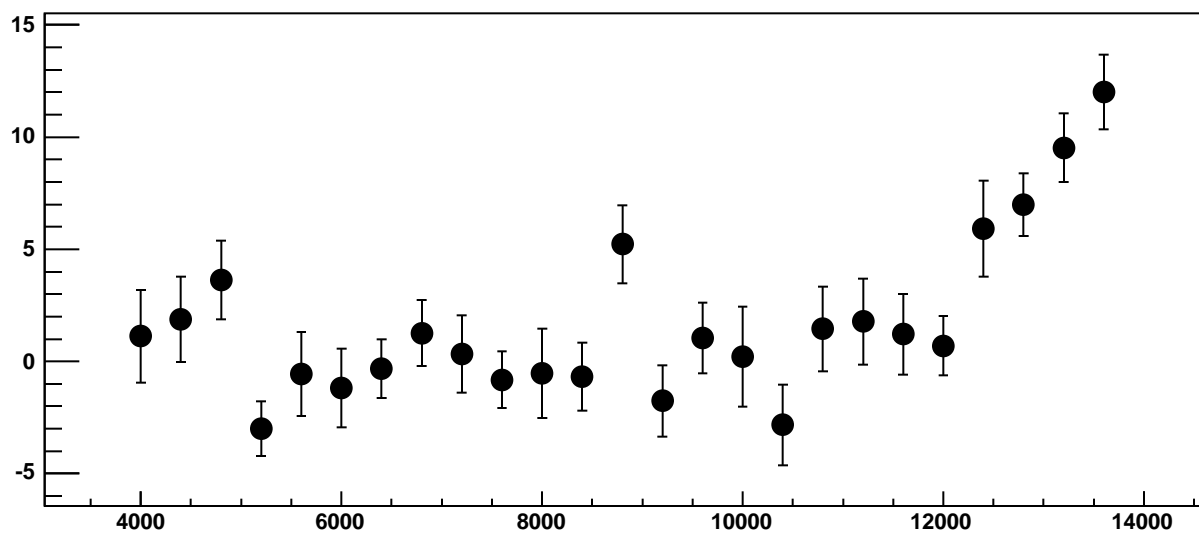
p1

0.01563 ± 0.0002014

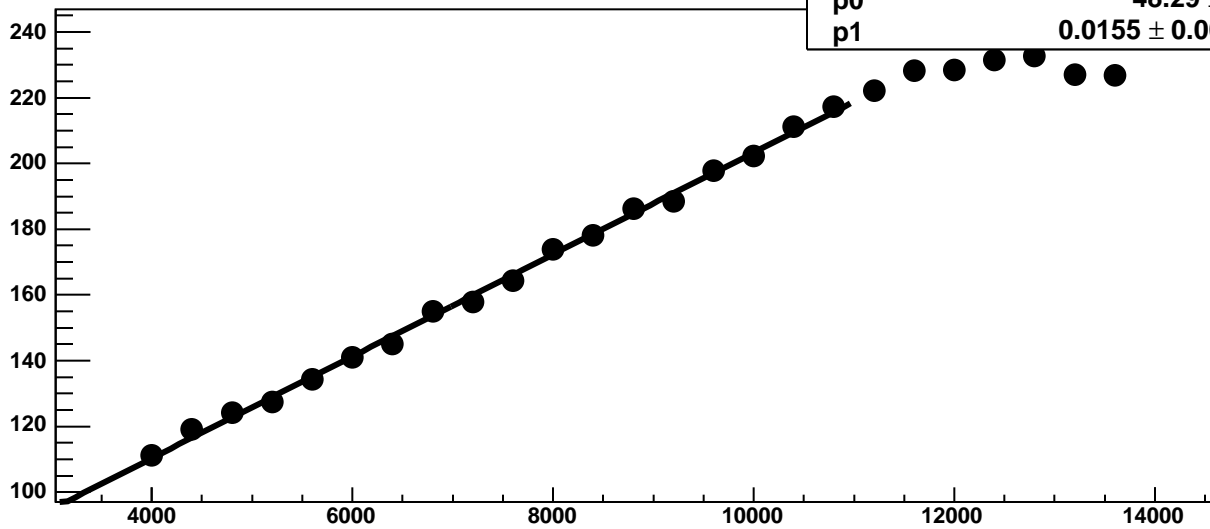
Chip 3, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



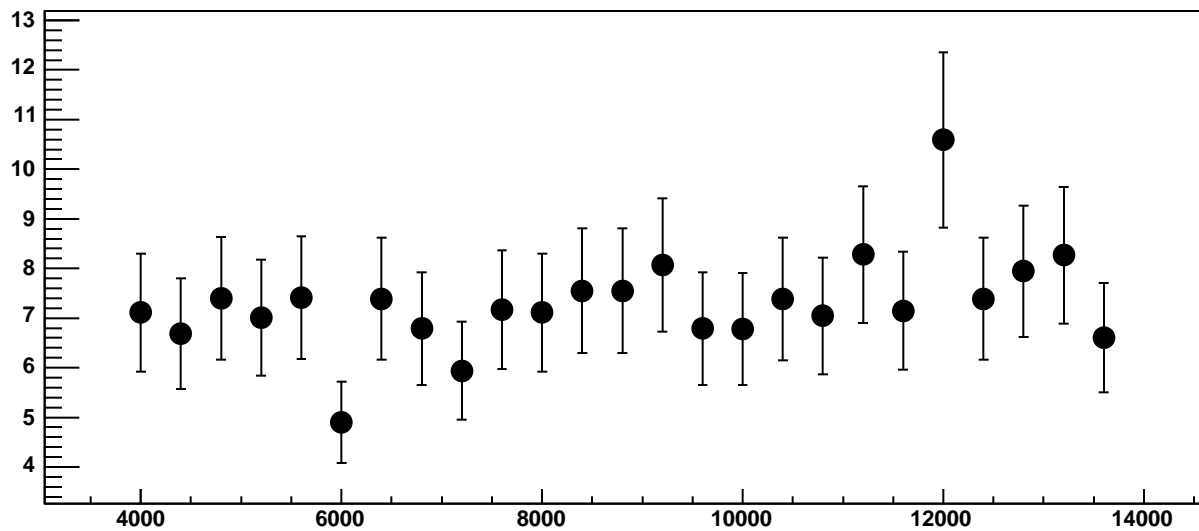
Chip 3, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC



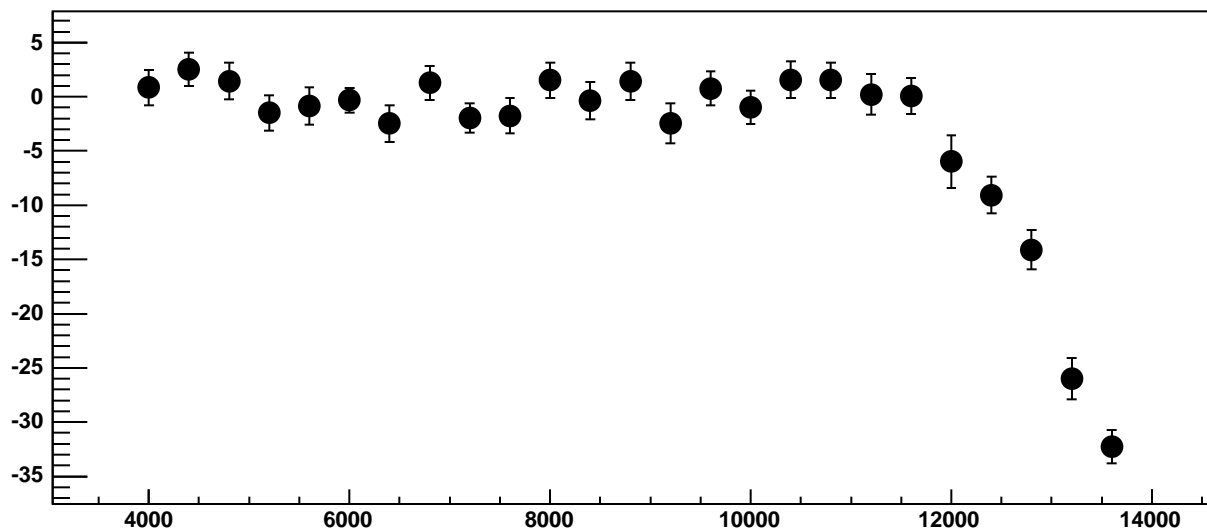
Chip 3, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



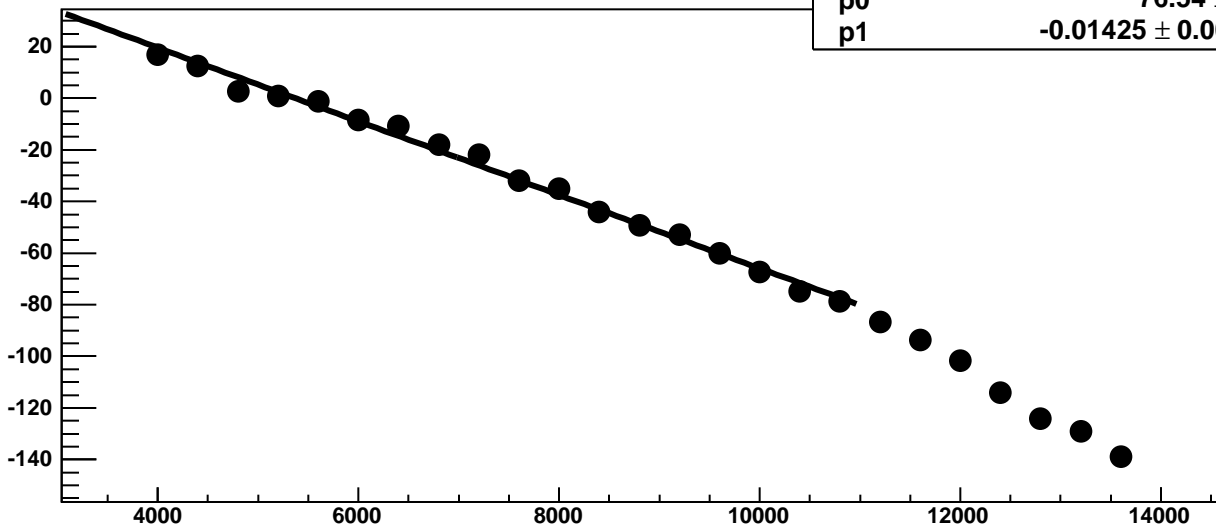
Chip 3, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

39.12 / 16

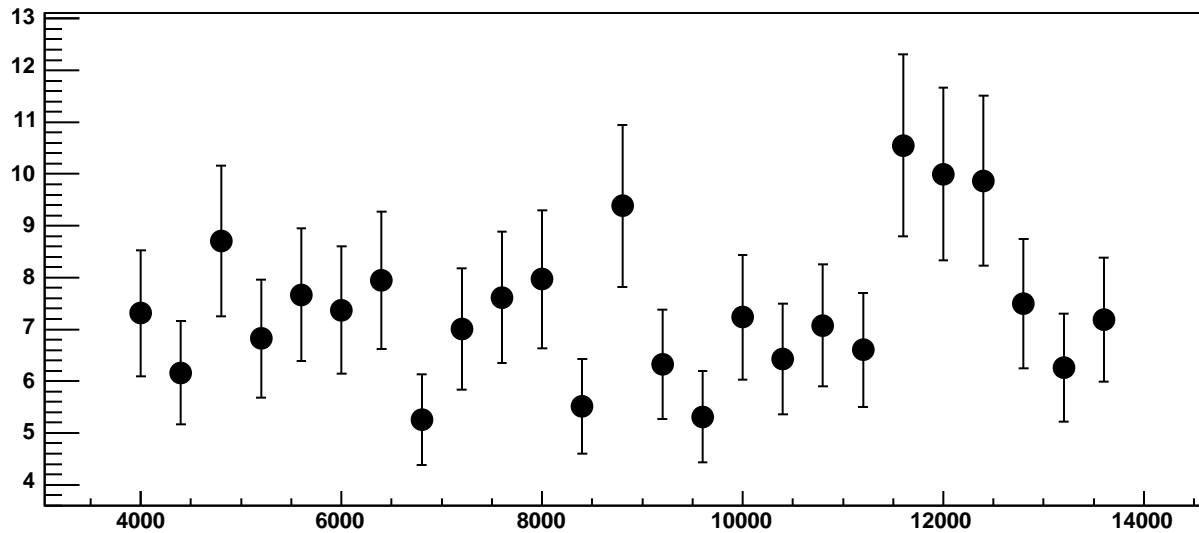
p0

76.54 ± 1.387

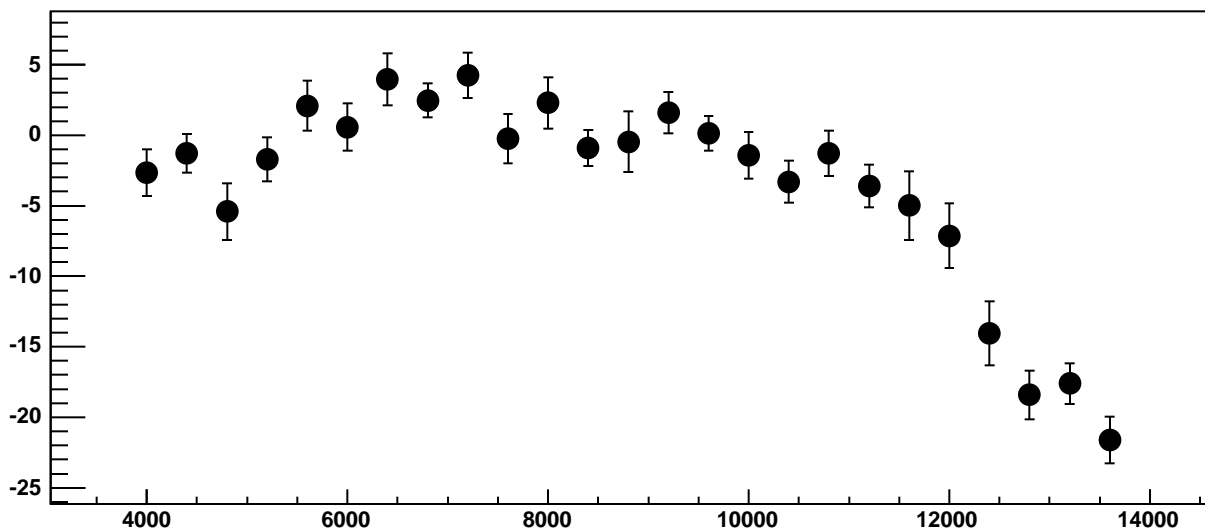
p1

-0.01425 ± 0.0001777

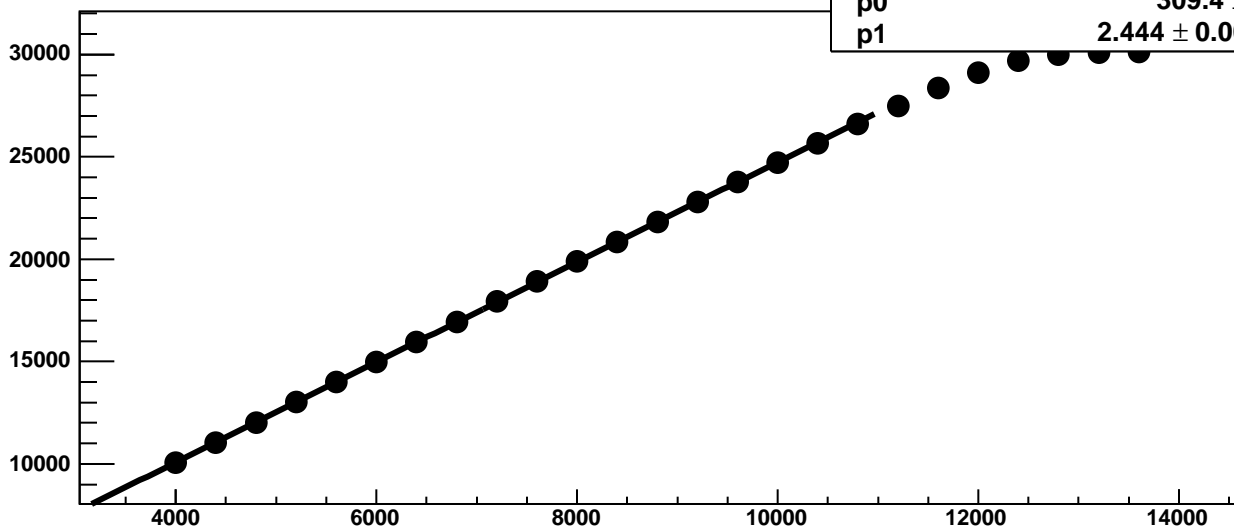
Chip 3, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



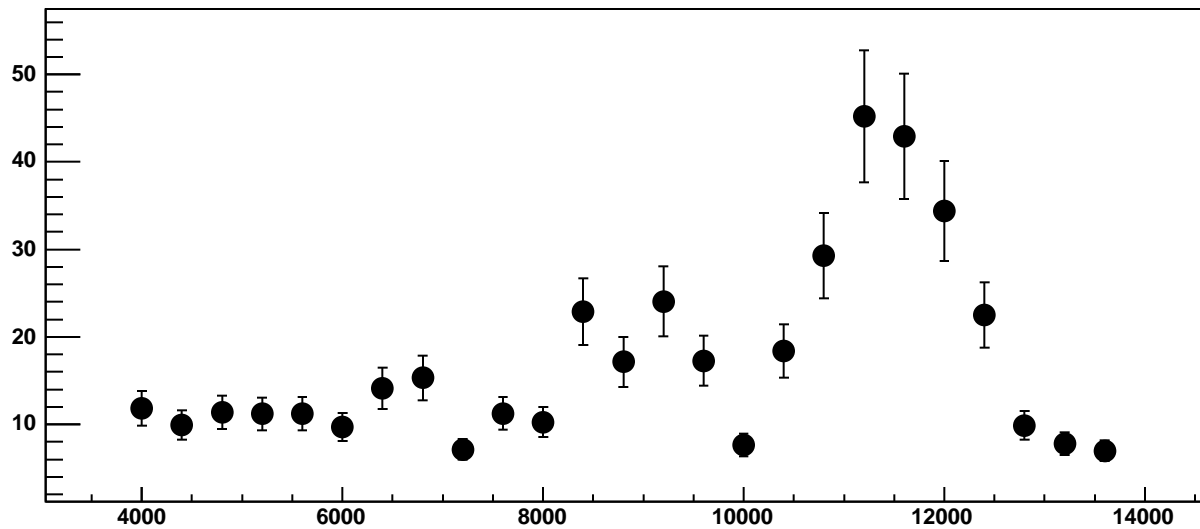
Chip 3, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



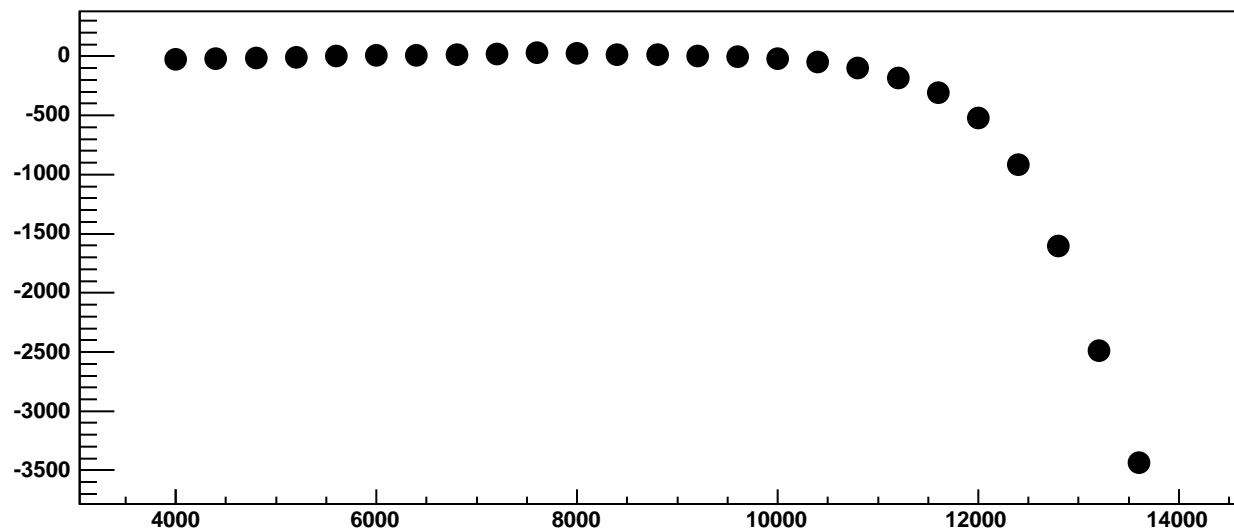
Chip 3, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC



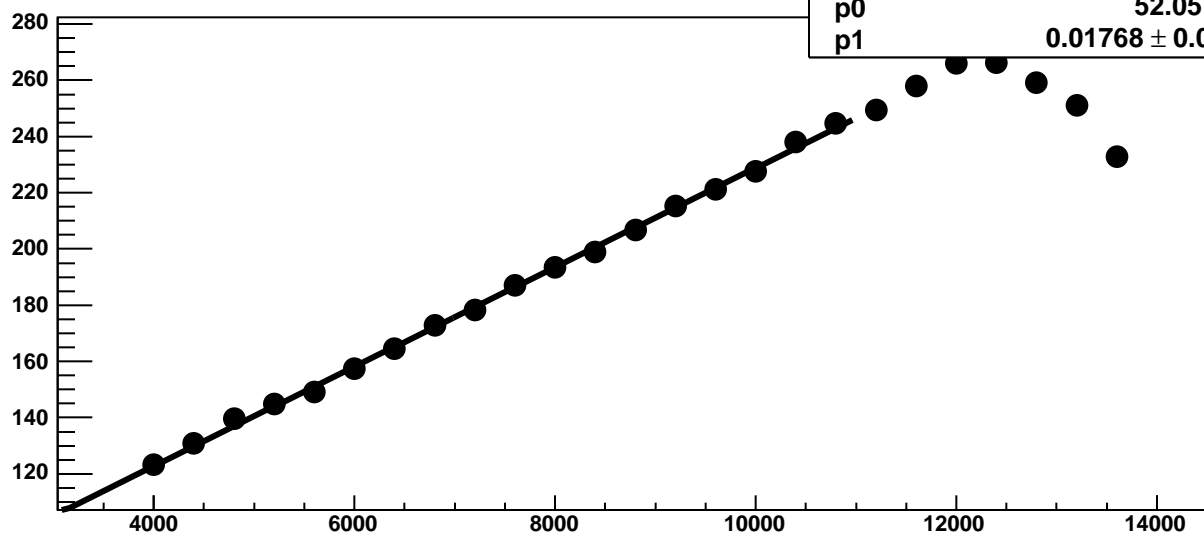
Chip 3, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



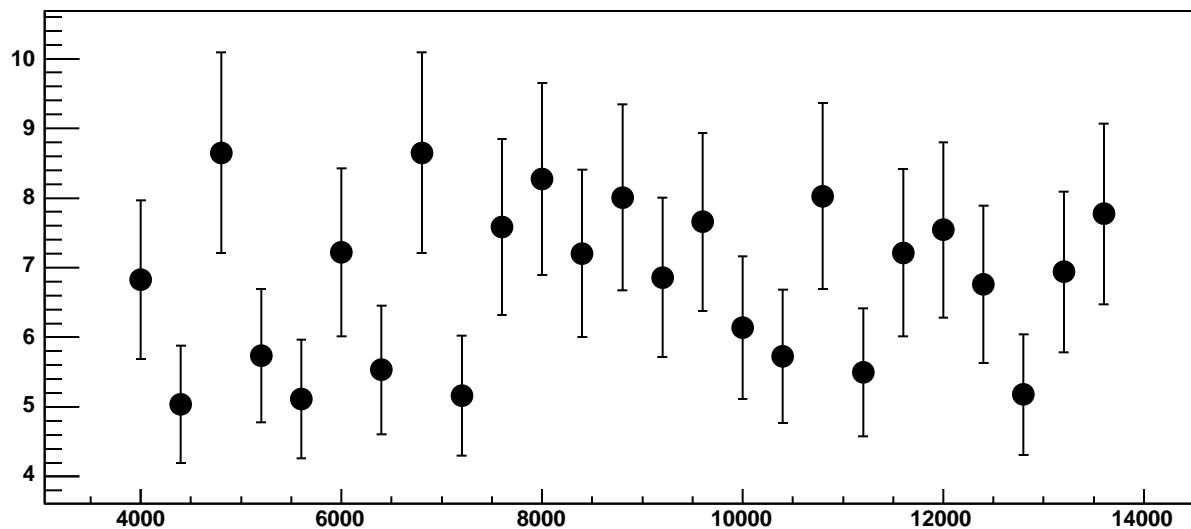
Chip 3, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC



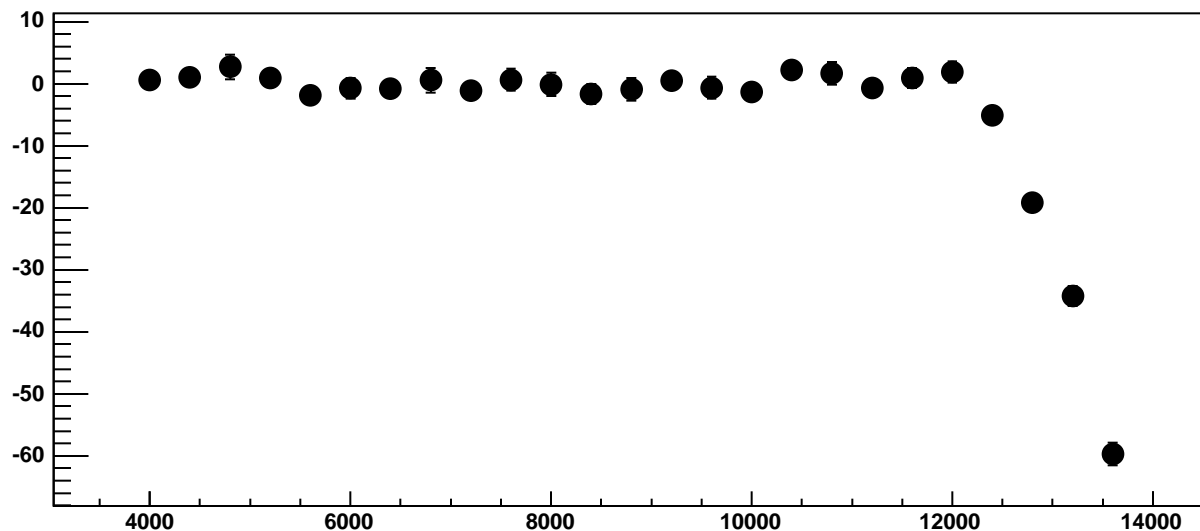
Chip 3, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



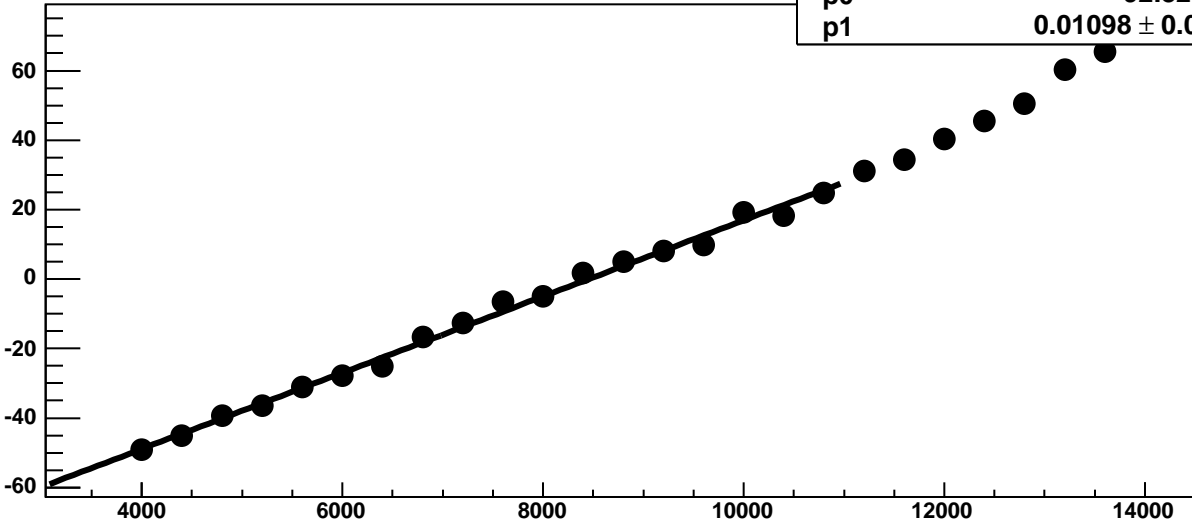
Chip 3, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



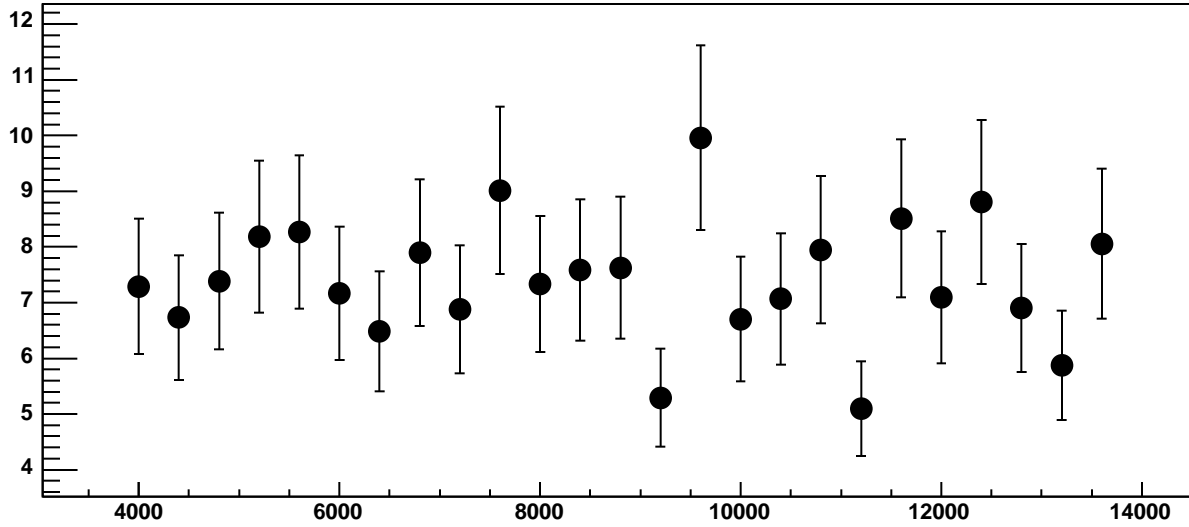
Chip 3, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



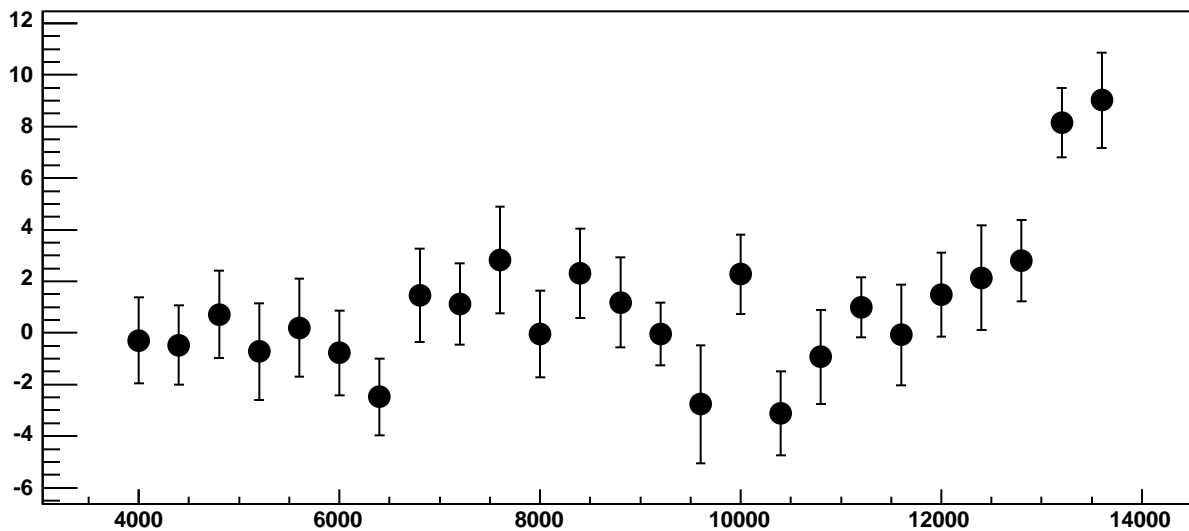
Chip 3, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



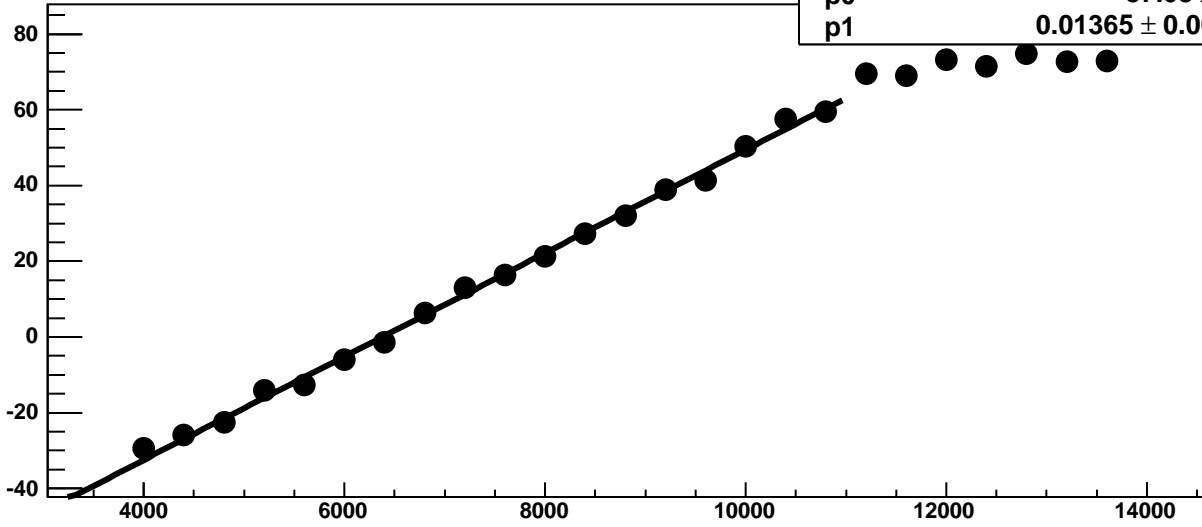
Chip 3, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



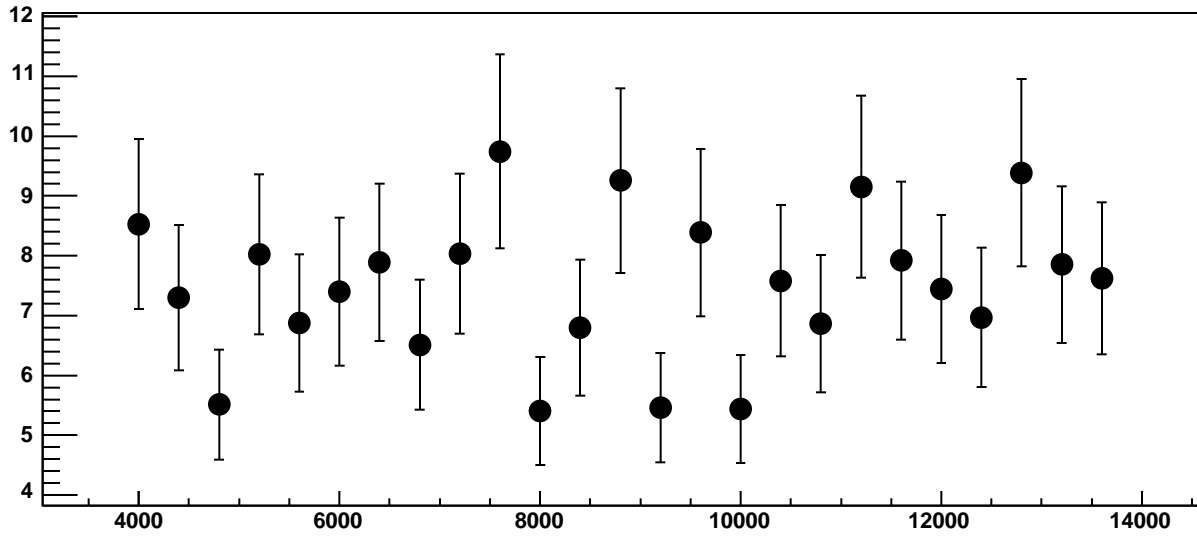
Chip 3, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



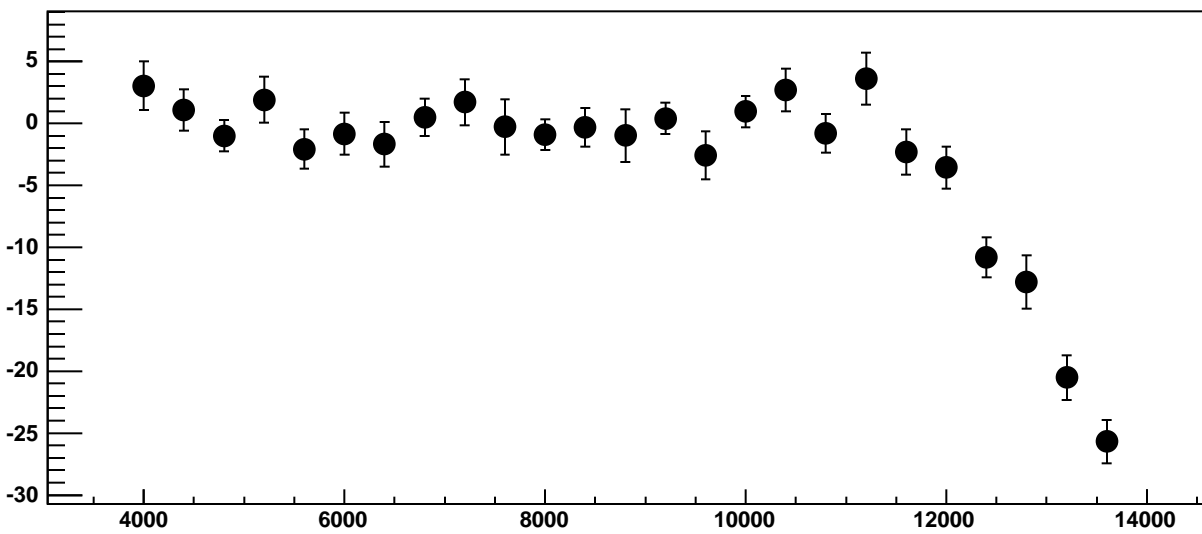
Chip 3, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC



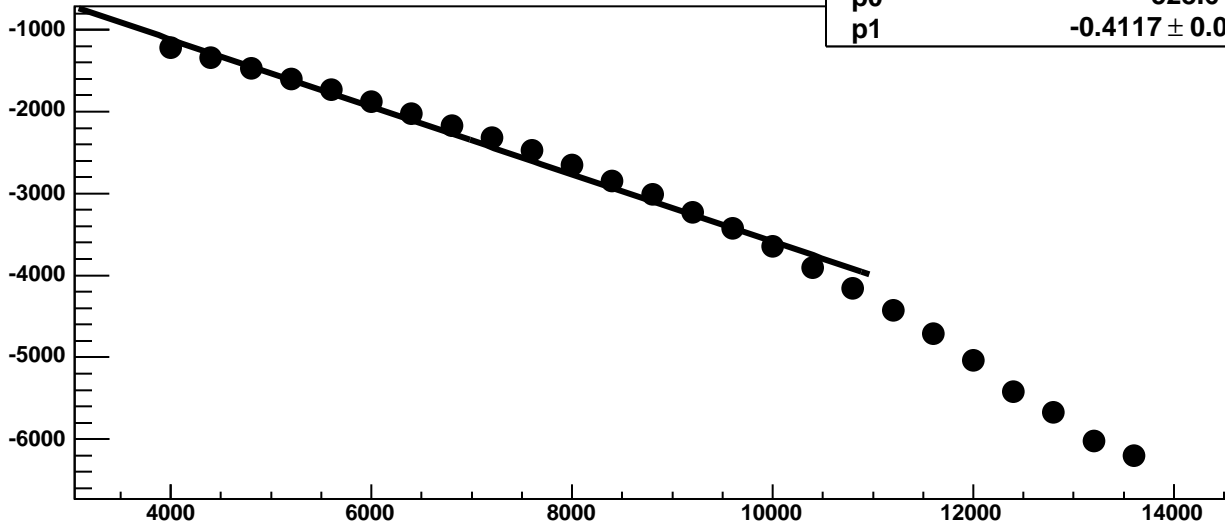
Chip 3, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



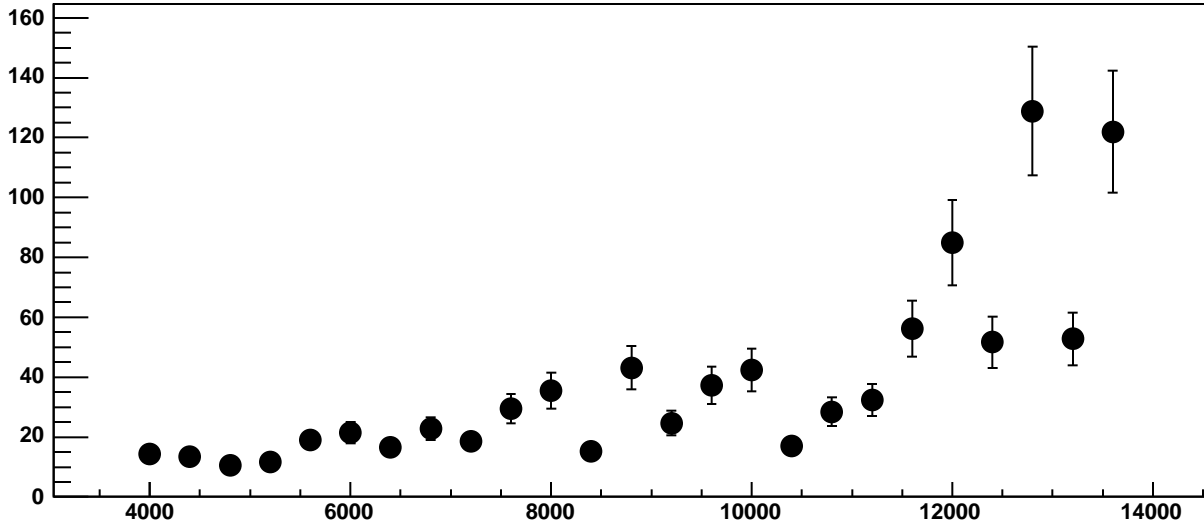
Chip 3, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC



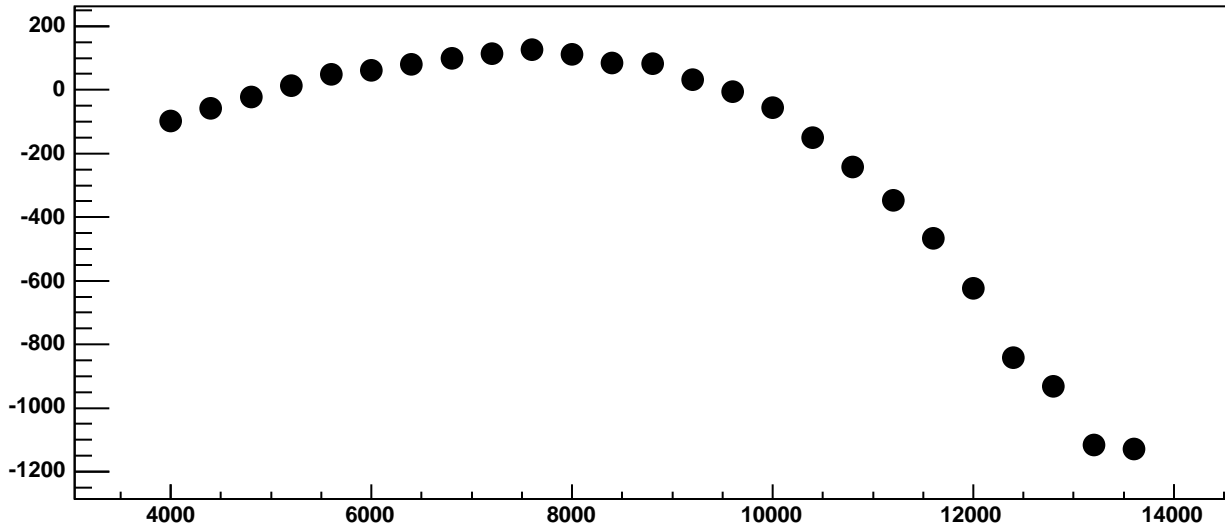
Chip 3, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



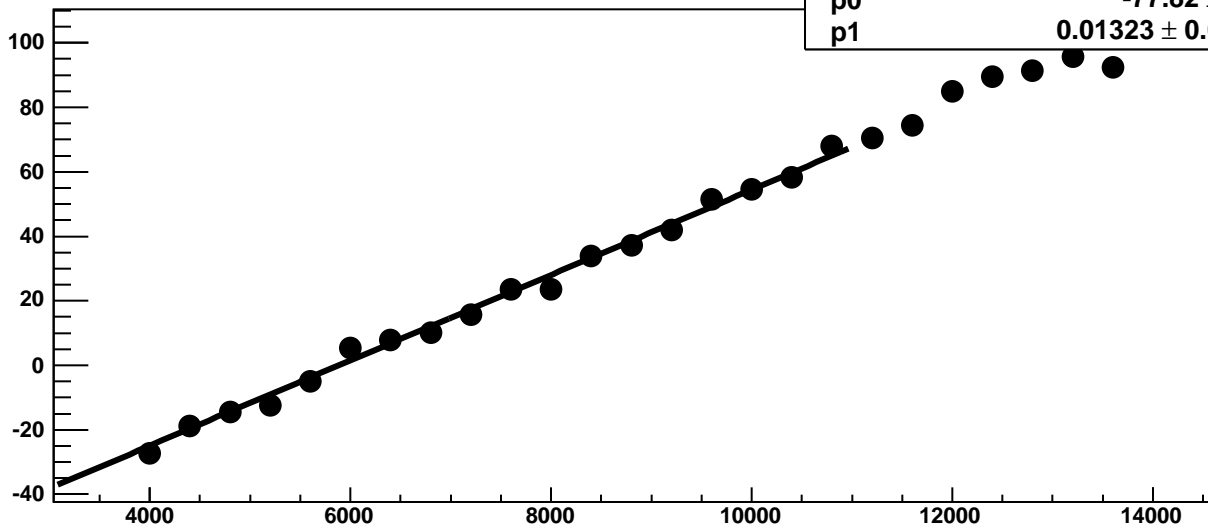
Chip 3, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



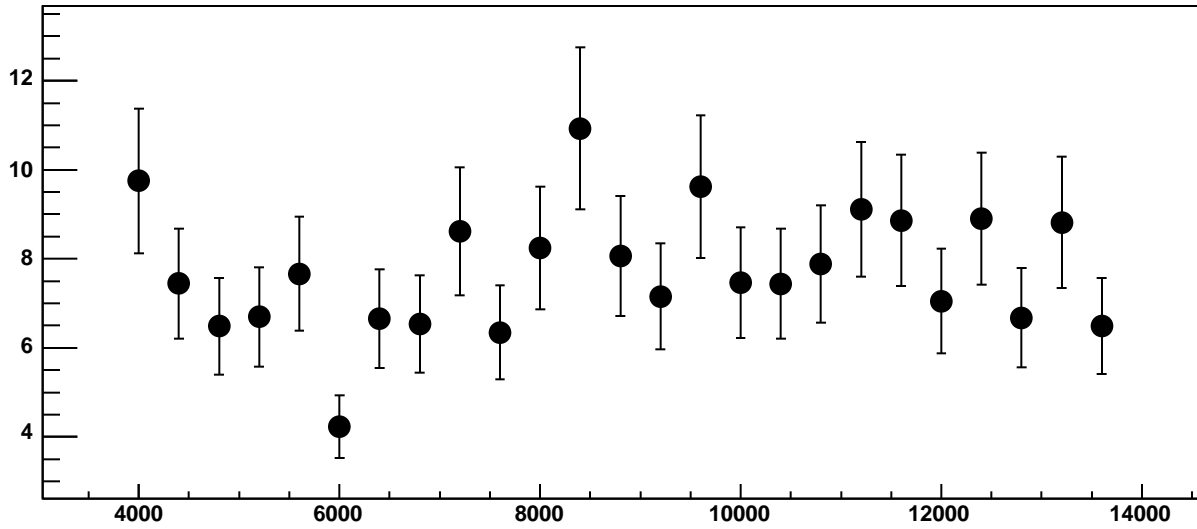
Chip 3, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



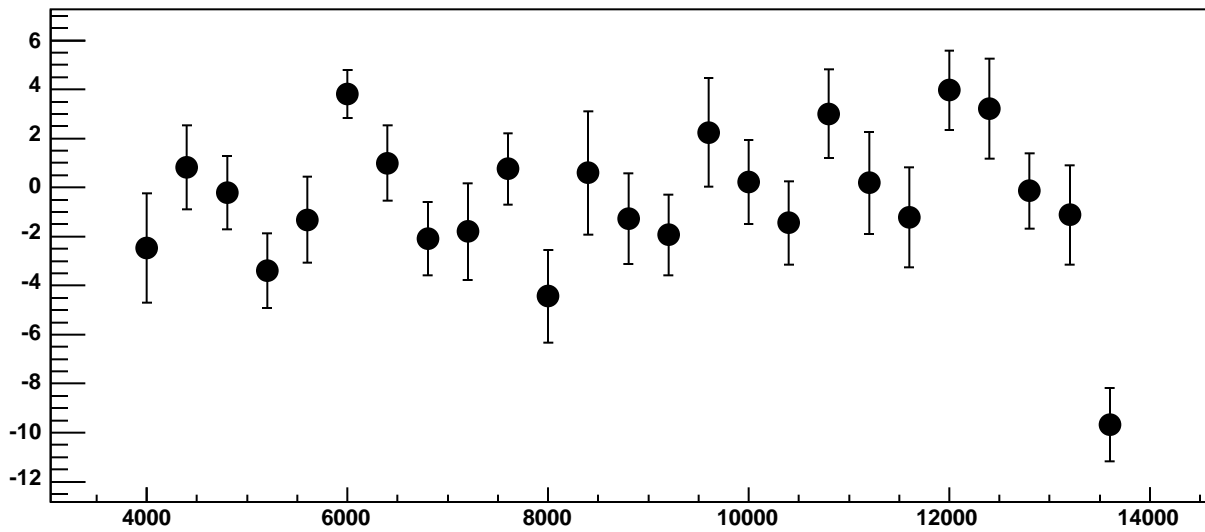
Chip 3, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



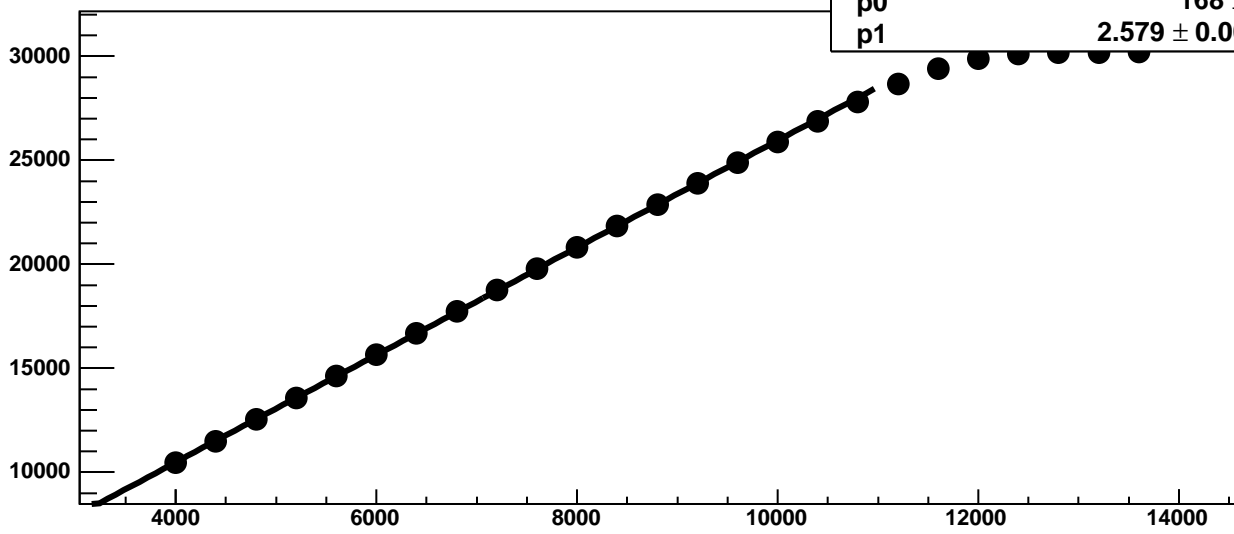
Chip 3, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

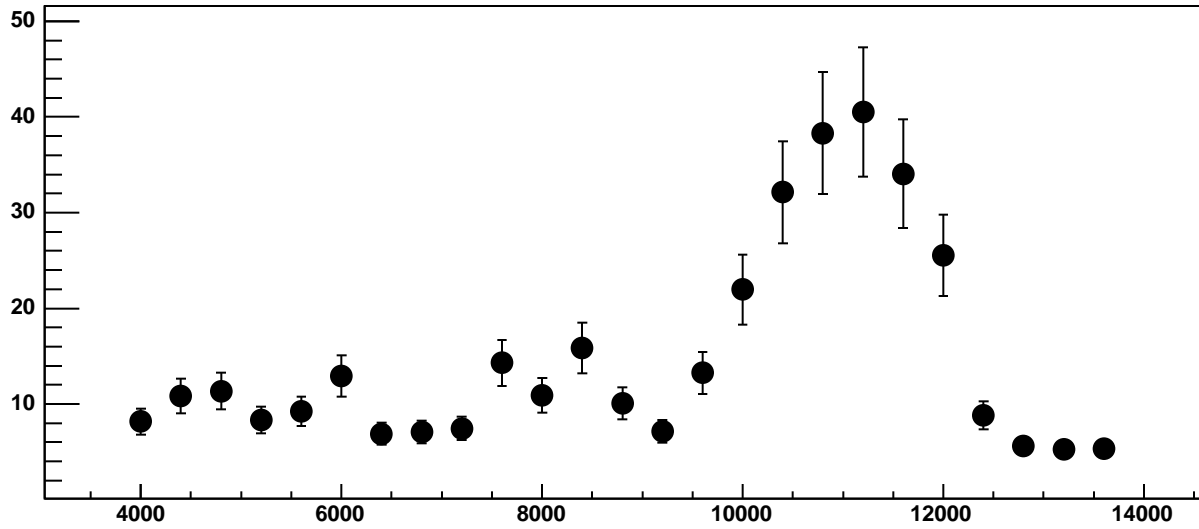


Chip 3, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC

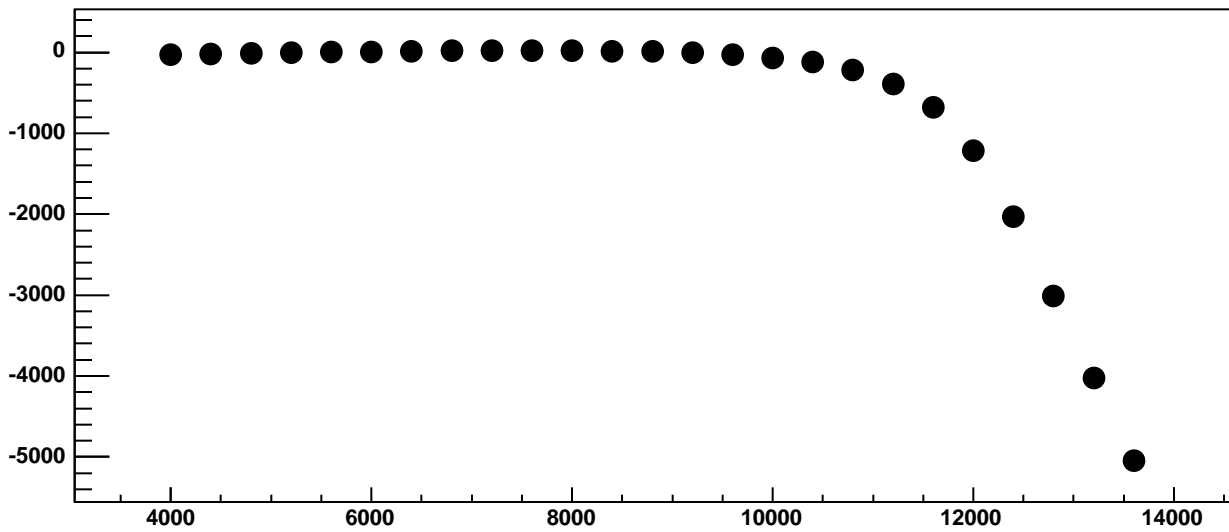


χ^2 / ndf 1824 / 16
p0 168 ± 2.204
p1 2.579 ± 0.0003155

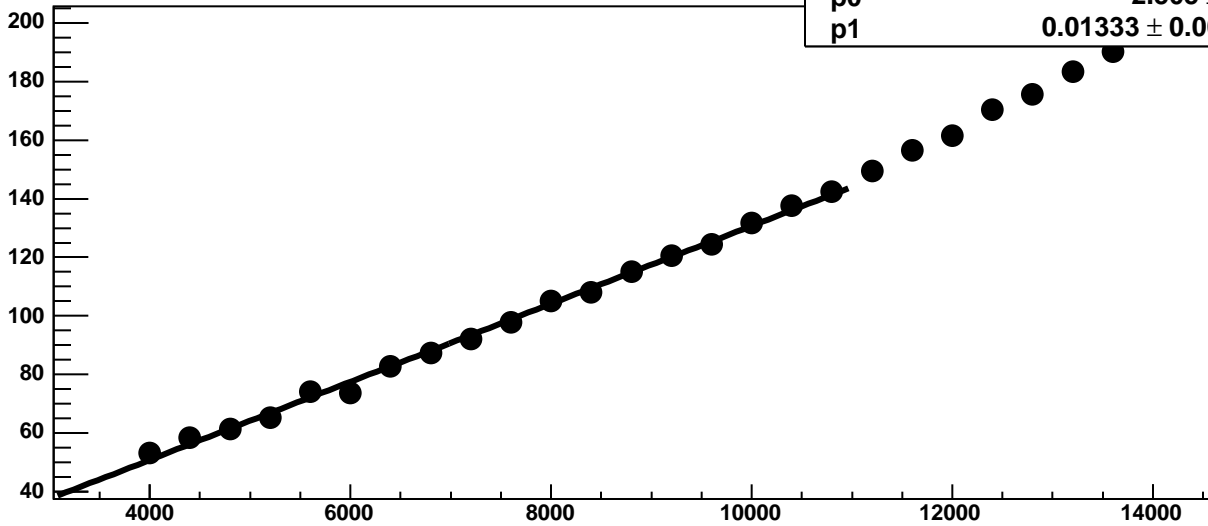
Chip 3, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC

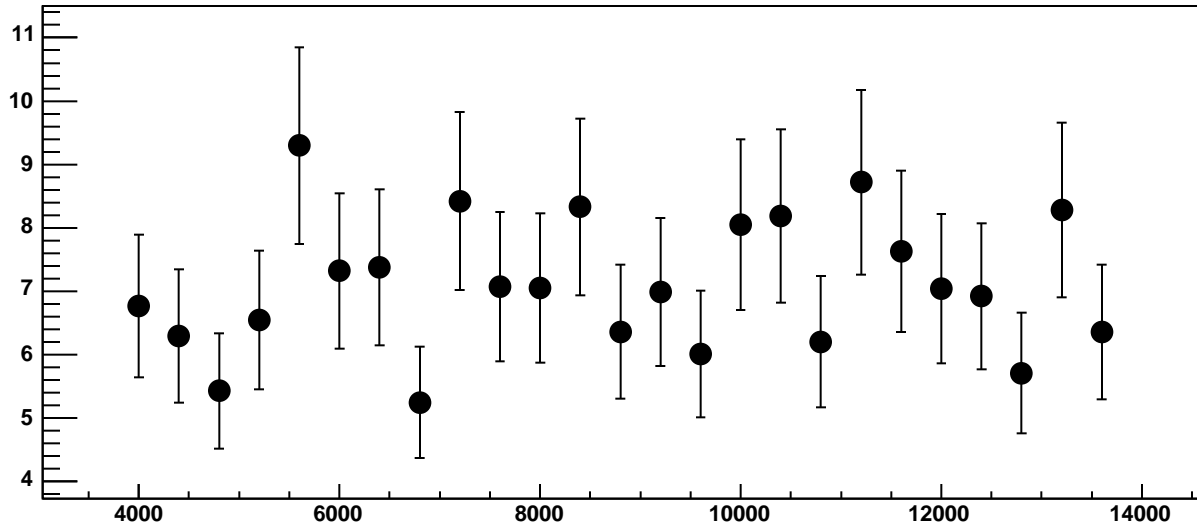


Chip 3, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

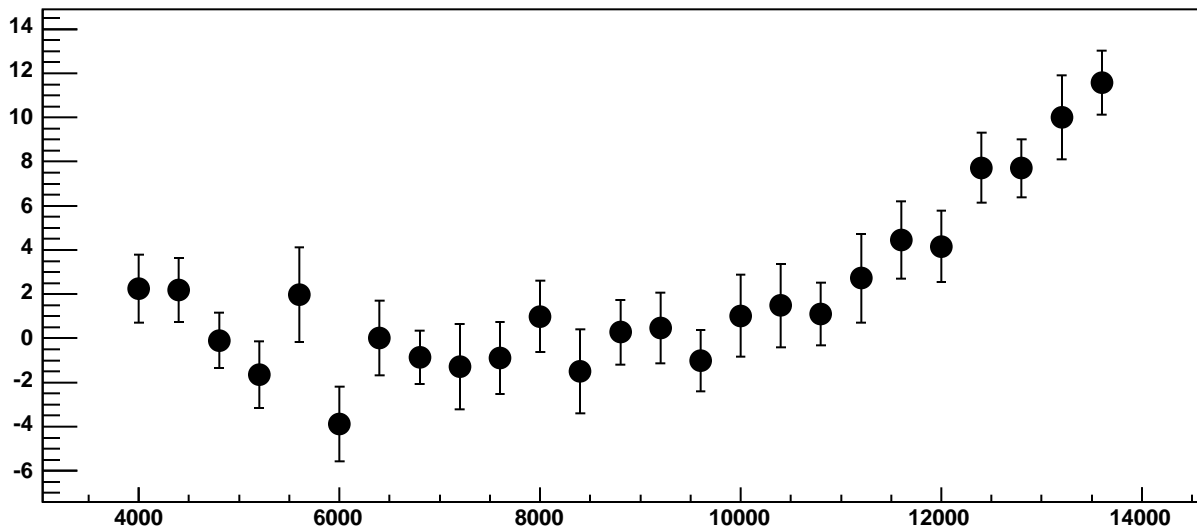


χ^2 / ndf 16.2 / 16
p0 -2.505 ± 1.323
p1 0.01333 ± 0.0001743

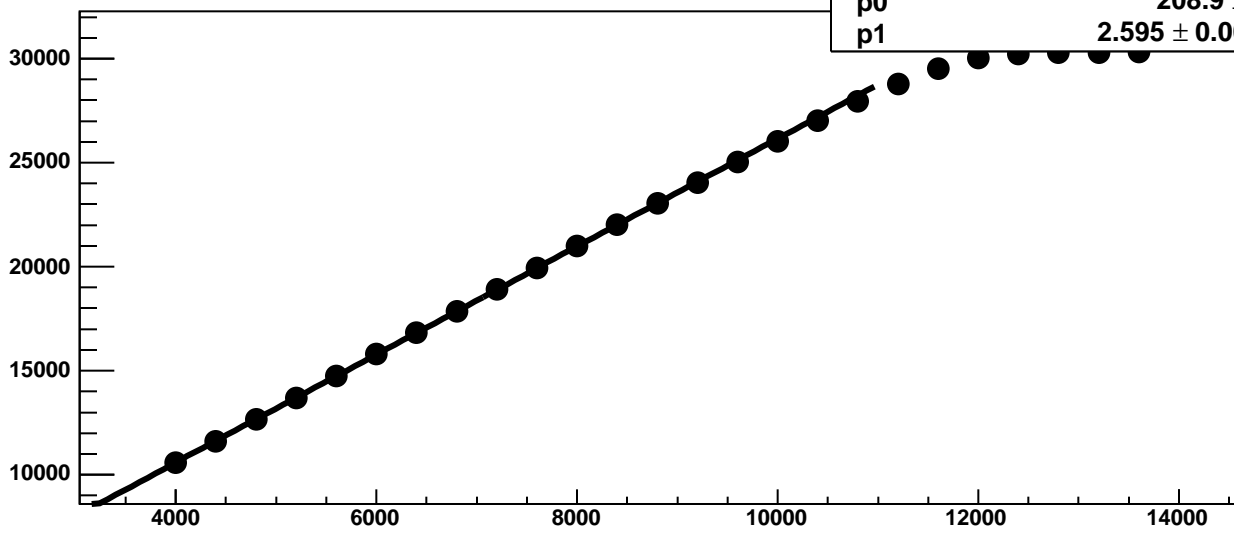
Chip 3, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



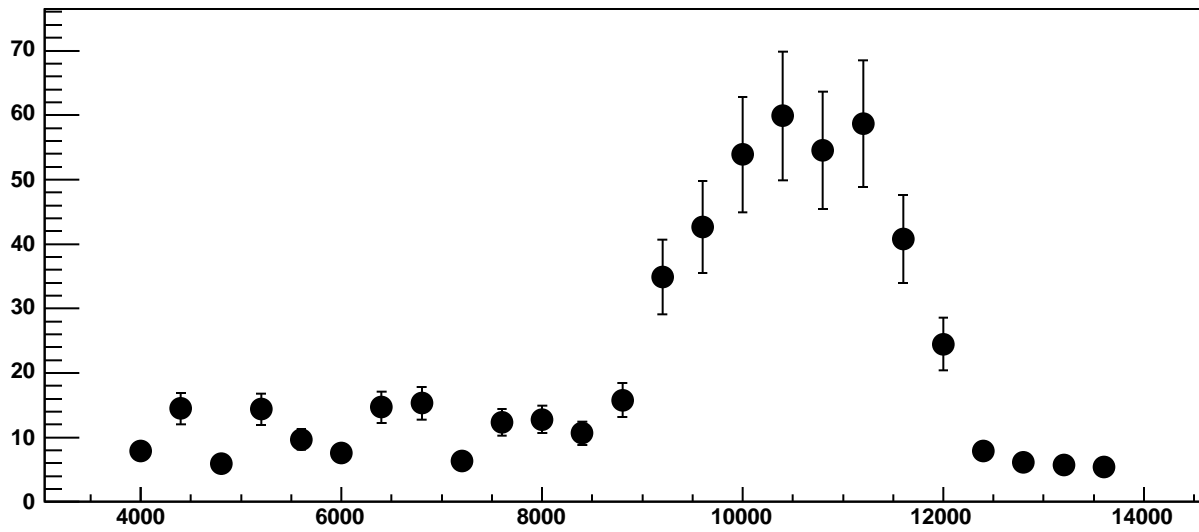
Chip 3, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



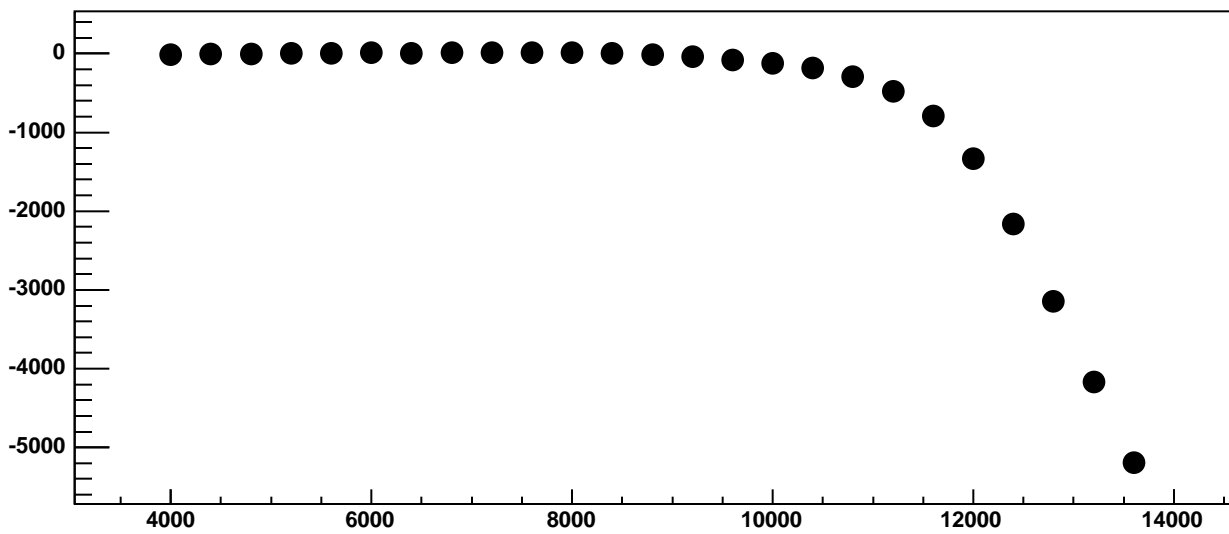
Chip 3, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC



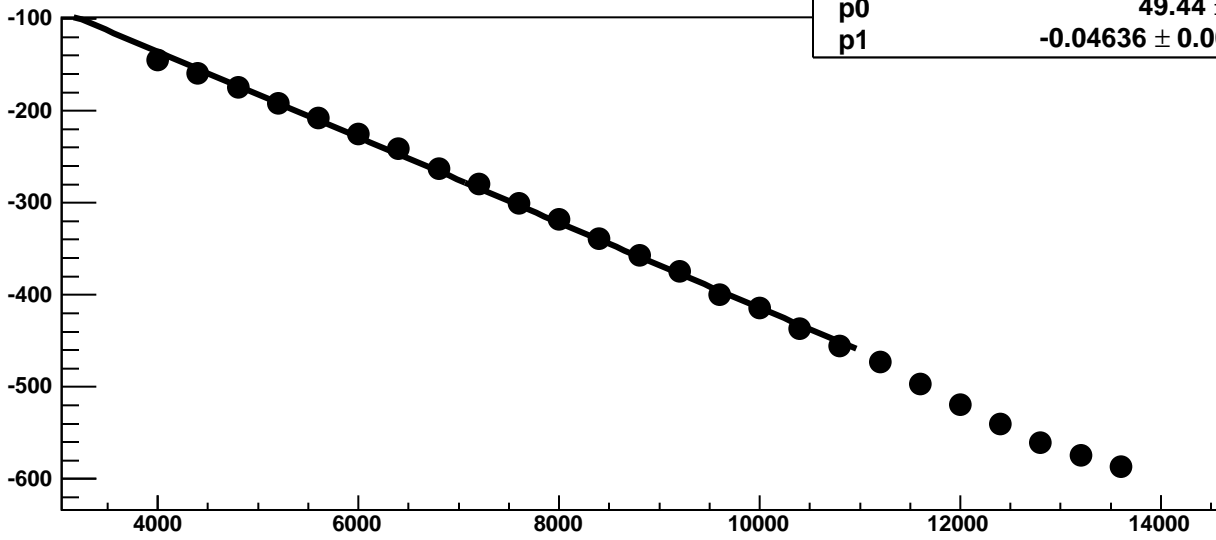
Chip 3, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC



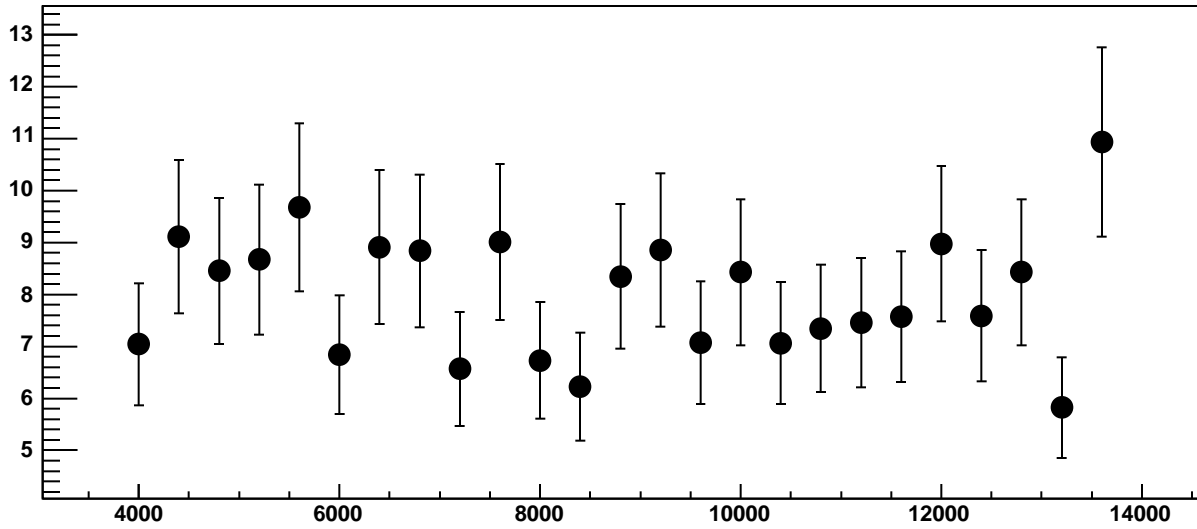
Chip 3, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC



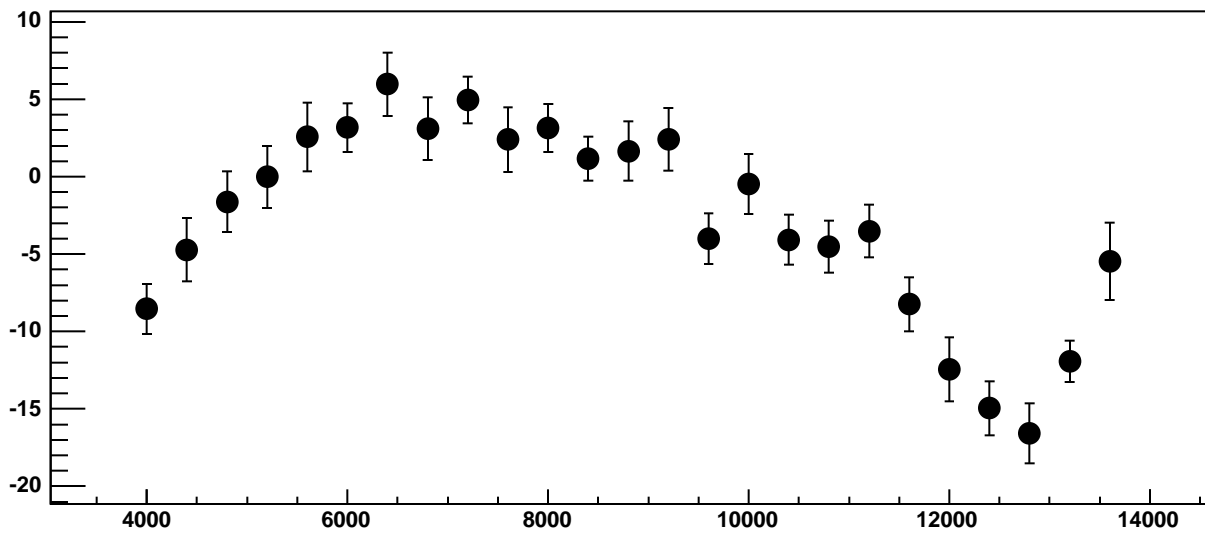
Chip 3, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



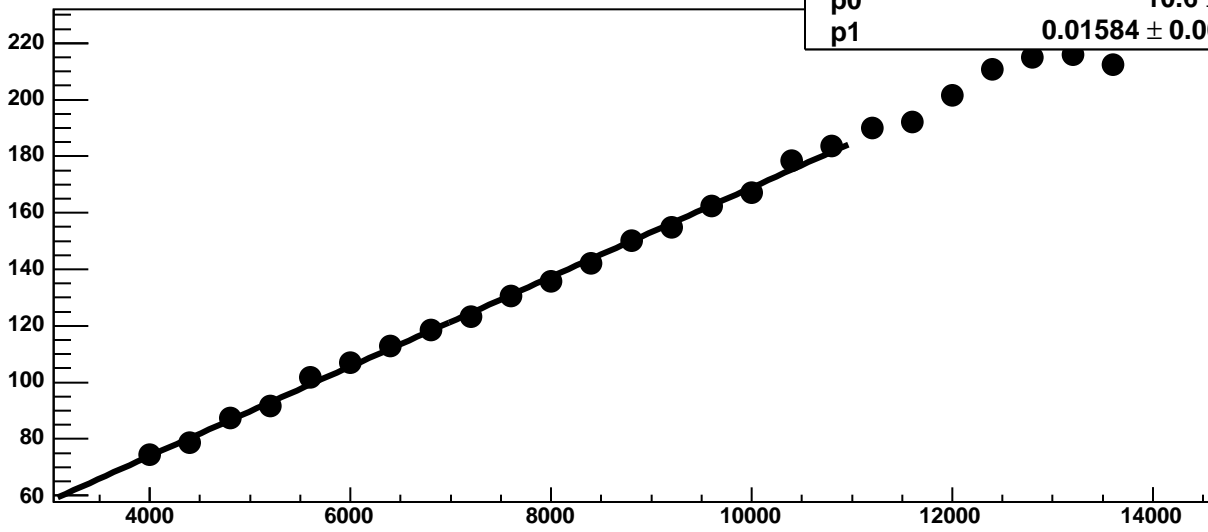
Chip 3, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



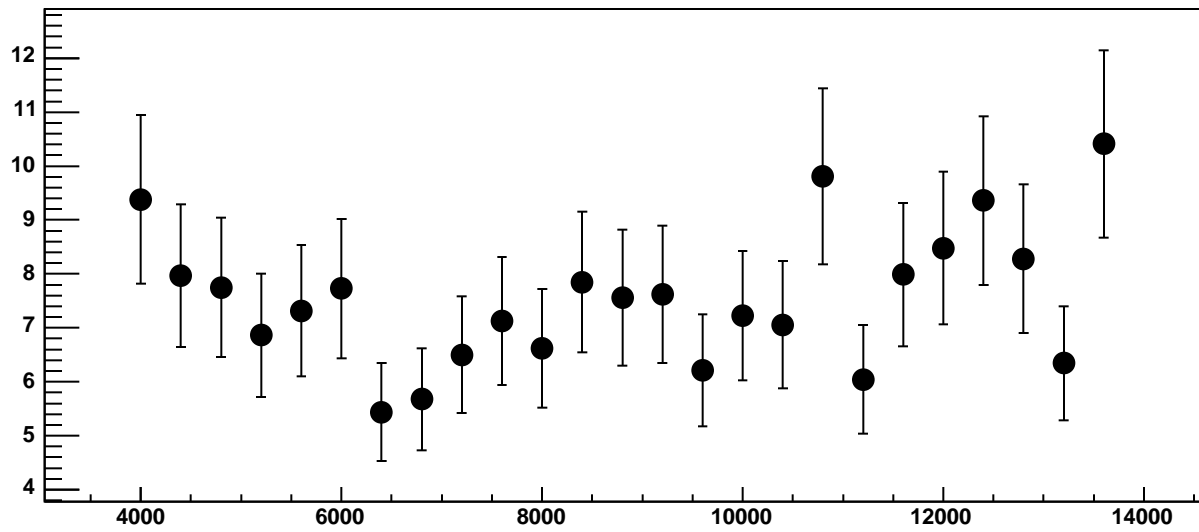
Chip 3, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



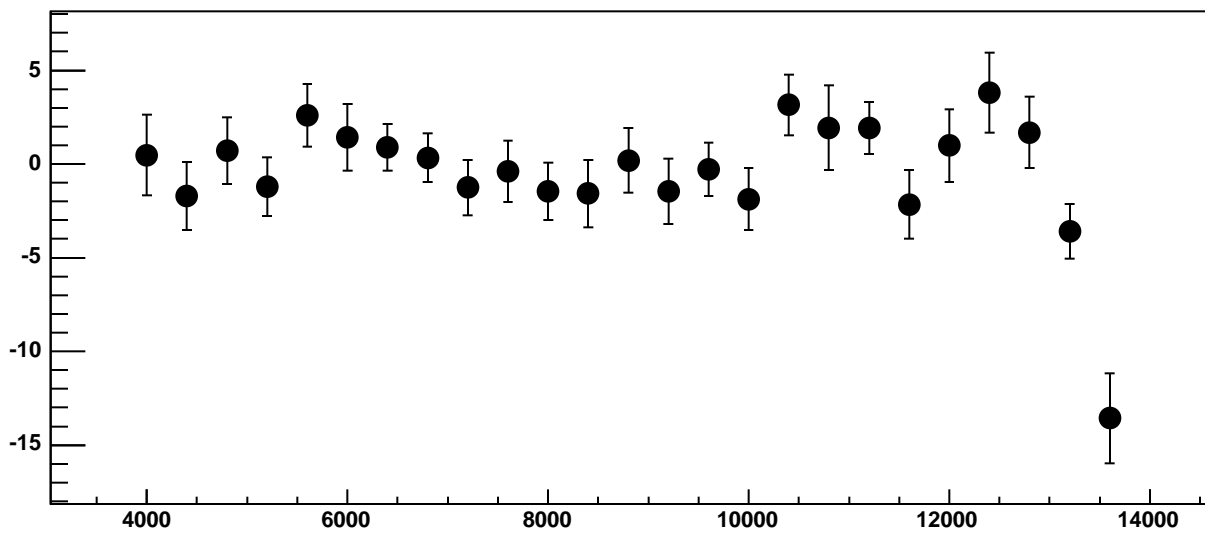
Chip 3, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC



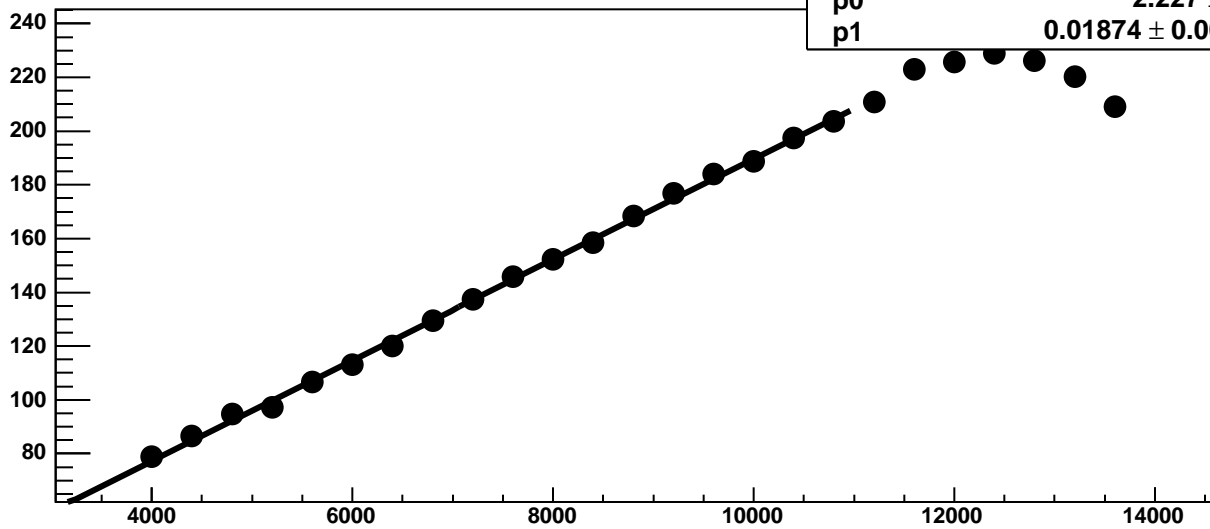
Chip 3, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



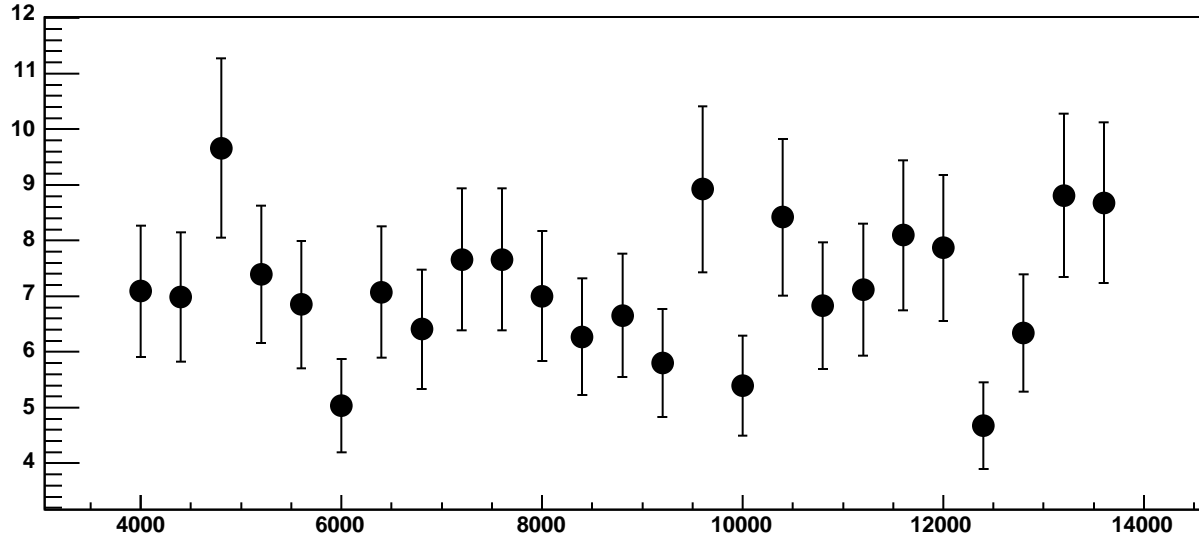
Chip 3, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



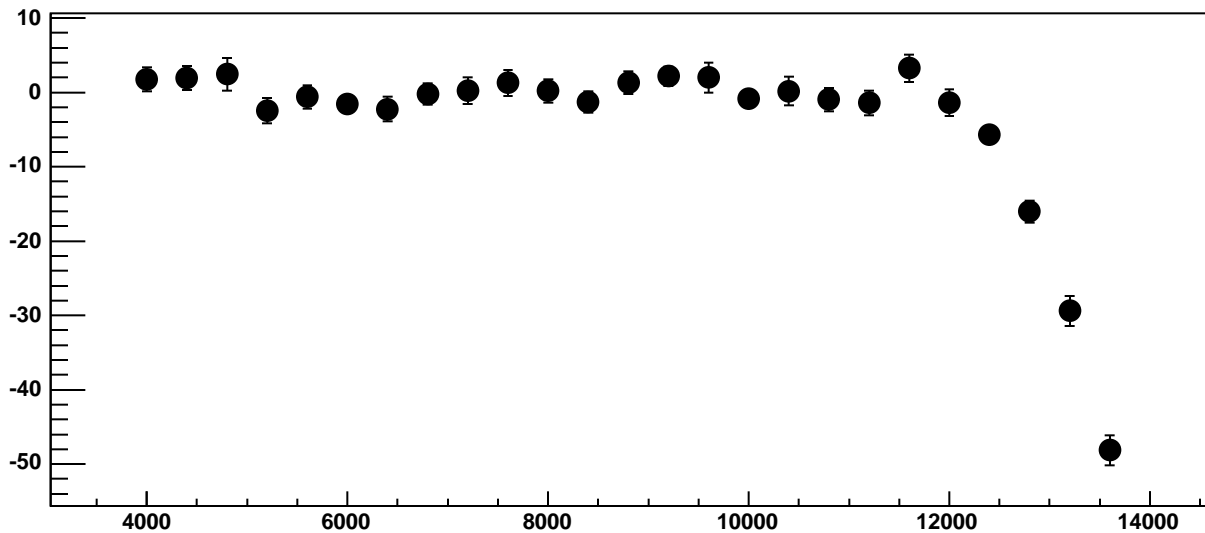
Chip 3, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC



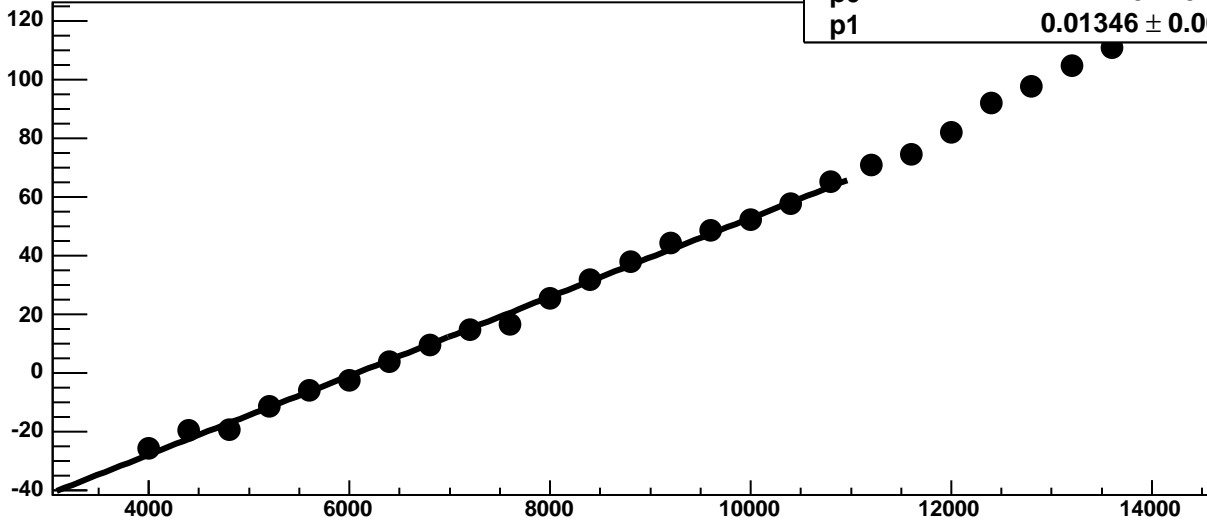
Chip 3, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

24.42 / 16

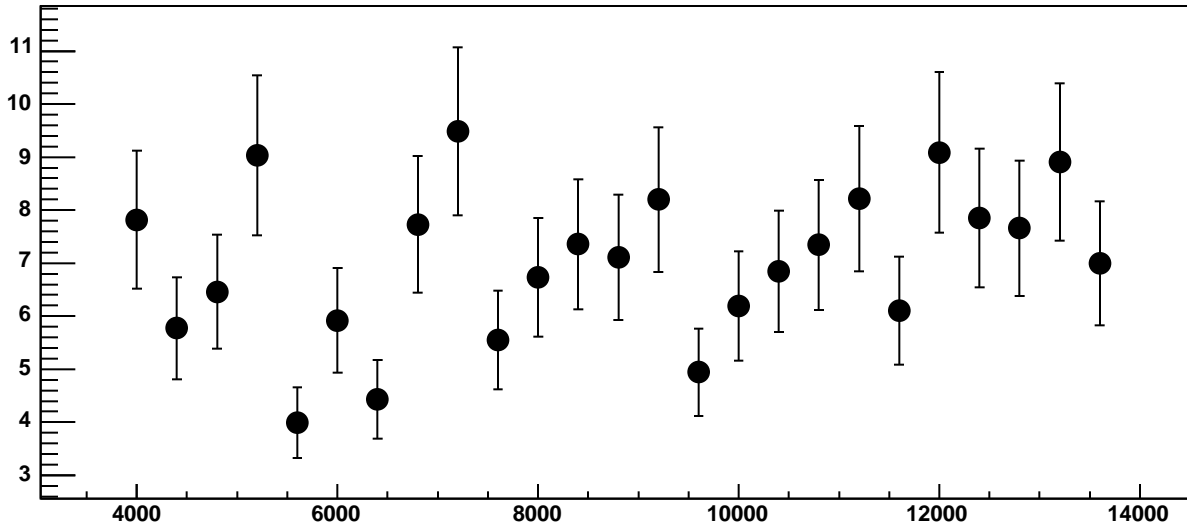
p0

-81.79 ± 1.257

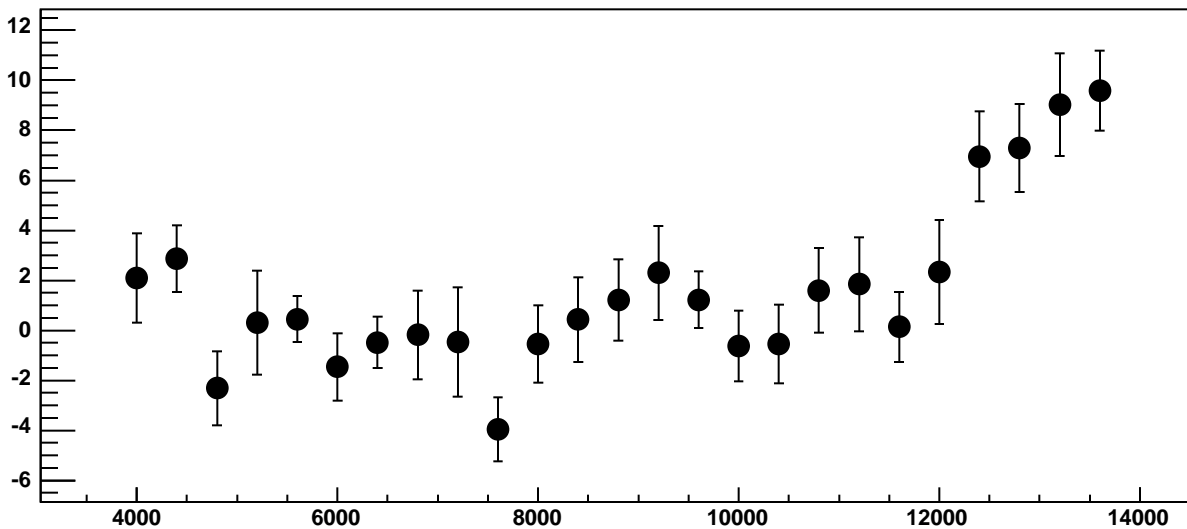
p1

0.01346 ± 0.0001673

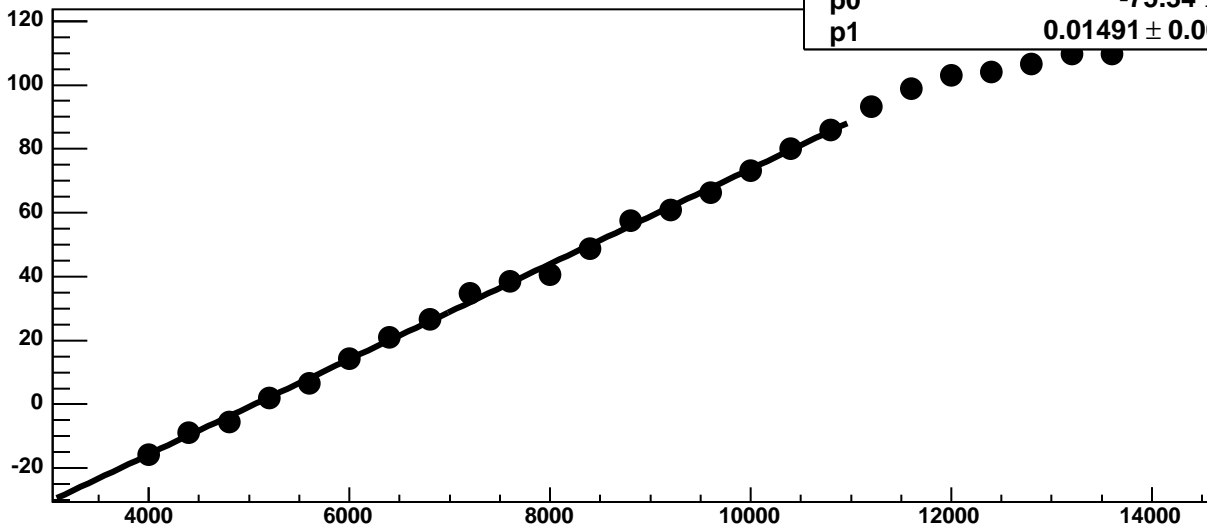
Chip 3, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



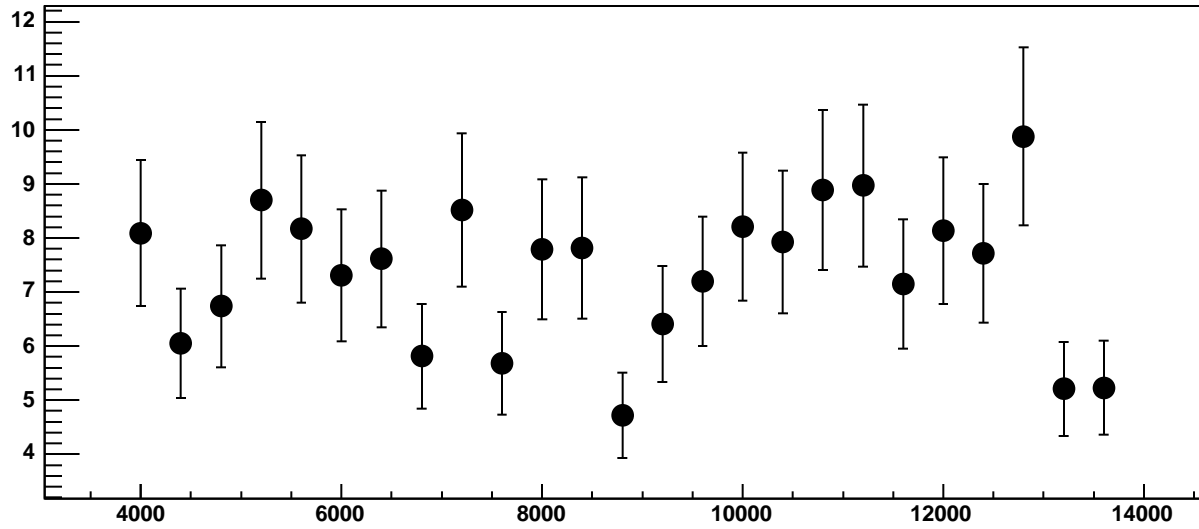
Chip 3, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



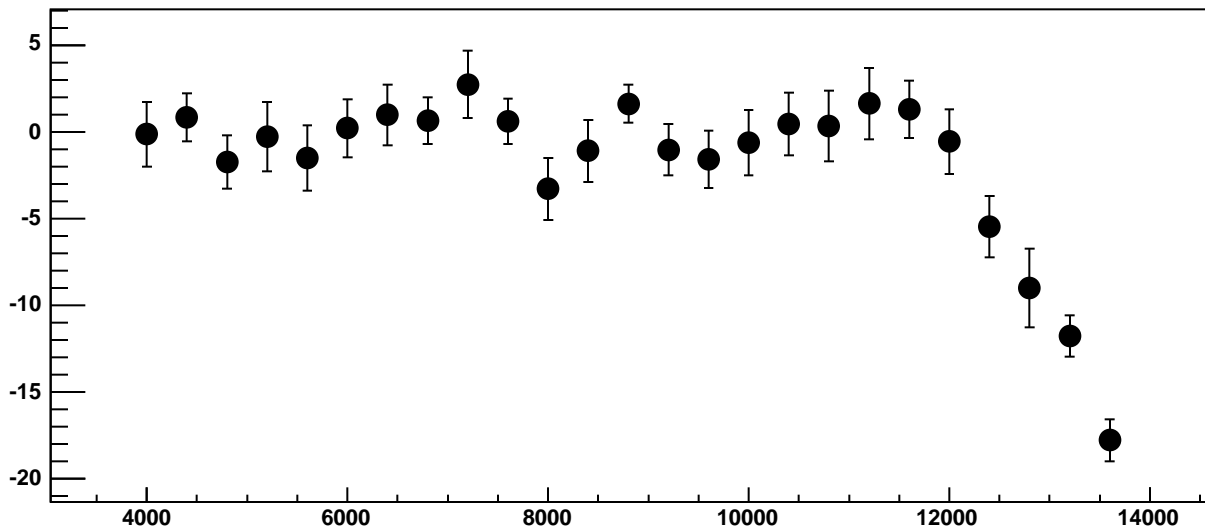
Chip 3, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC



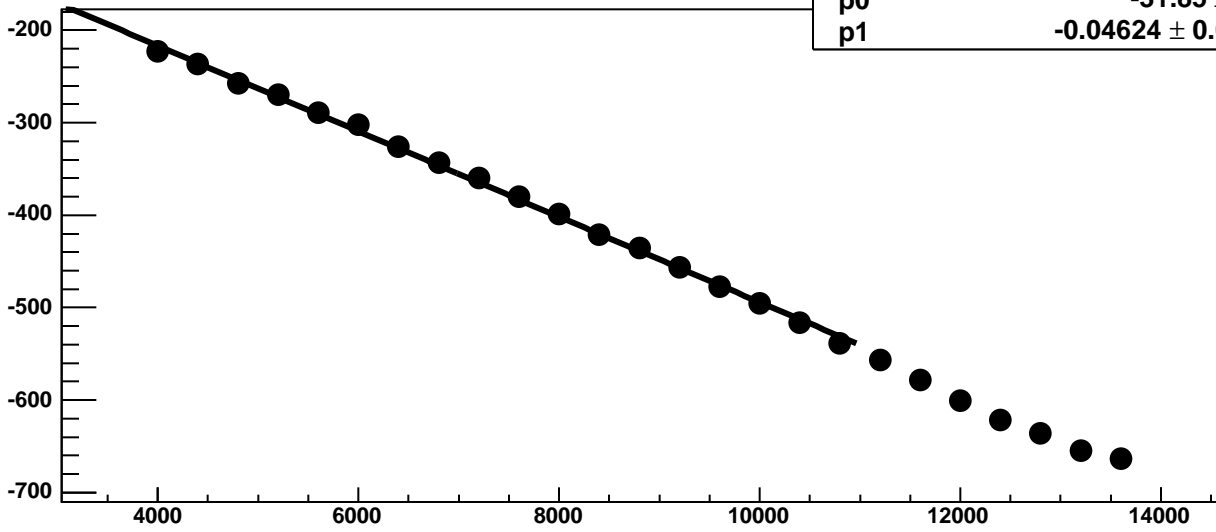
Chip 3, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



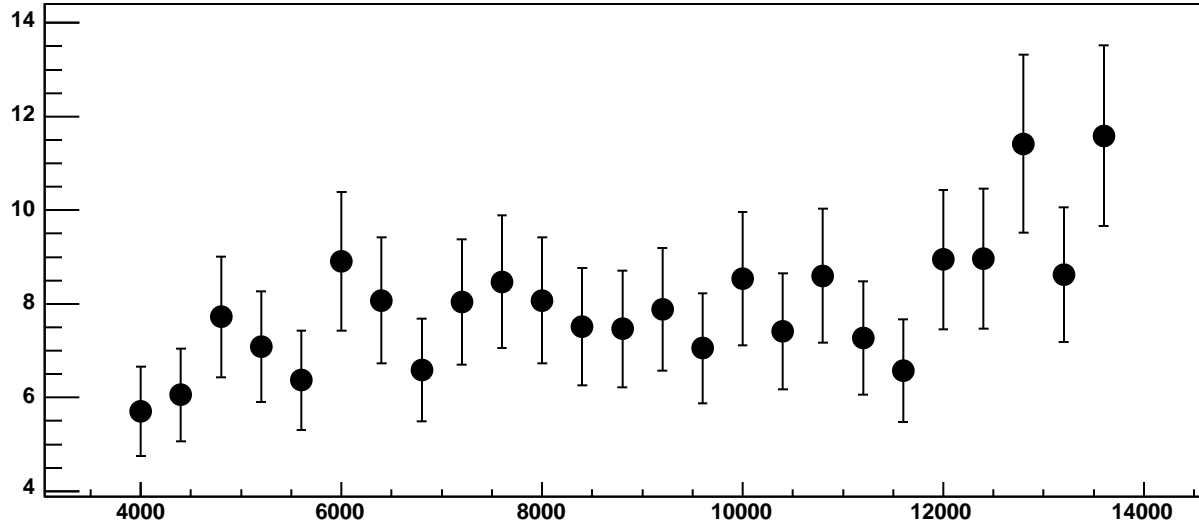
Chip 3, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC



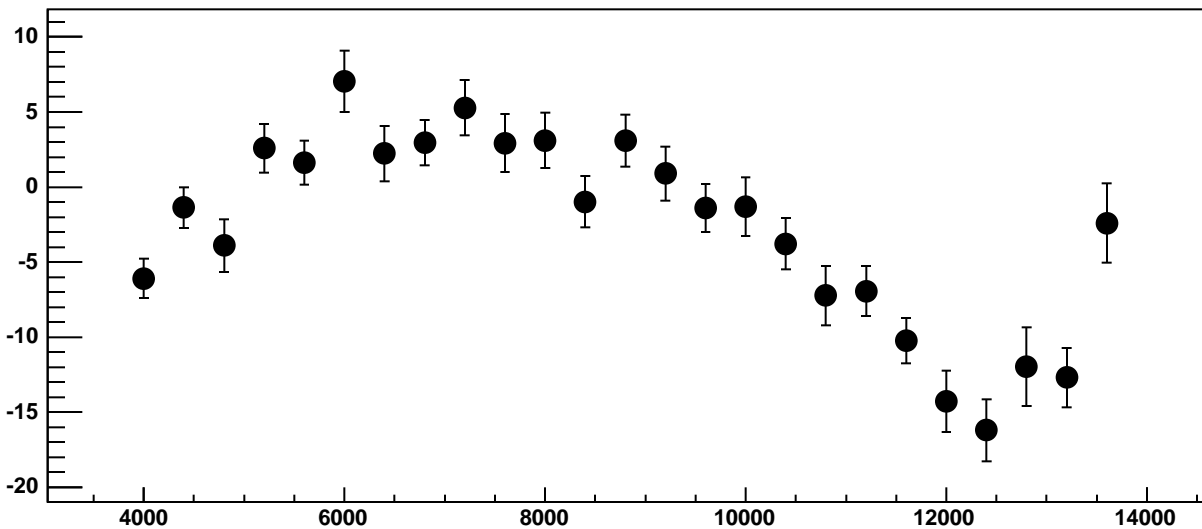
Chip 3, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



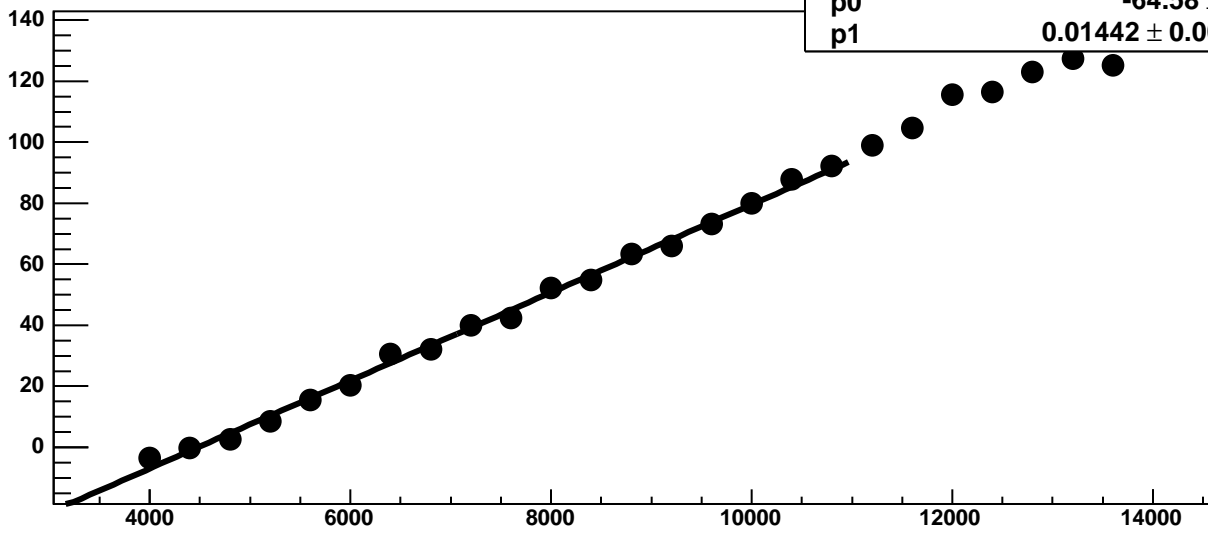
Chip 3, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



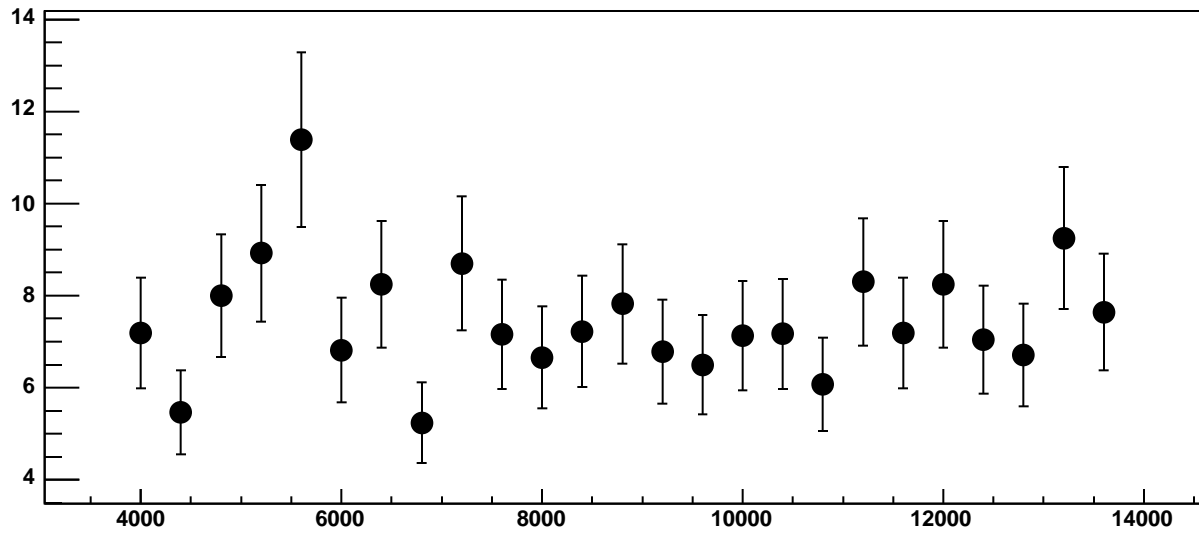
Chip 3, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



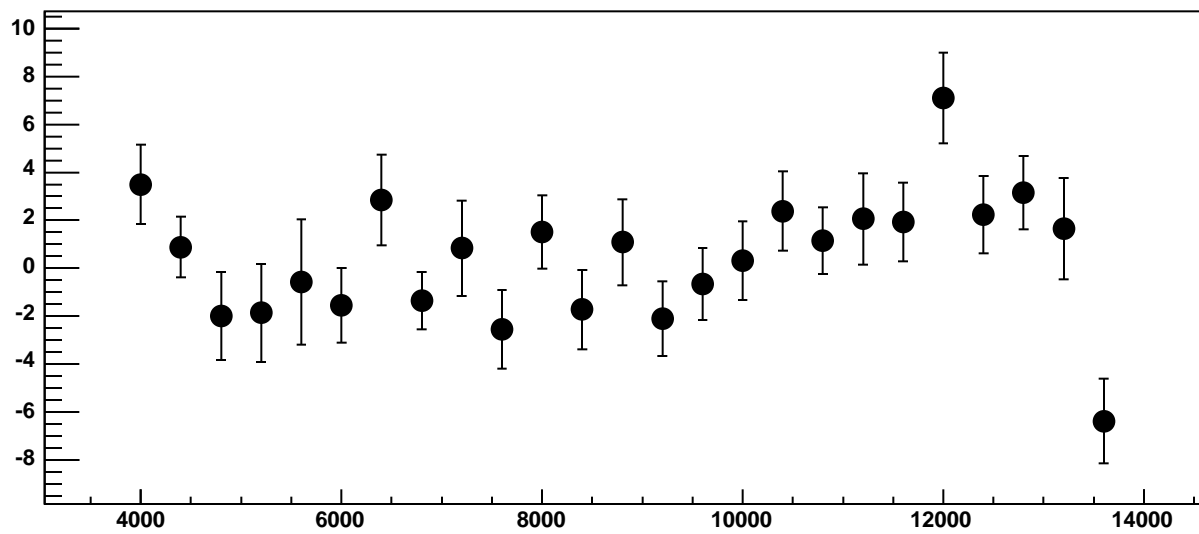
Chip 3, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



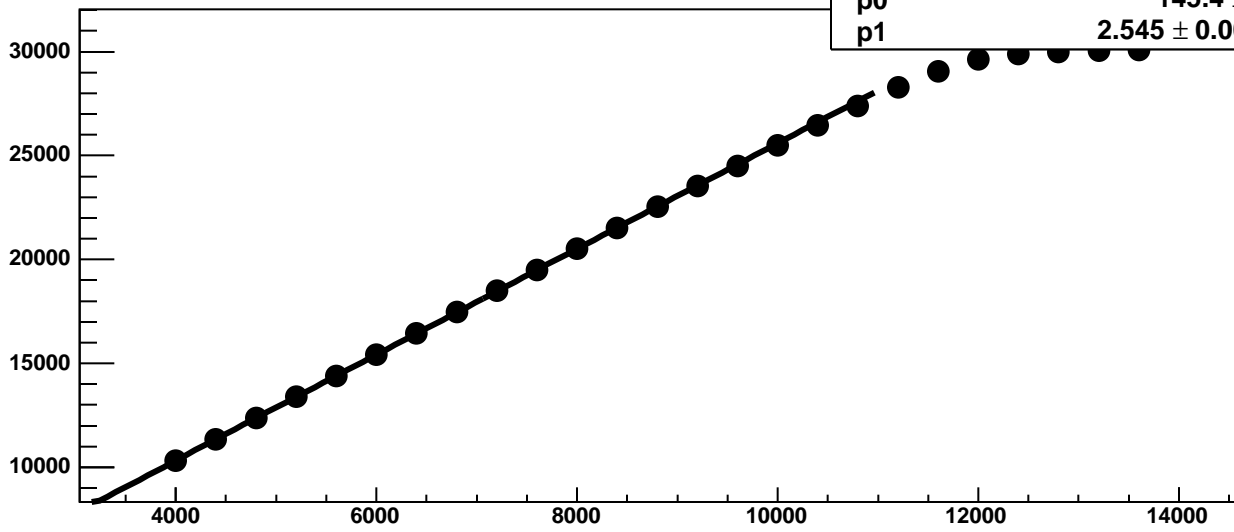
Chip 3, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



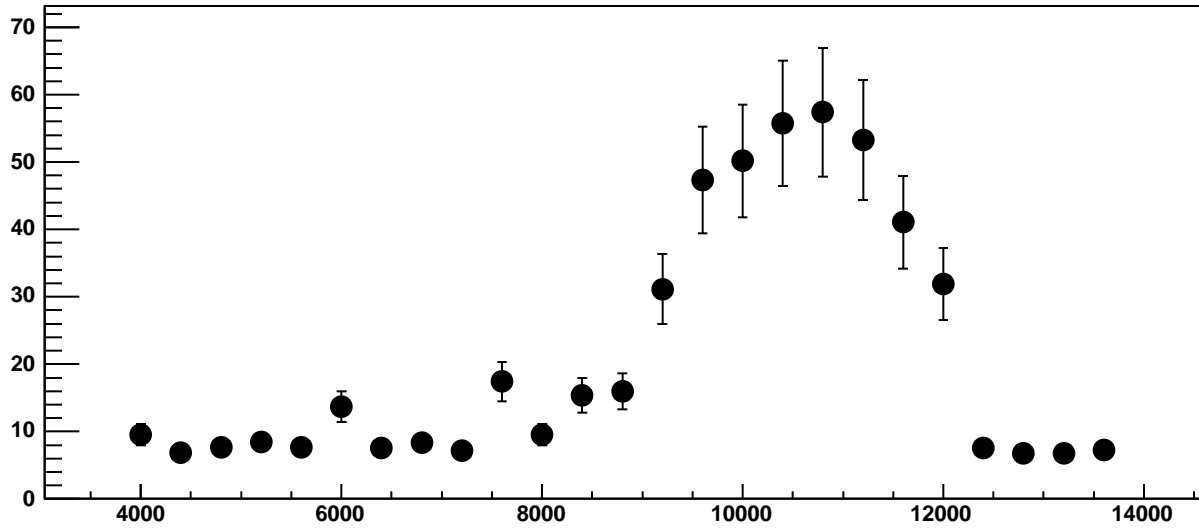
Chip 3, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



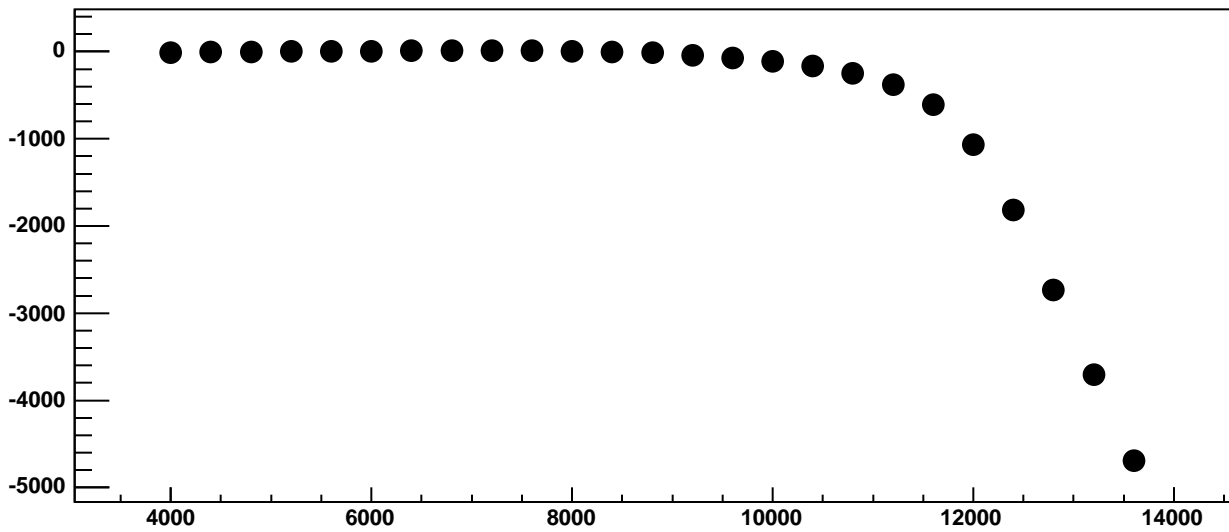
Chip 3, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC



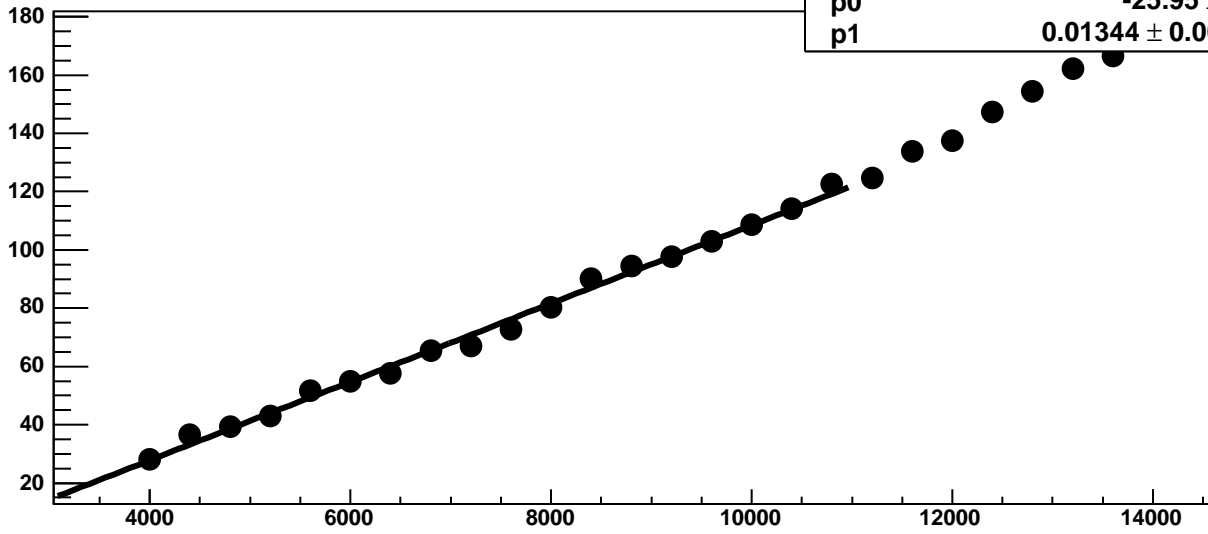
Chip 3, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



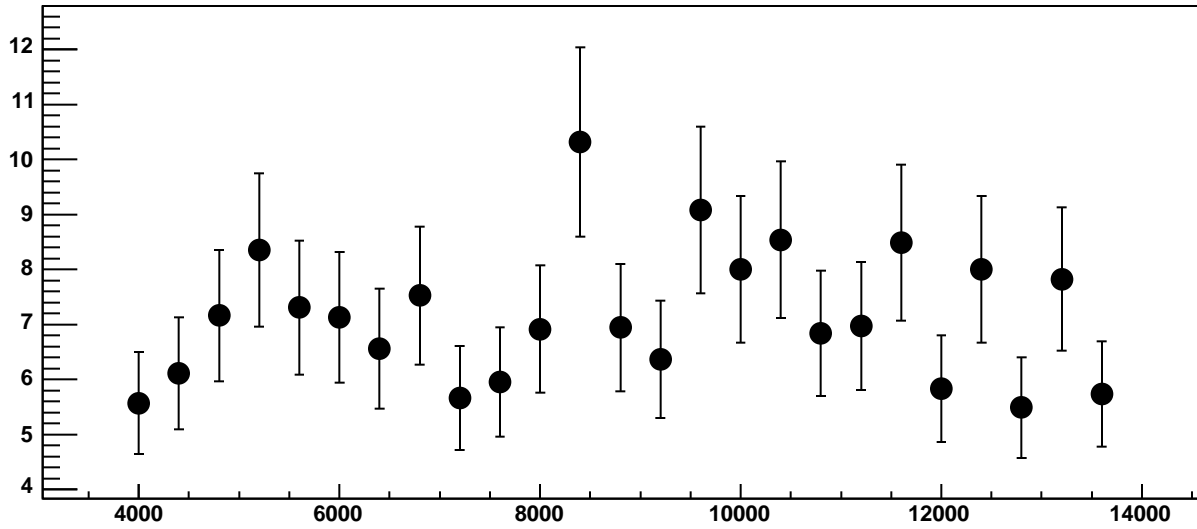
Chip 3, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC



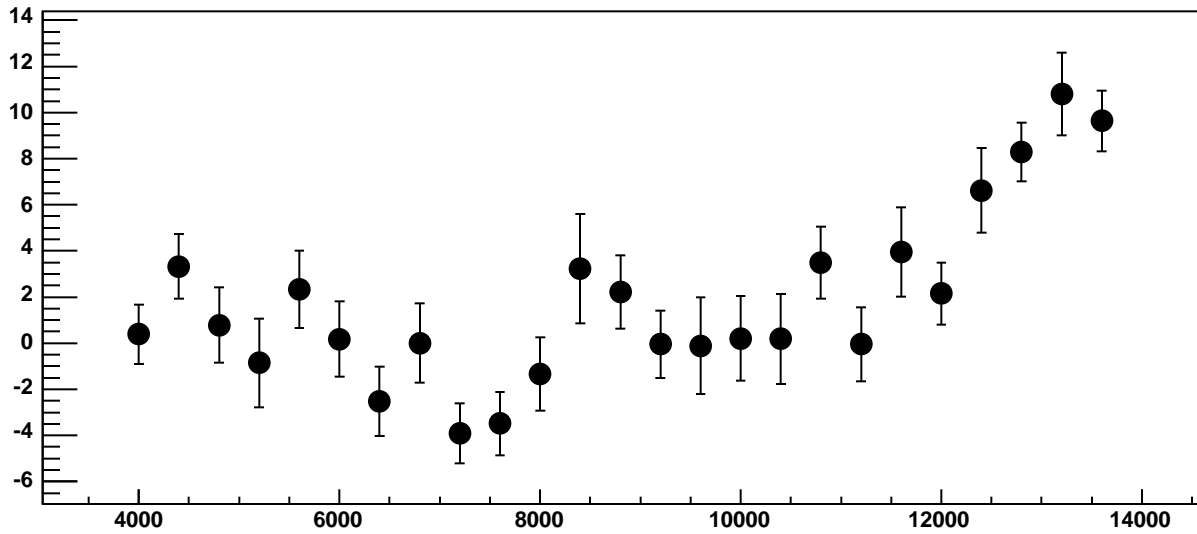
Chip 3, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



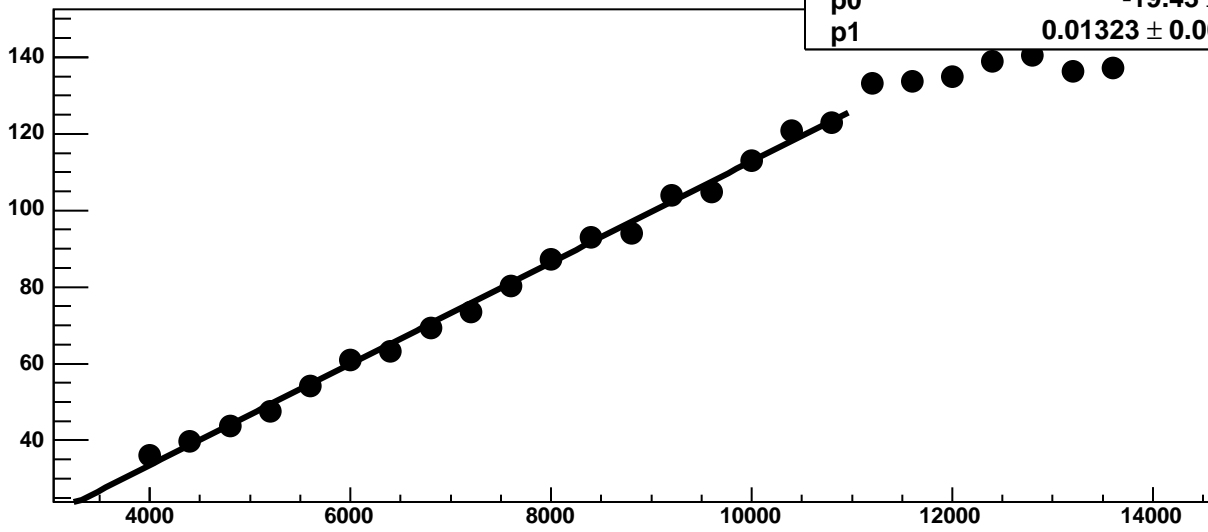
Chip 3, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



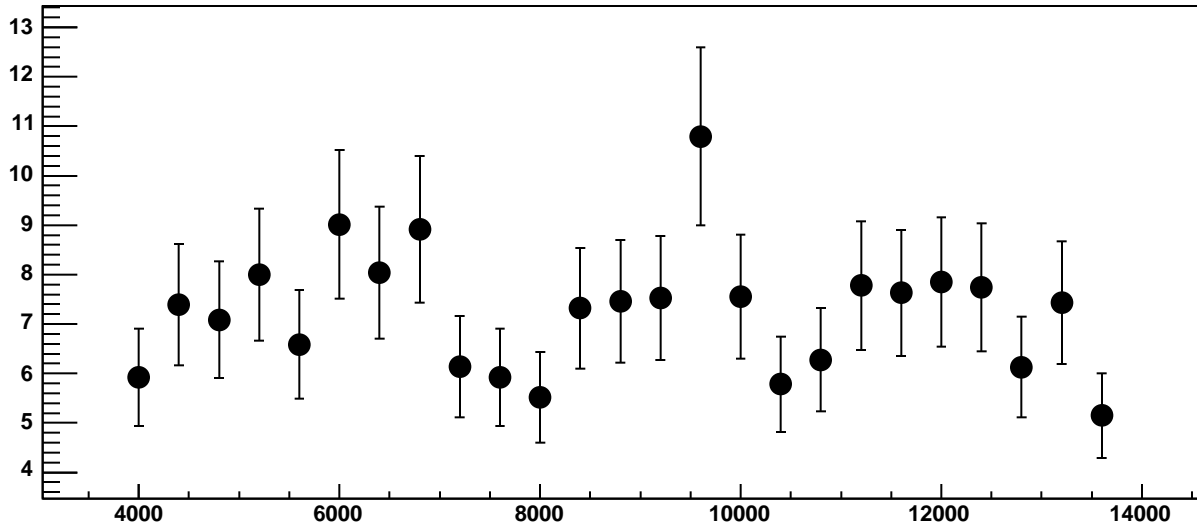
Chip 3, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



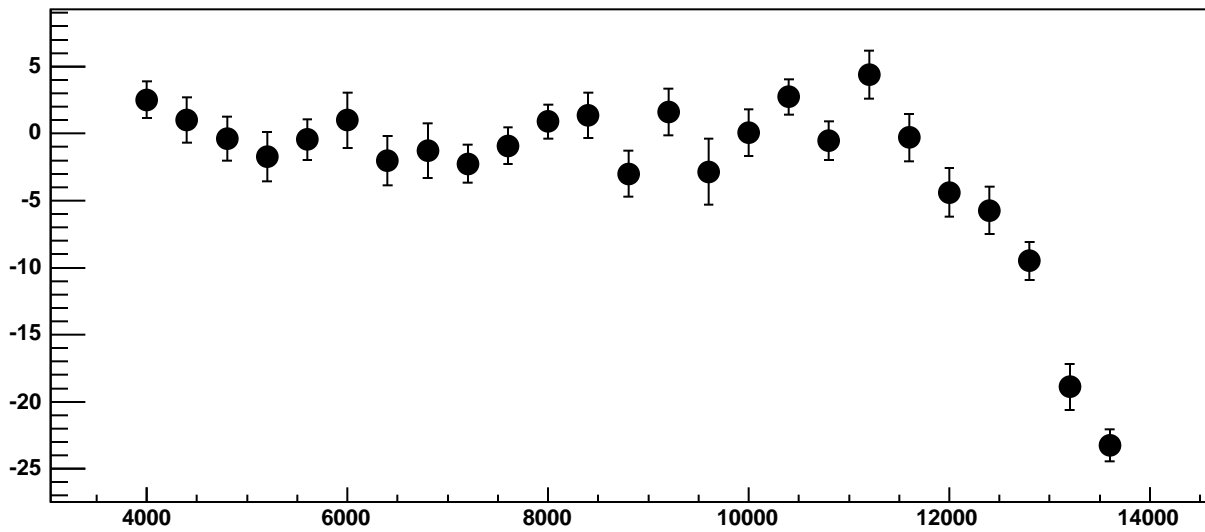
Chip 3, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



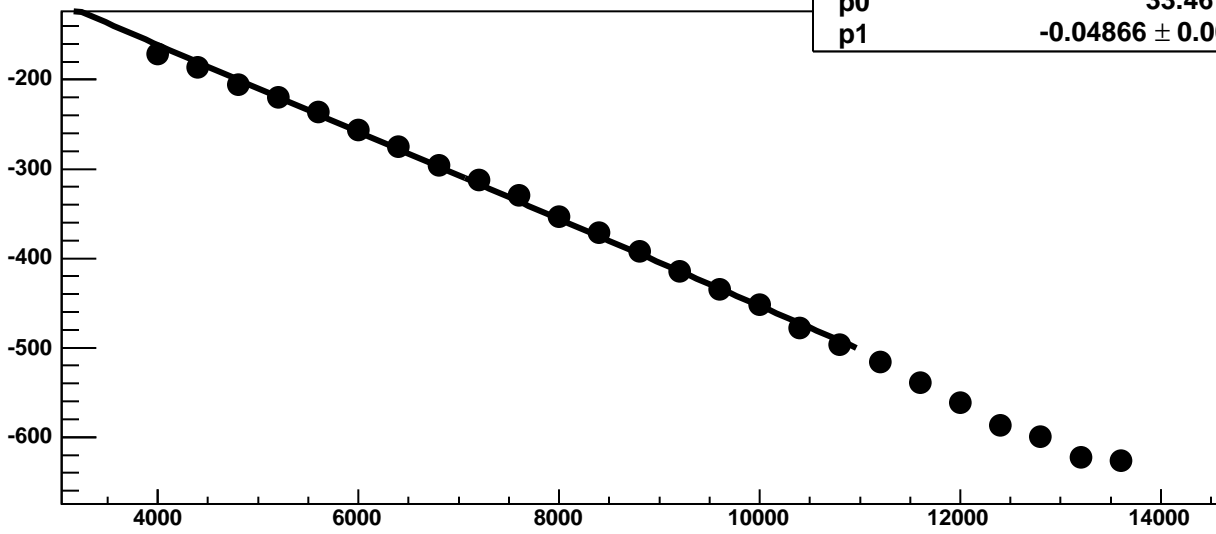
Chip 3, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

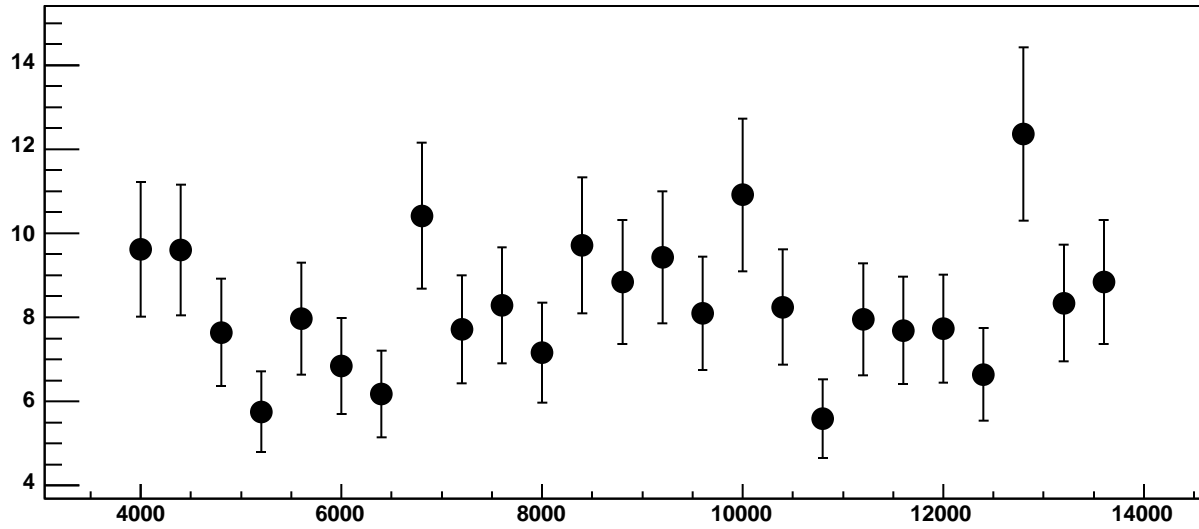


Chip 3, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

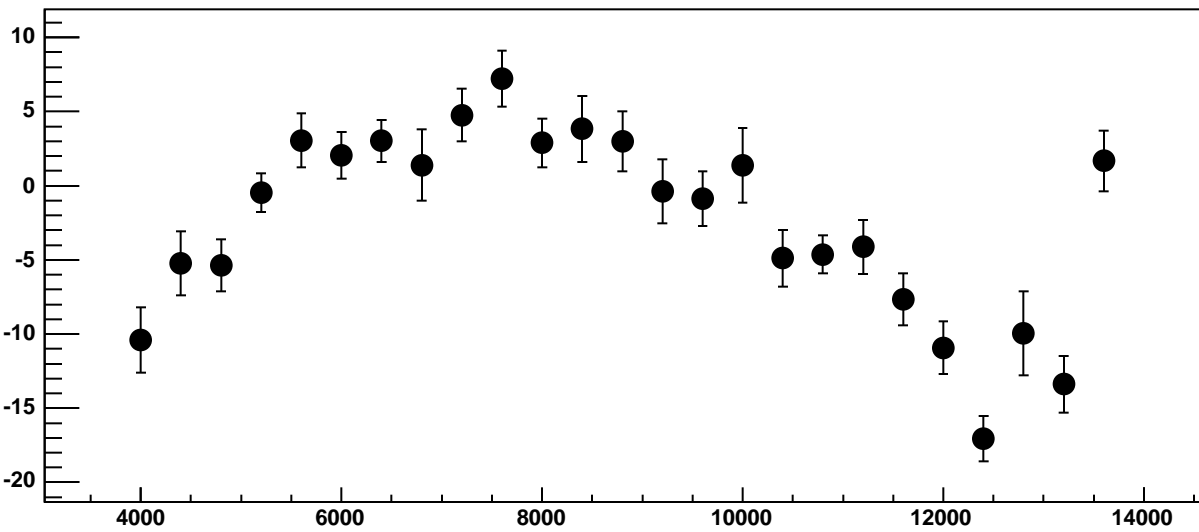


χ^2 / ndf 97.24 / 16
p0 33.46 ± 1.53
p1 -0.04866 ± 0.0001992

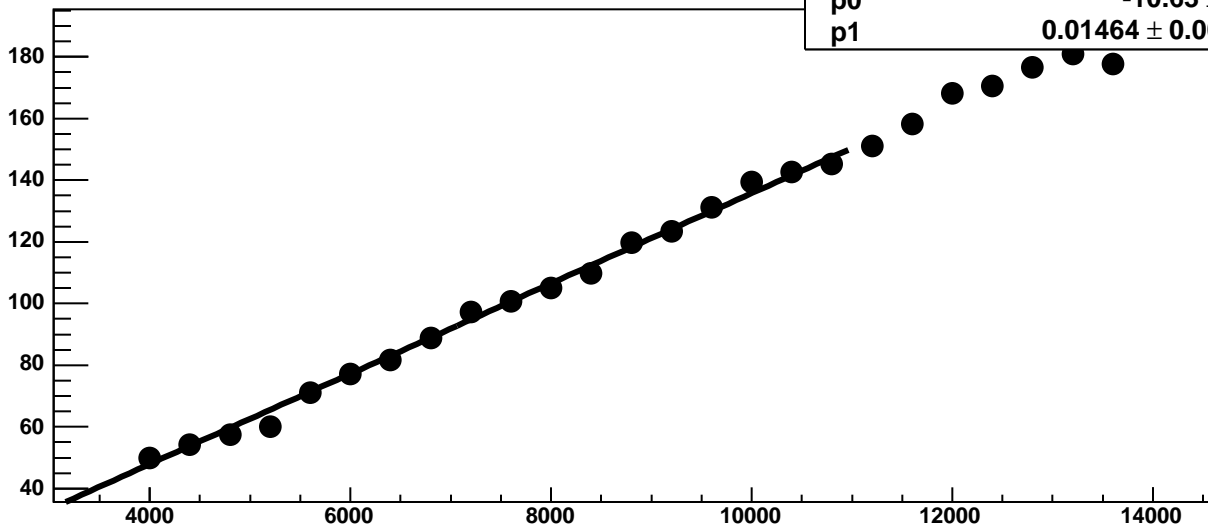
Chip 3, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



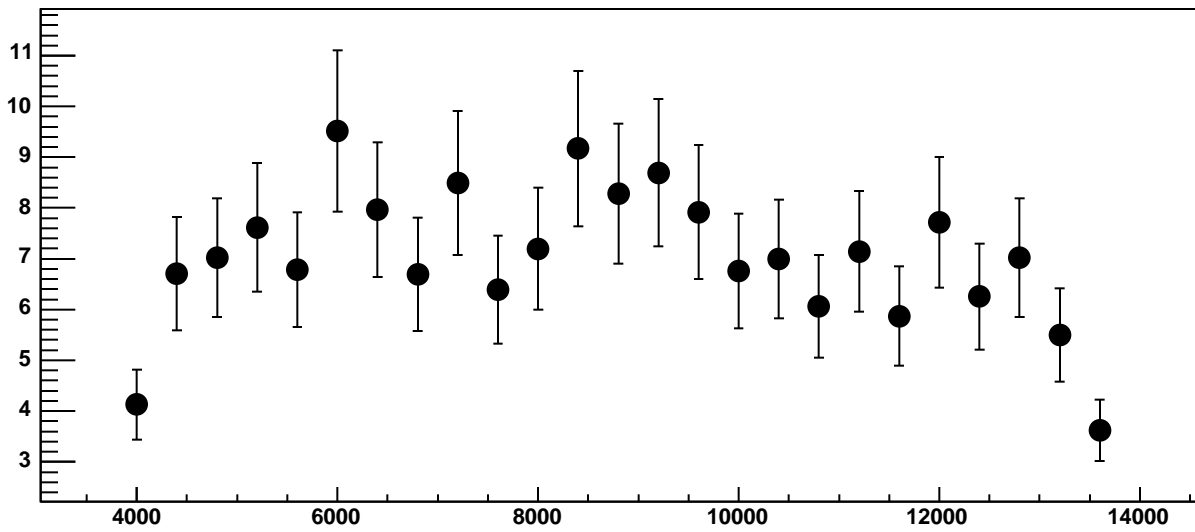
Chip 3, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC



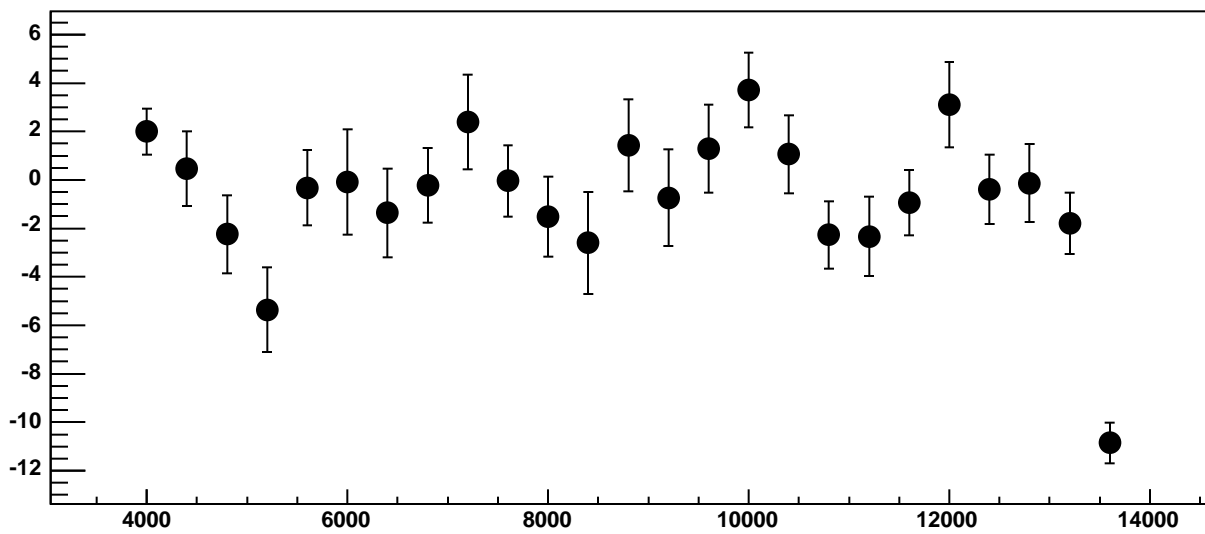
Chip 3, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



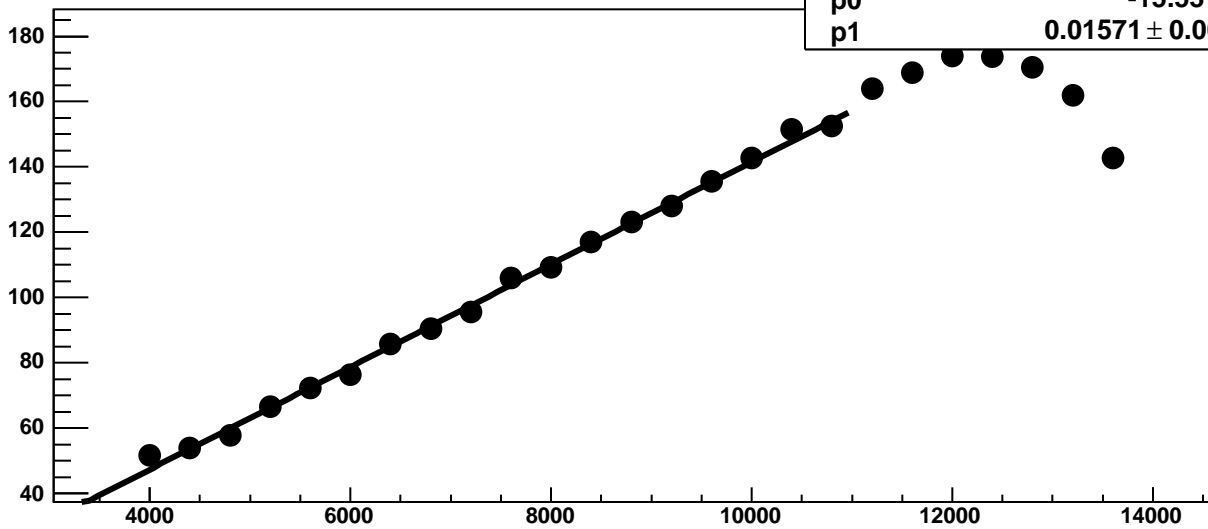
Chip 3, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



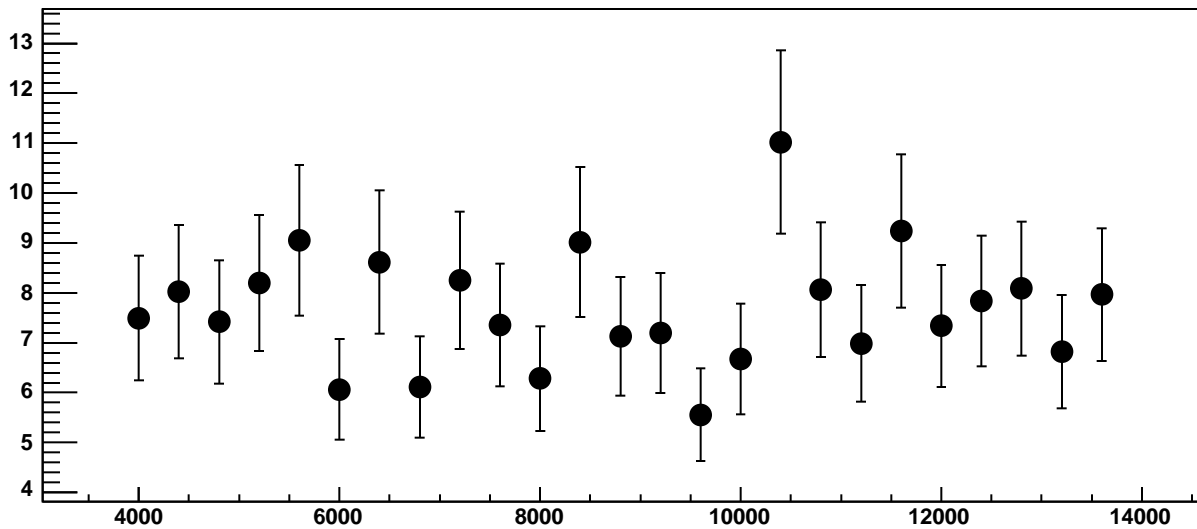
Chip 3, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



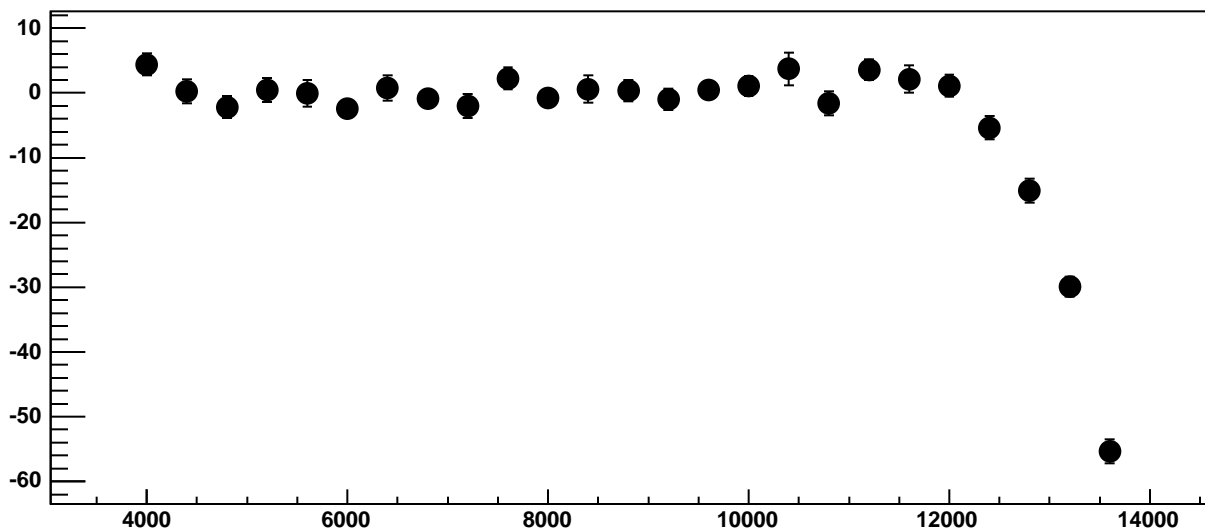
Chip 3, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



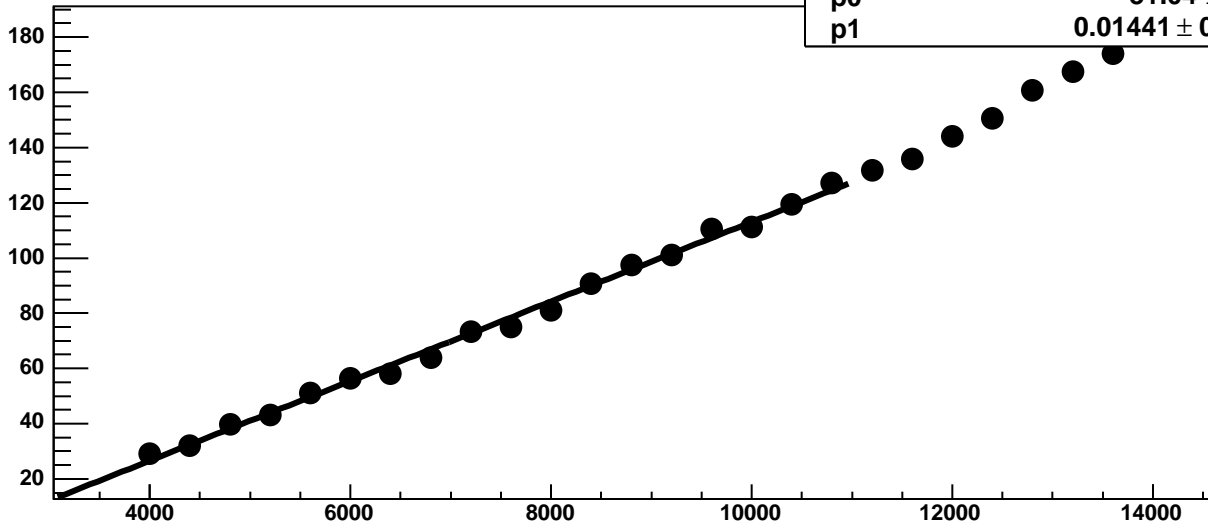
Chip 3, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

28.82 / 16

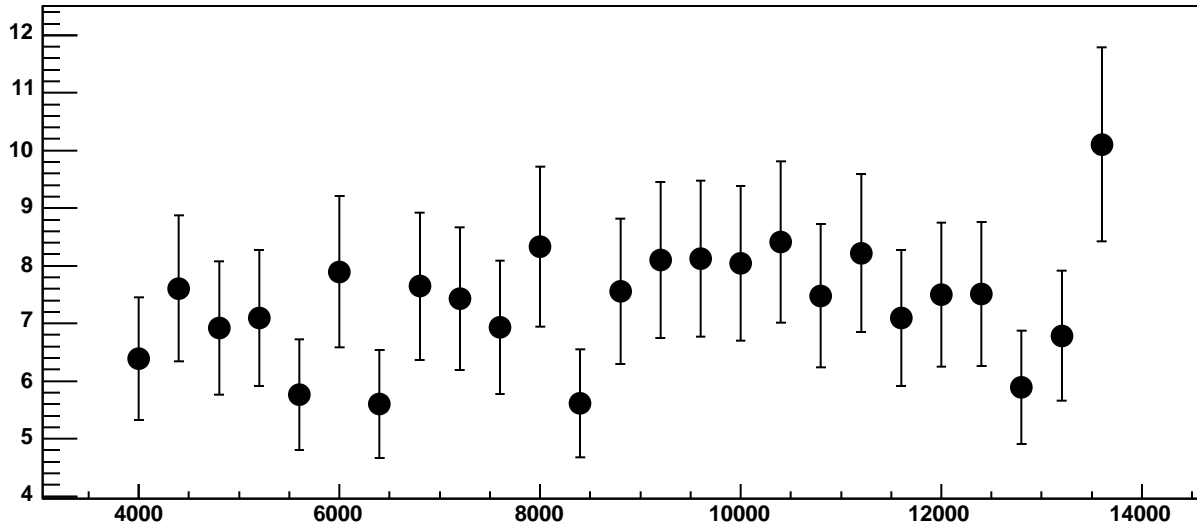
p0

-31.04 ± 1.415

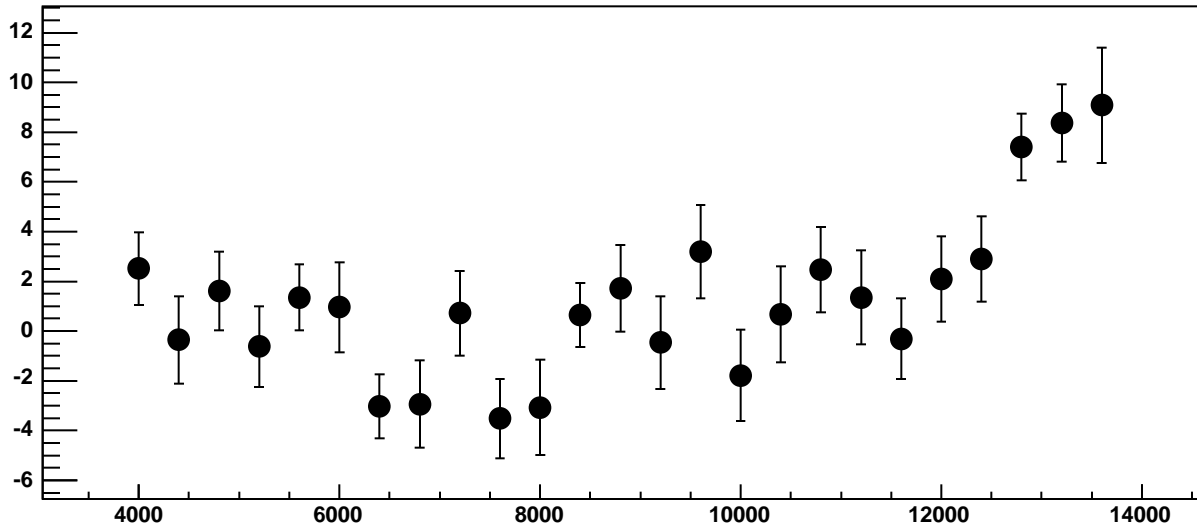
p1

0.01441 ± 0.00019

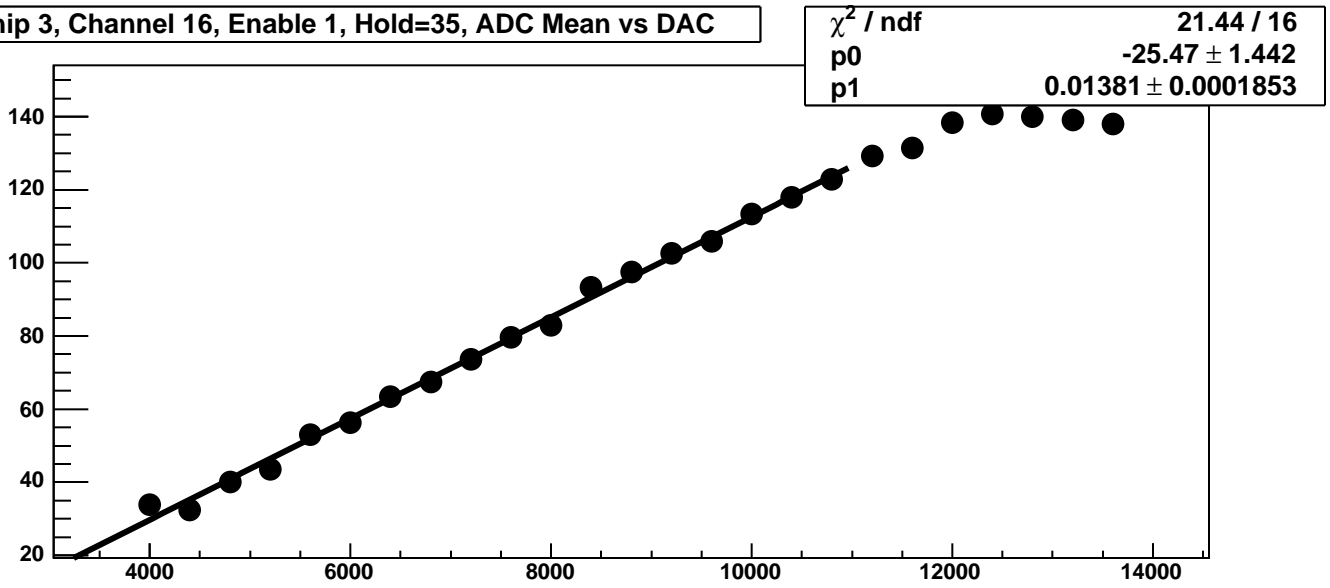
Chip 3, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



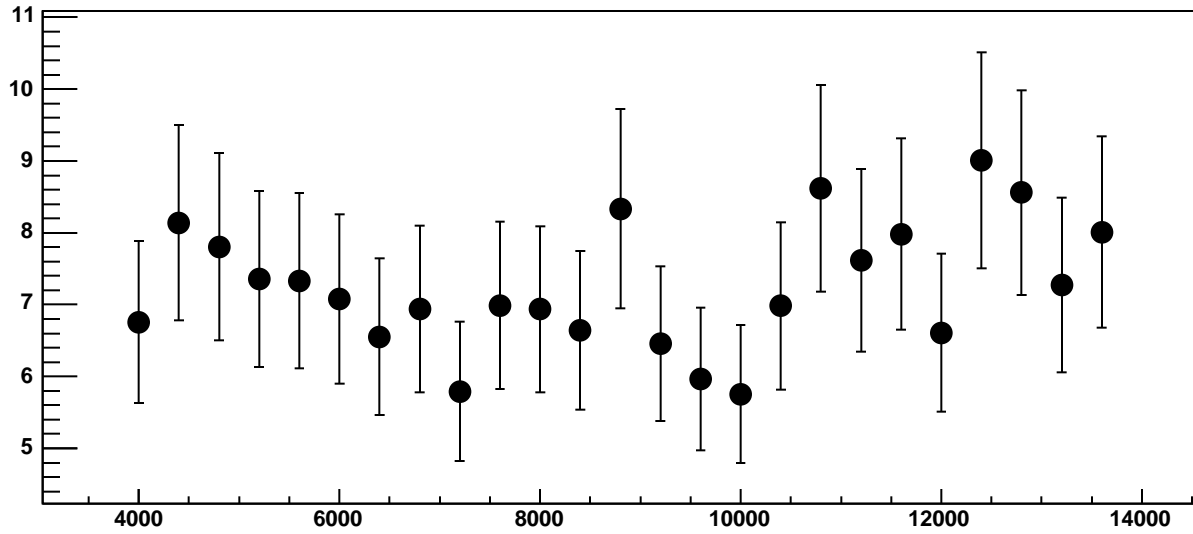
Chip 3, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



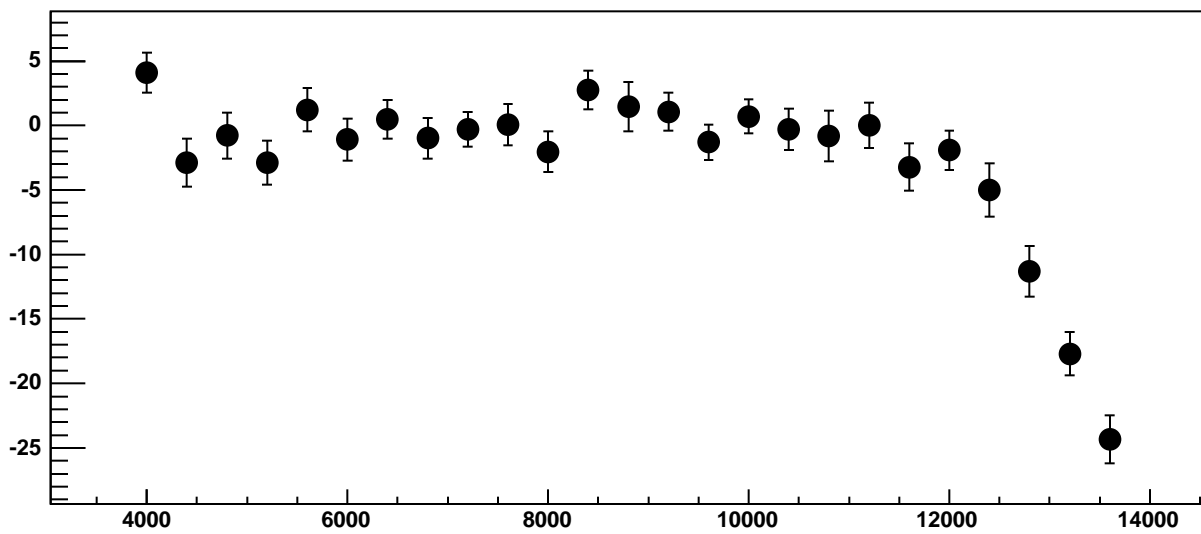
Chip 3, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



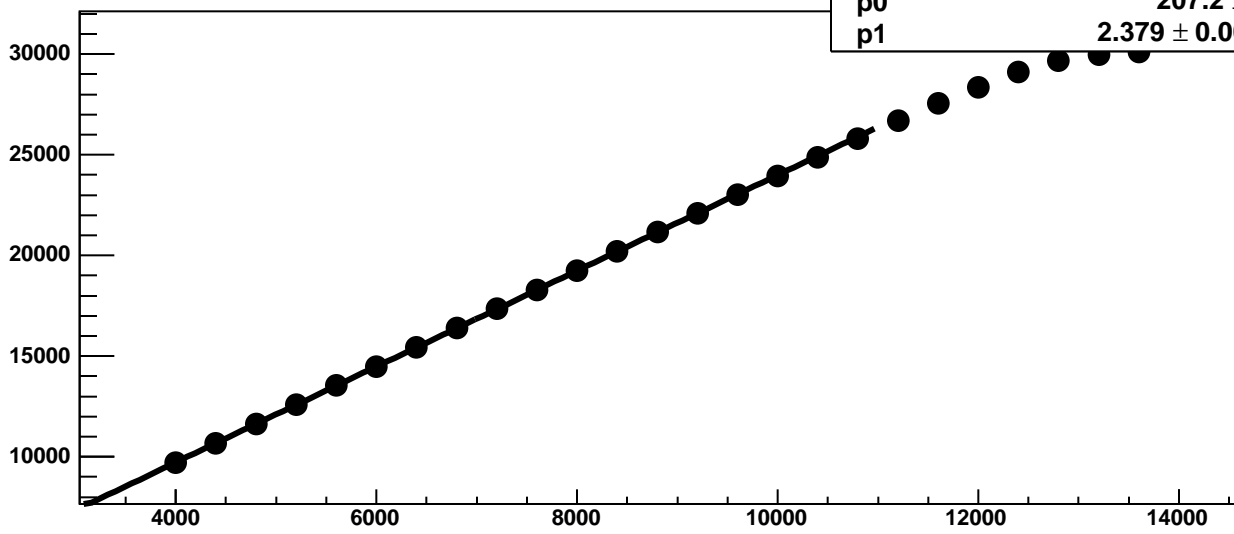
Chip 3, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



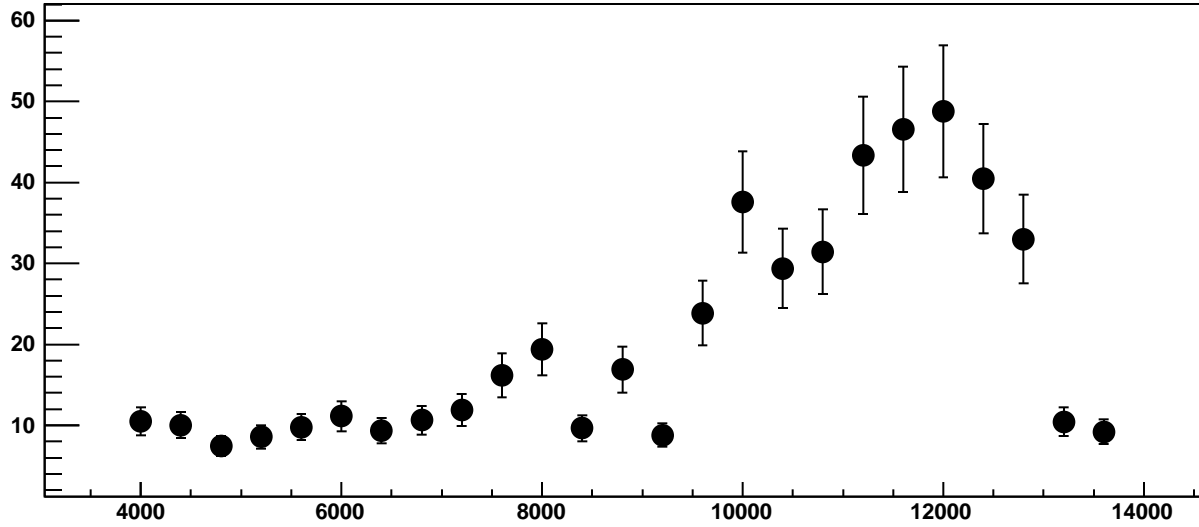
Chip 3, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



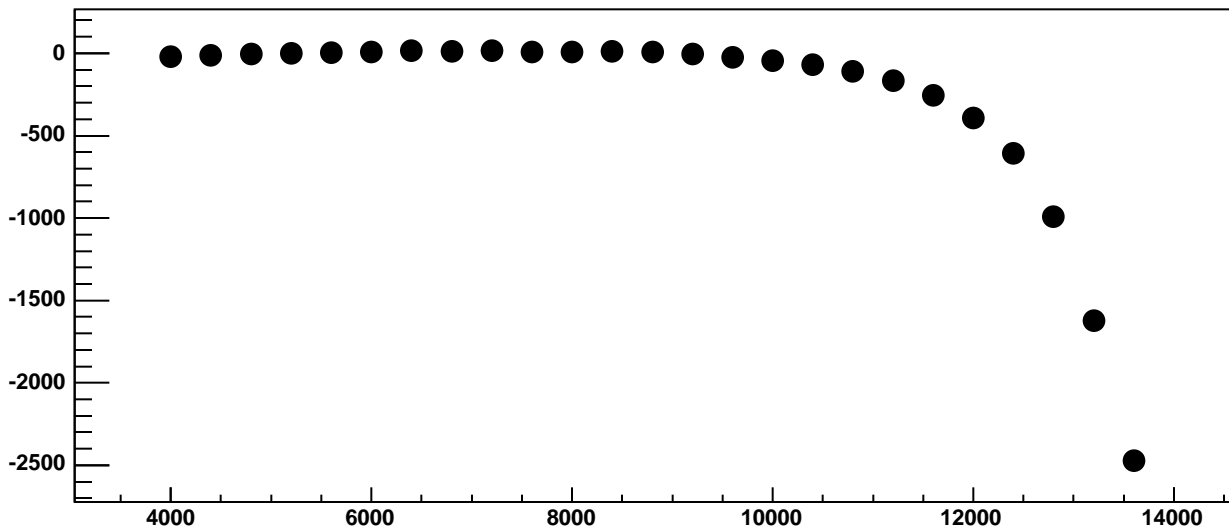
Chip 3, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC



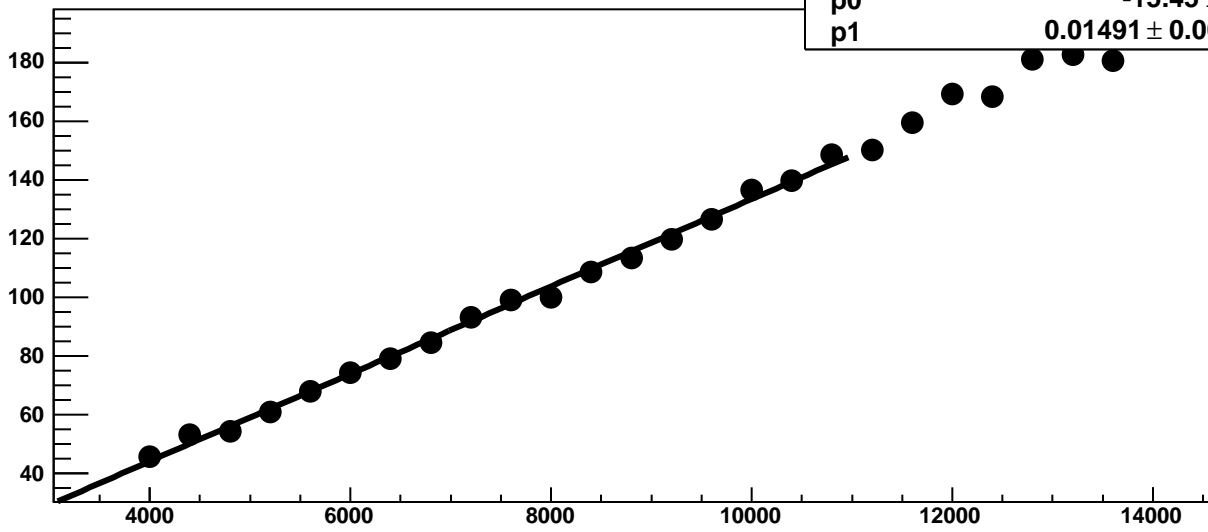
Chip 3, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

24.21 / 16

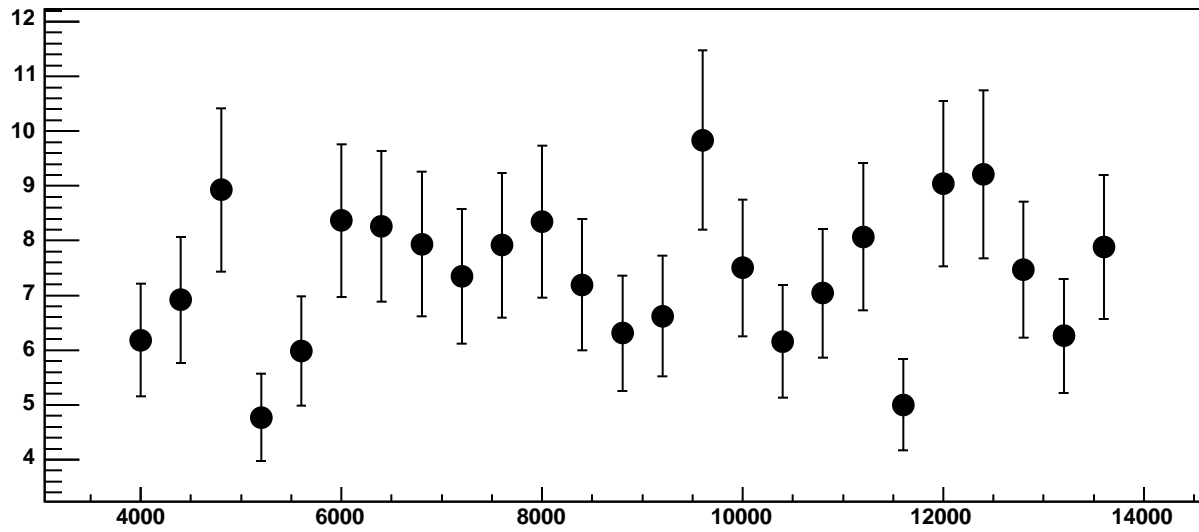
p0

-15.45 ± 1.328

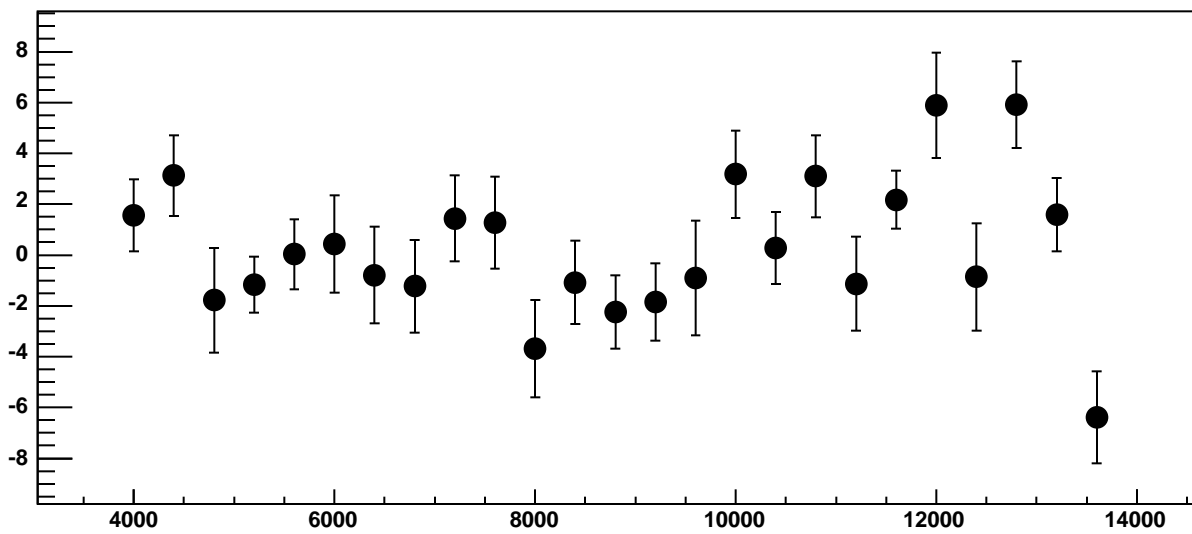
p1

0.01491 ± 0.0001755

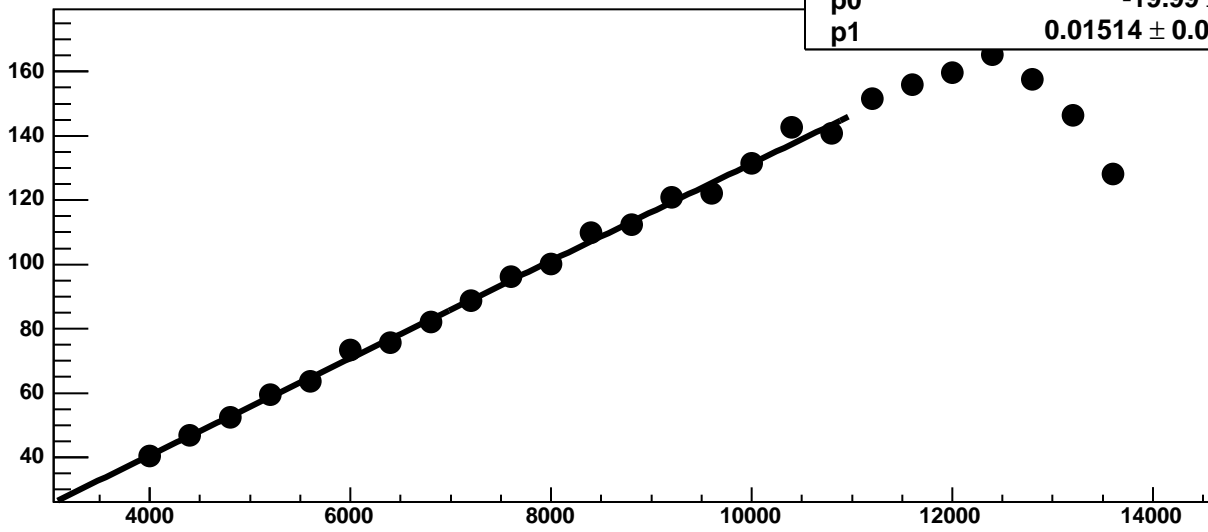
Chip 3, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



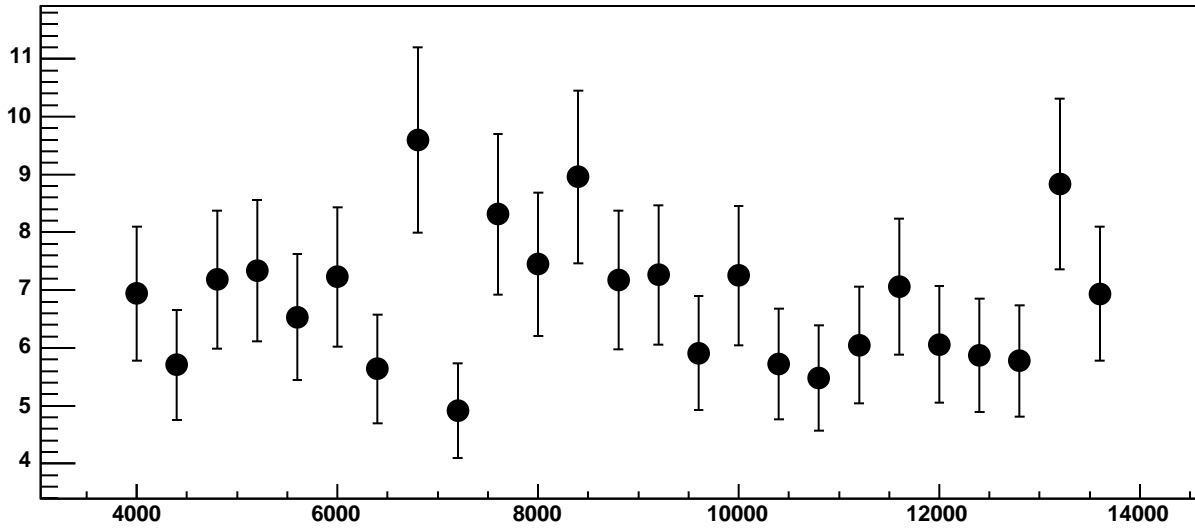
Chip 3, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



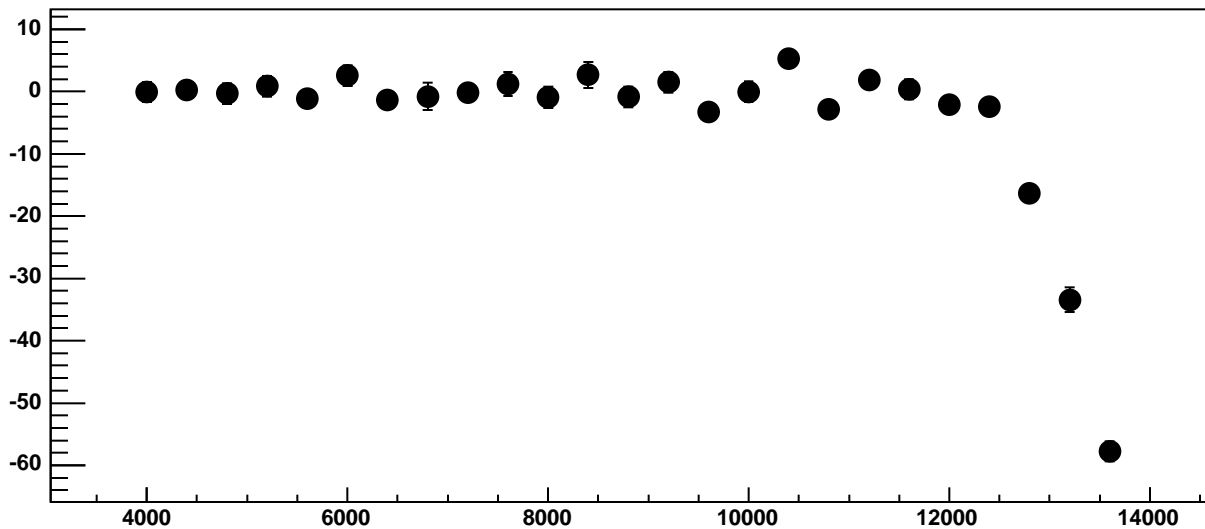
Chip 3, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



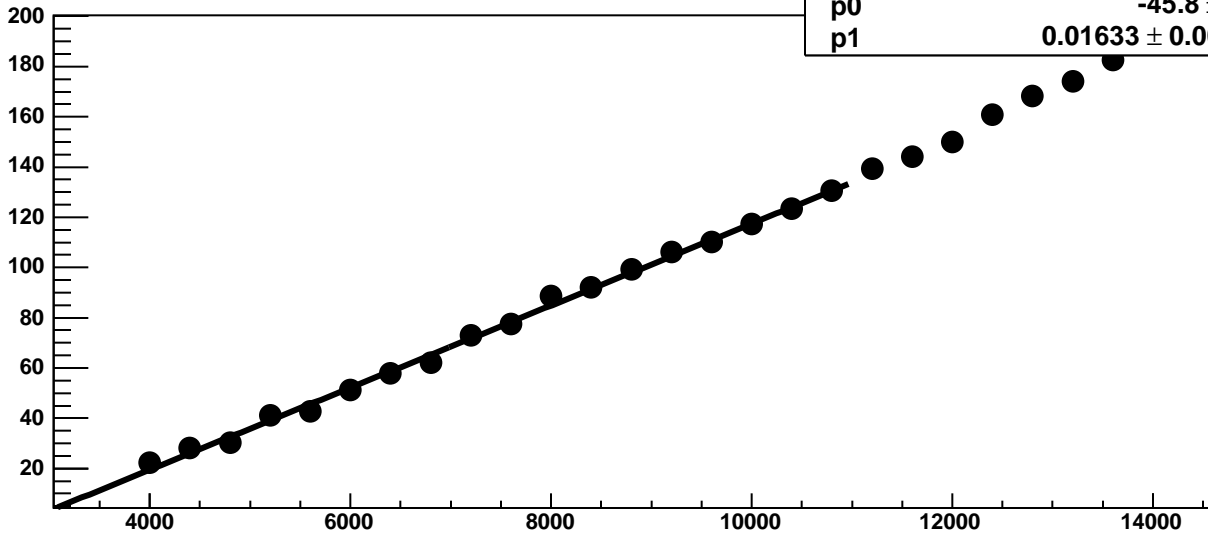
Chip 3, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



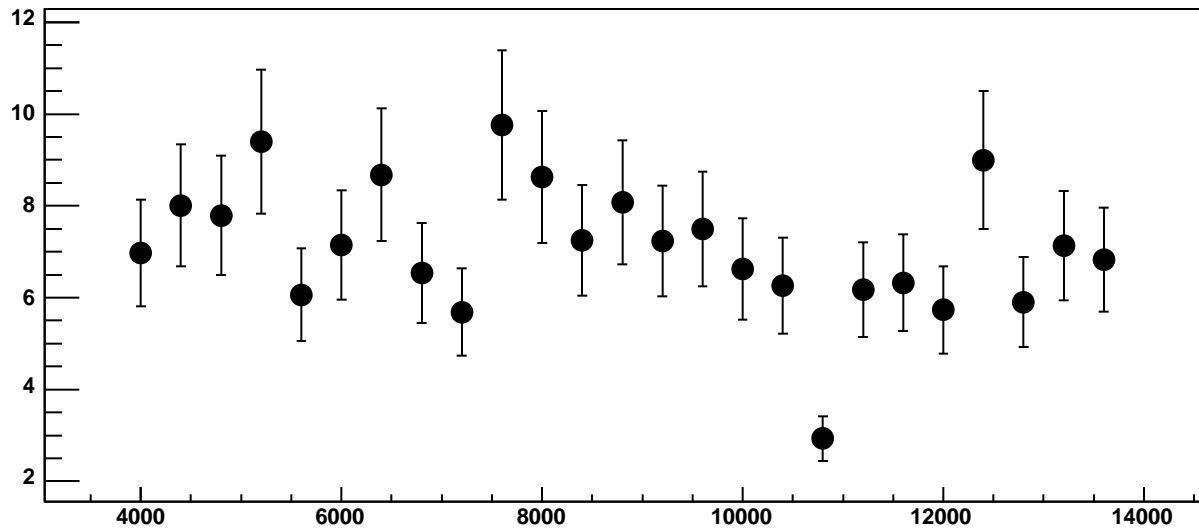
Chip 3, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



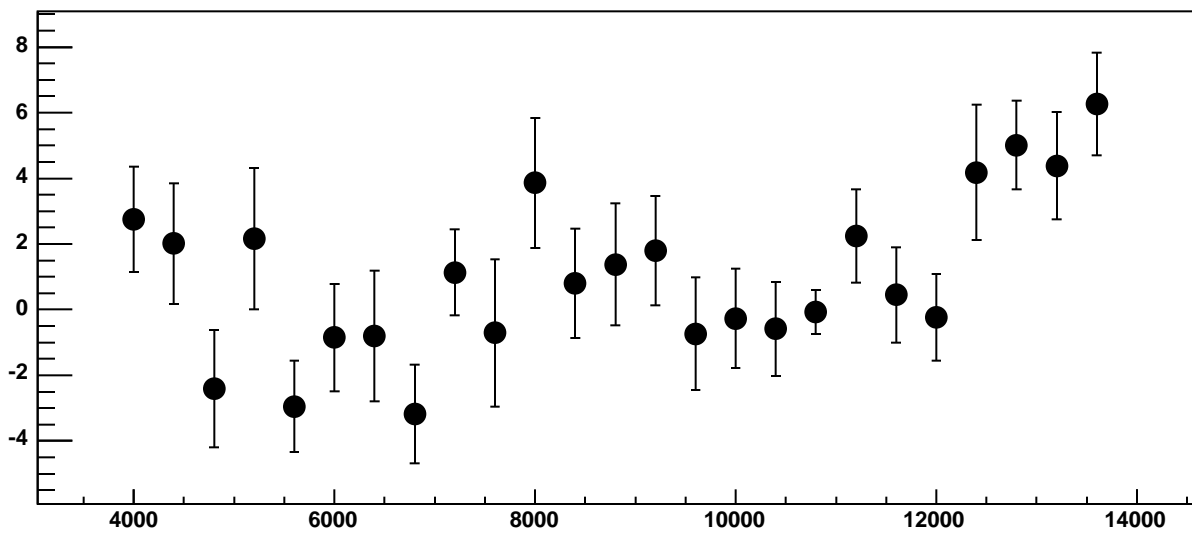
Chip 3, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



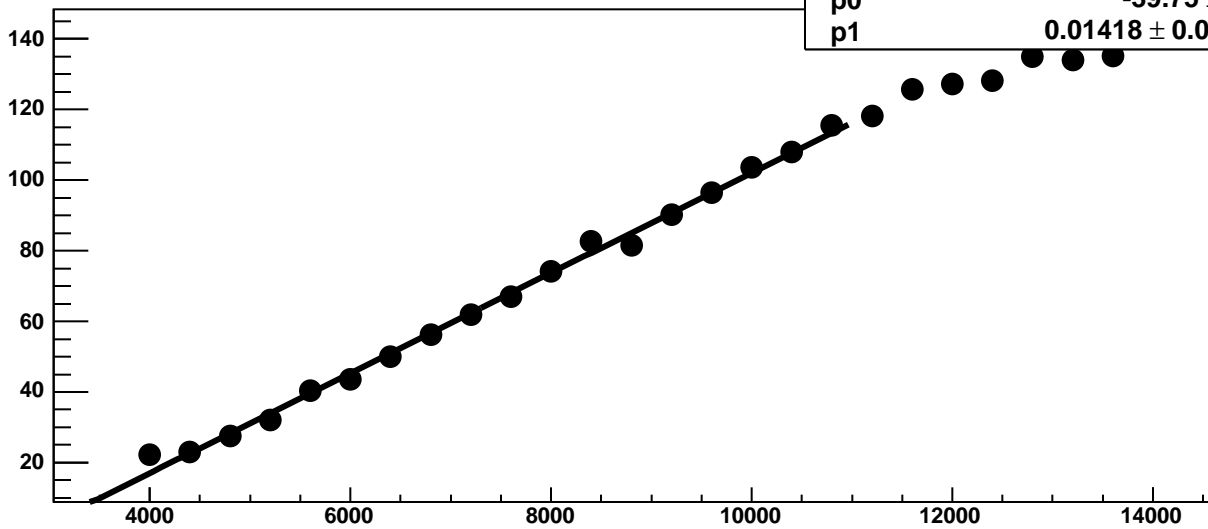
Chip 3, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

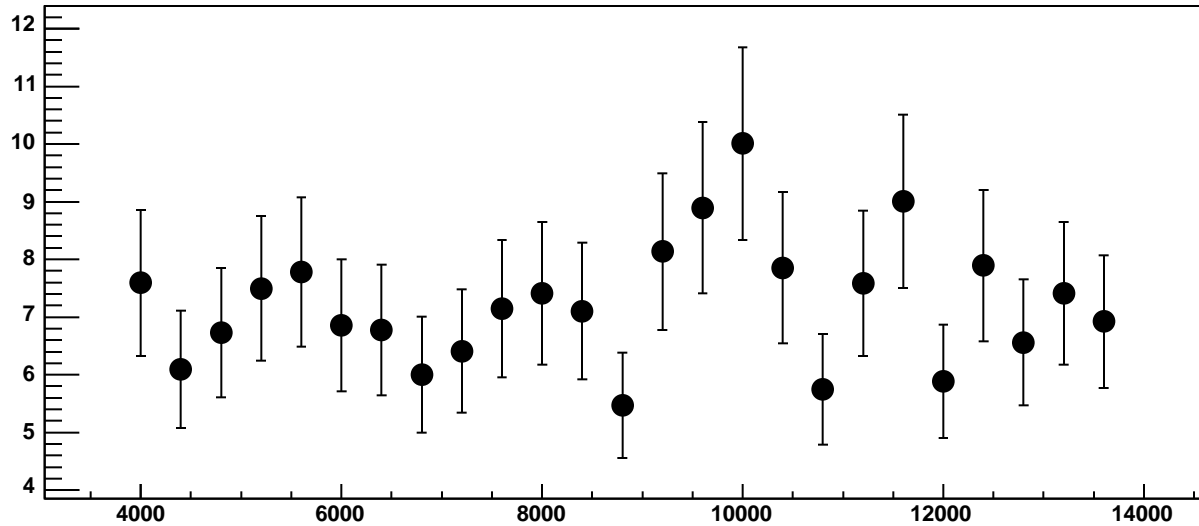


Chip 3, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

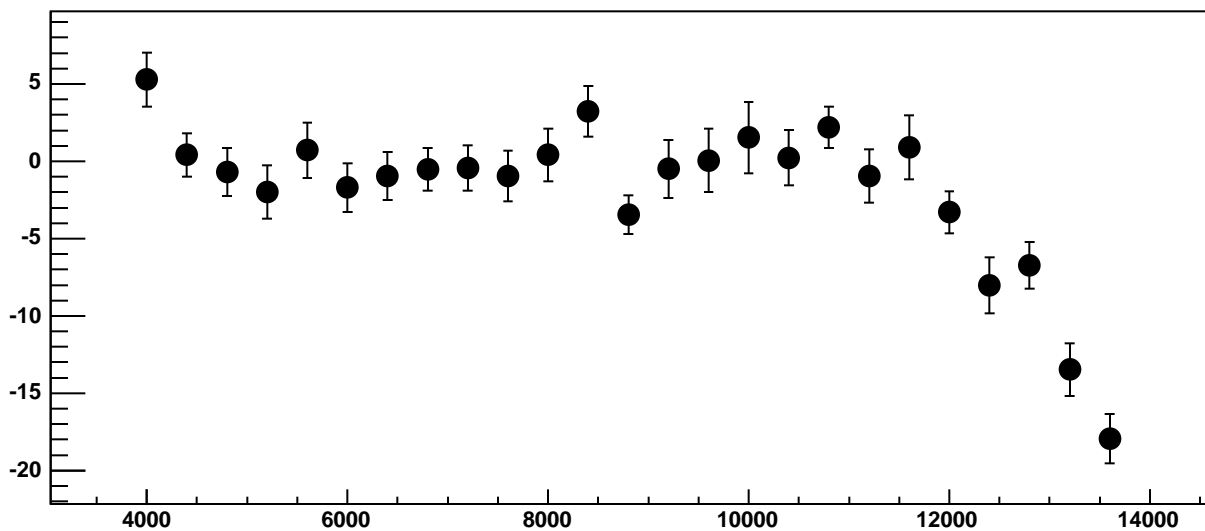


χ^2 / ndf 27.94 / 16
p0 -39.75 ± 1.394
p1 0.01418 ± 0.0001831

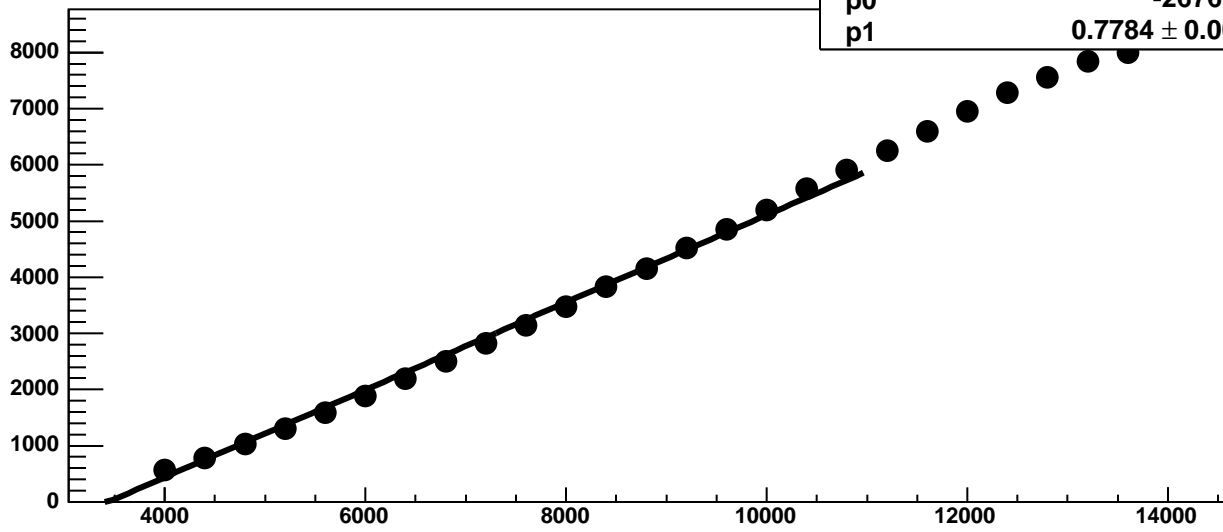
Chip 3, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

7149 / 16

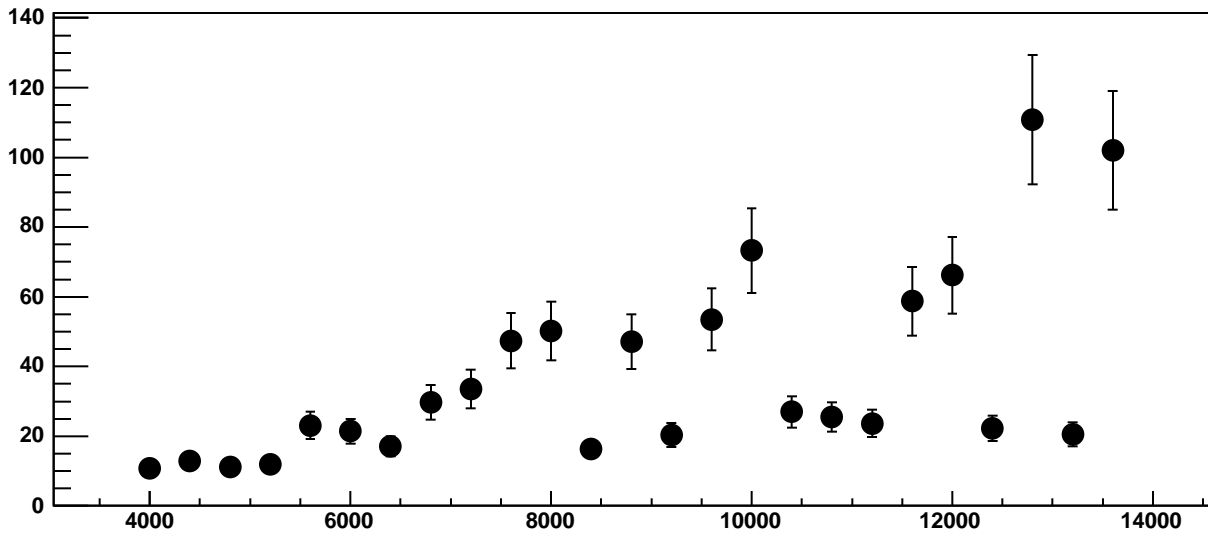
p0

-2676 ± 3.26

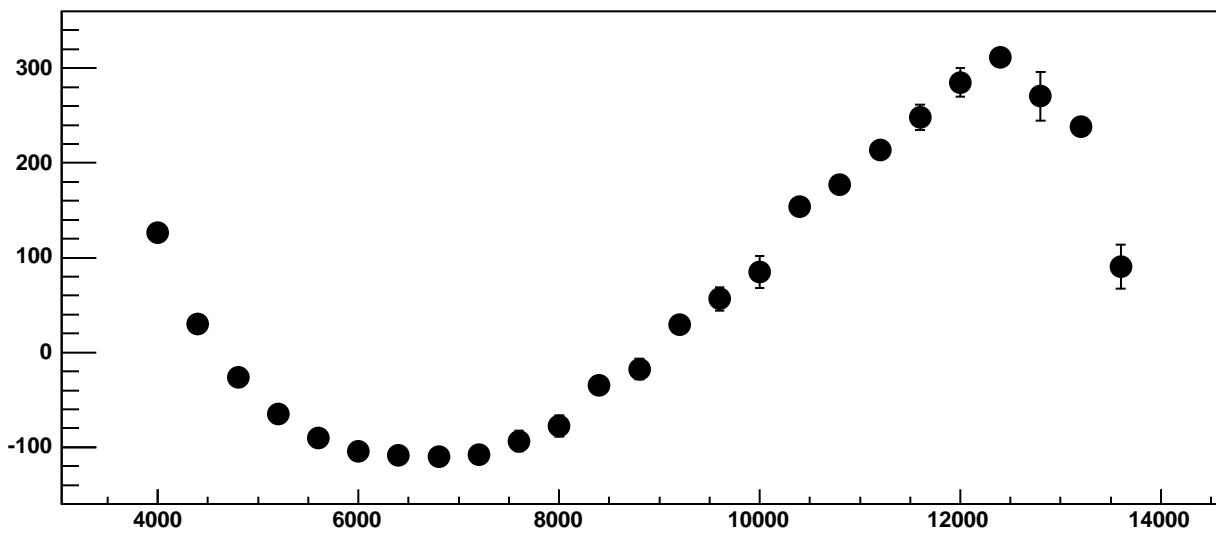
p1

0.7784 ± 0.0005249

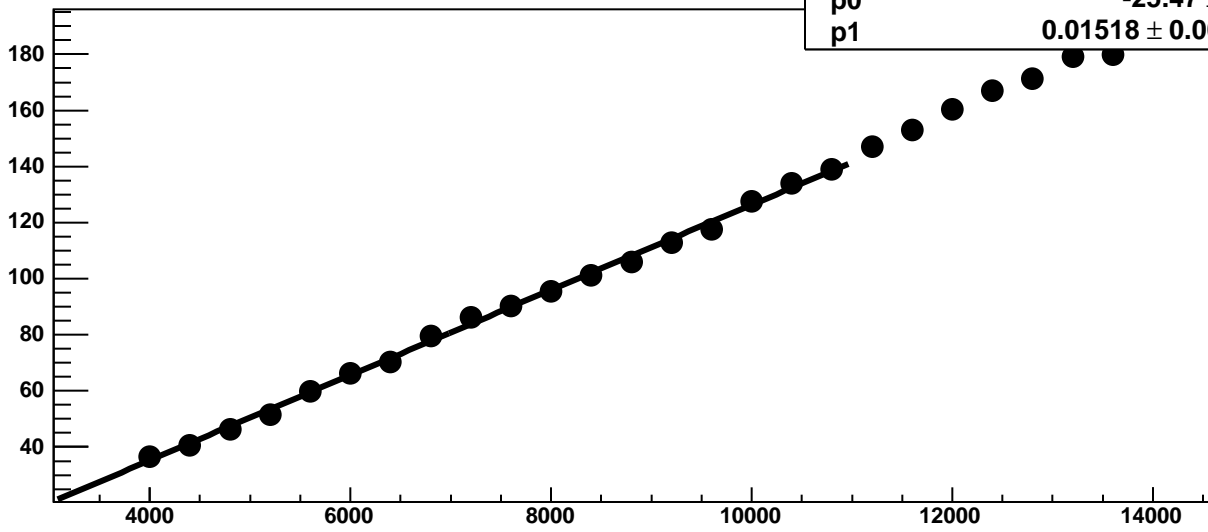
Chip 3, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC

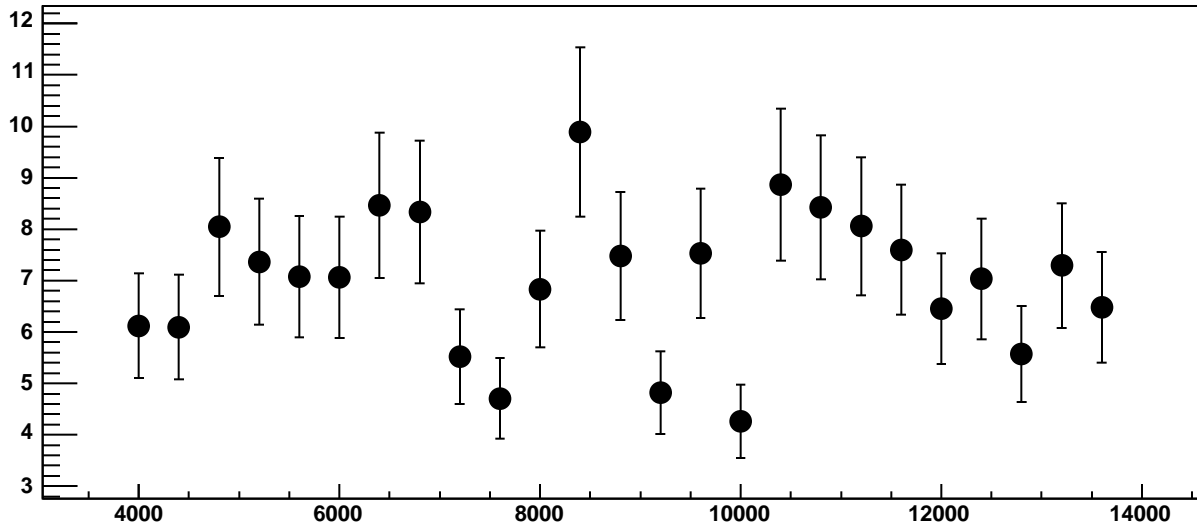


Chip 3, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

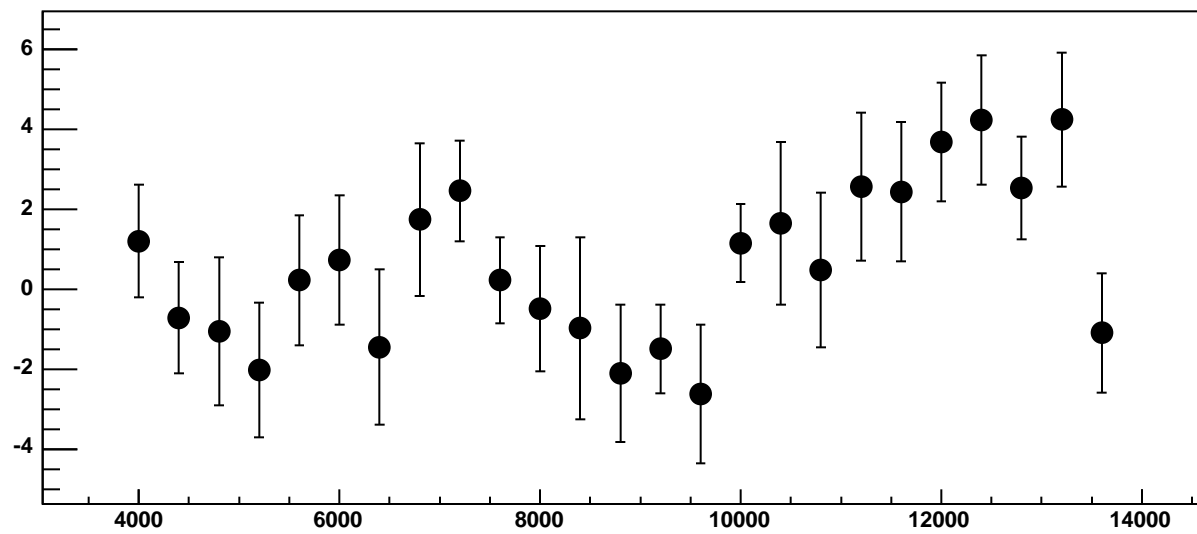


χ^2 / ndf 16.17 / 16
p0 -25.47 ± 1.339
p1 0.01518 ± 0.0001714

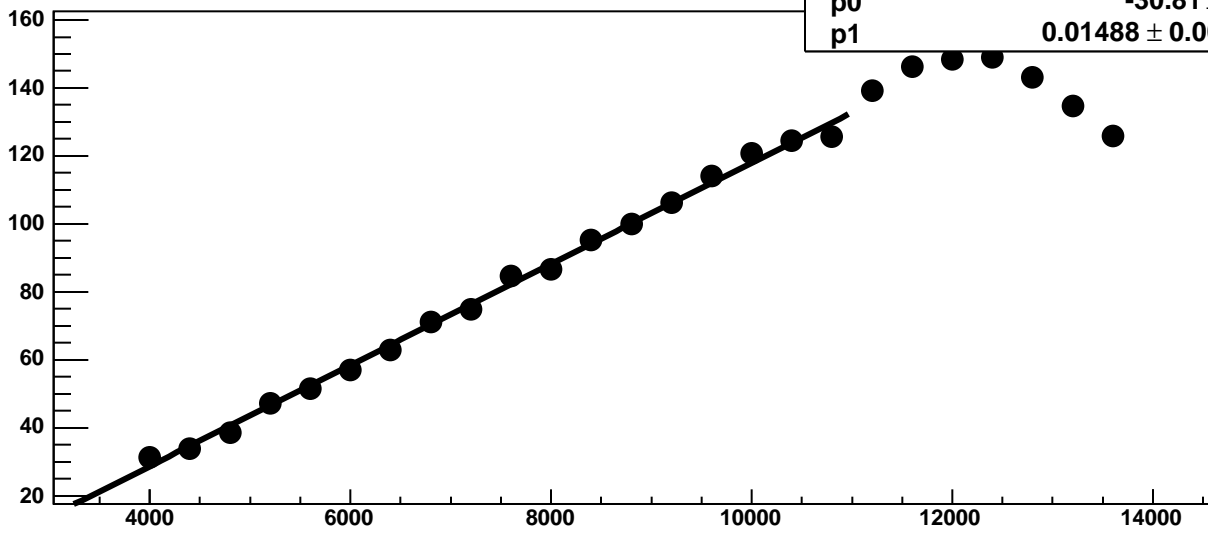
Chip 3, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



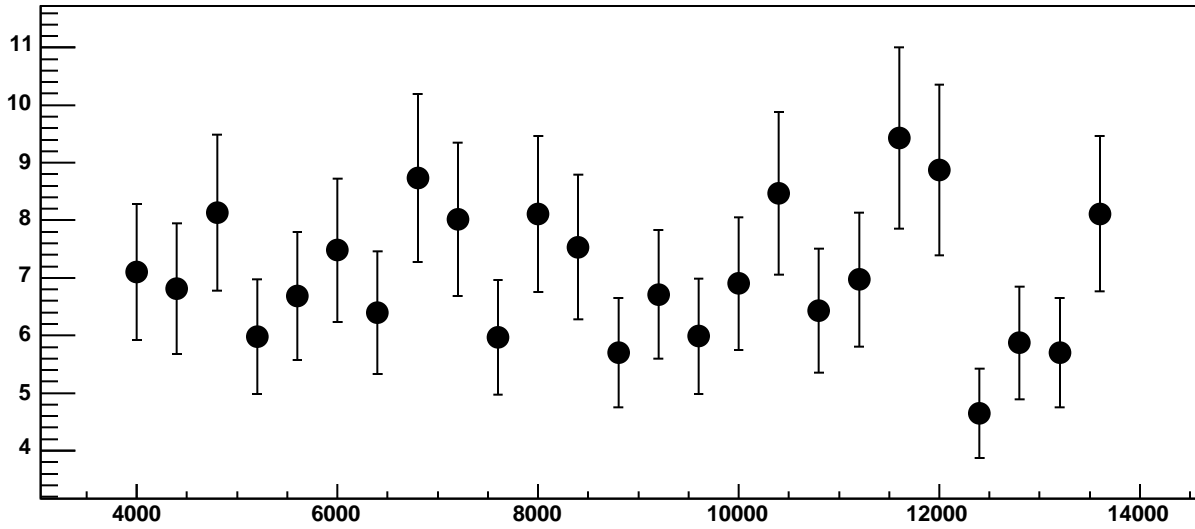
Chip 3, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



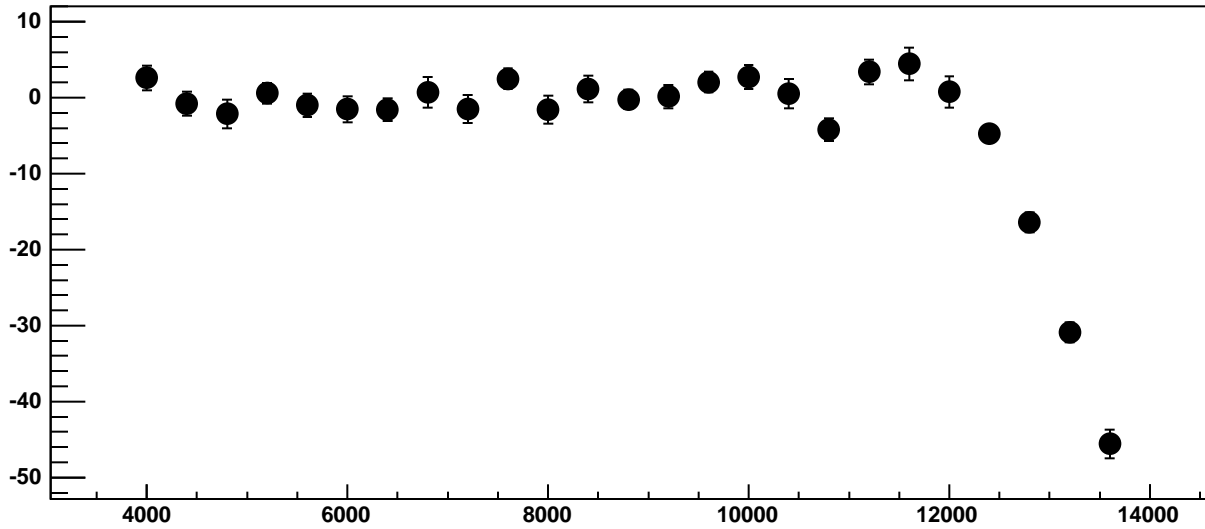
Chip 3, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



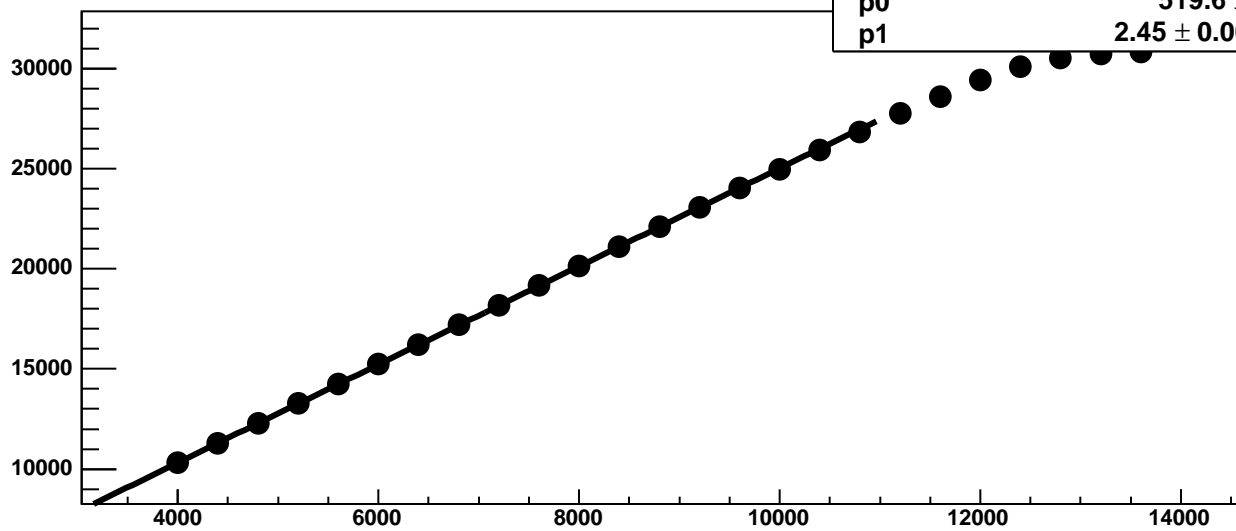
Chip 3, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



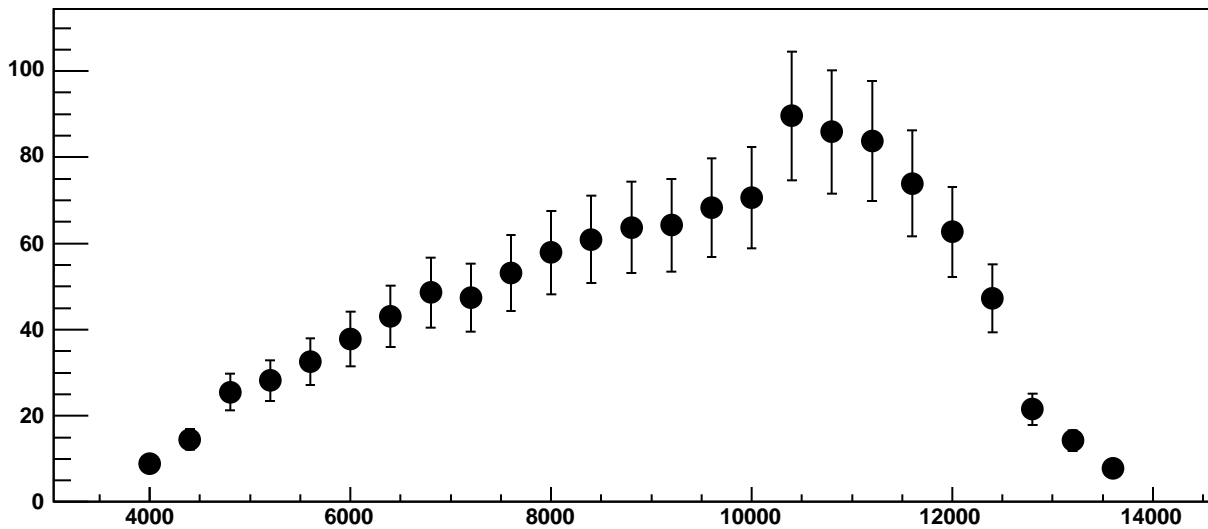
Chip 3, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



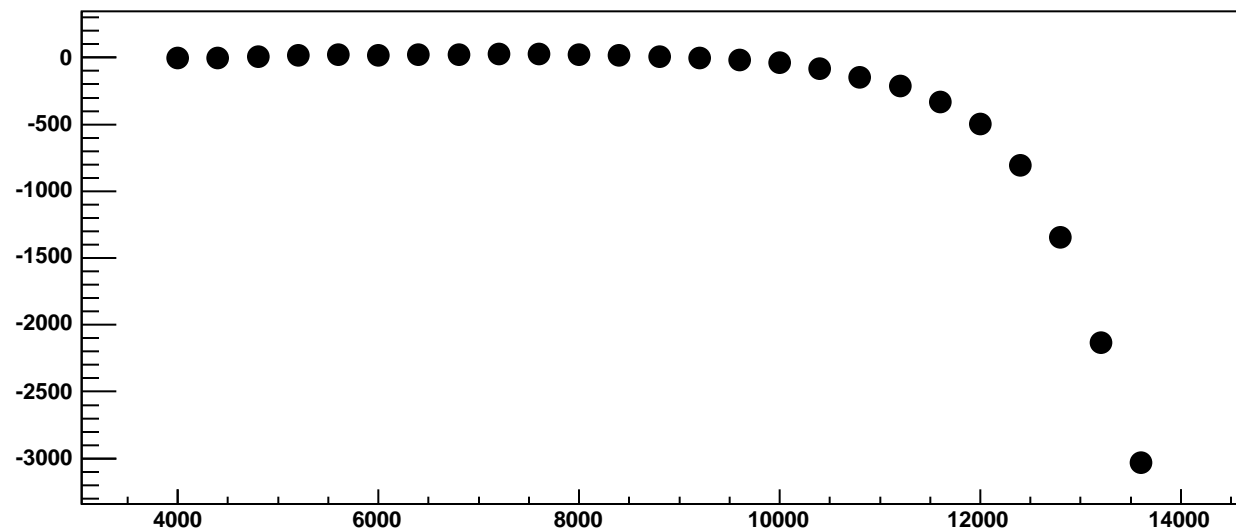
Chip 4, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



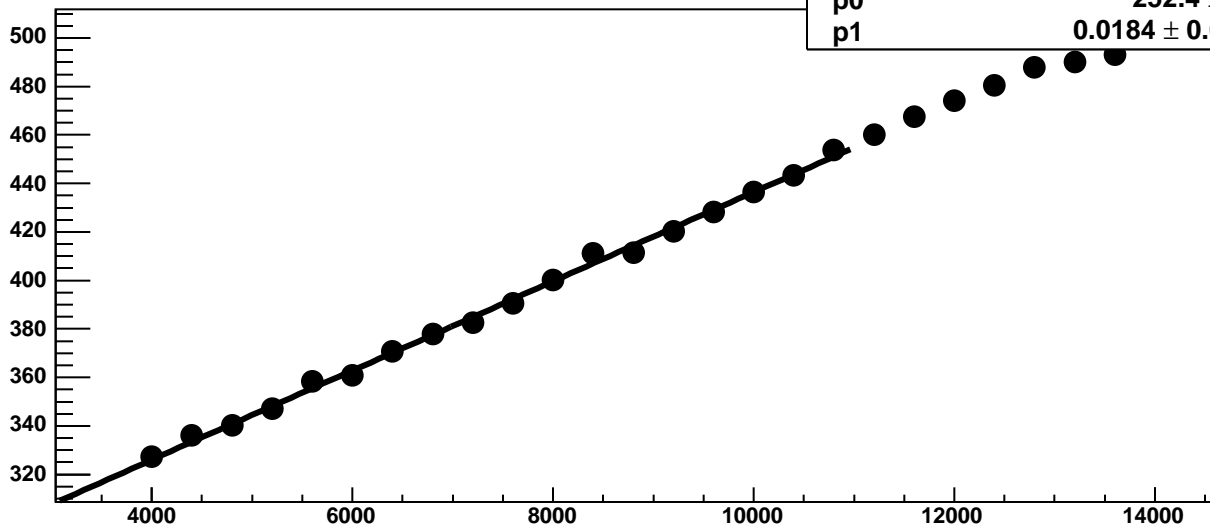
Chip 4, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC

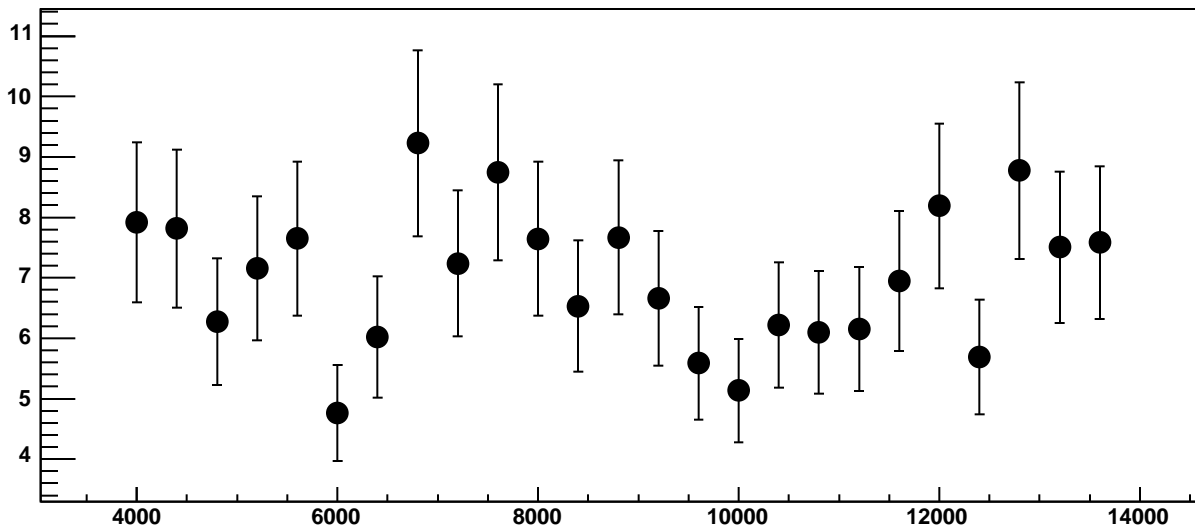


Chip 4, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

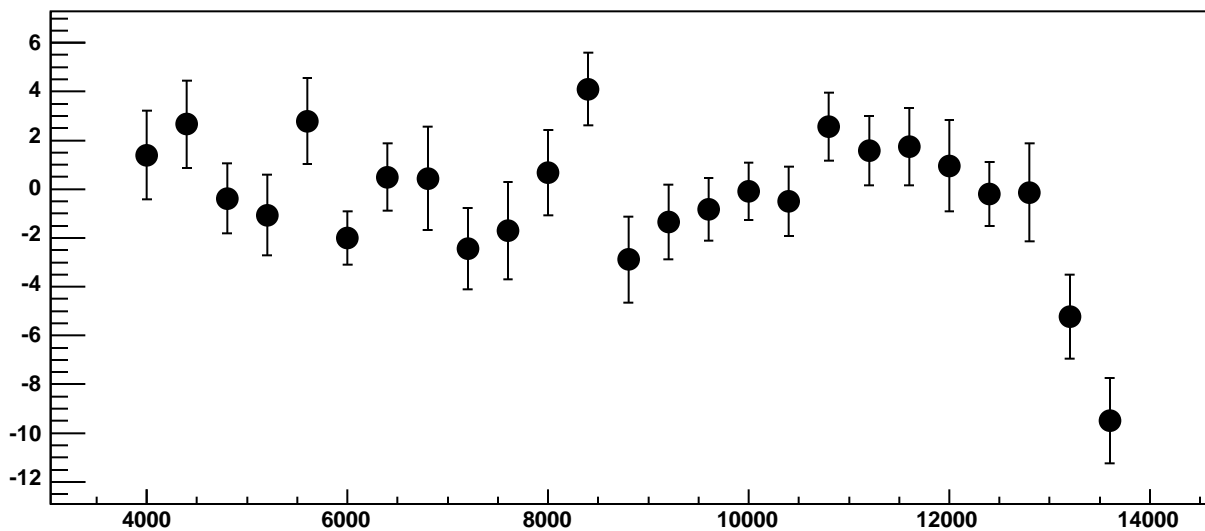


χ^2 / ndf 27.22 / 16
p0 252.4 ± 1.339
p1 0.0184 ± 0.000169

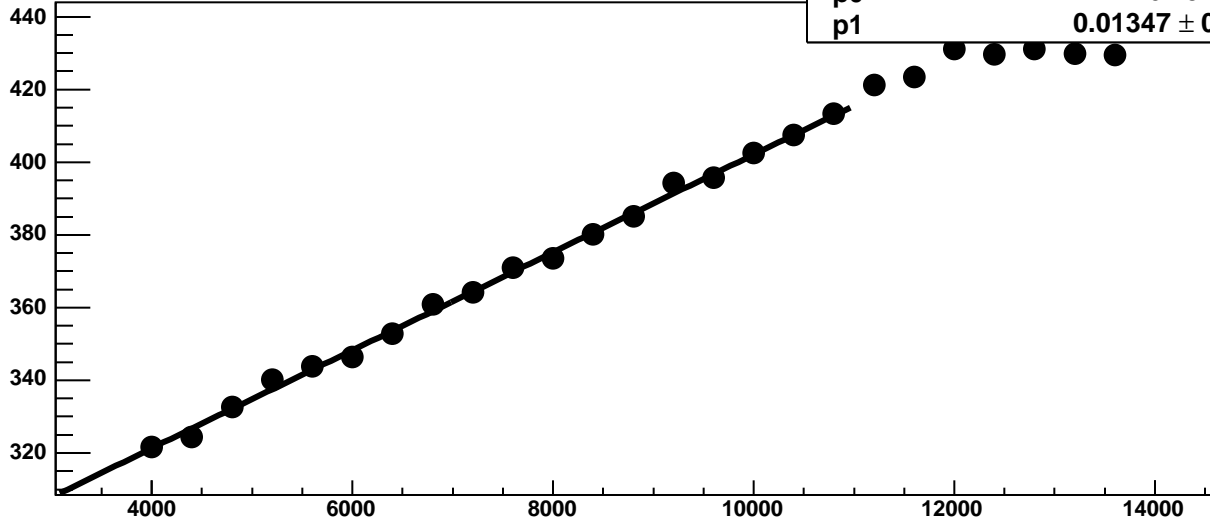
Chip 4, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



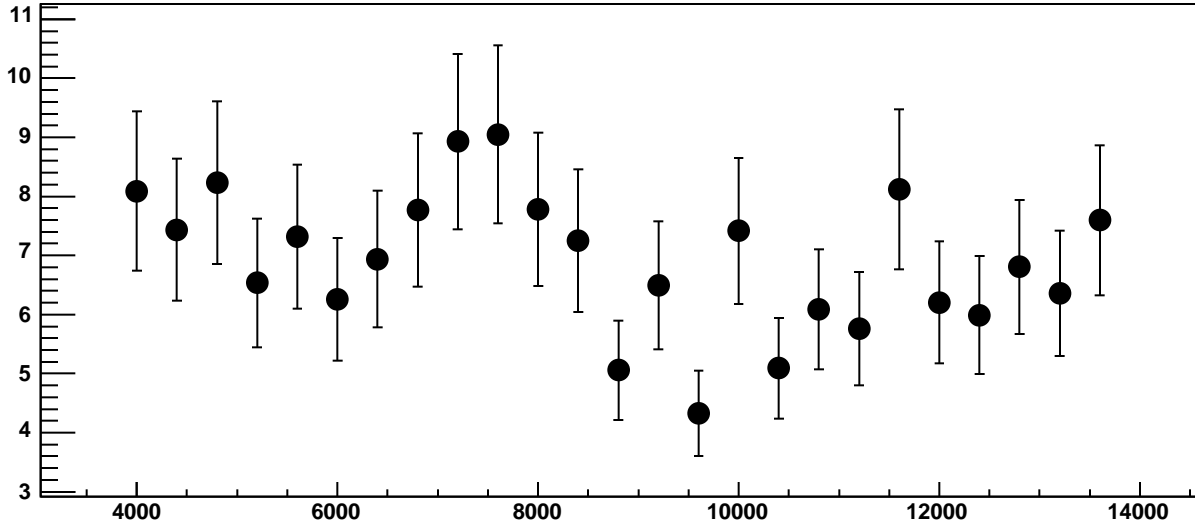
Chip 4, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



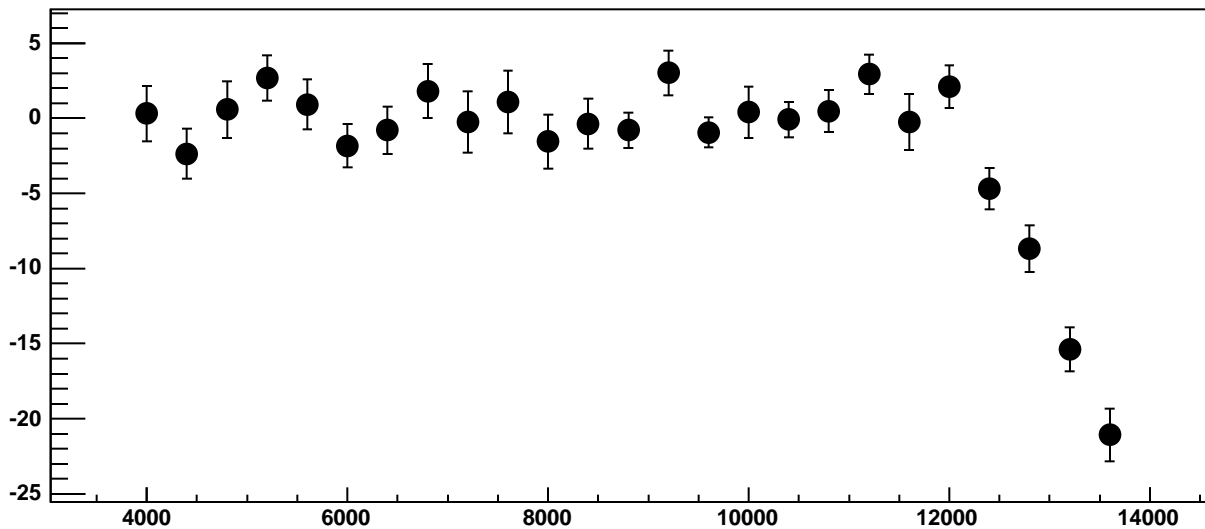
Chip 4, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



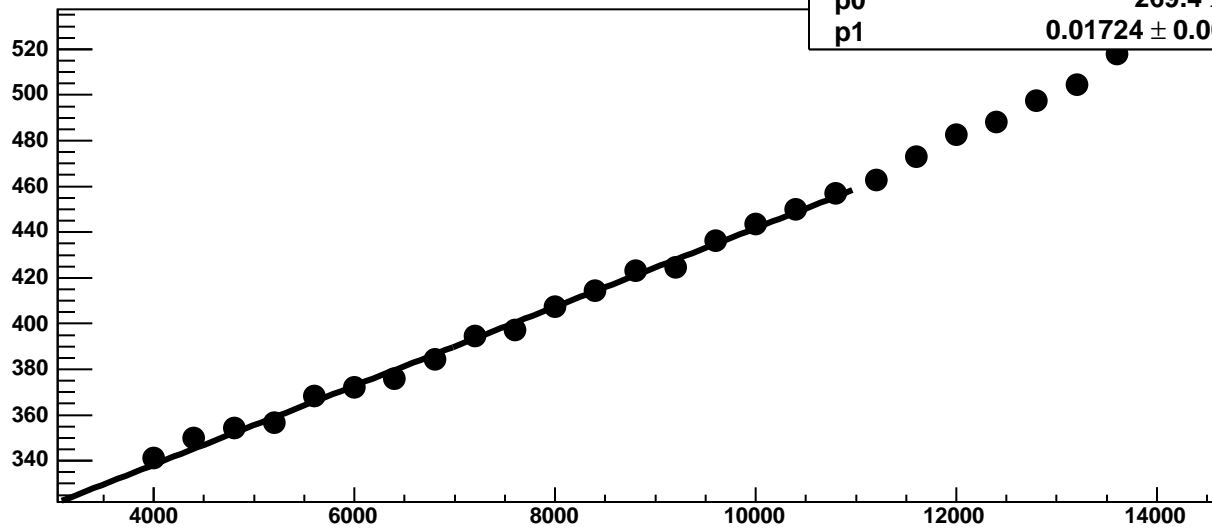
Chip 4, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

38.86 / 16

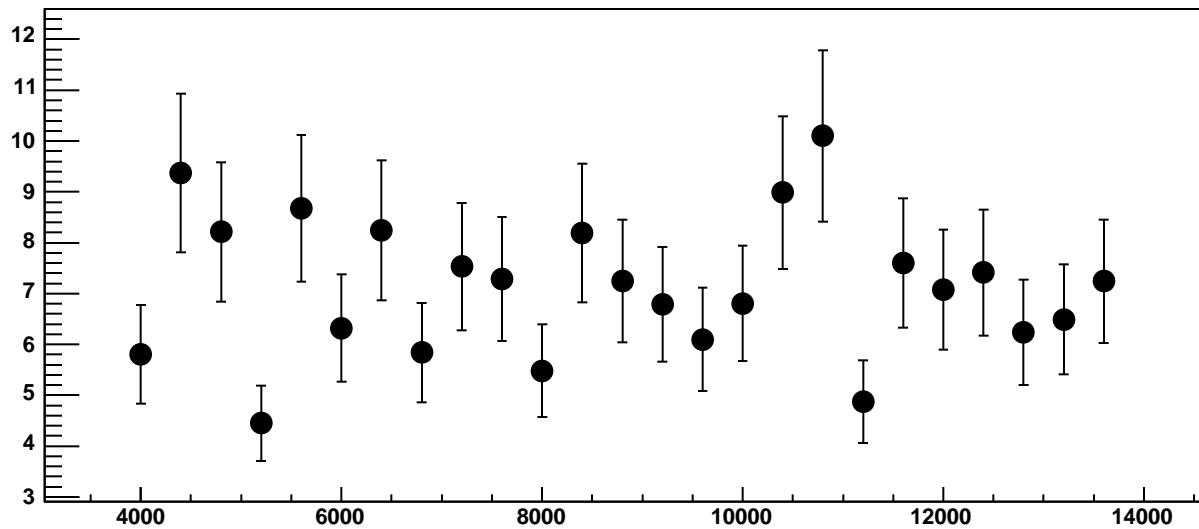
p0

269.4 ± 1.383

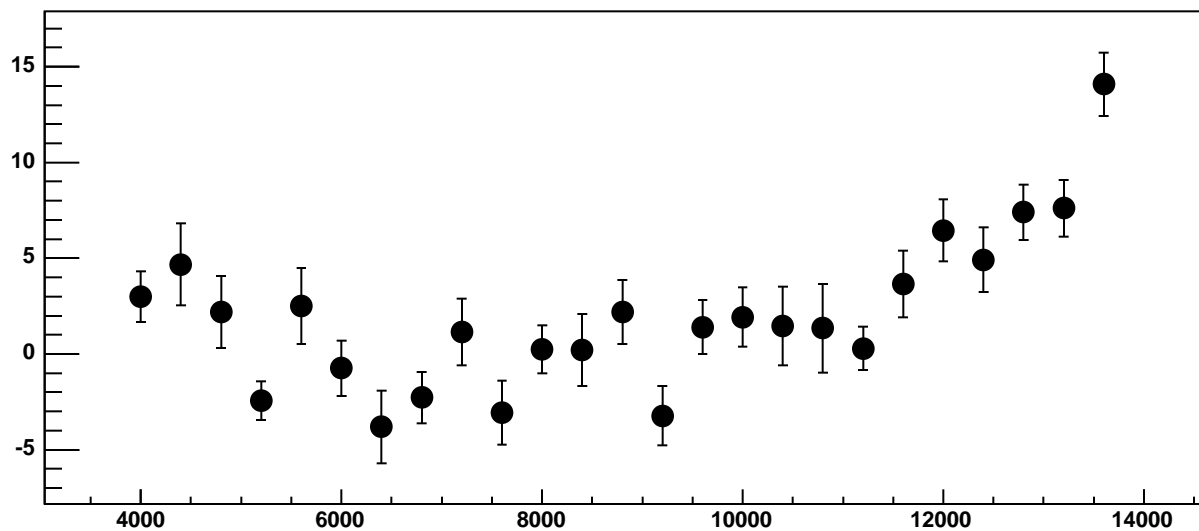
p1

0.01724 ± 0.0001859

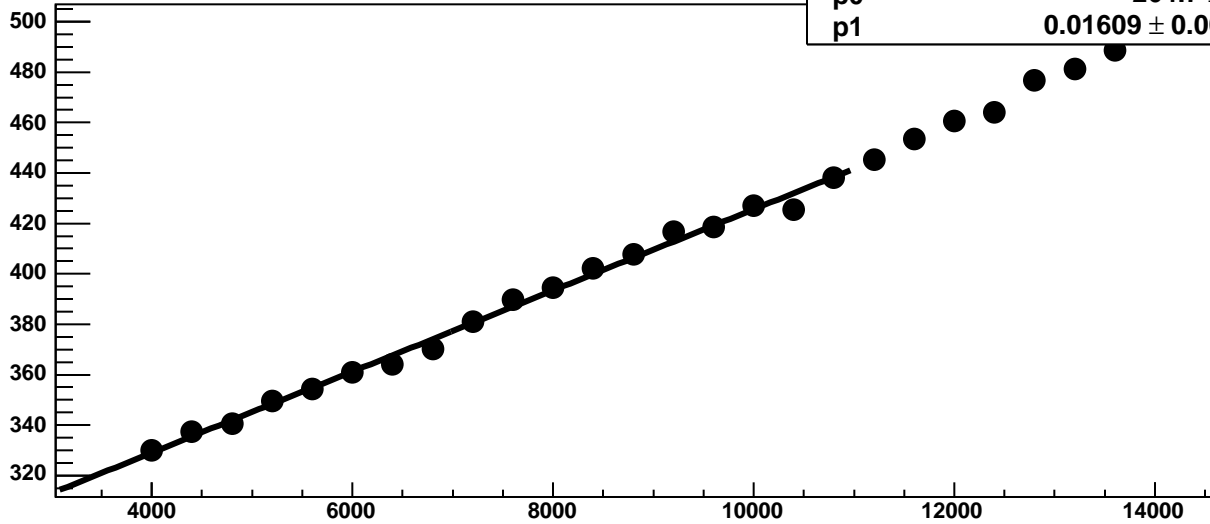
Chip 4, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

43.15 / 16

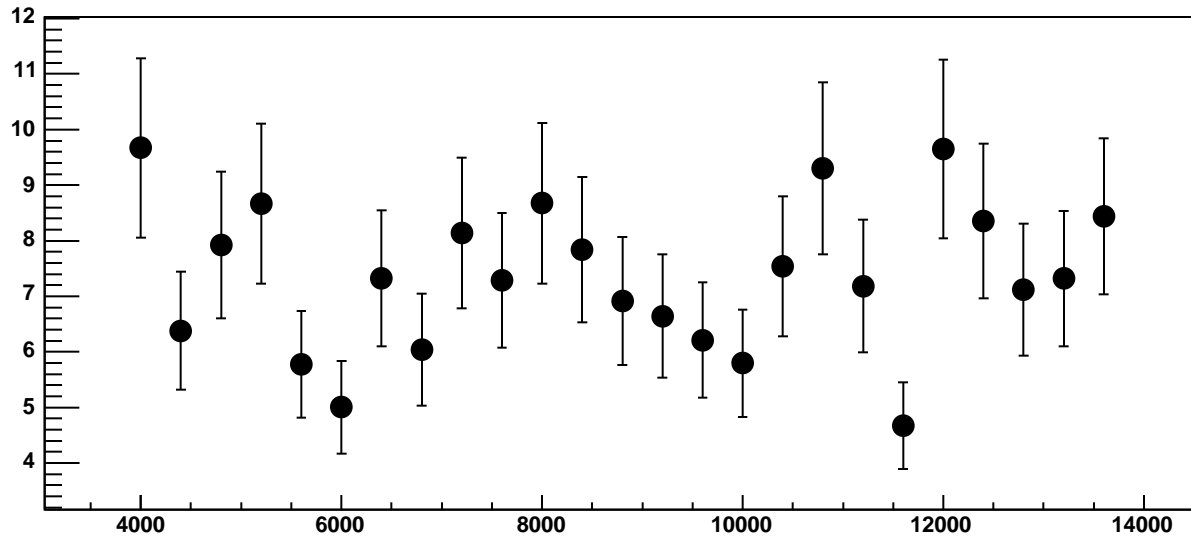
p0

264.7 ± 1.445

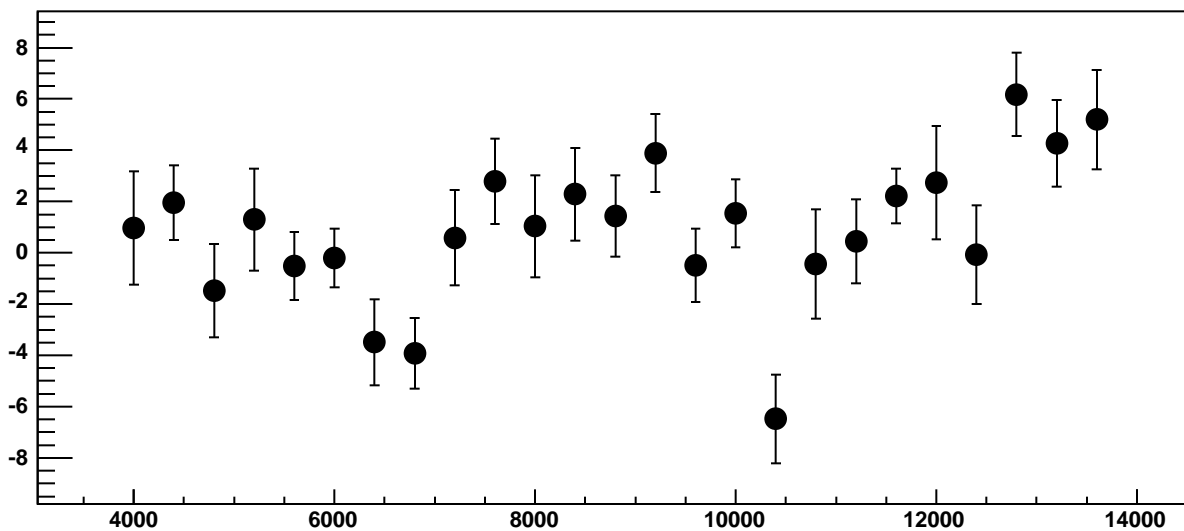
p1

0.01609 ± 0.0001888

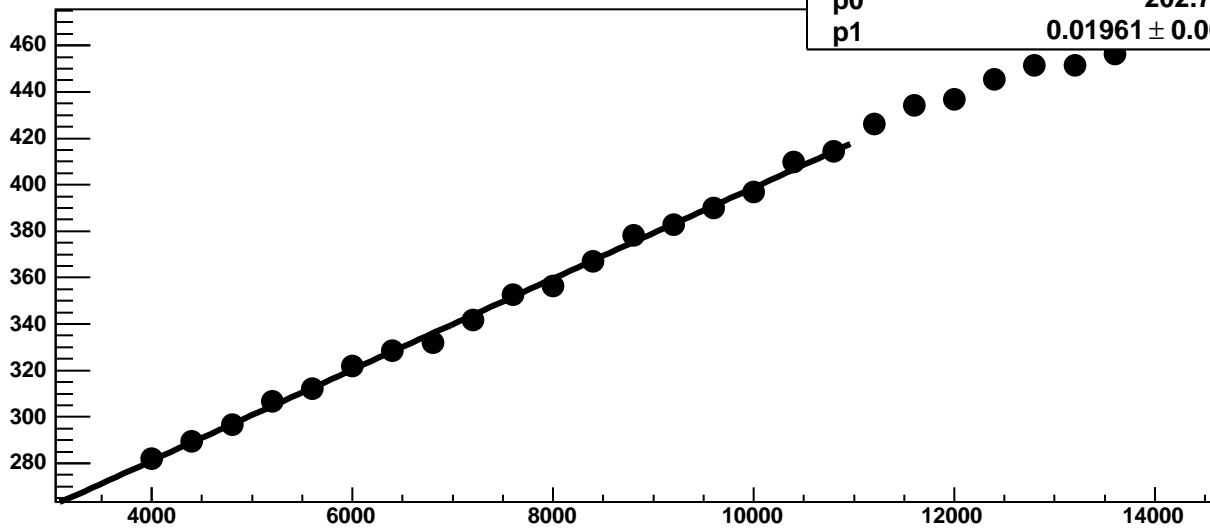
Chip 4, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

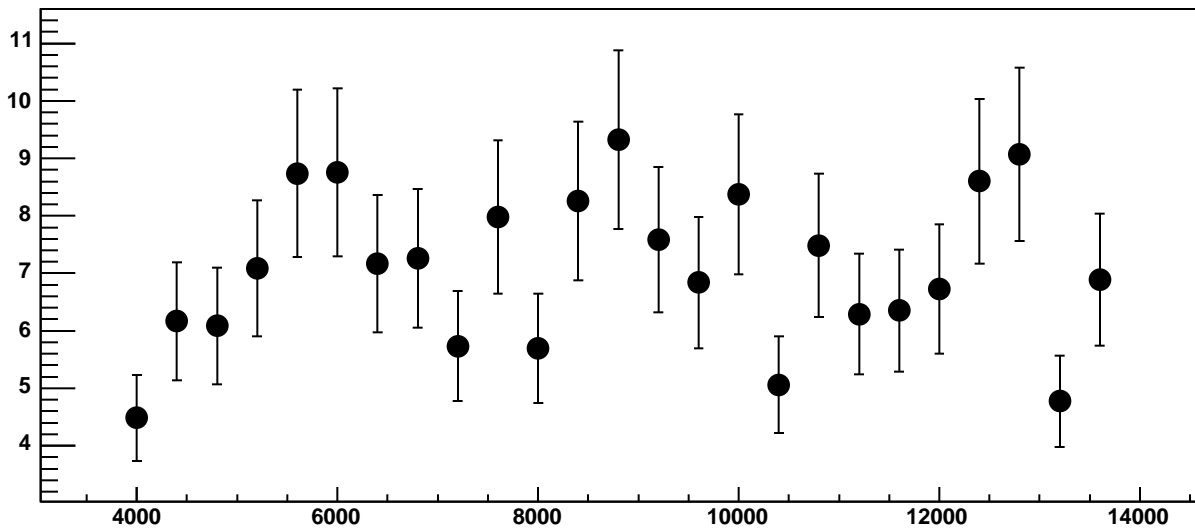


Chip 4, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC

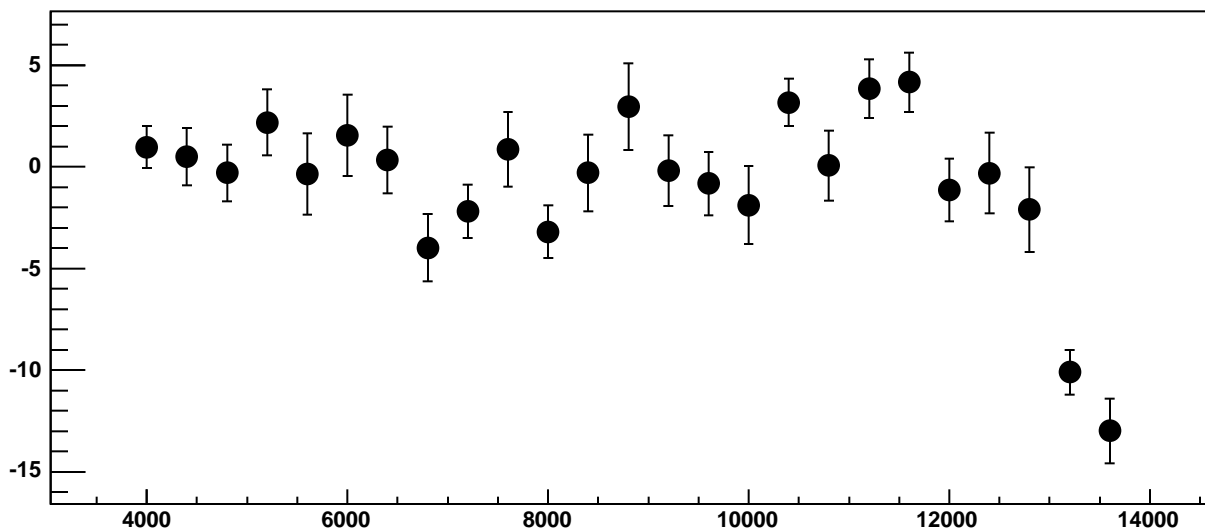


χ^2 / ndf 28.85 / 16
p0 202.7 ± 1.21
p1 0.01961 ± 0.0001612

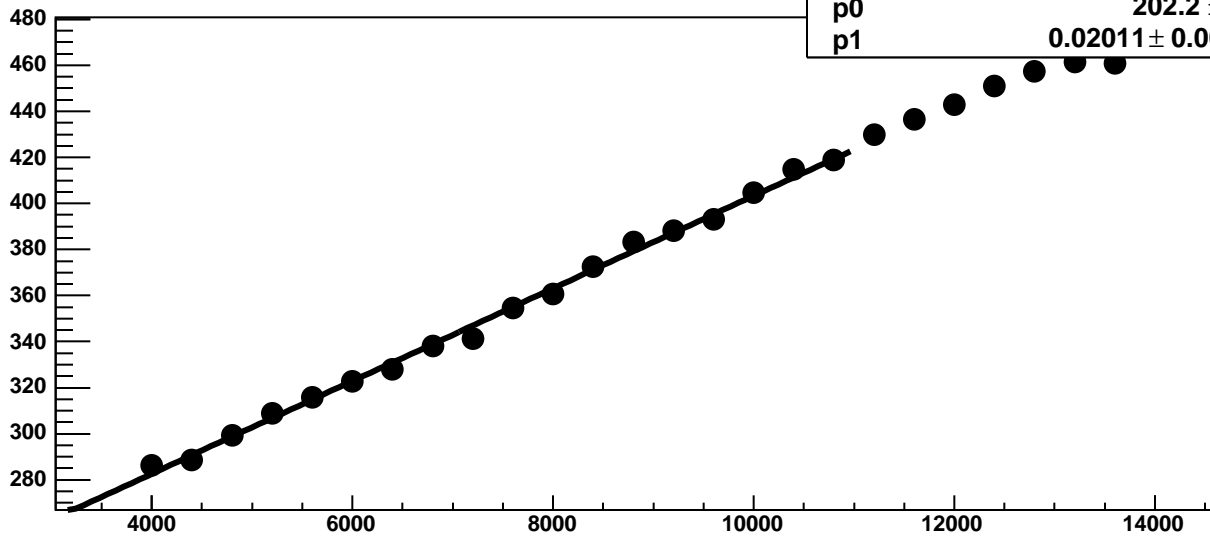
Chip 4, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC

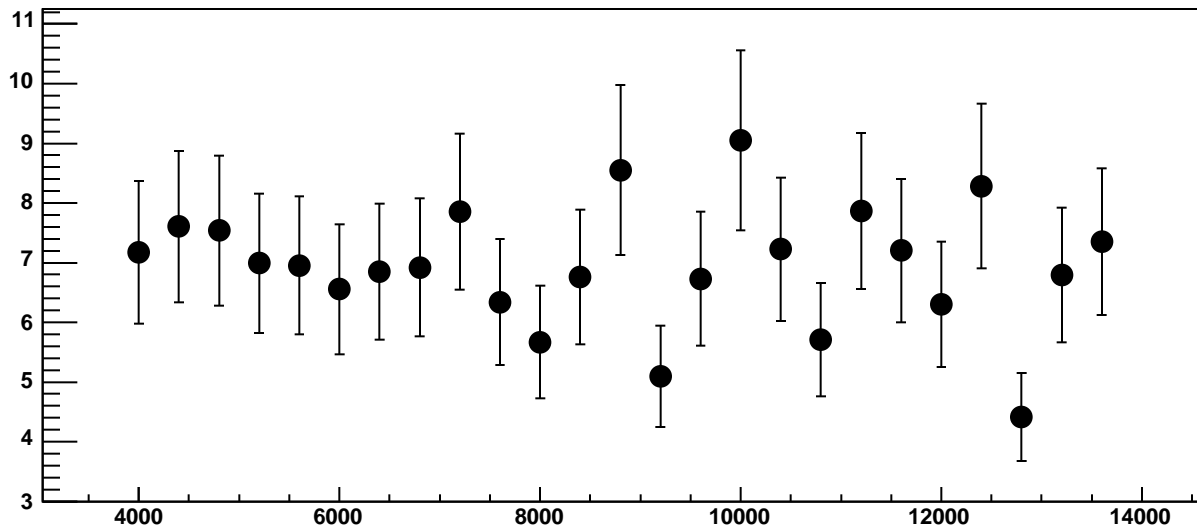


Chip 4, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

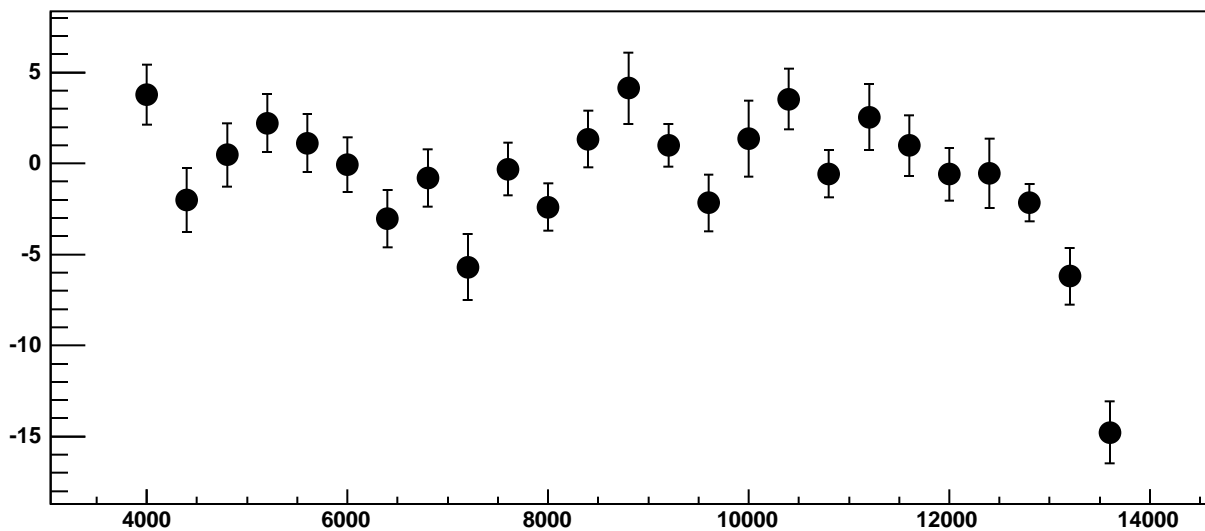


χ^2 / ndf 39.48 / 16
p0 202.2 ± 1.405
p1 0.02011 ± 0.0001795

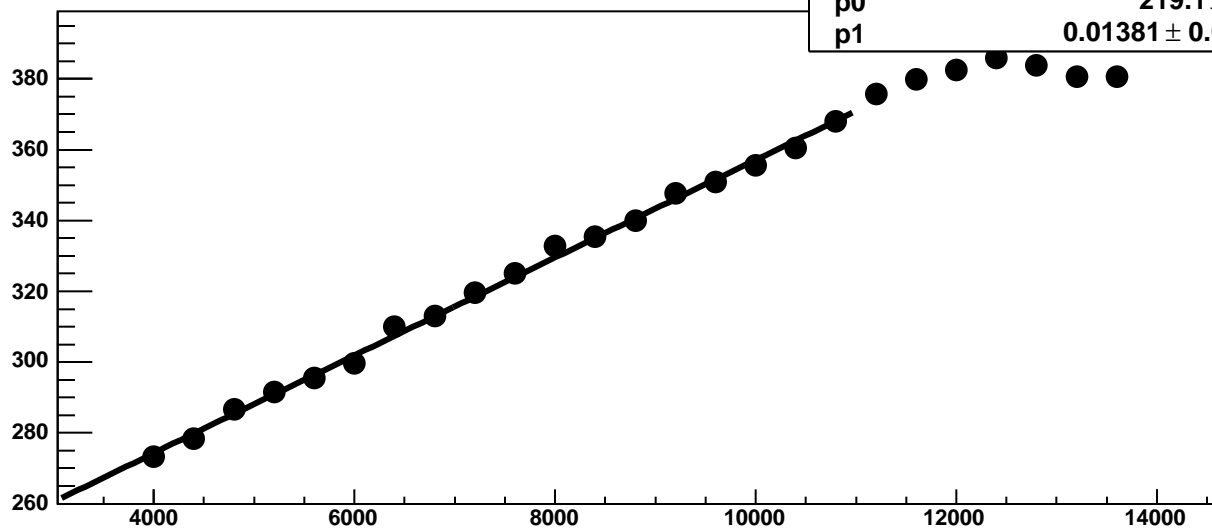
Chip 4, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



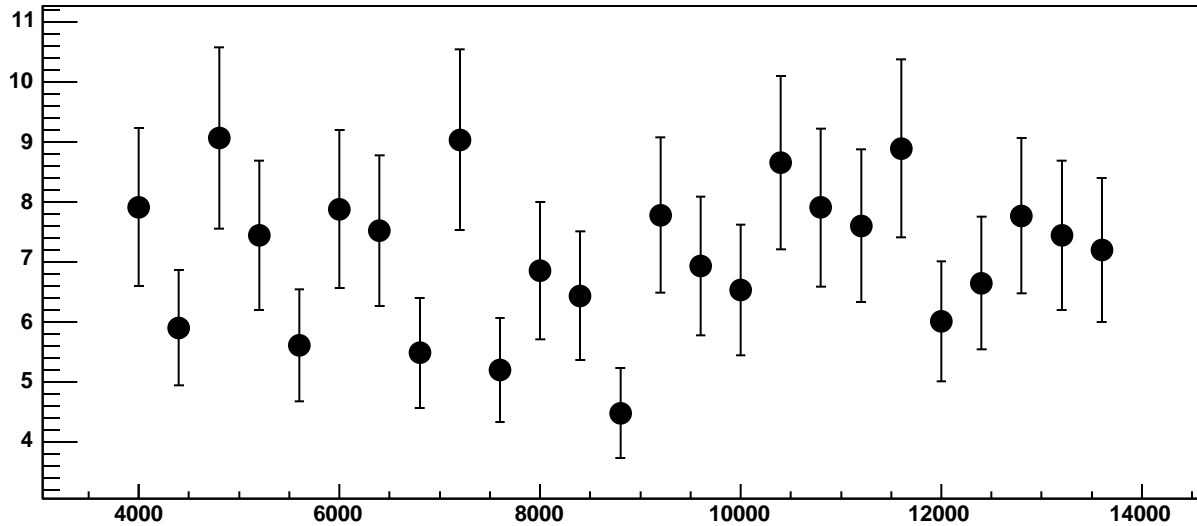
Chip 4, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



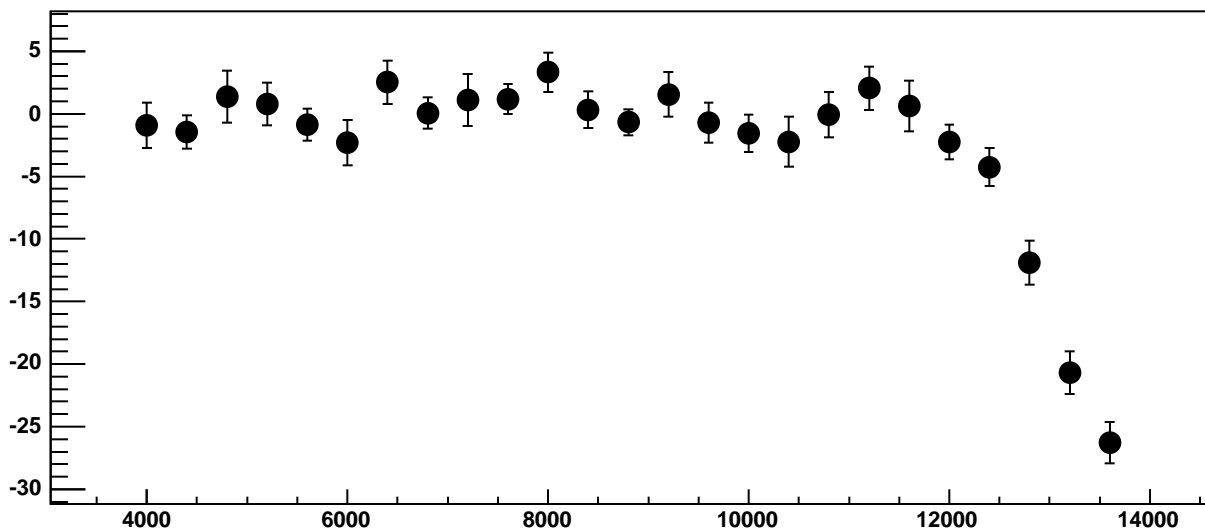
Chip 4, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



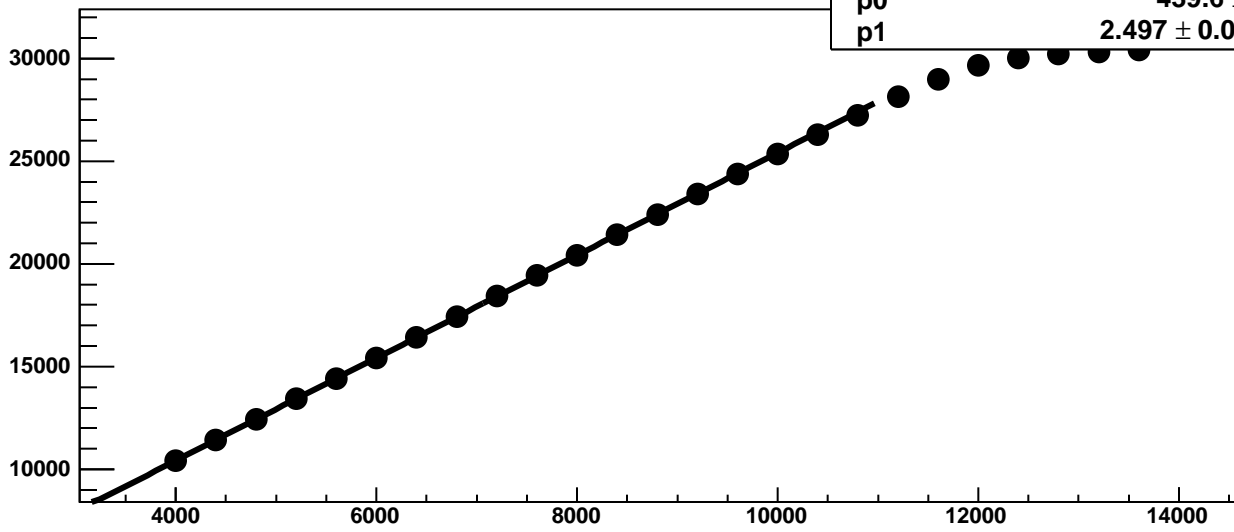
Chip 4, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

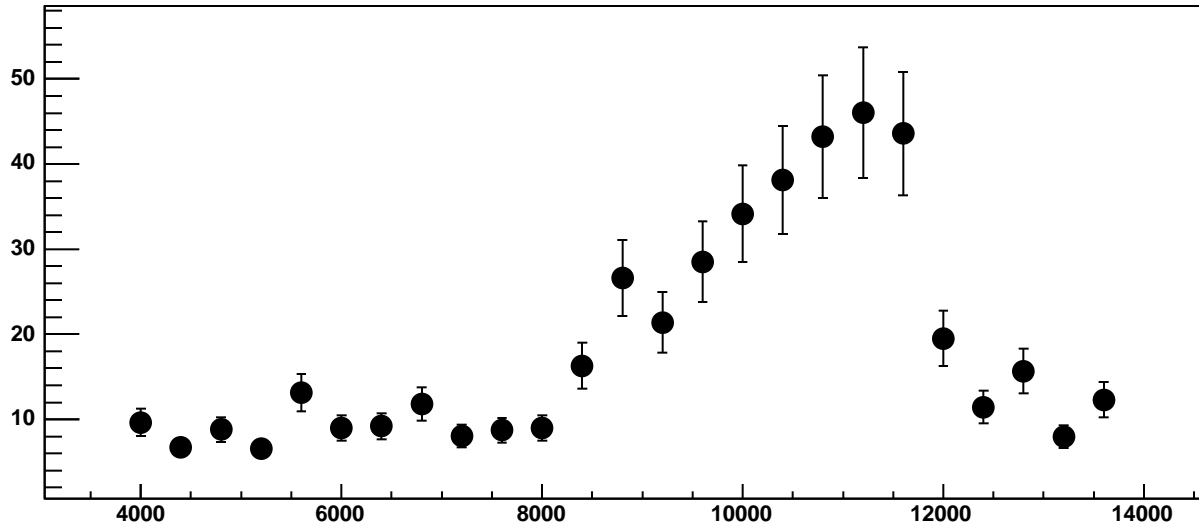


Chip 4, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC

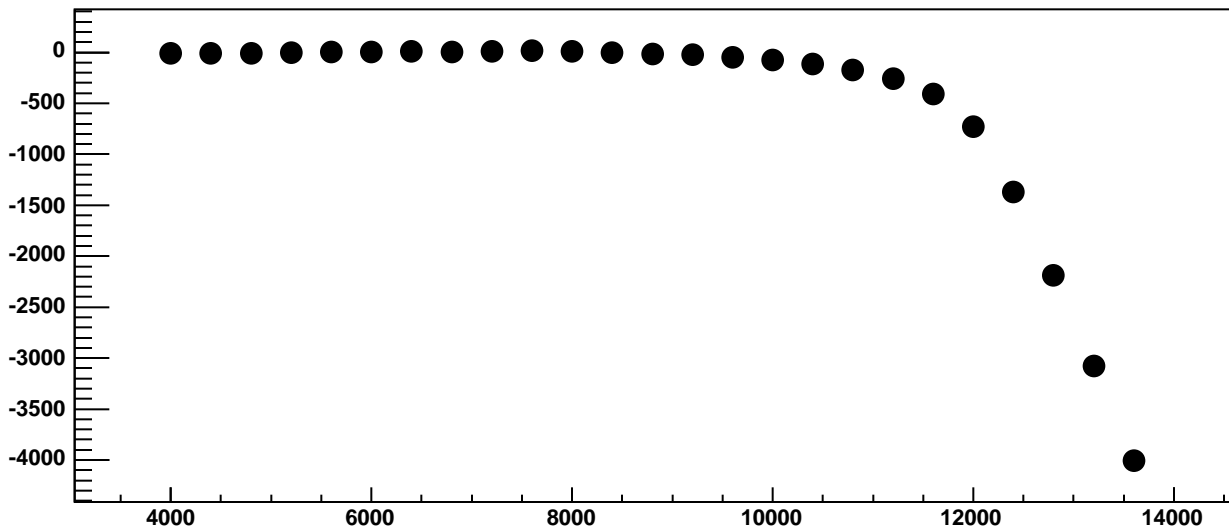


χ^2 / ndf 845.3 / 16
p0 439.6 ± 2.418
p1 2.497 ± 0.0003851

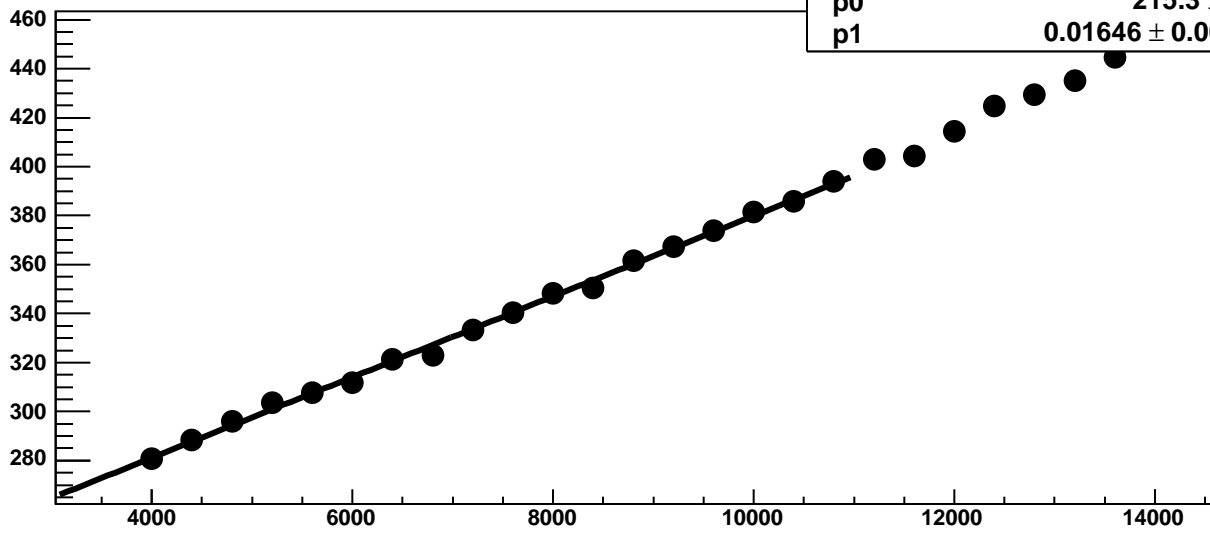
Chip 4, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

28.31 / 16

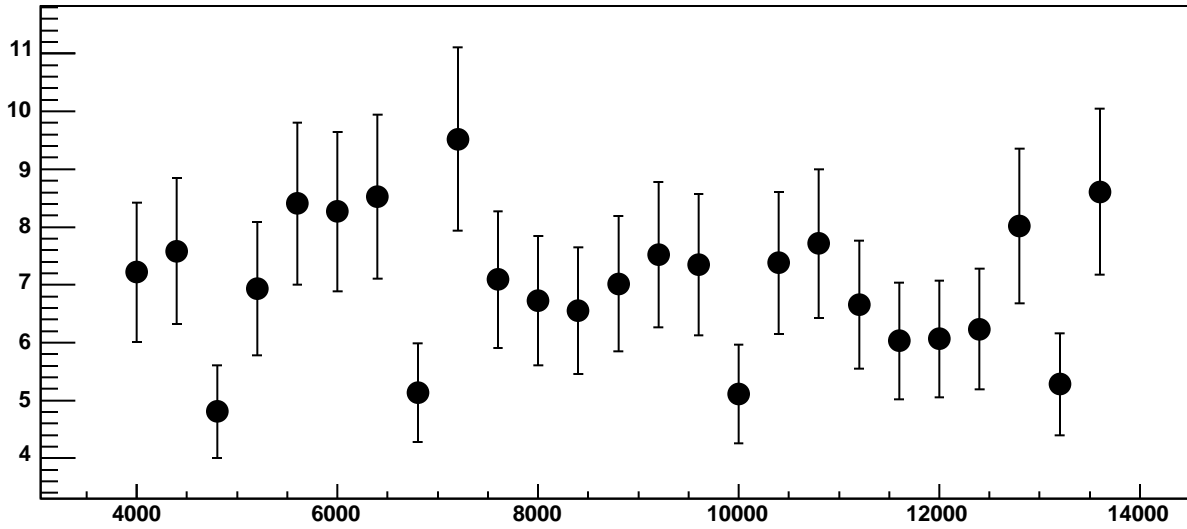
p0

215.3 ± 1.346

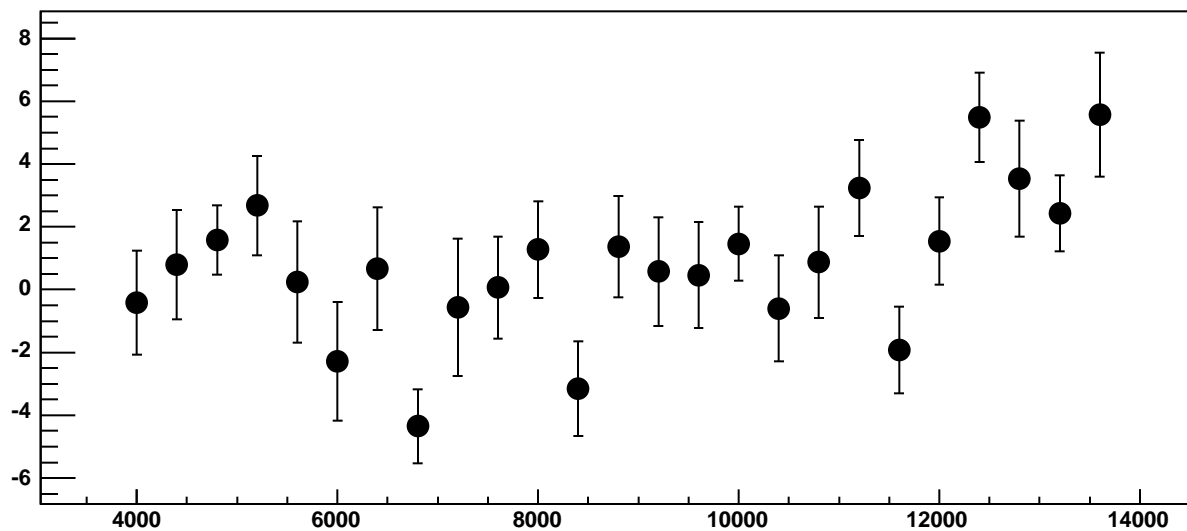
p1

0.01646 ± 0.0001754

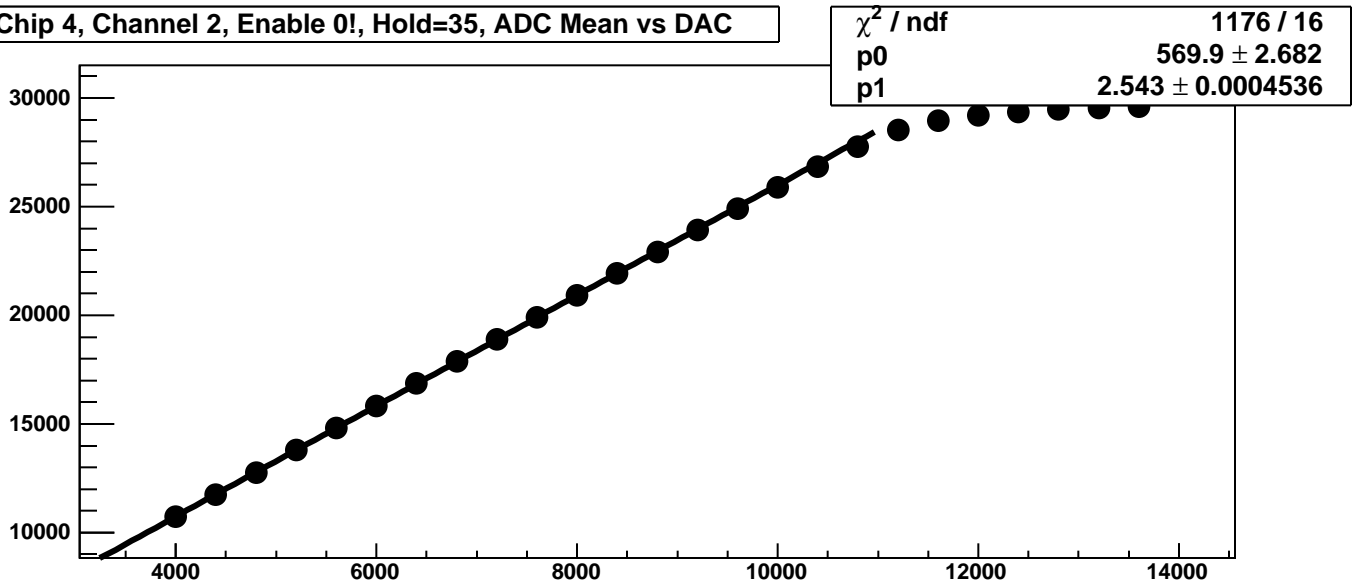
Chip 4, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



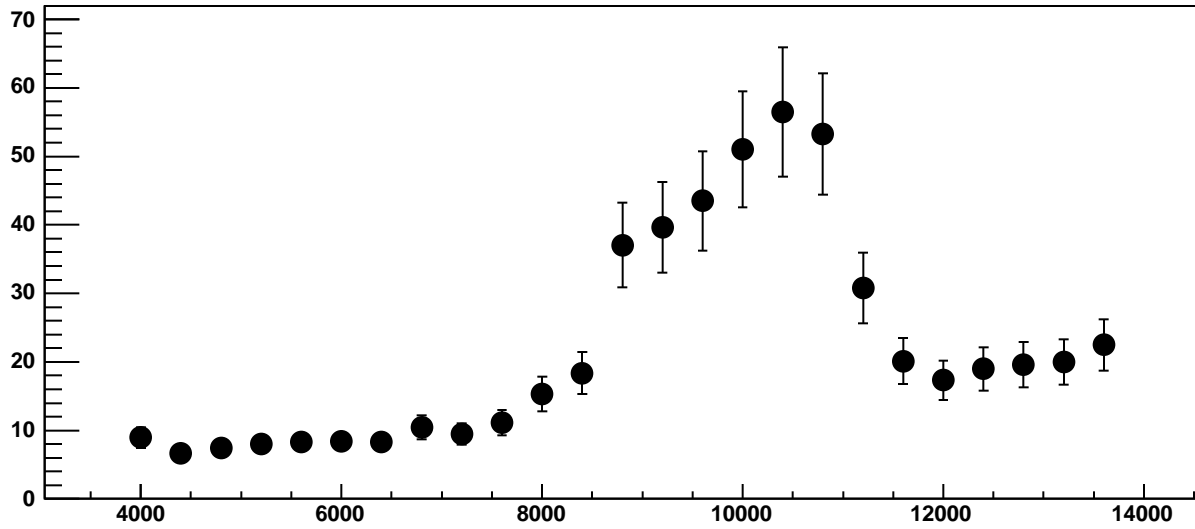
Chip 4, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



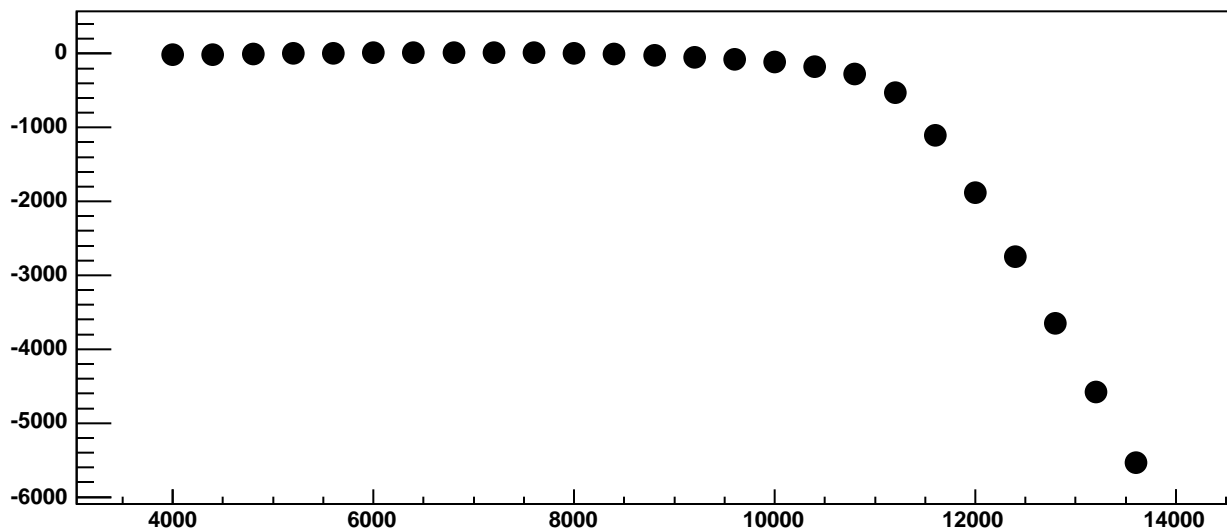
Chip 4, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC



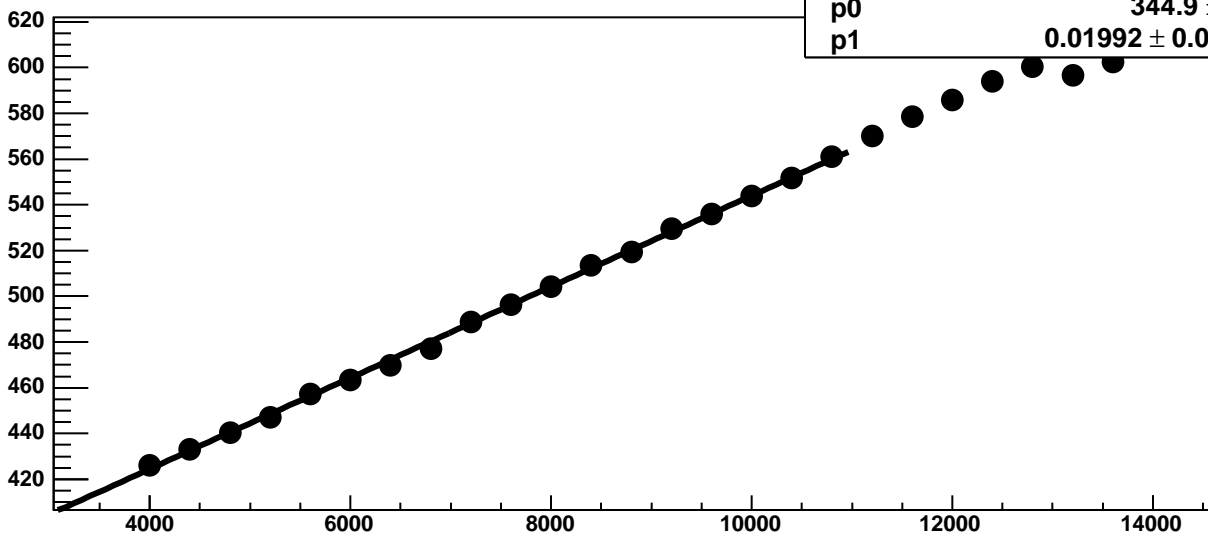
Chip 4, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC

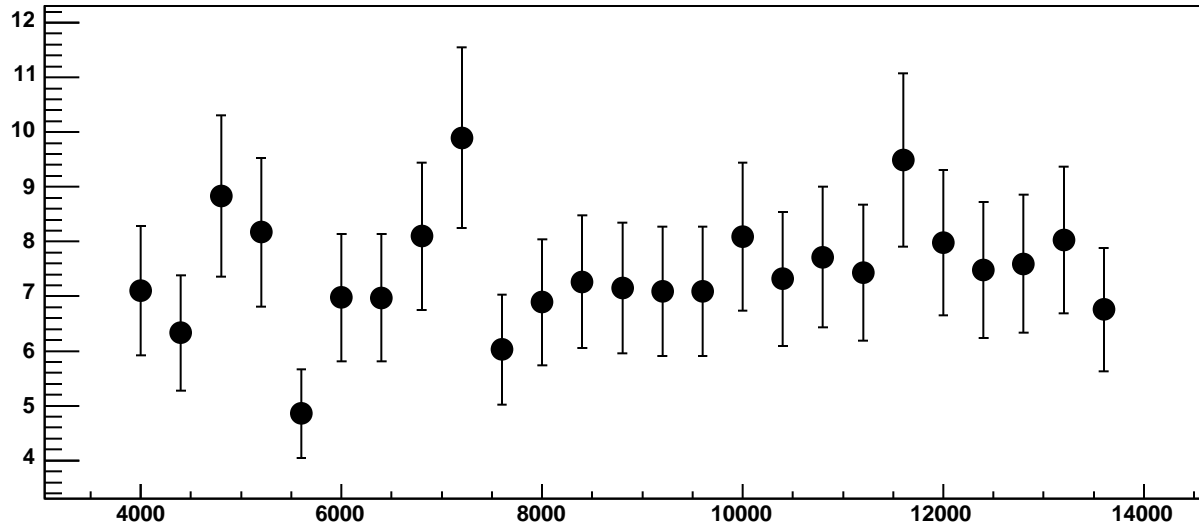


Chip 4, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

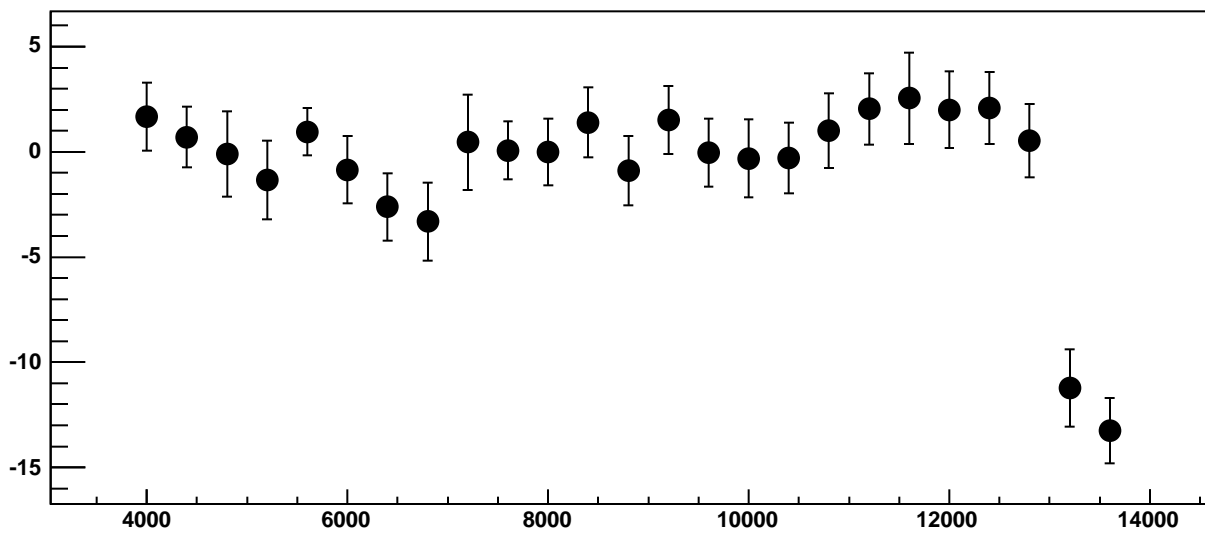


χ^2 / ndf 10.95 / 16
p0 344.9 ± 1.412
p1 0.01992 ± 0.0001871

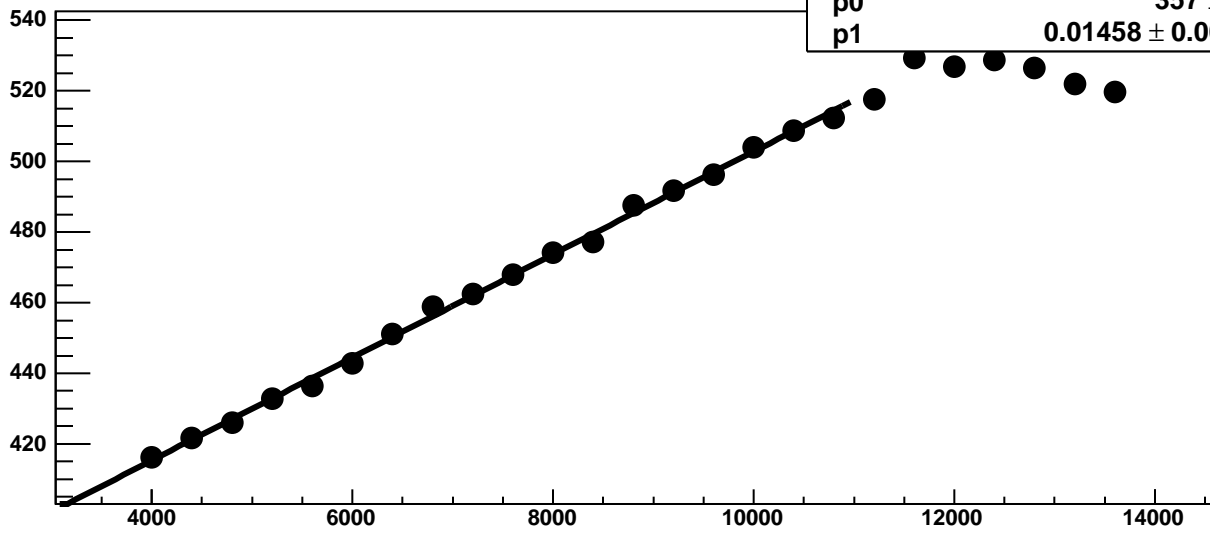
Chip 4, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



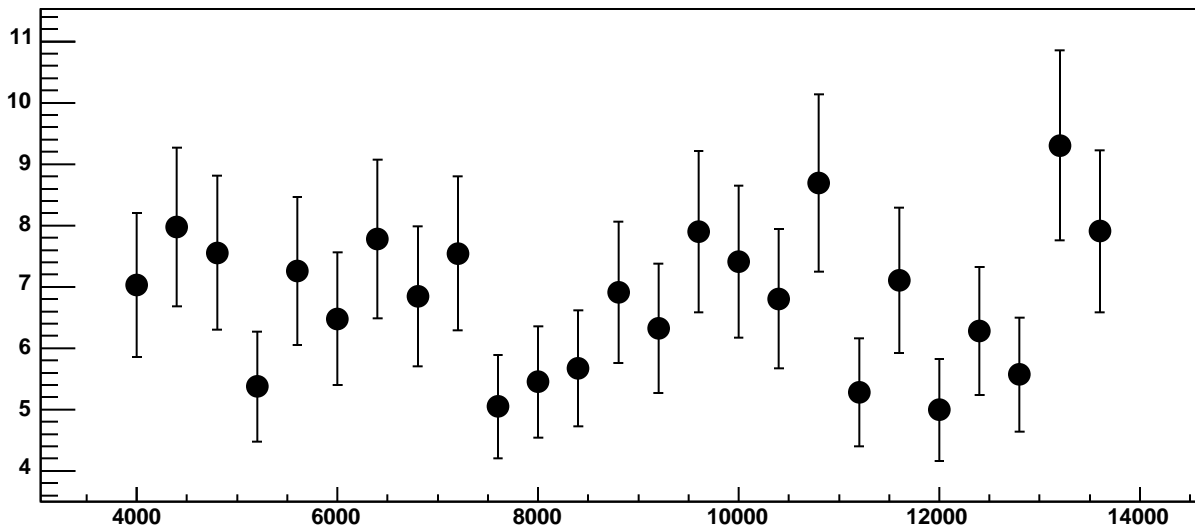
Chip 4, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



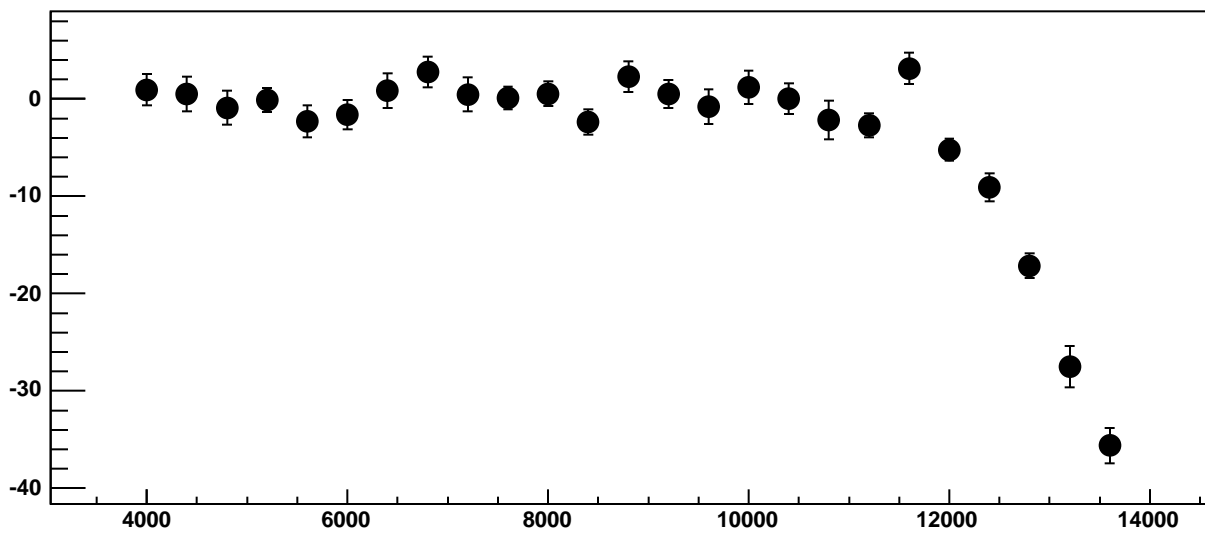
Chip 4, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



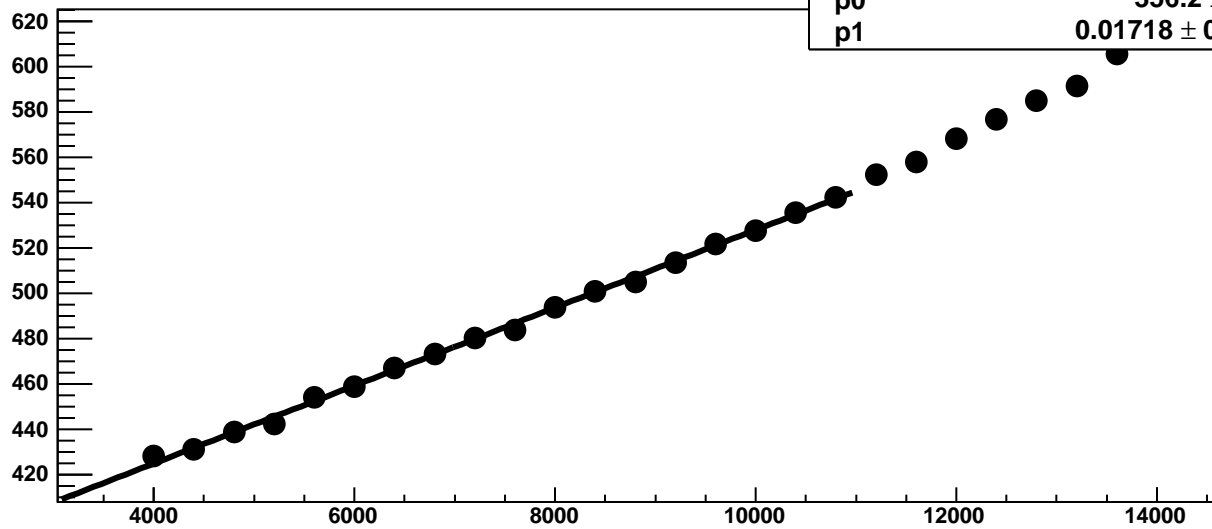
Chip 4, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



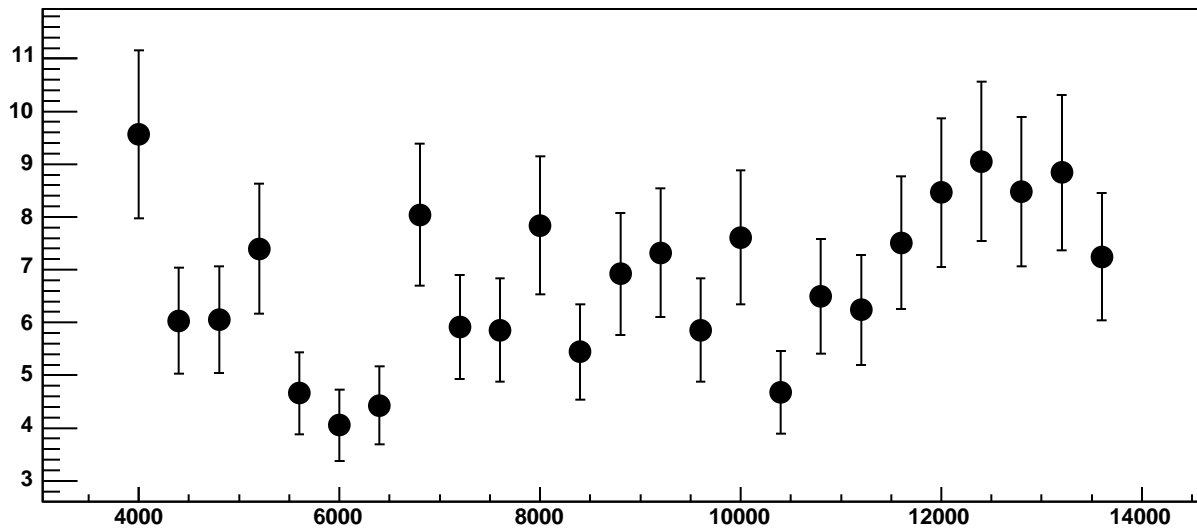
Chip 4, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



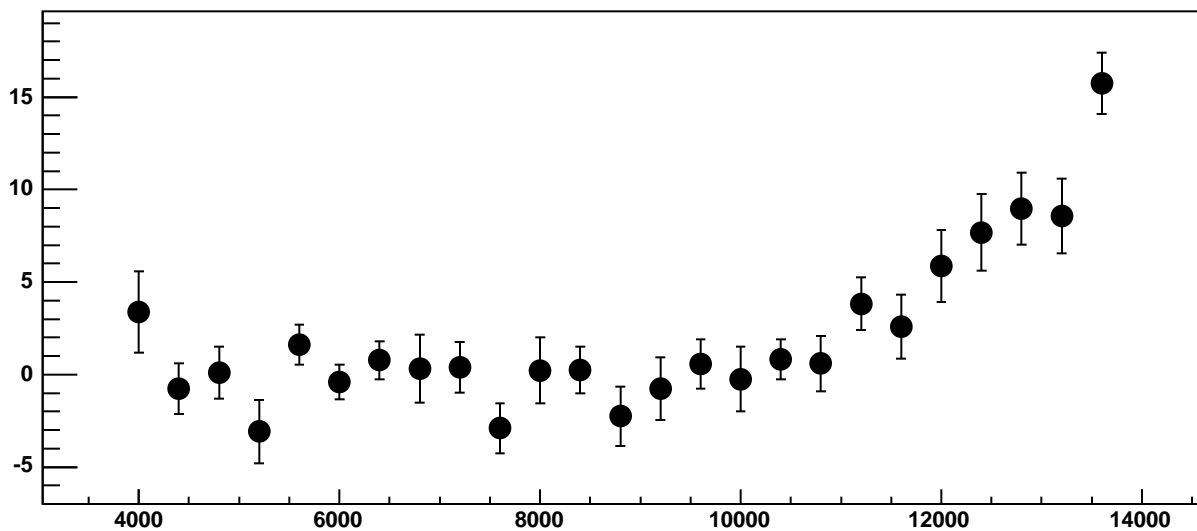
Chip 4, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC



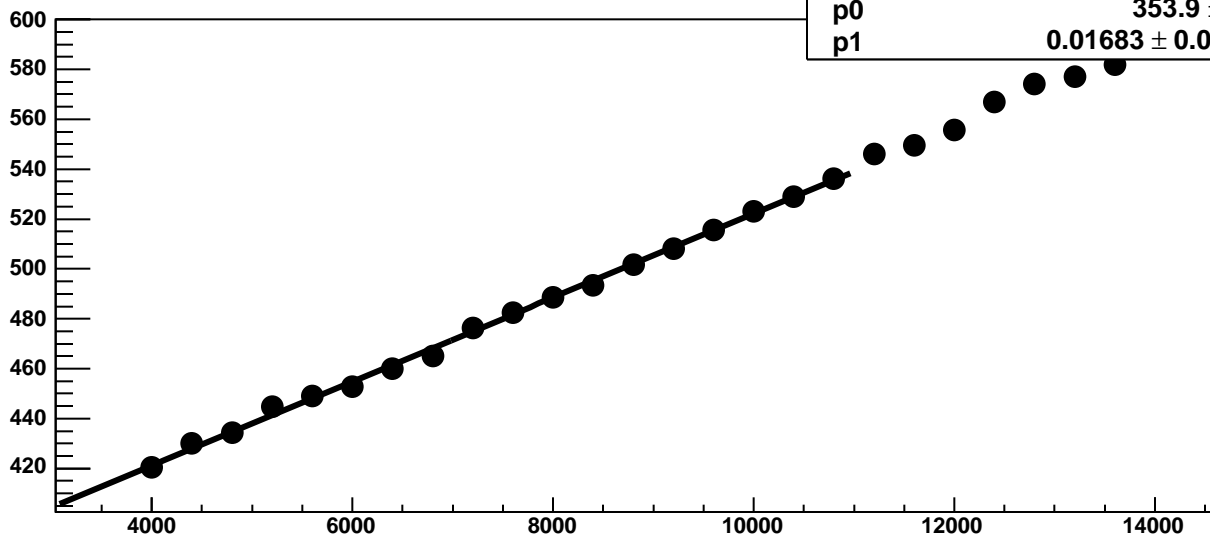
Chip 4, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

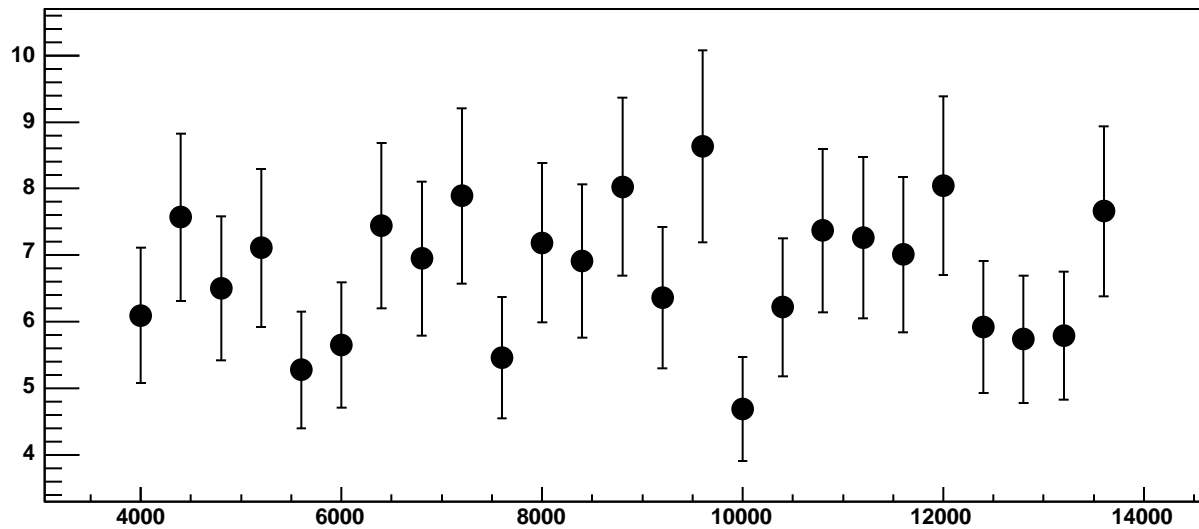


Chip 4, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

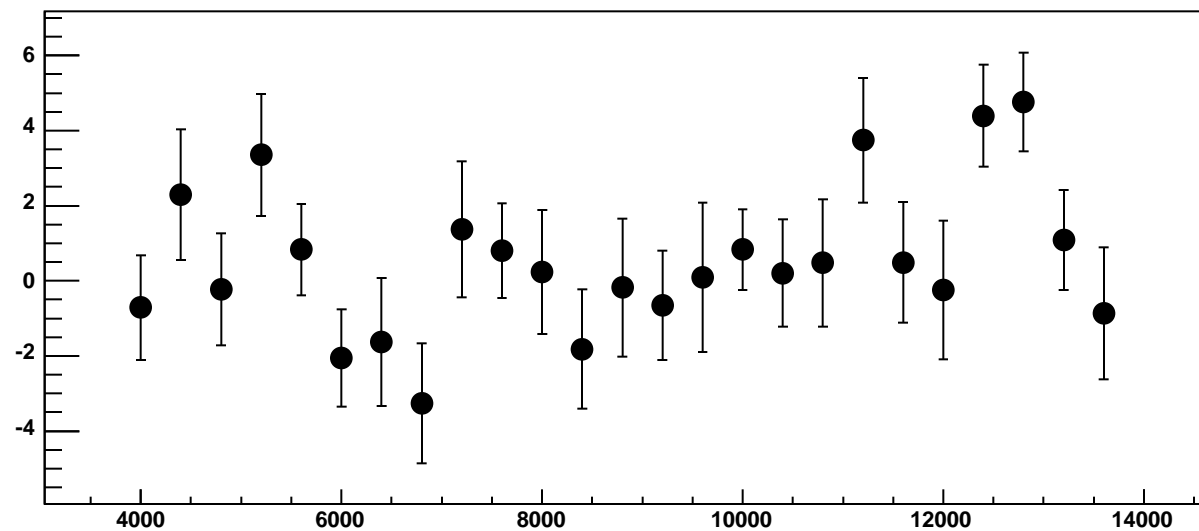


χ^2 / ndf 17.57 / 16
p0 353.9 ± 1.286
p1 0.01683 ± 0.0001671

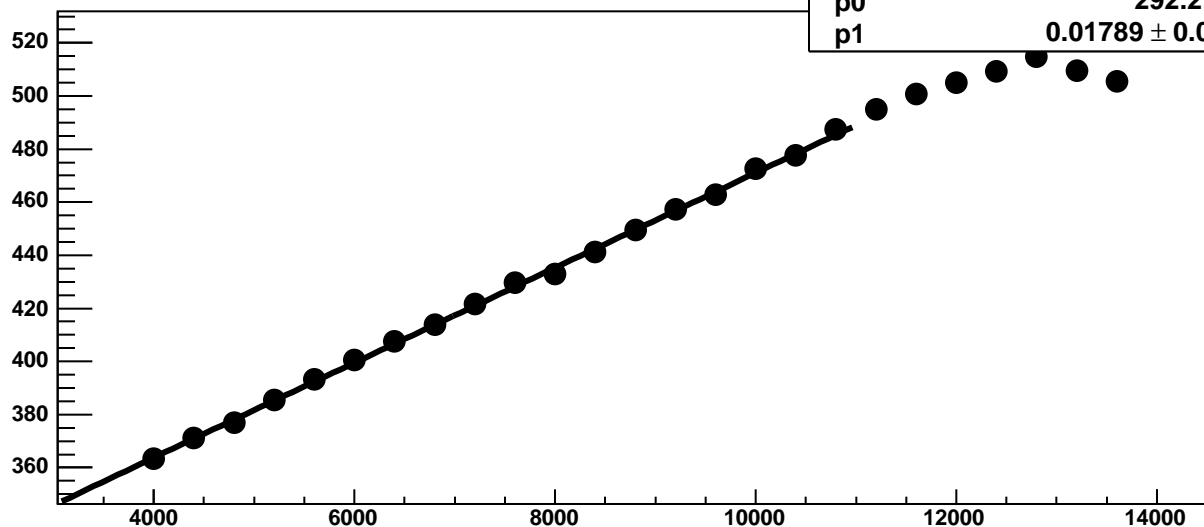
Chip 4, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



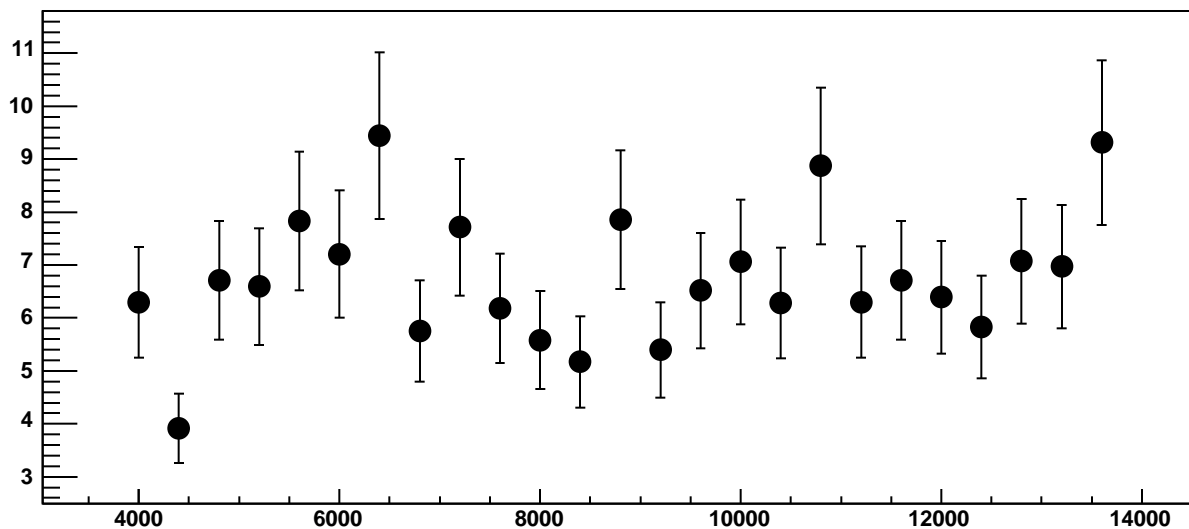
Chip 4, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



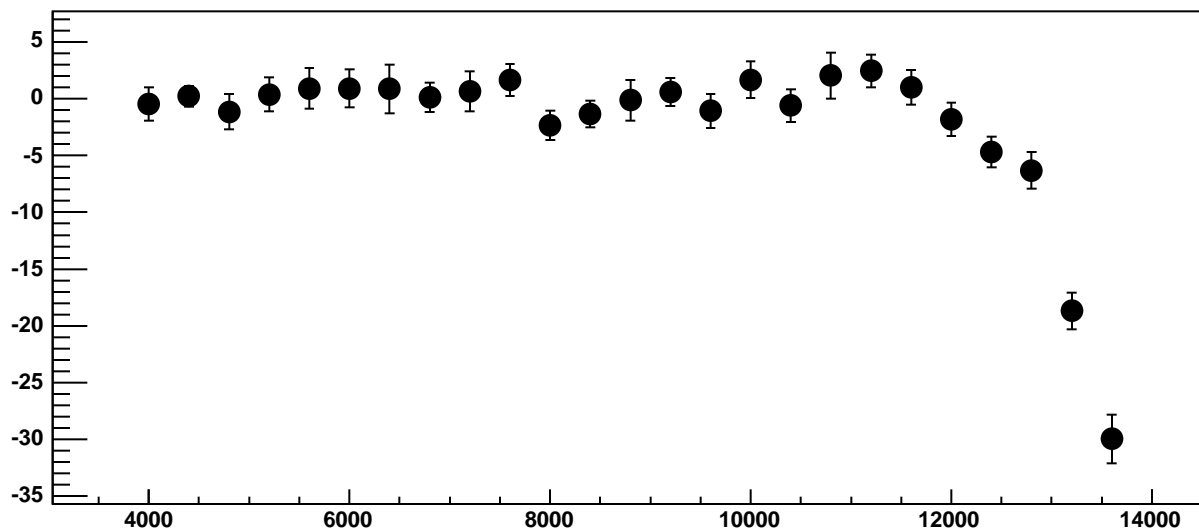
Chip 4, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



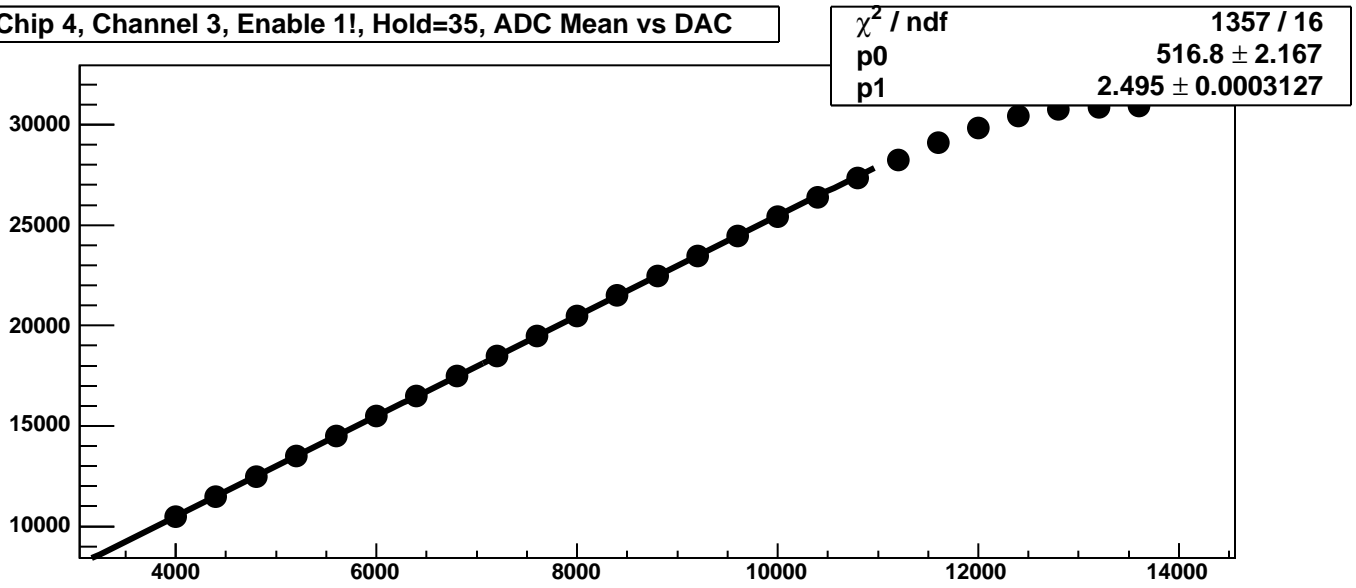
Chip 4, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



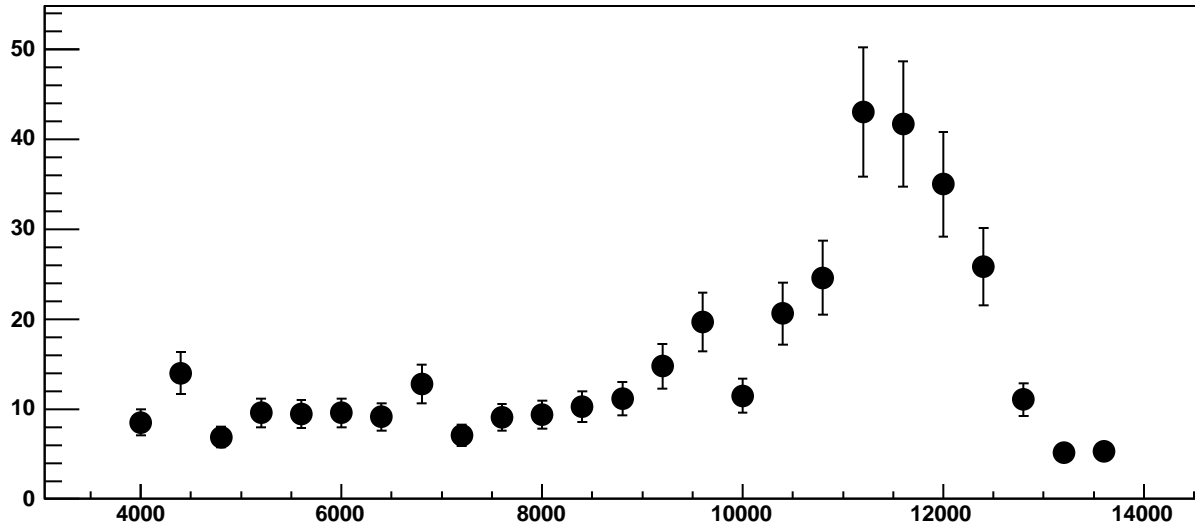
Chip 4, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



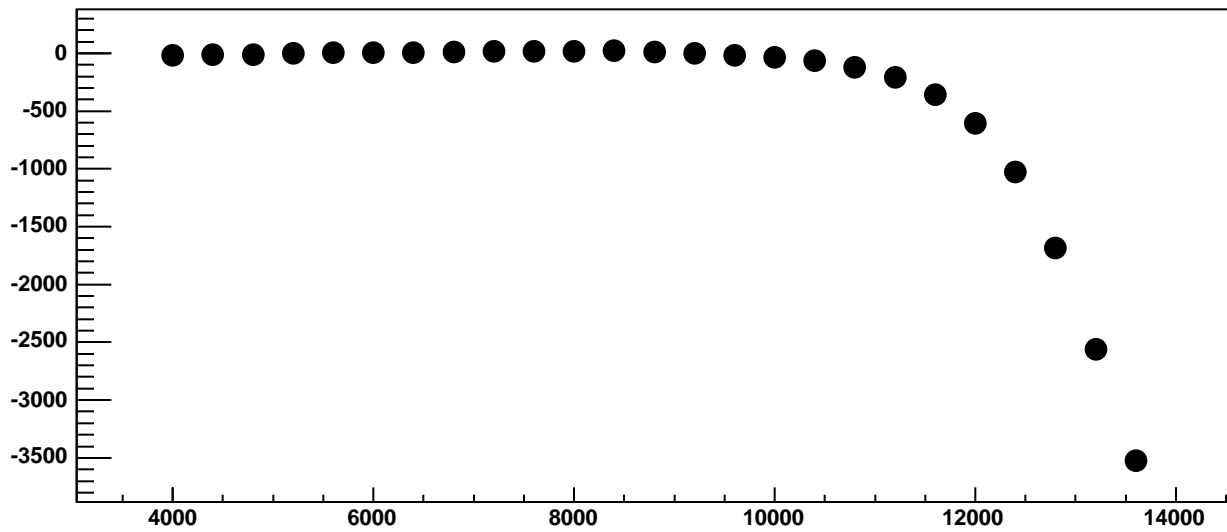
Chip 4, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC



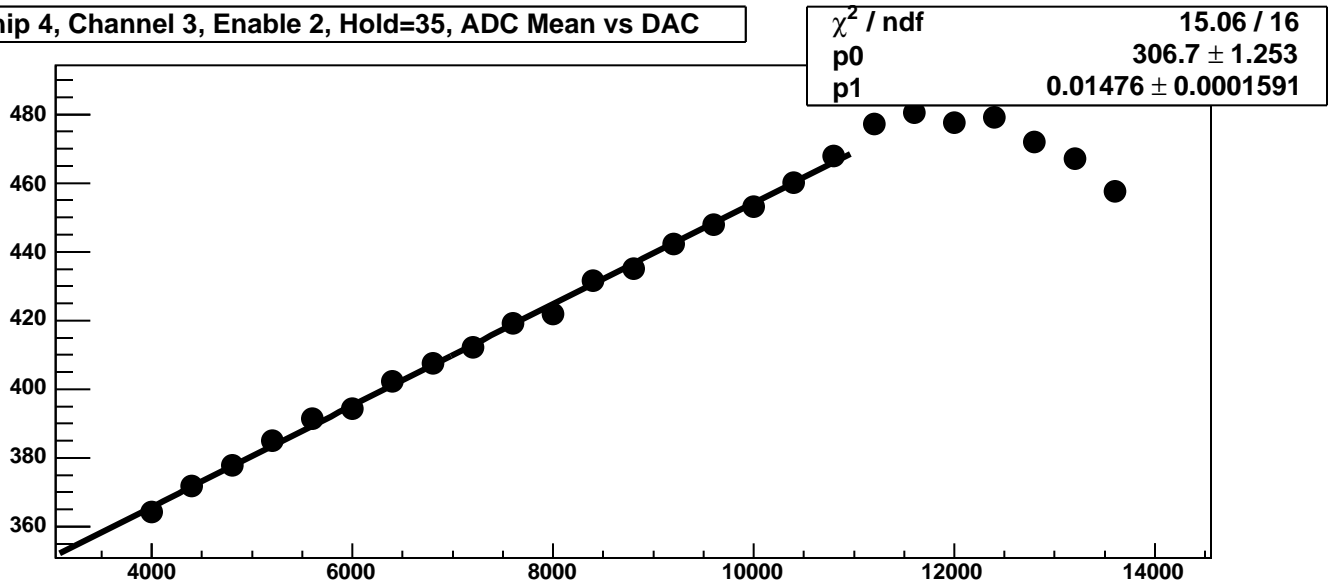
Chip 4, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



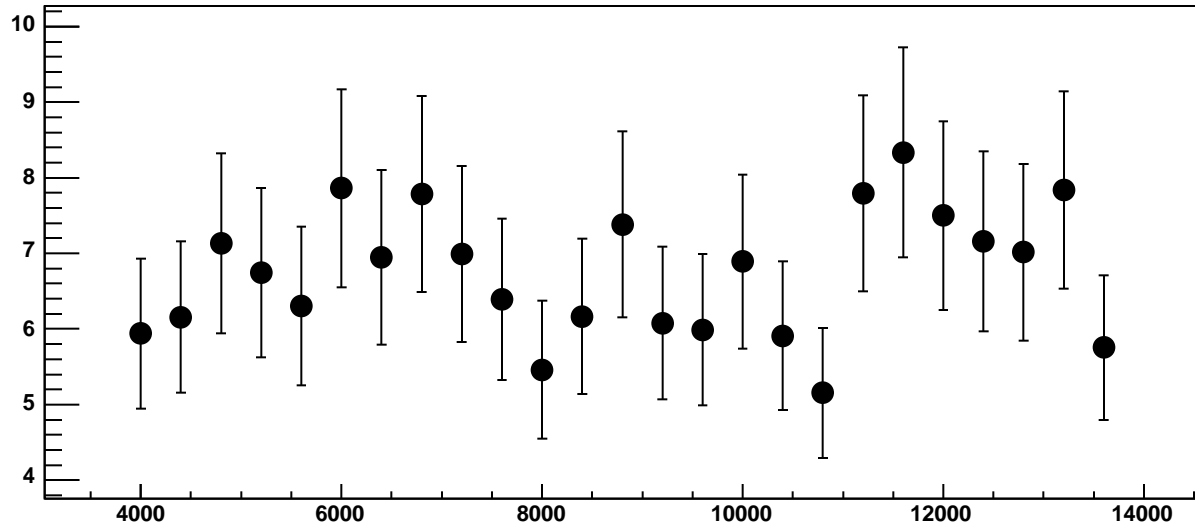
Chip 4, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC



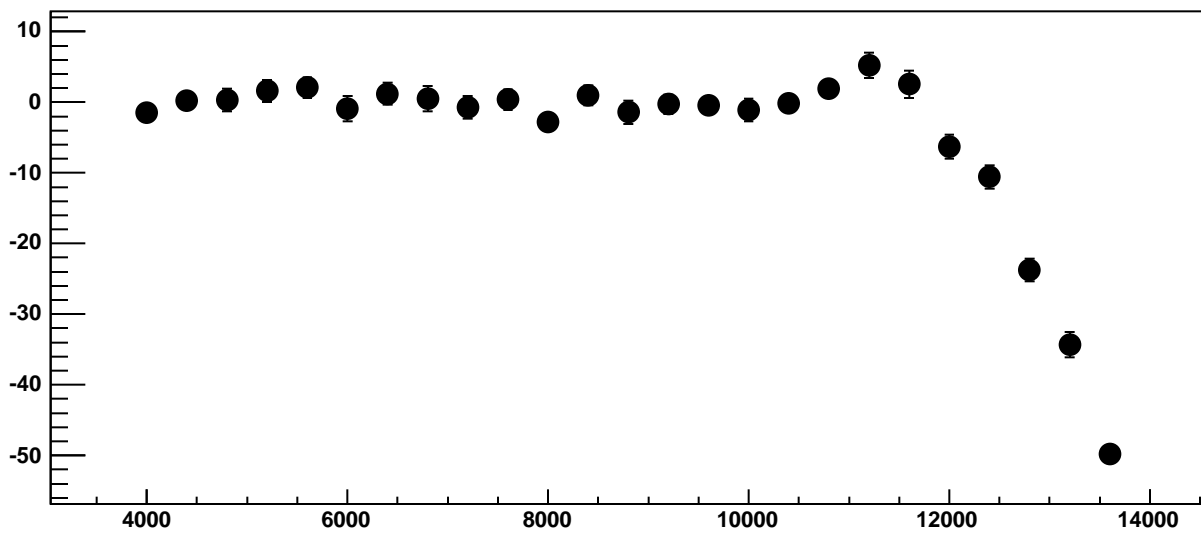
Chip 4, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



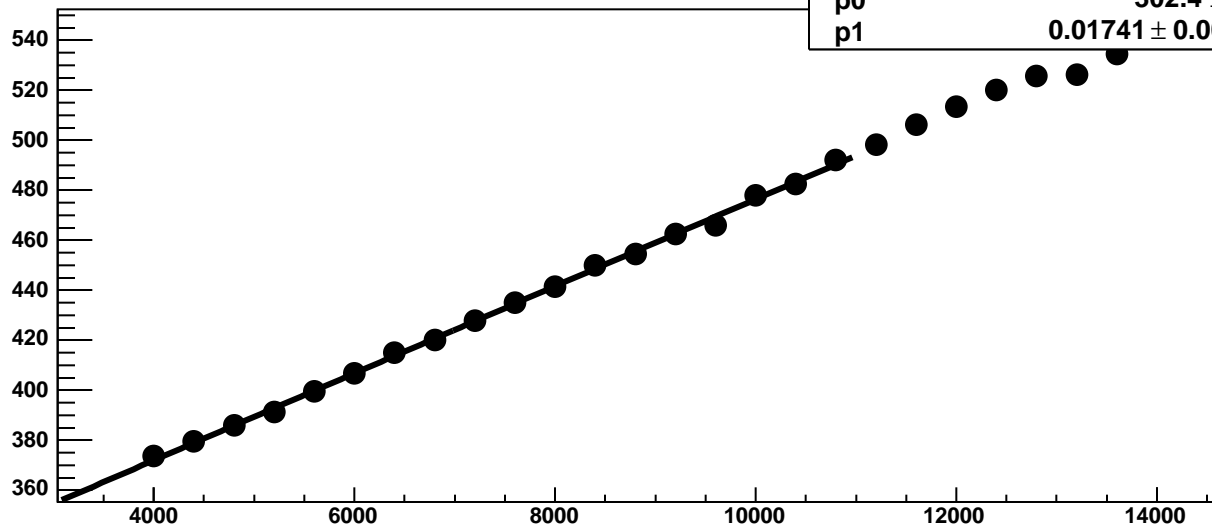
Chip 4, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



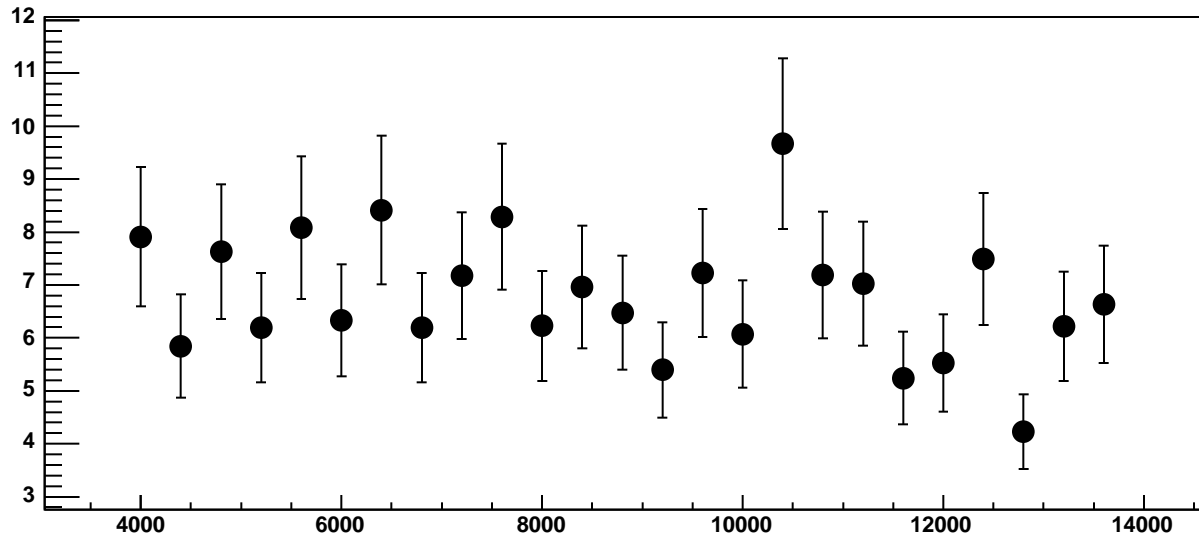
Chip 4, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC



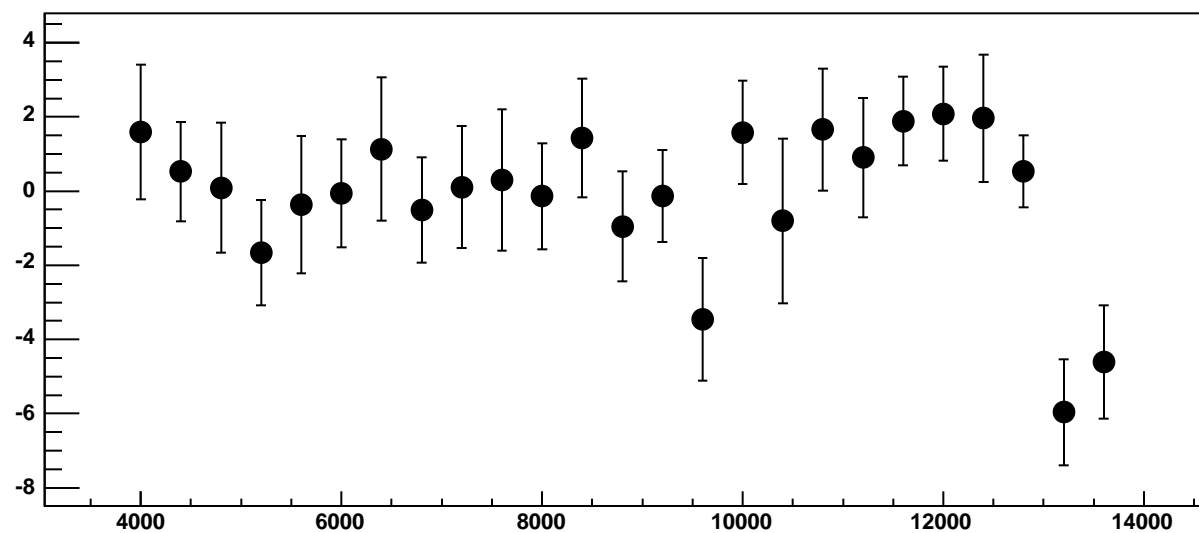
Chip 4, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC



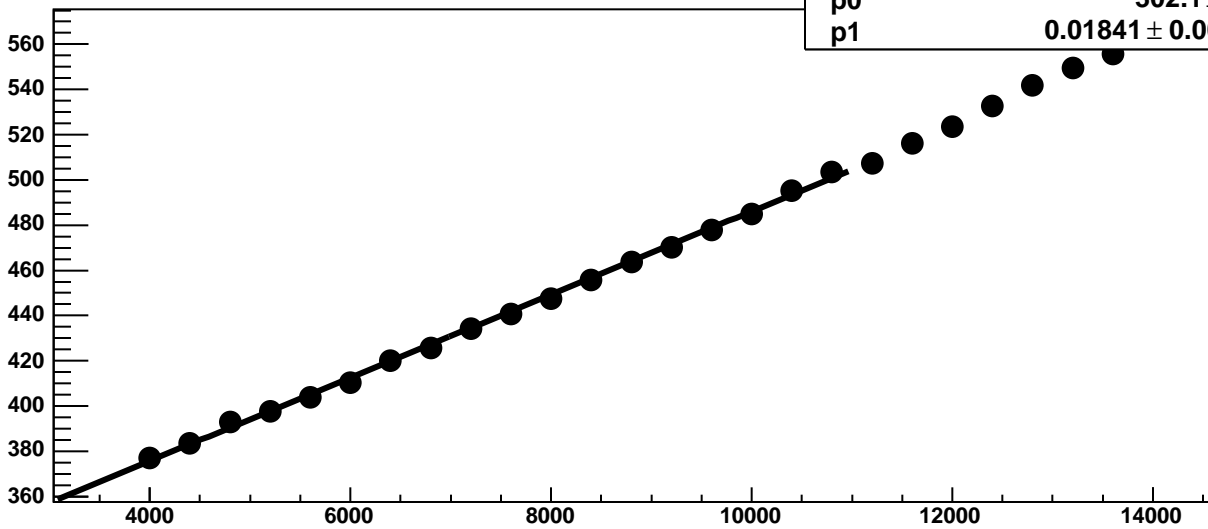
Chip 4, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

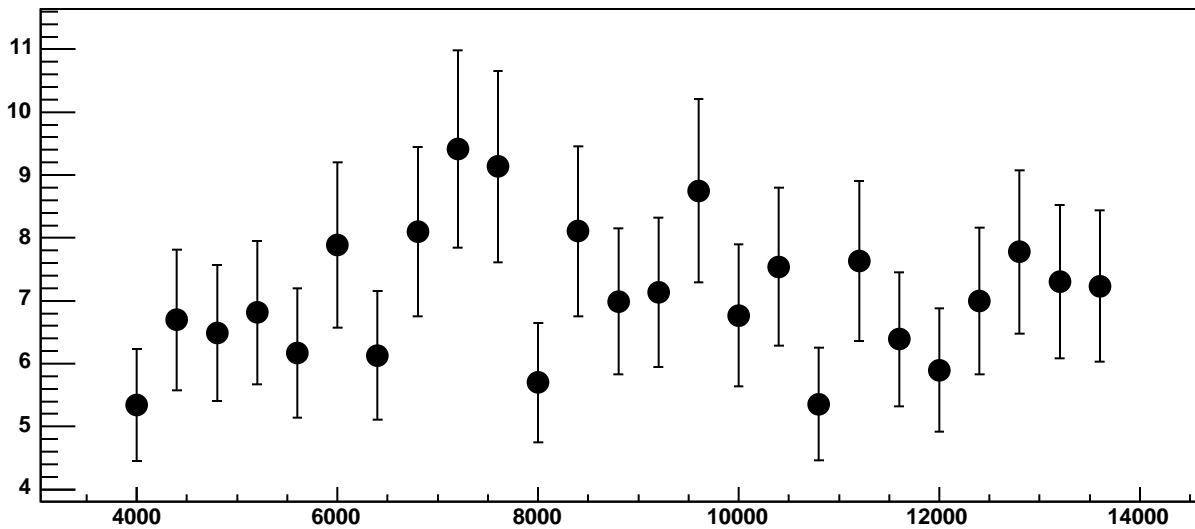


Chip 4, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

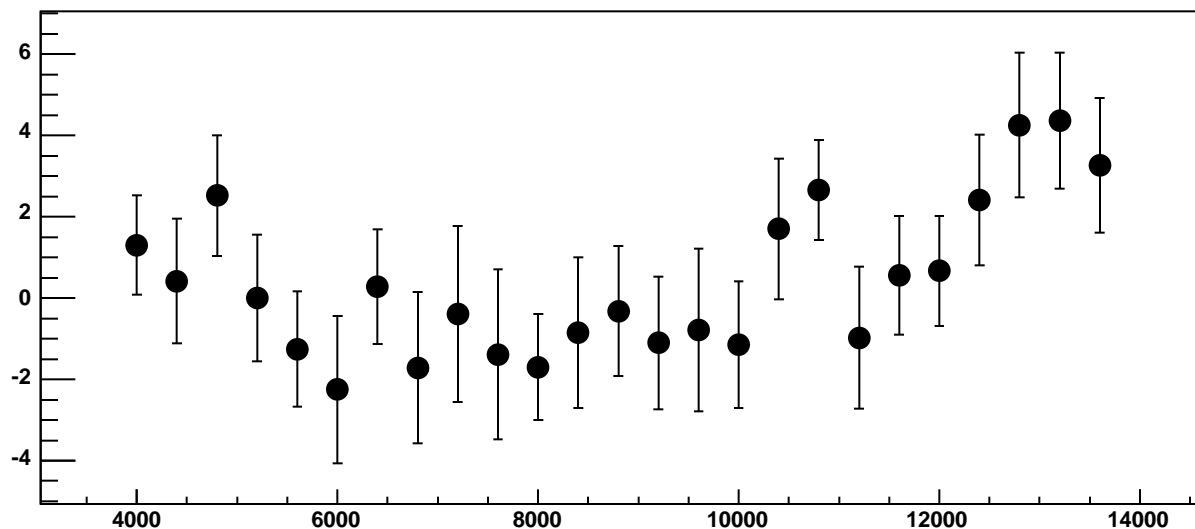


χ^2 / ndf 16.49 / 16
p0 302.1 ± 1.262
p1 0.01841 ± 0.0001657

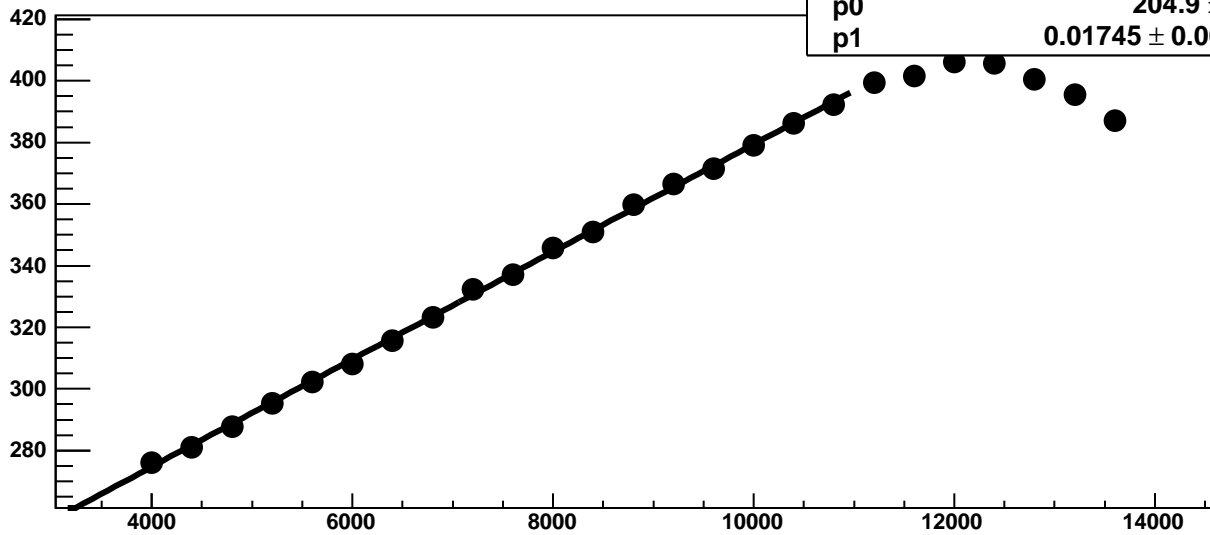
Chip 4, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



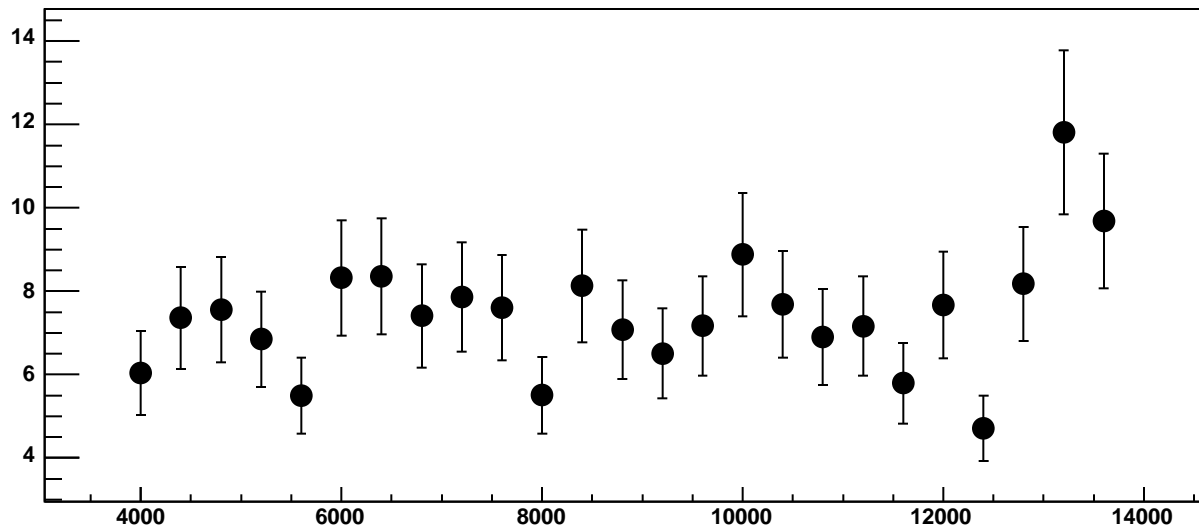
Chip 4, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC



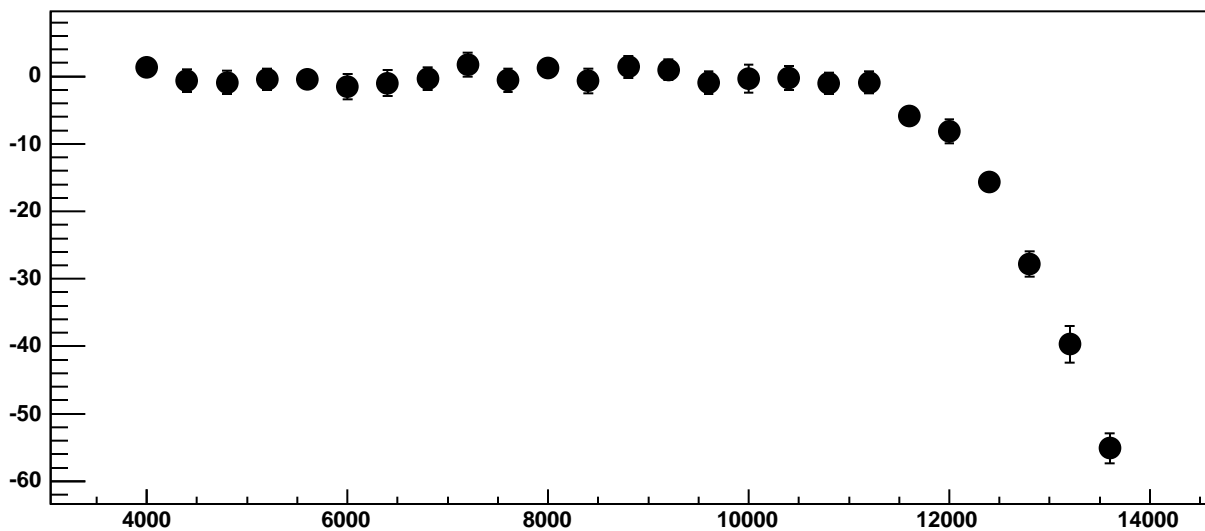
Chip 4, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



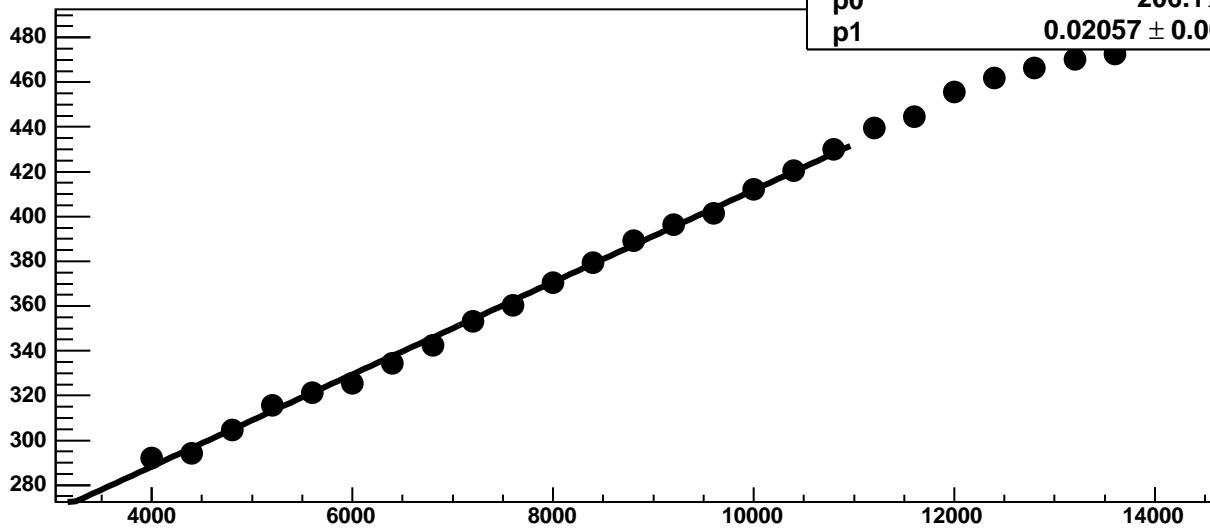
Chip 4, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

33.24 / 16

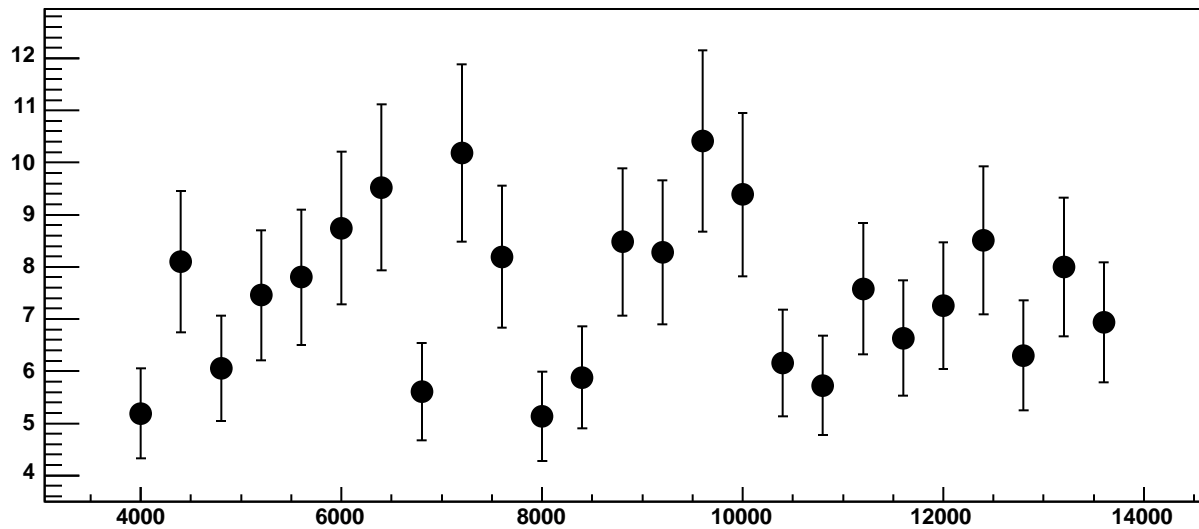
p0

206.1 ± 1.319

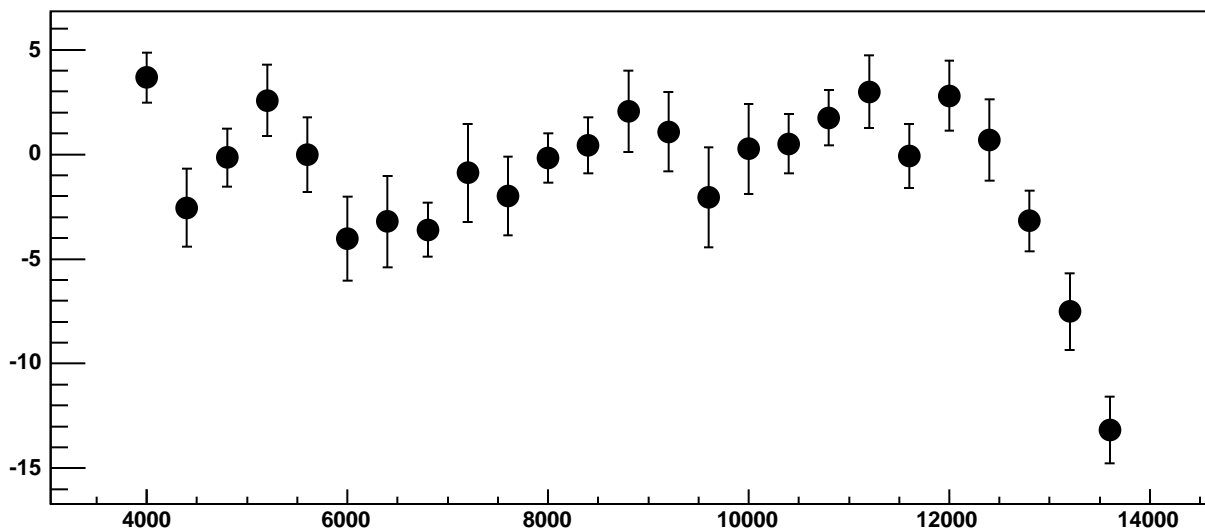
p1

0.02057 ± 0.0001726

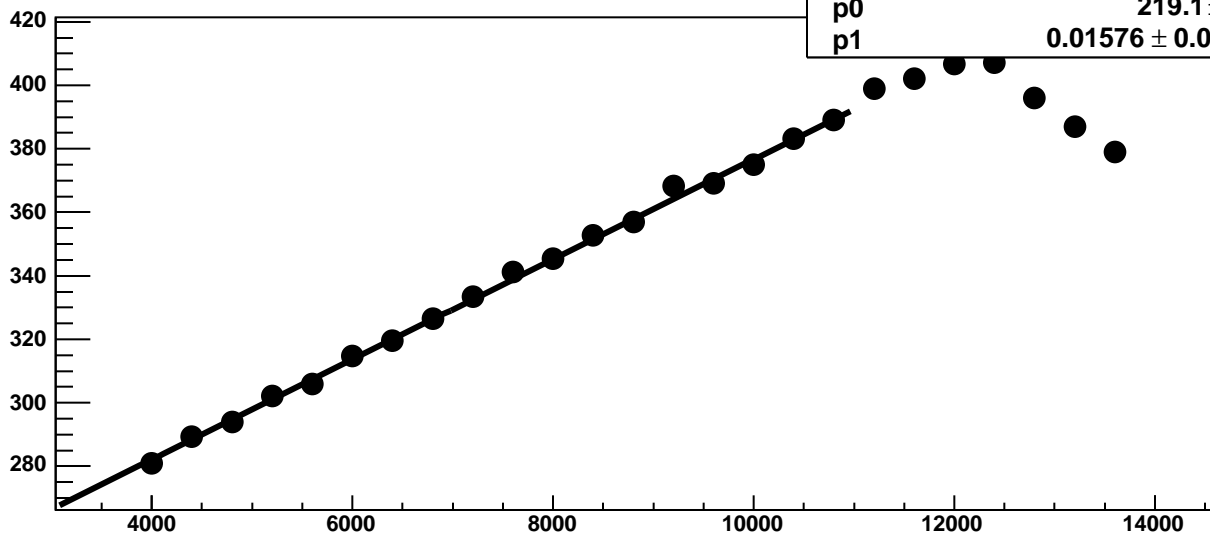
Chip 4, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



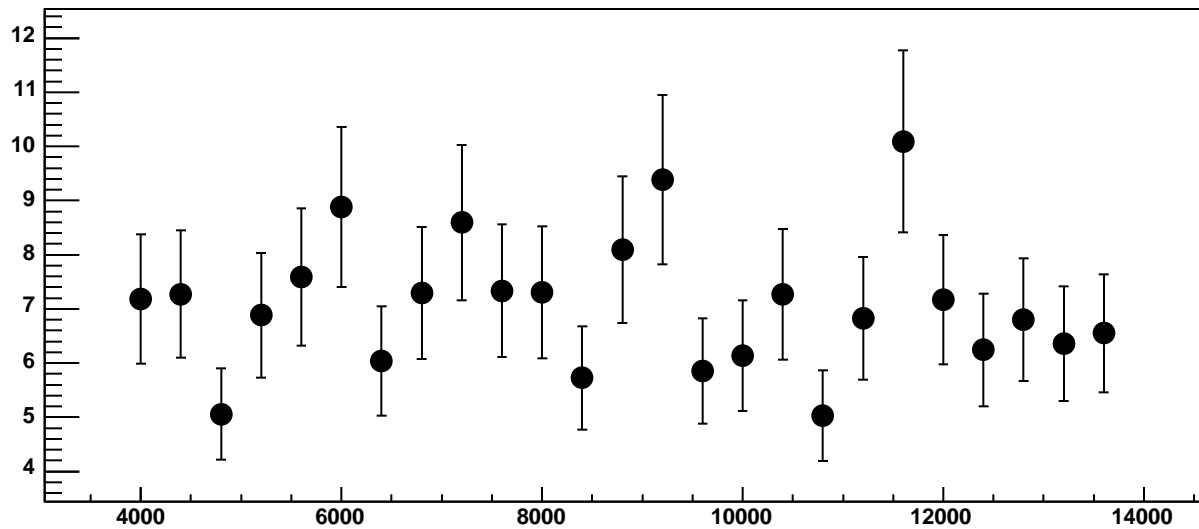
Chip 4, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC



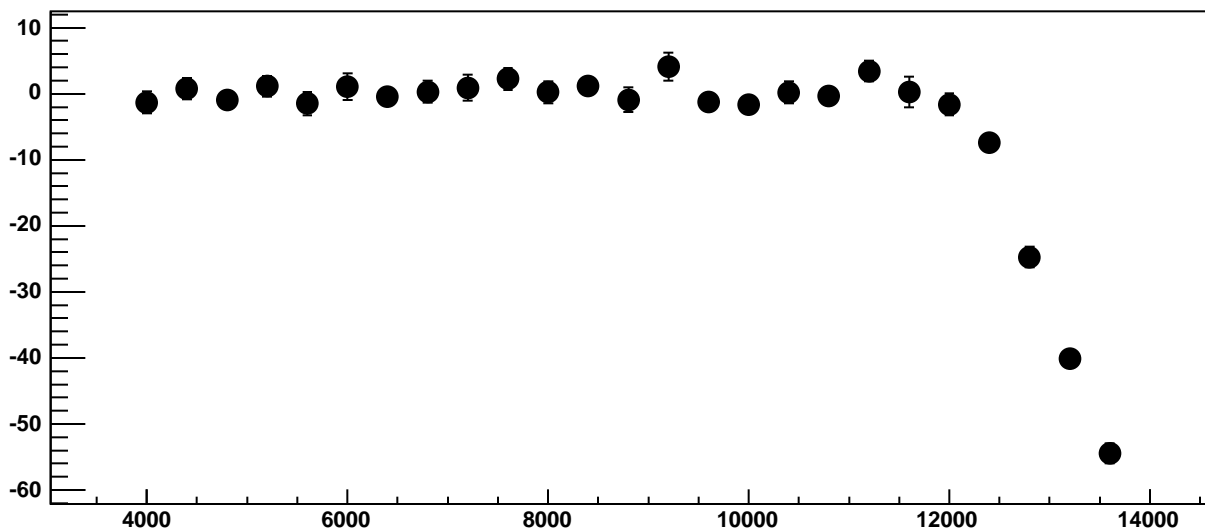
Chip 4, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



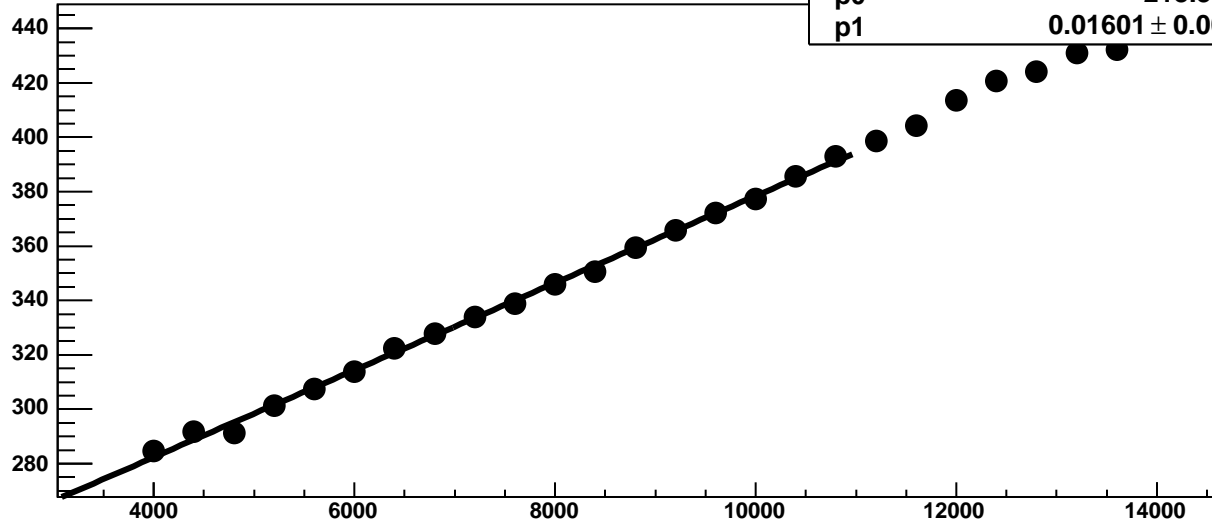
Chip 4, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

14.19 / 16

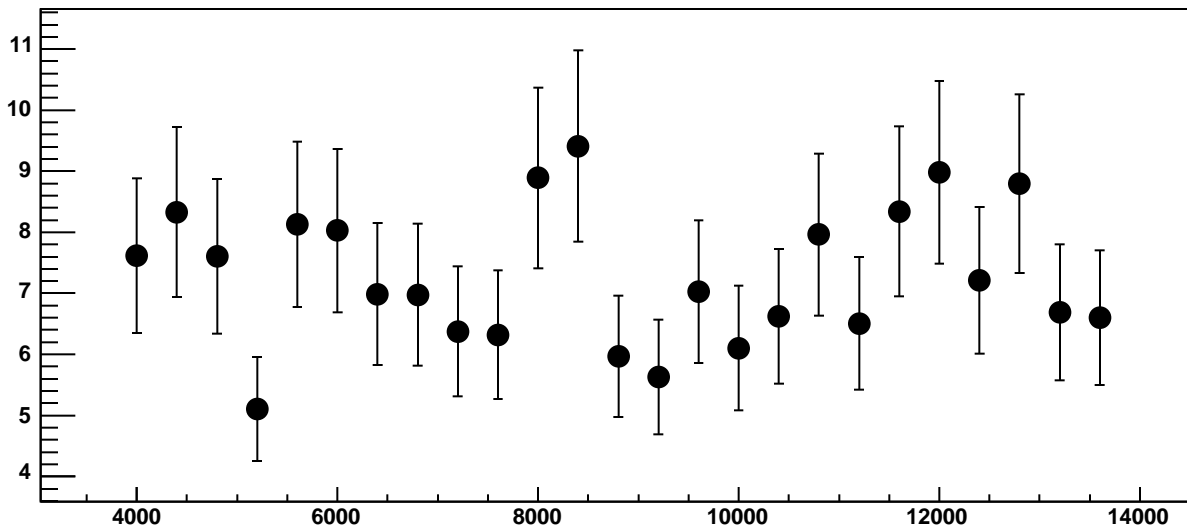
p0

218.3 ± 1.42

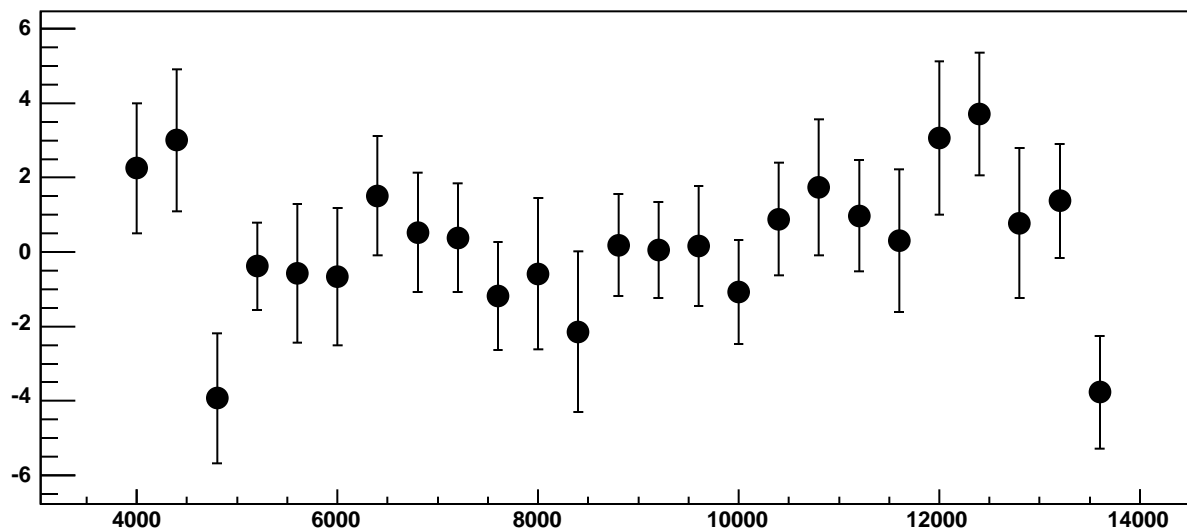
p1

0.01601 ± 0.0001827

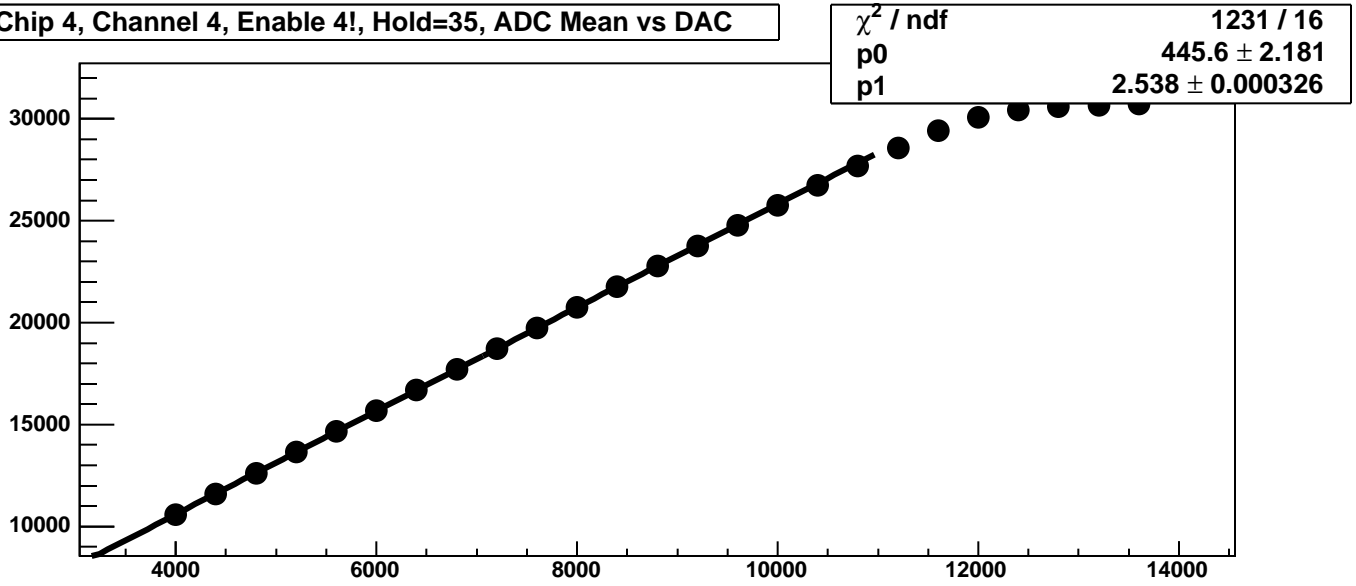
Chip 4, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



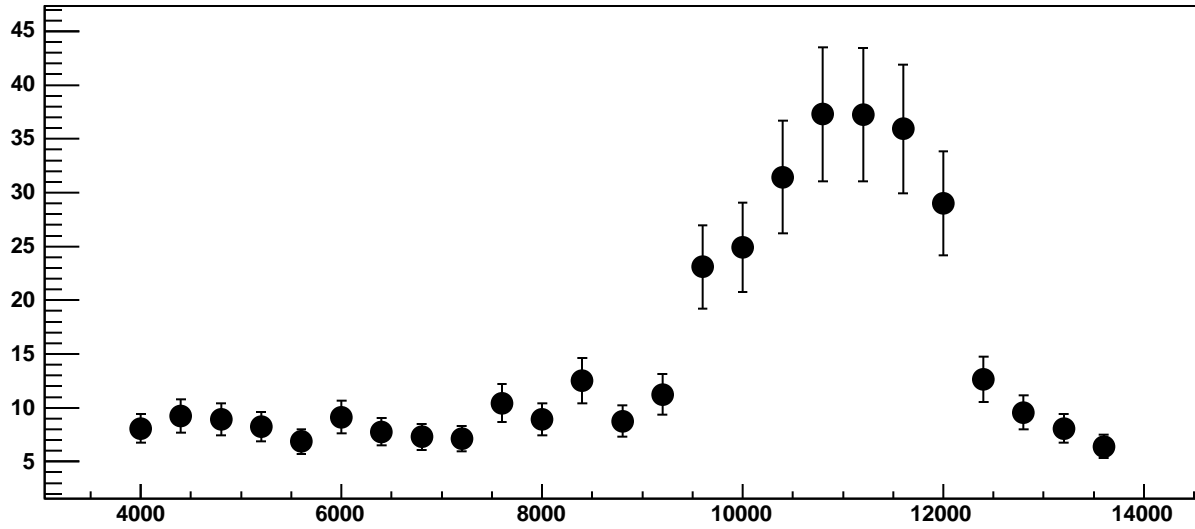
Chip 4, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



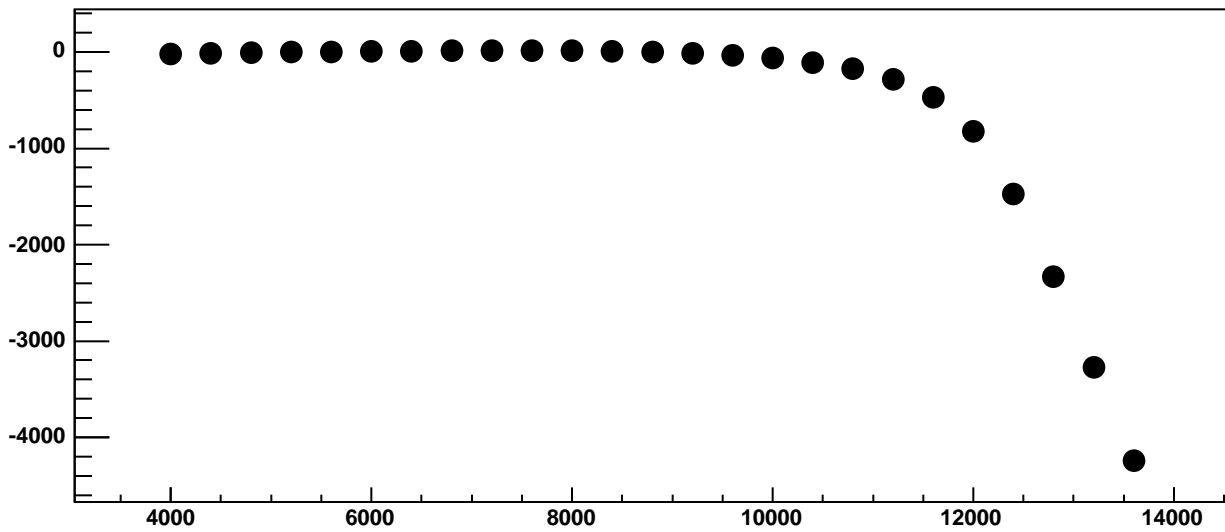
Chip 4, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC



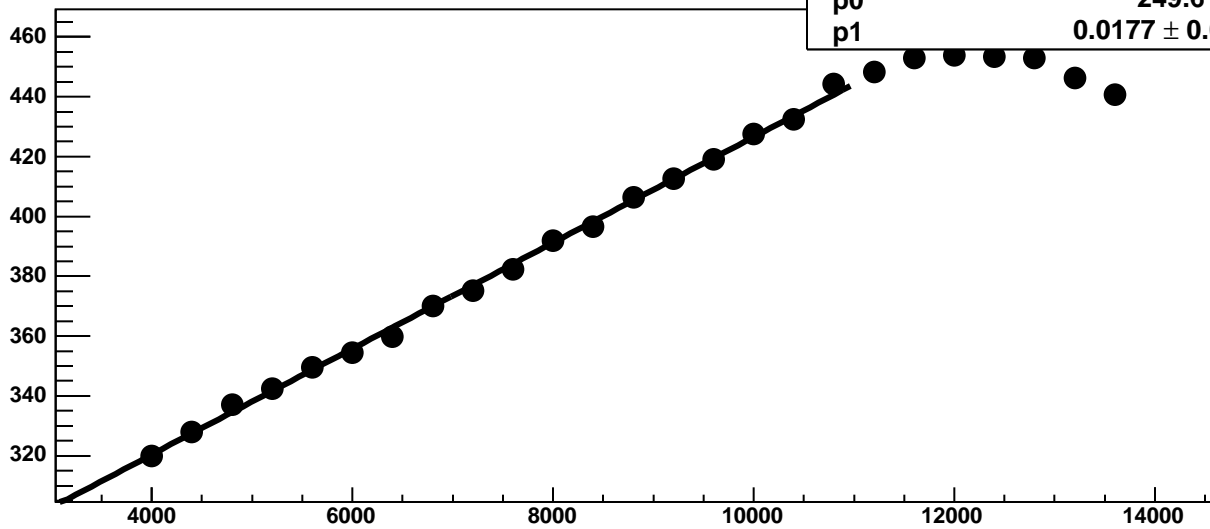
Chip 4, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



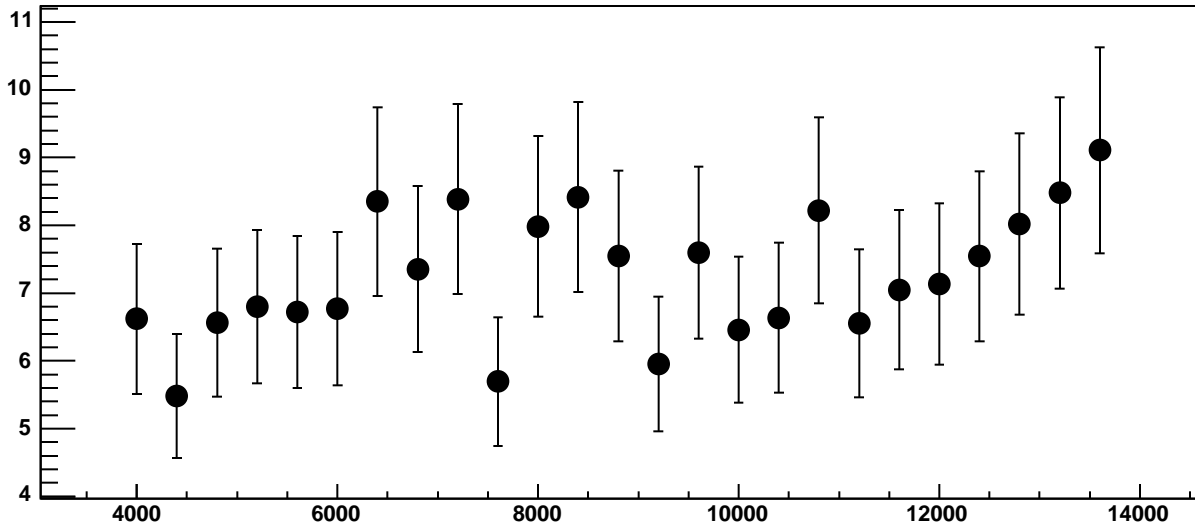
Chip 4, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC



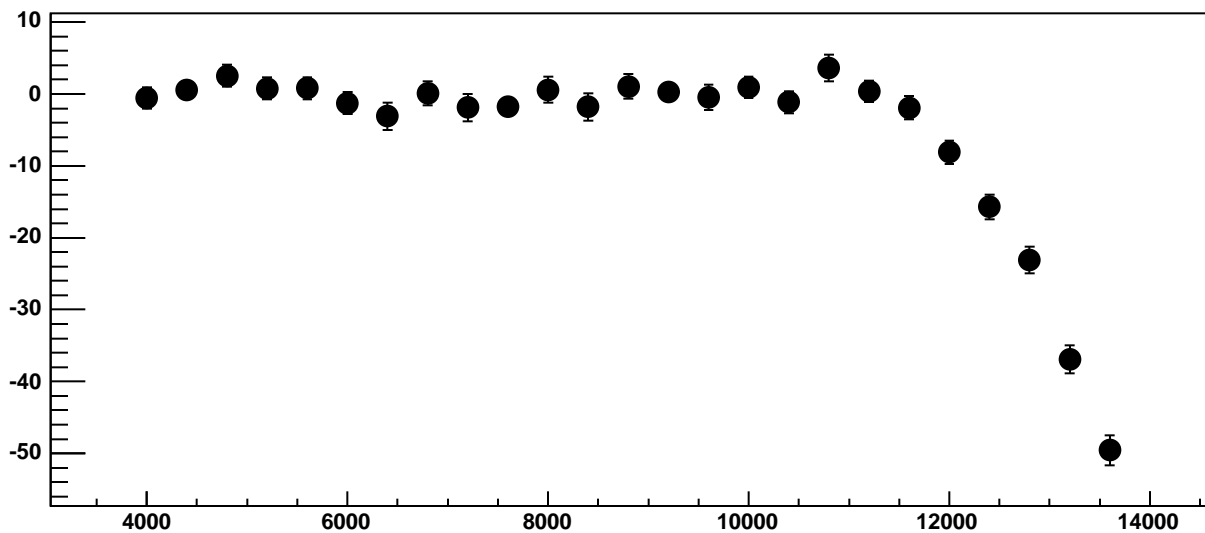
Chip 4, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



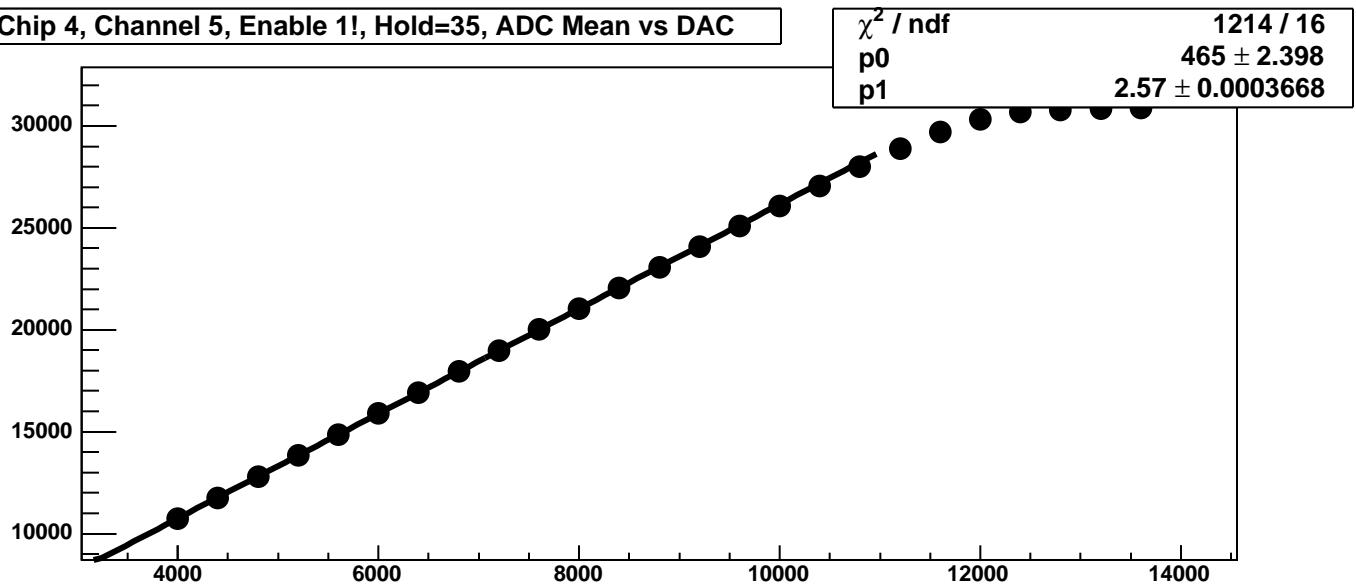
Chip 4, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



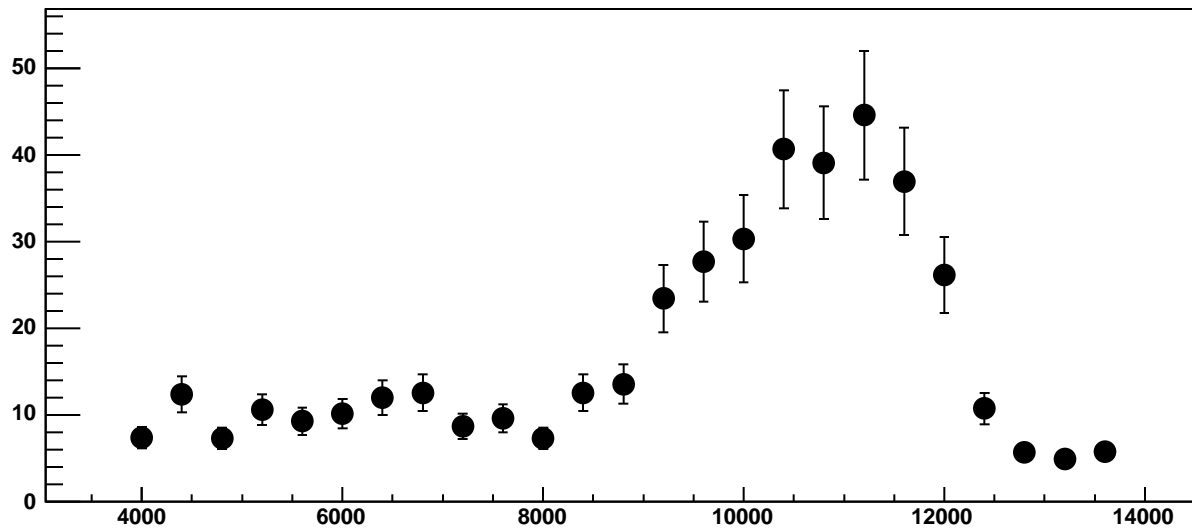
Chip 4, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



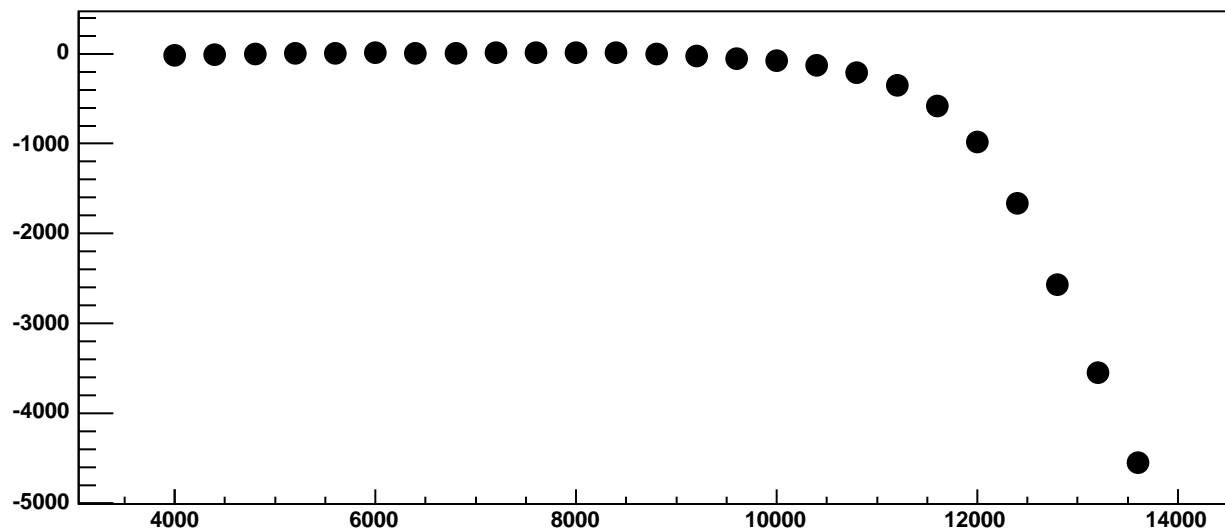
Chip 4, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC



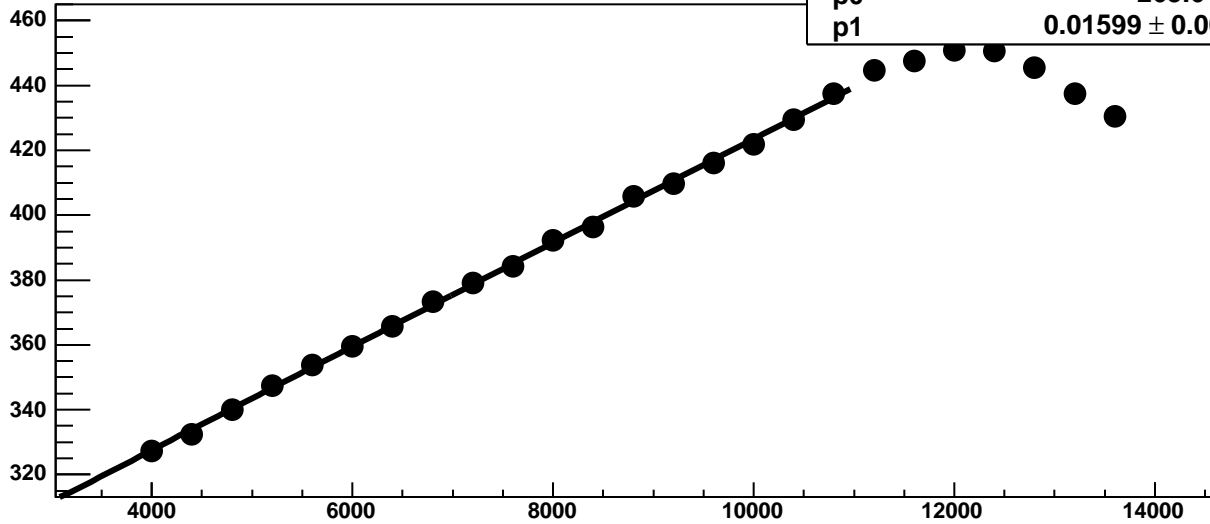
Chip 4, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



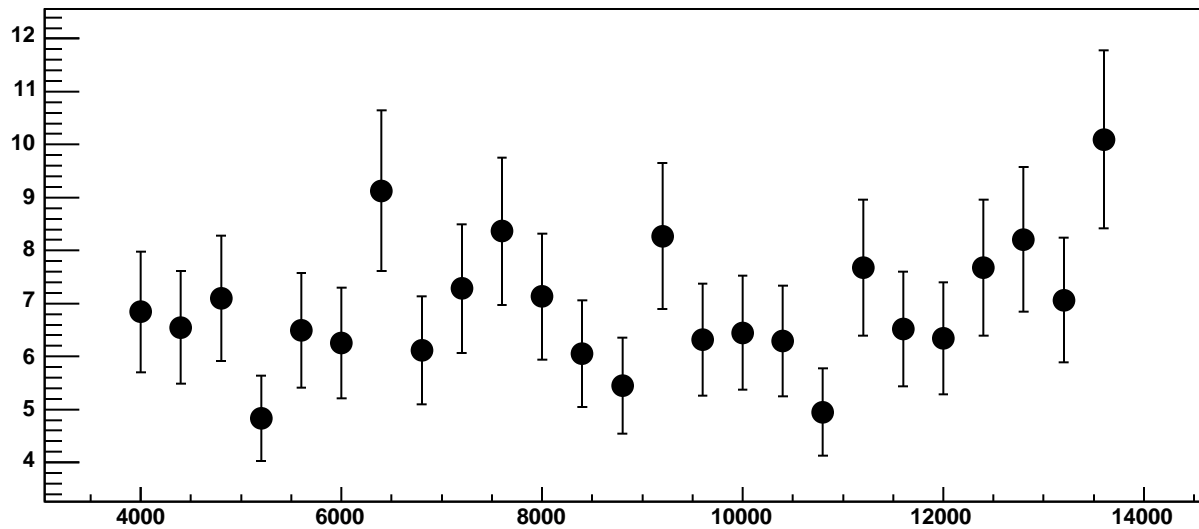
Chip 4, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC



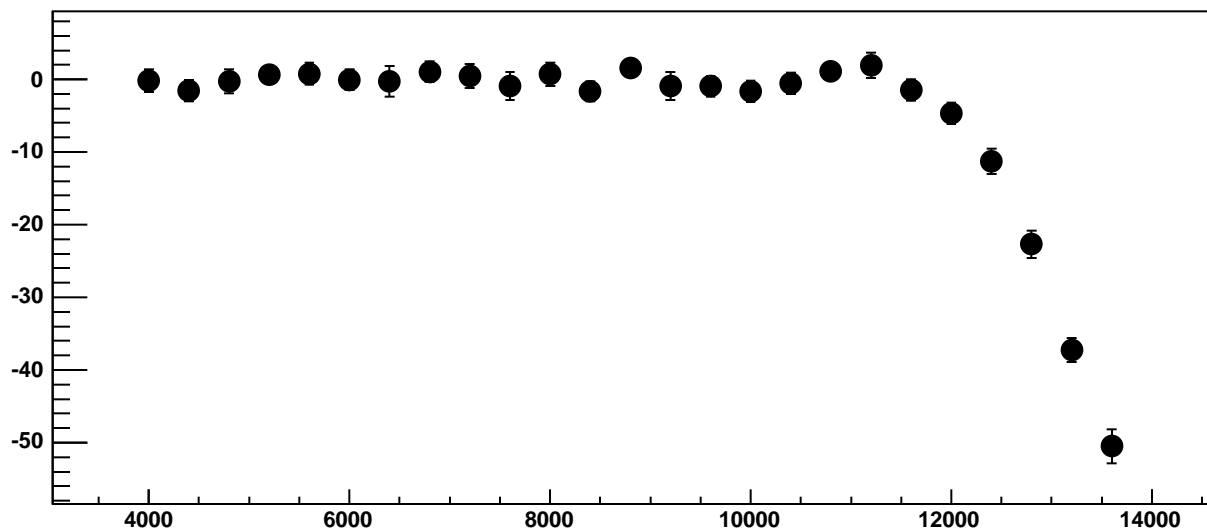
Chip 4, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



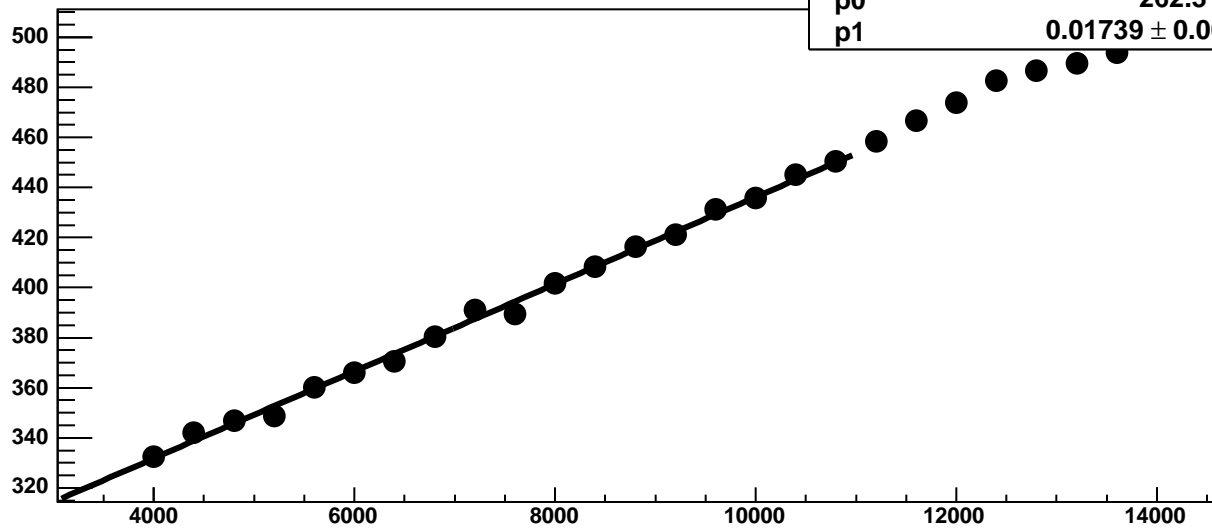
Chip 4, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



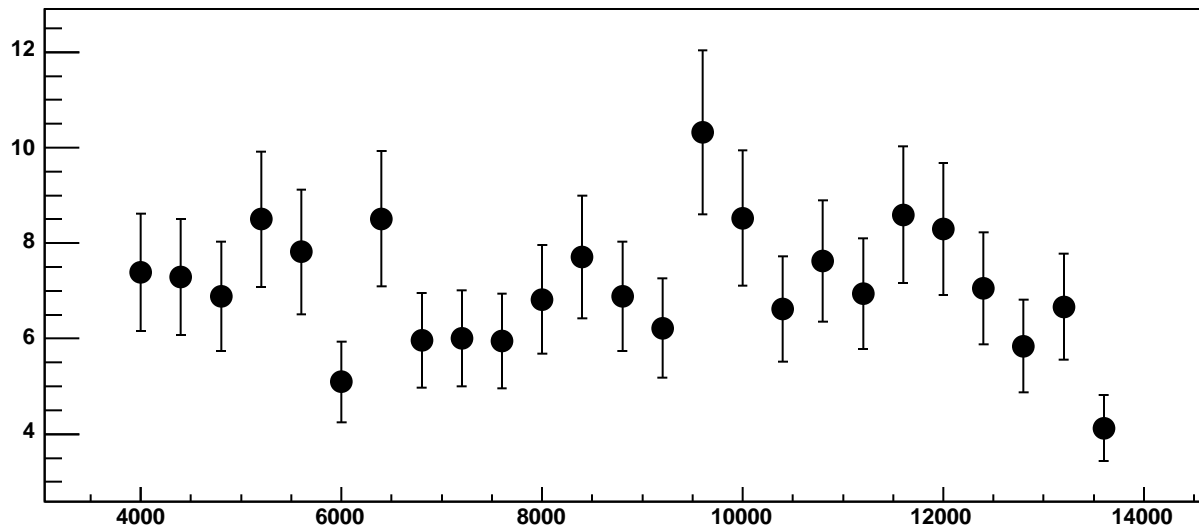
Chip 4, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



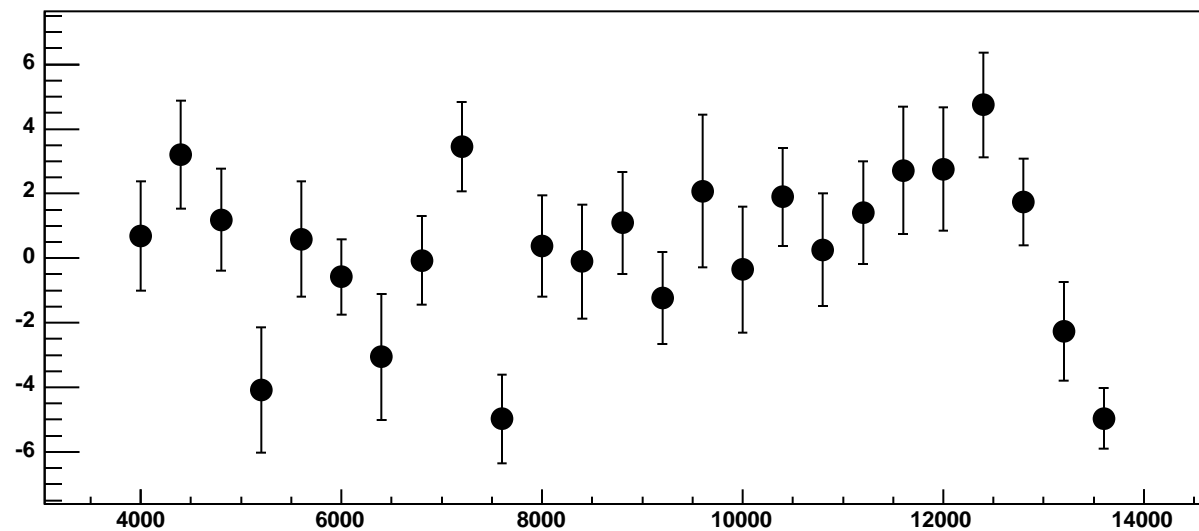
Chip 4, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



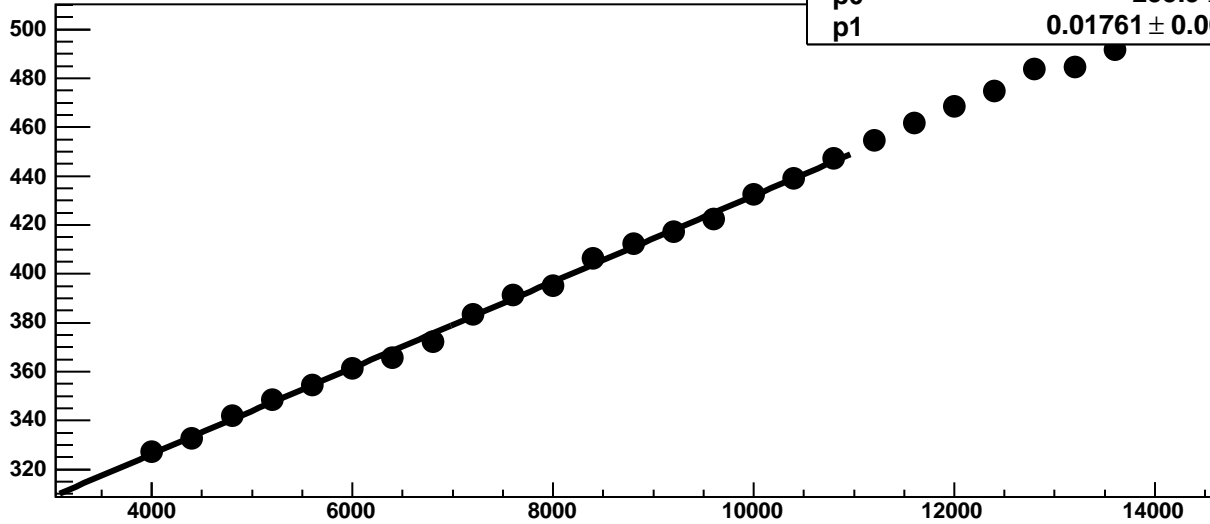
Chip 4, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

18 / 16

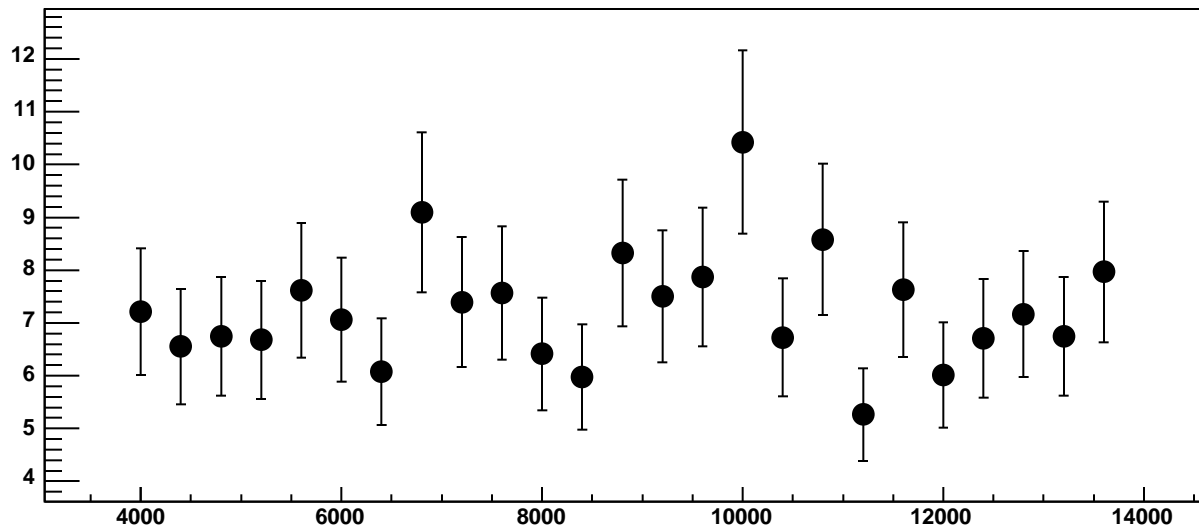
p0

255.9 ± 1.439

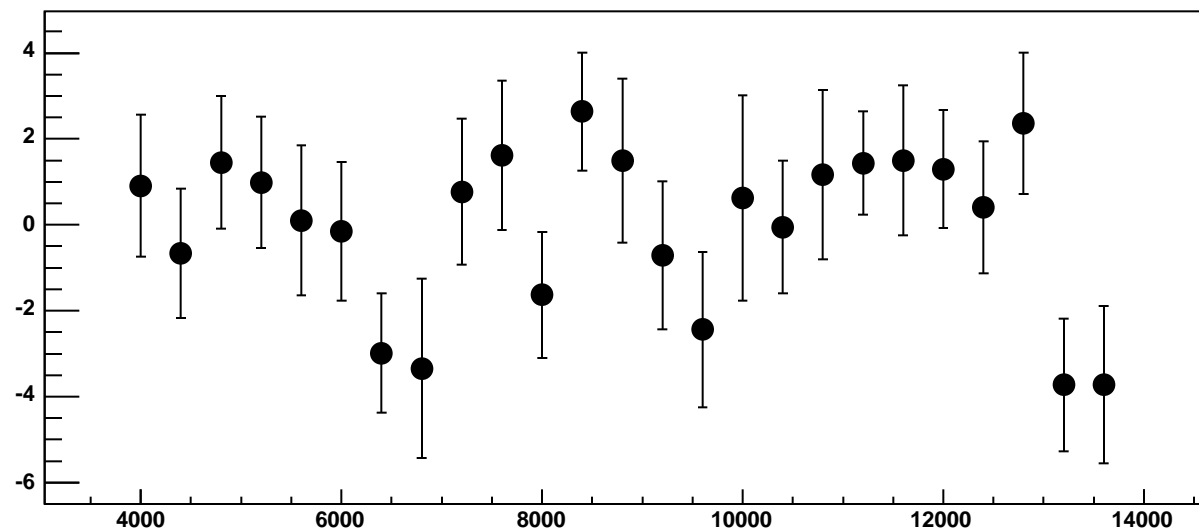
p1

0.01761 ± 0.0001924

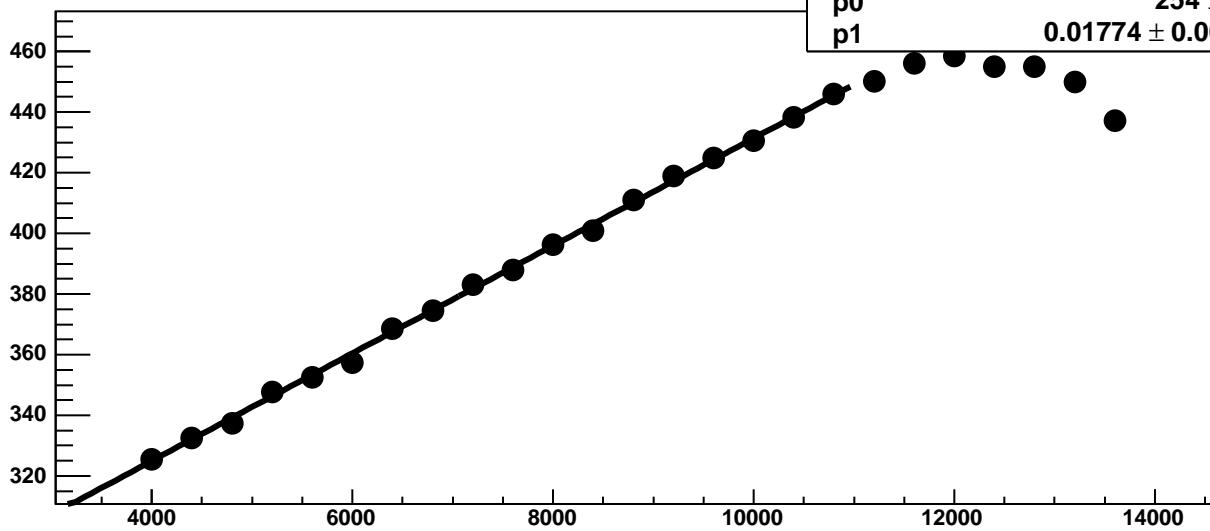
Chip 4, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



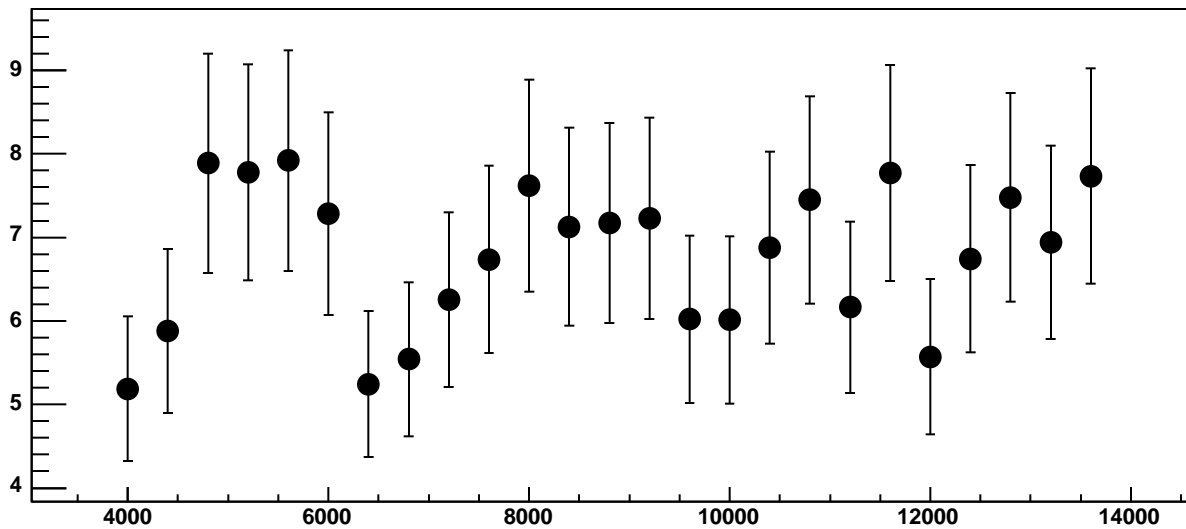
Chip 4, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



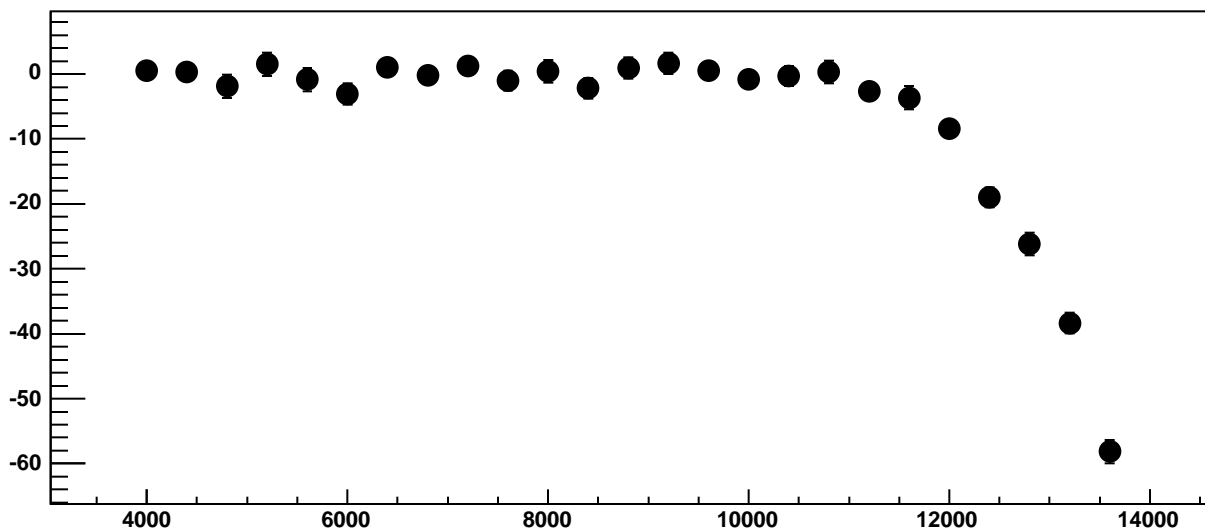
Chip 4, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



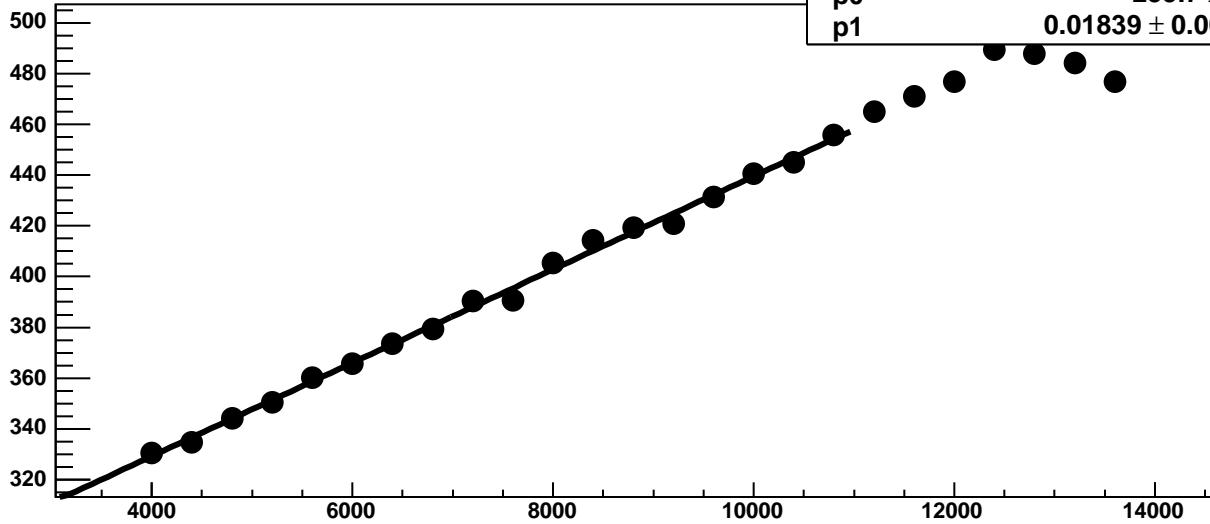
Chip 4, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

32.33 / 16

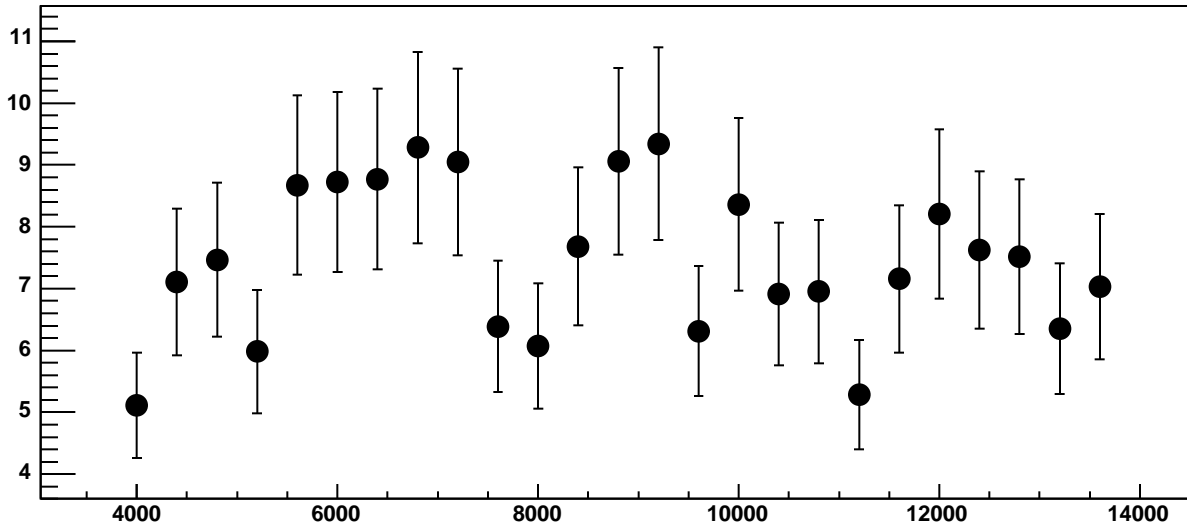
p0

255.7 ± 1.335

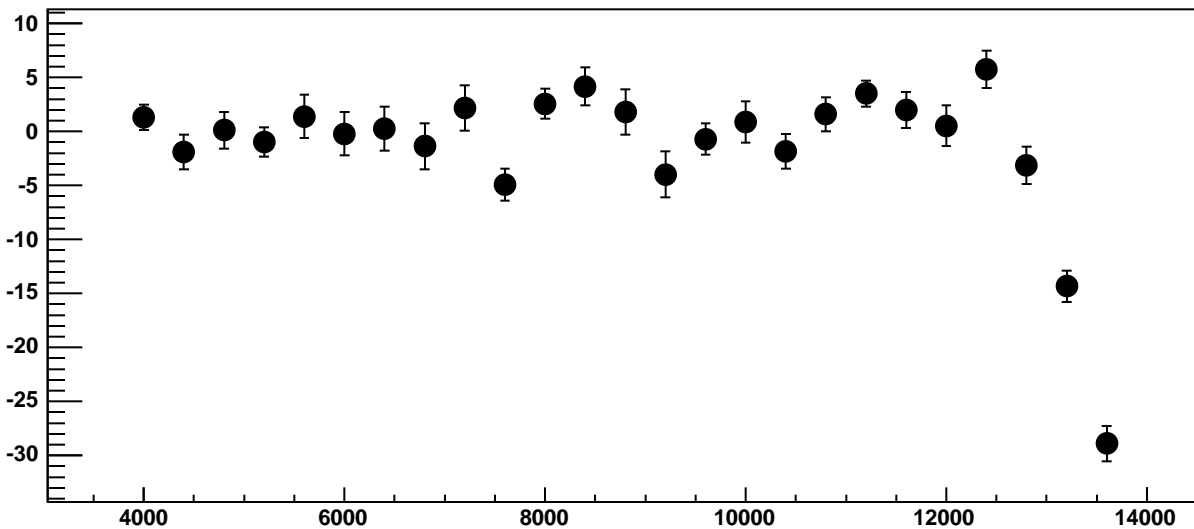
p1

0.01839 ± 0.0001766

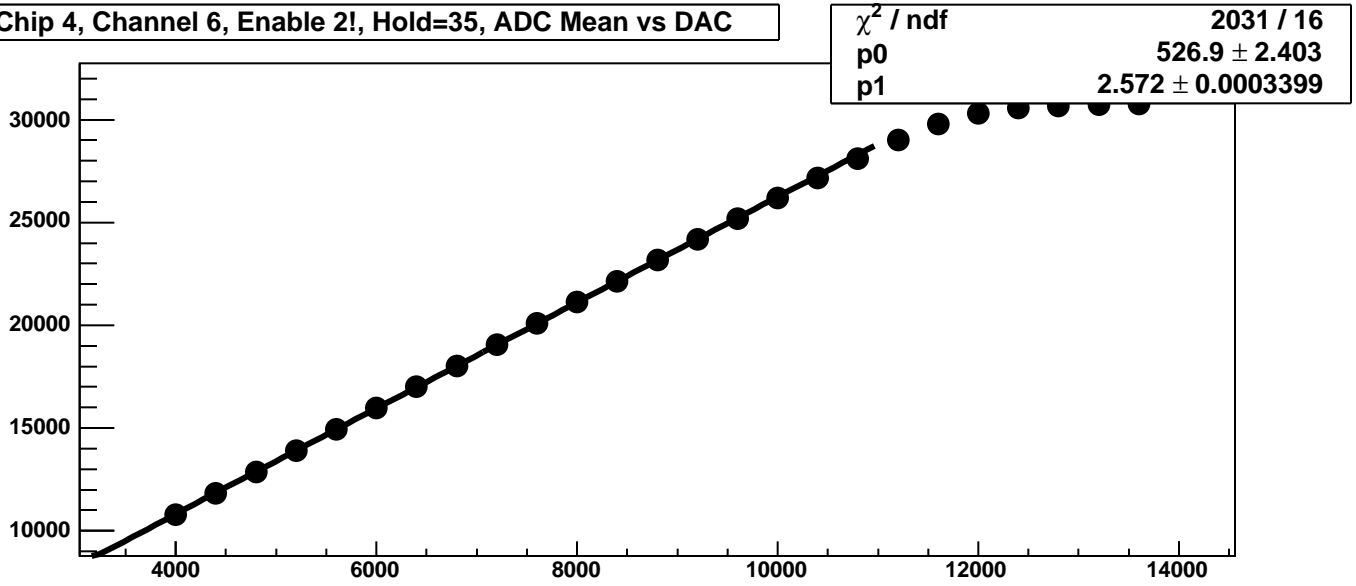
Chip 4, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



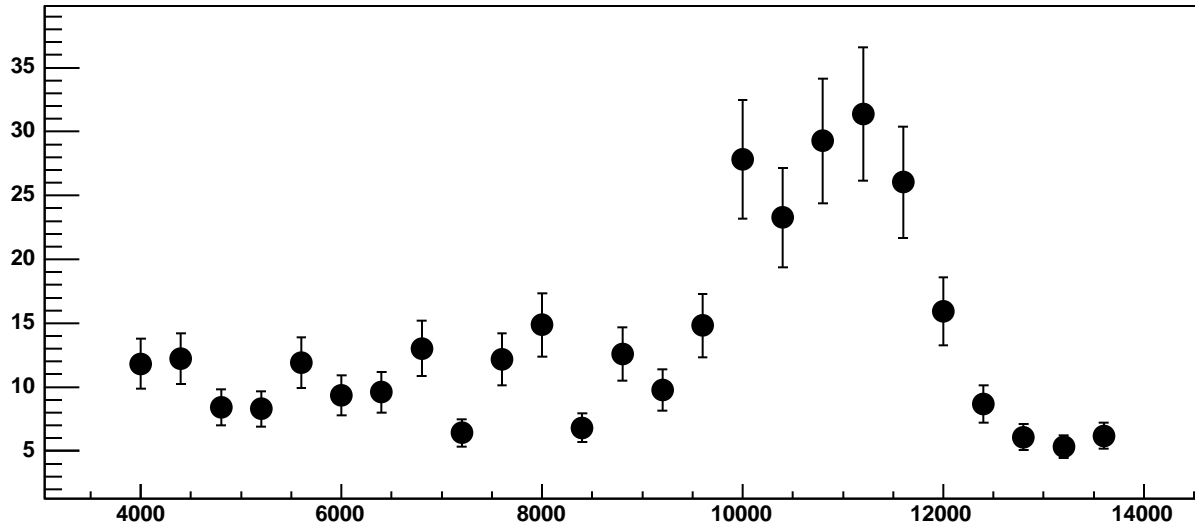
Chip 4, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



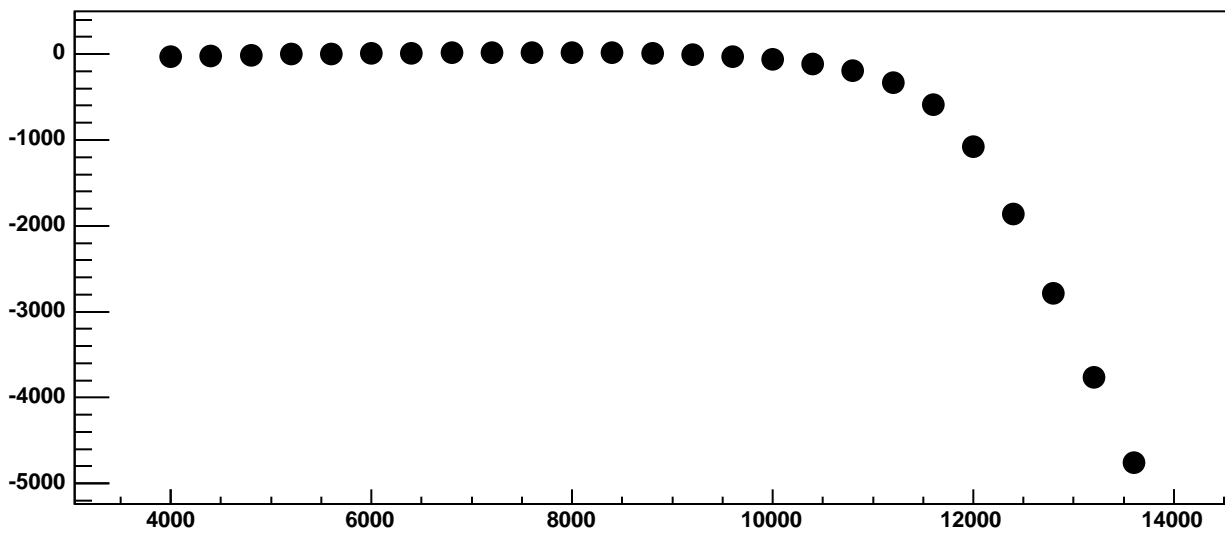
Chip 4, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



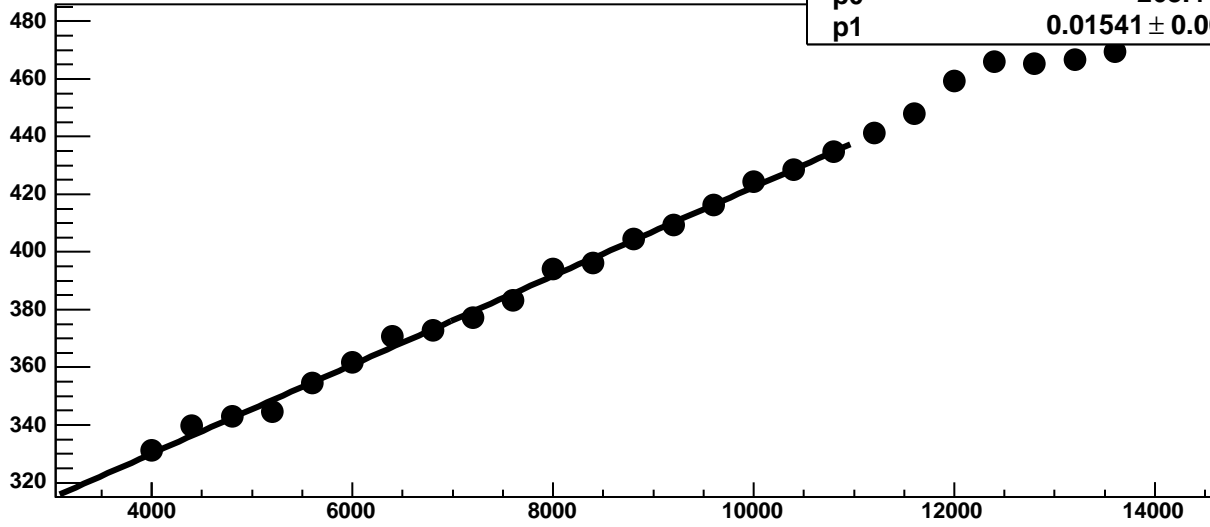
Chip 4, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC

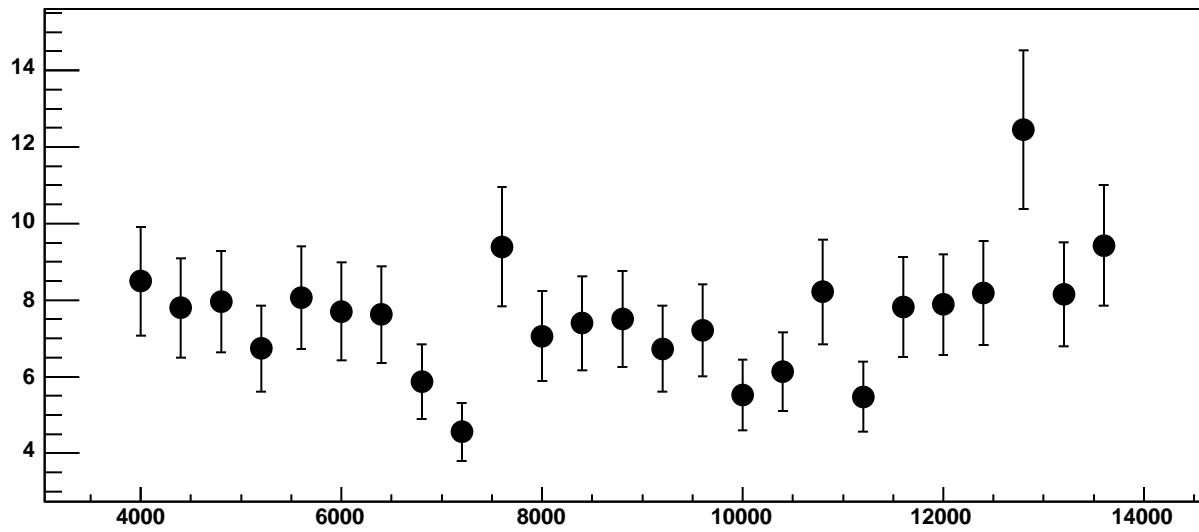


Chip 4, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

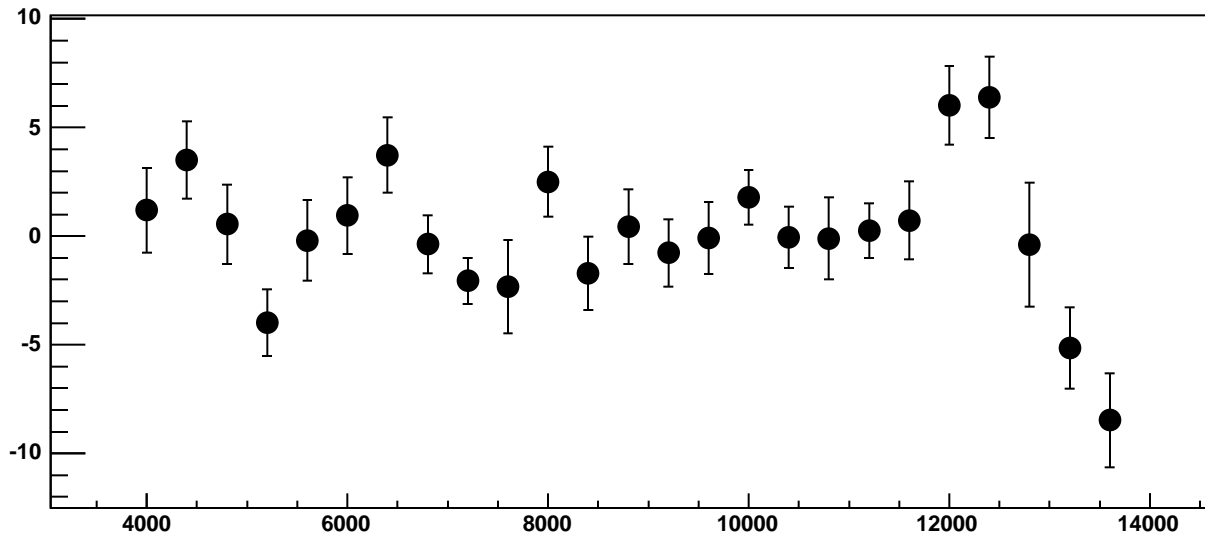


χ^2 / ndf 26.7 / 16
p0 268.4 ± 1.481
p1 0.01541 ± 0.0001887

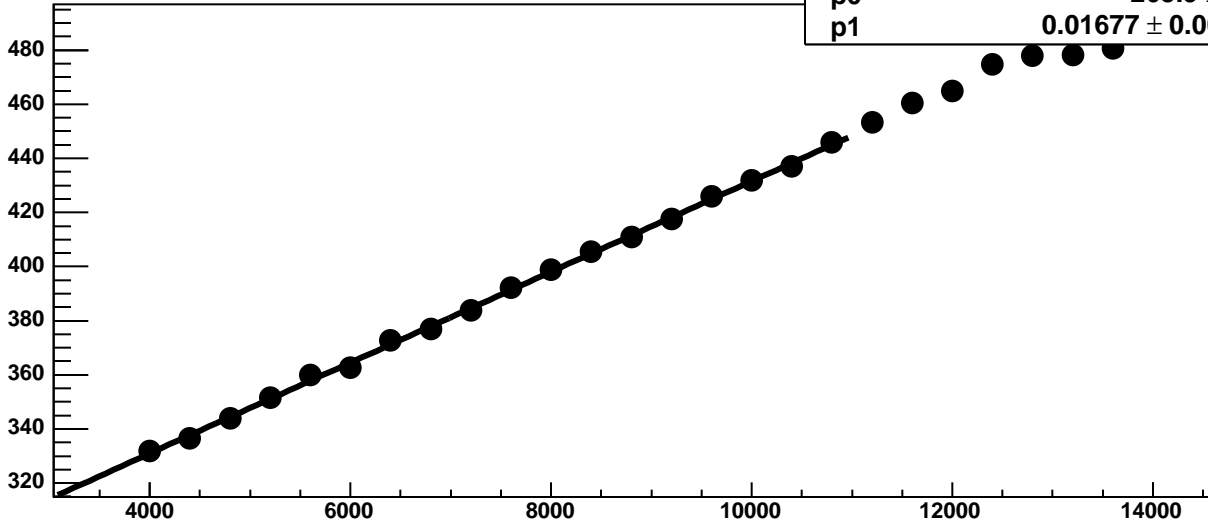
Chip 4, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

6.781 / 16

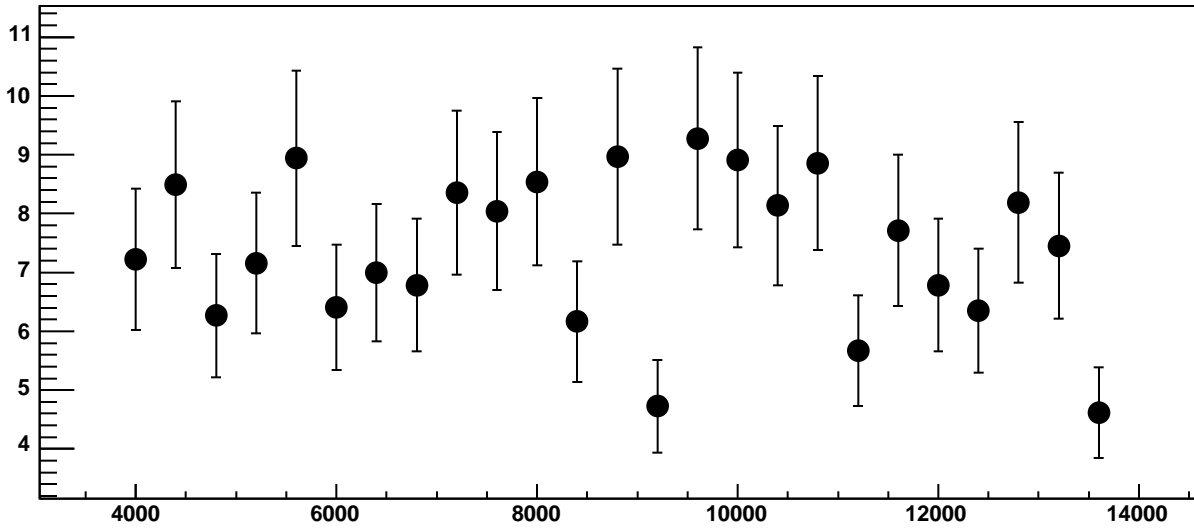
p0

263.9 ± 1.497

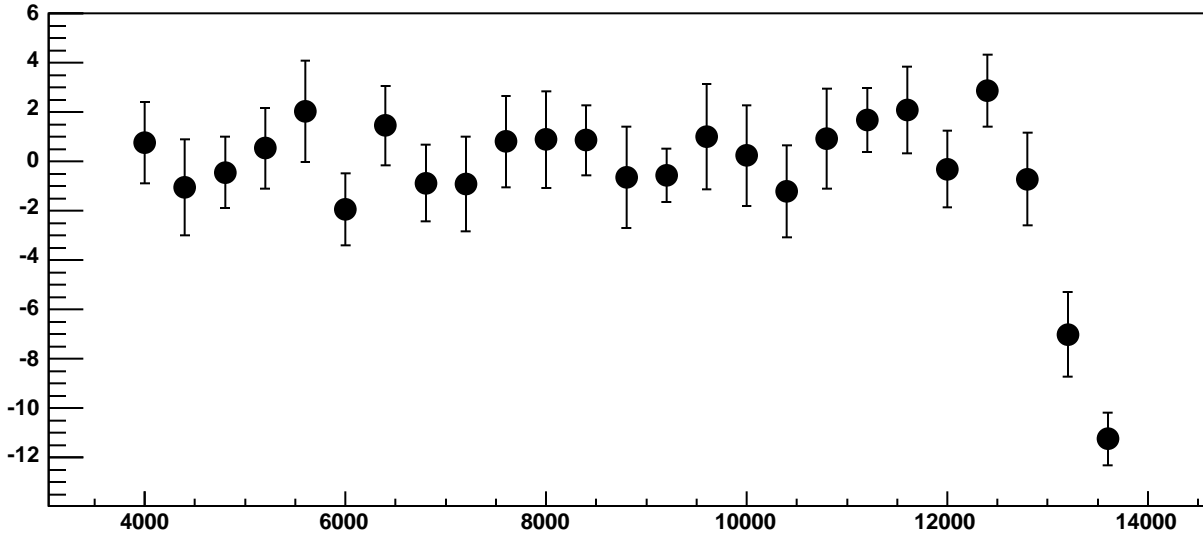
p1

0.01677 ± 0.0001972

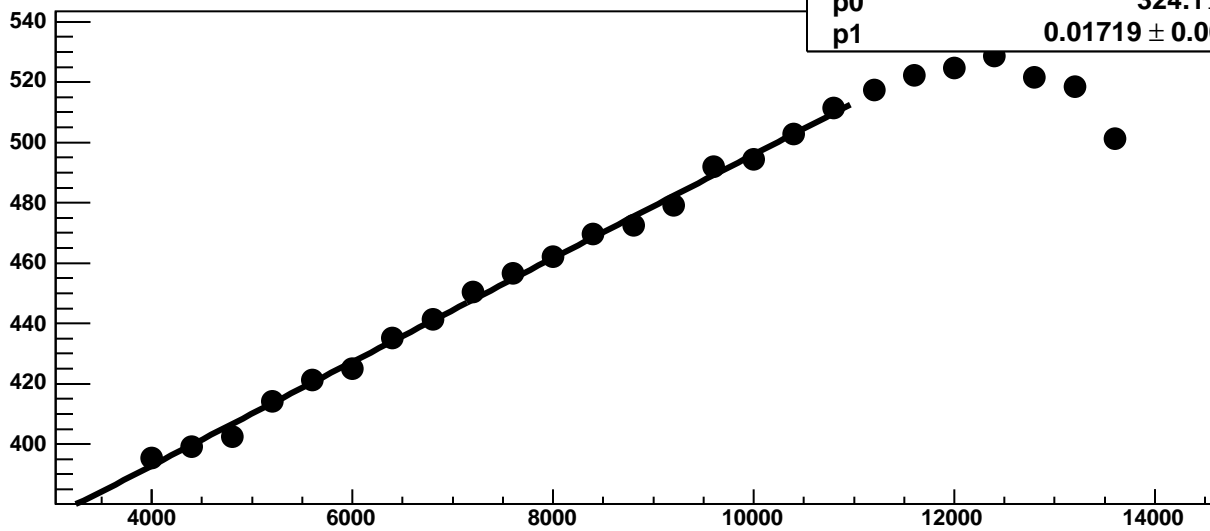
Chip 4, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



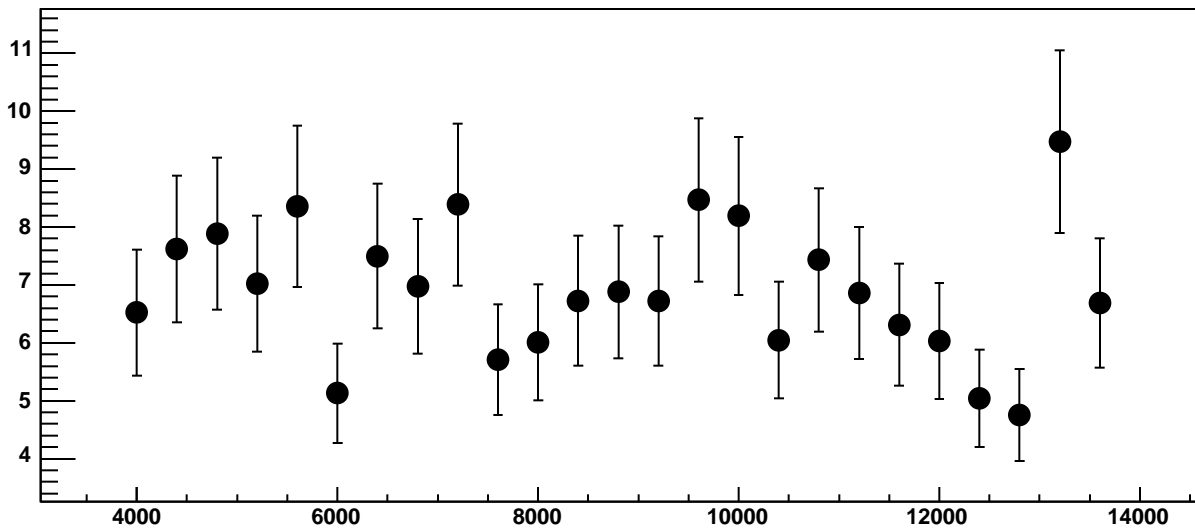
Chip 4, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



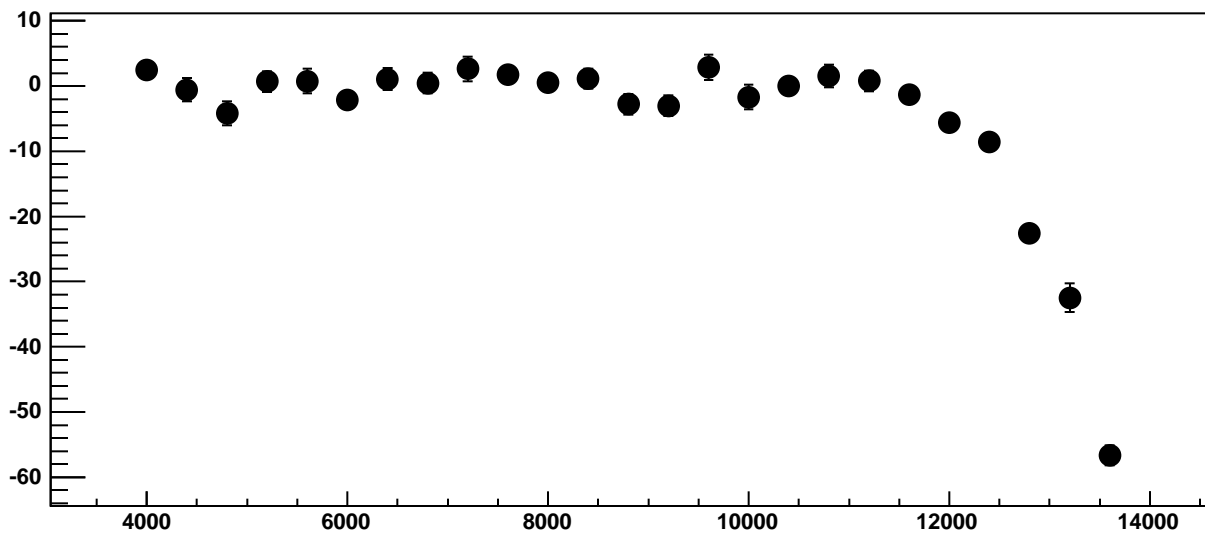
Chip 4, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



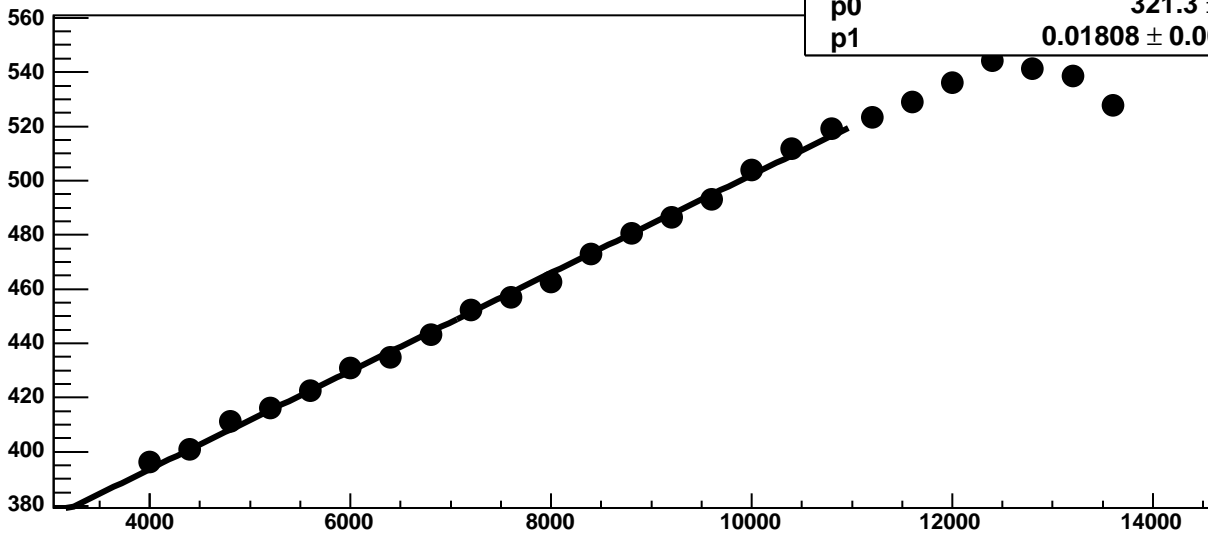
Chip 4, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



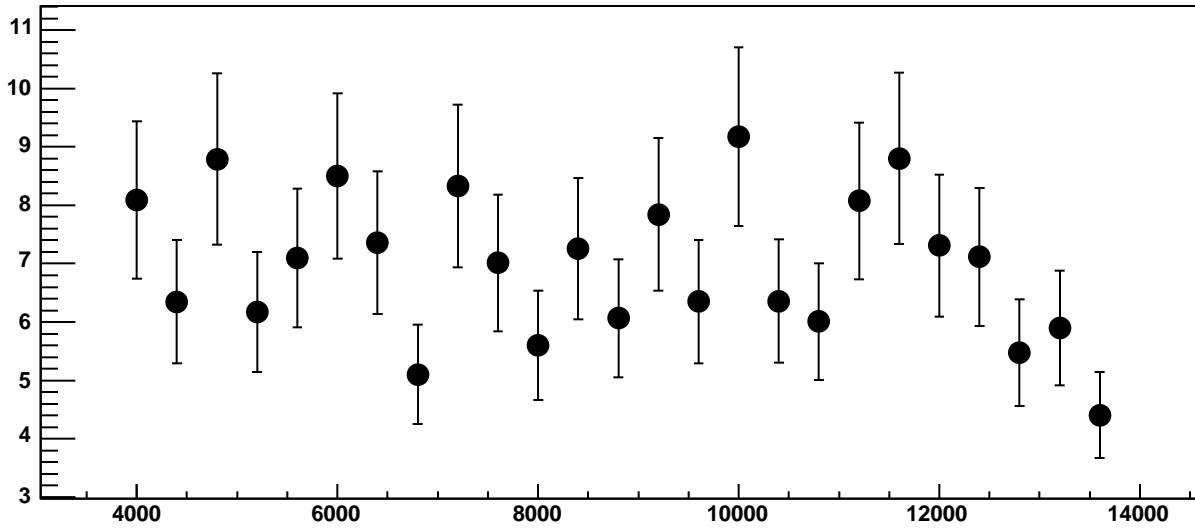
Chip 4, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



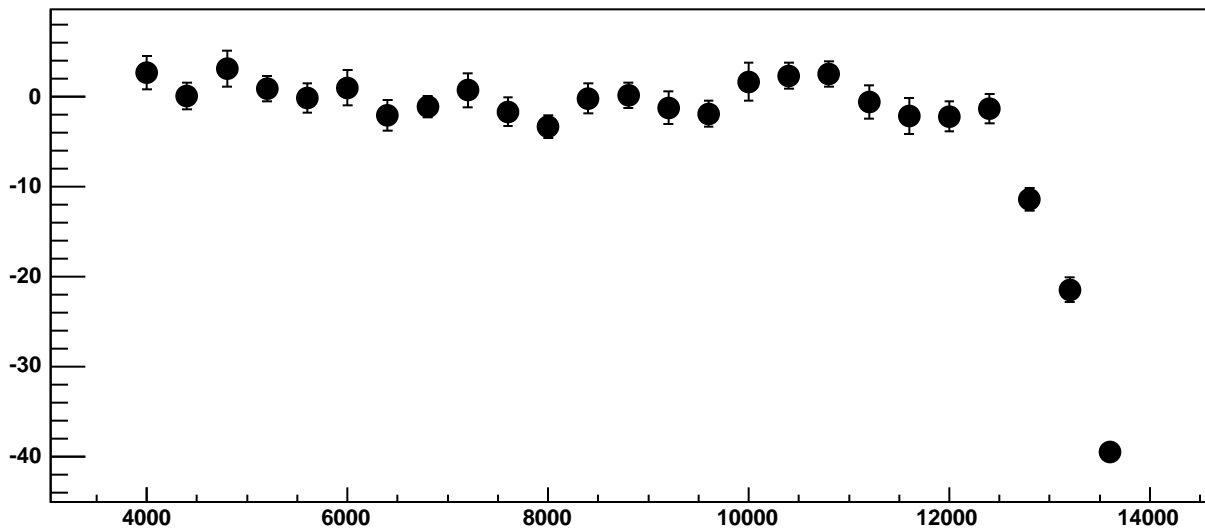
Chip 4, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



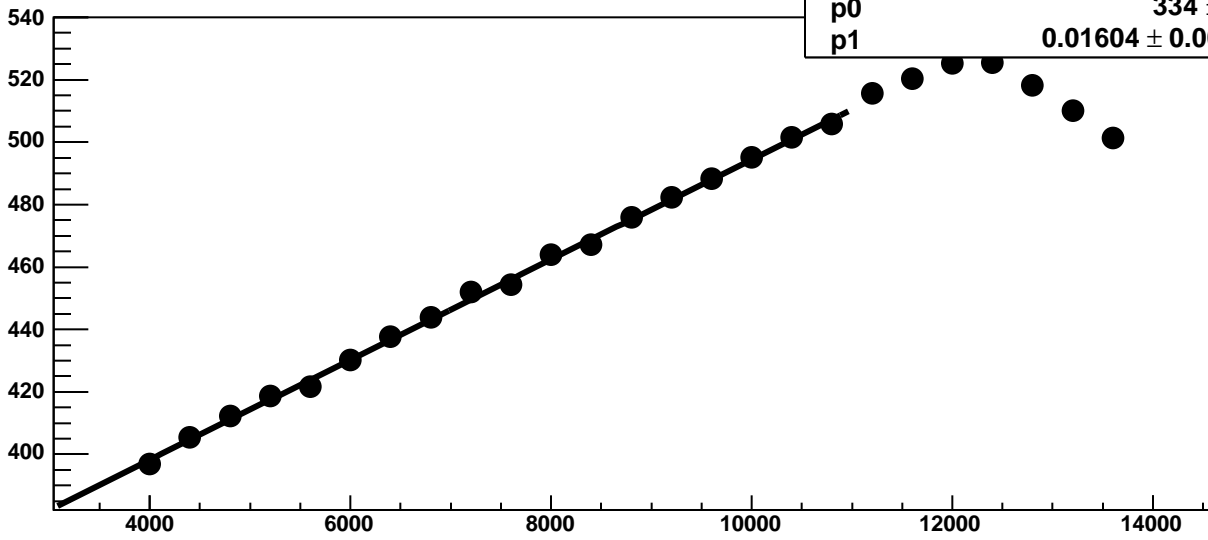
Chip 4, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

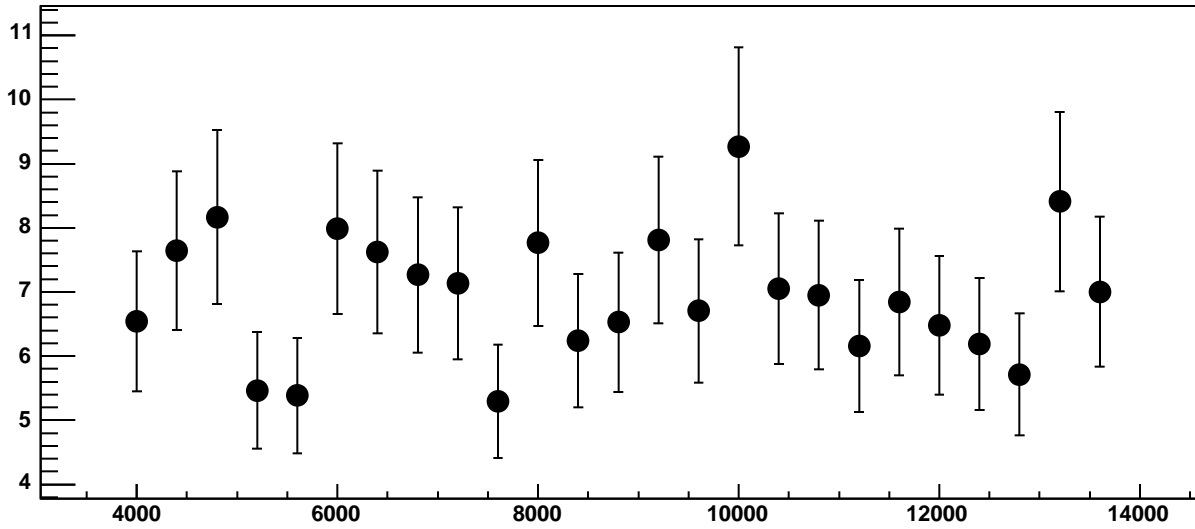


Chip 4, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC

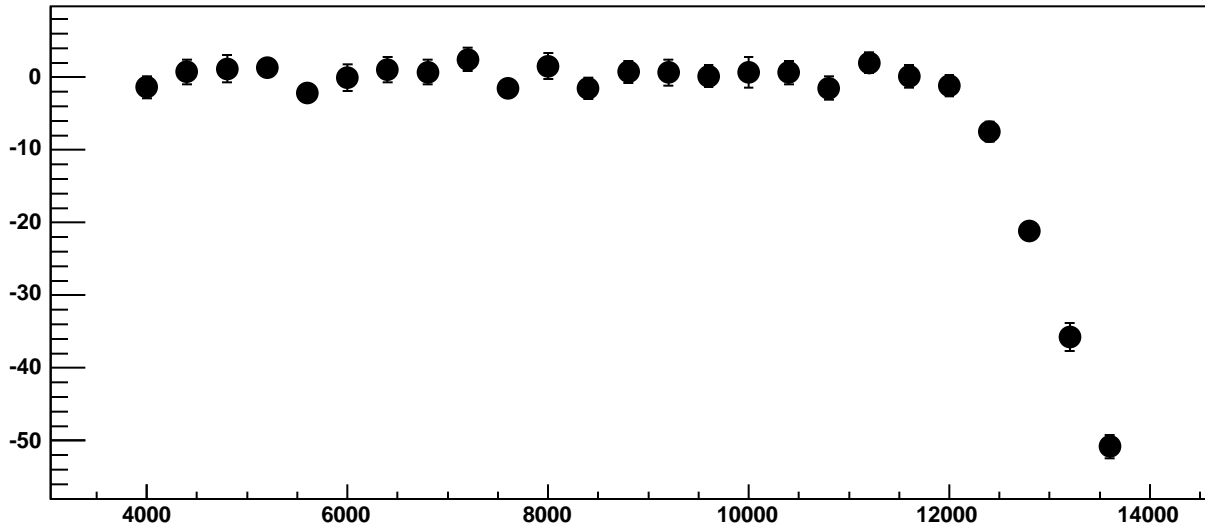


χ^2 / ndf 13.41 / 16
p0 334 ± 1.372
p1 0.01604 ± 0.0001816

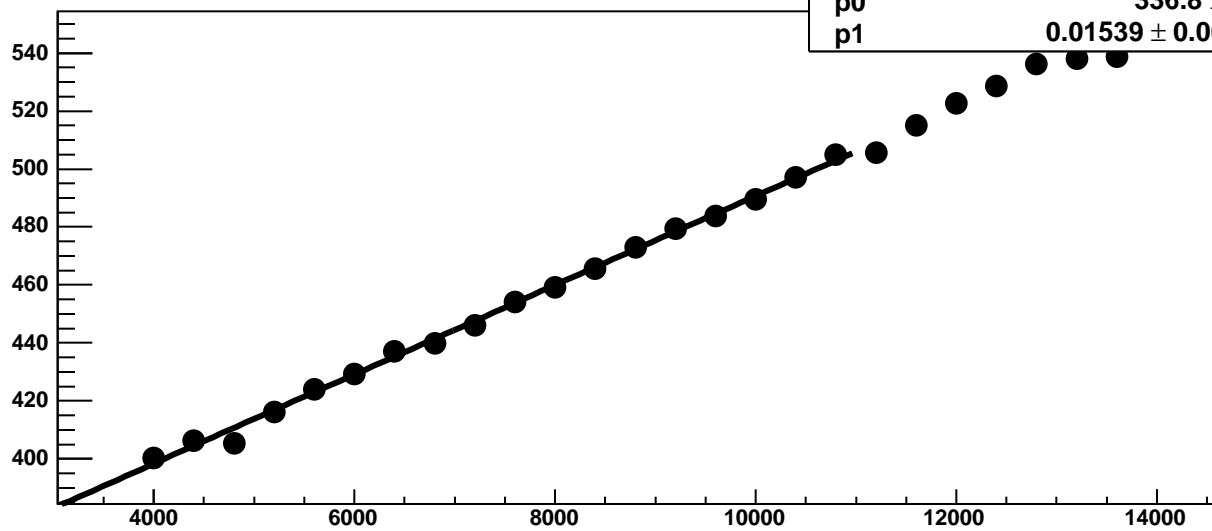
Chip 4, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

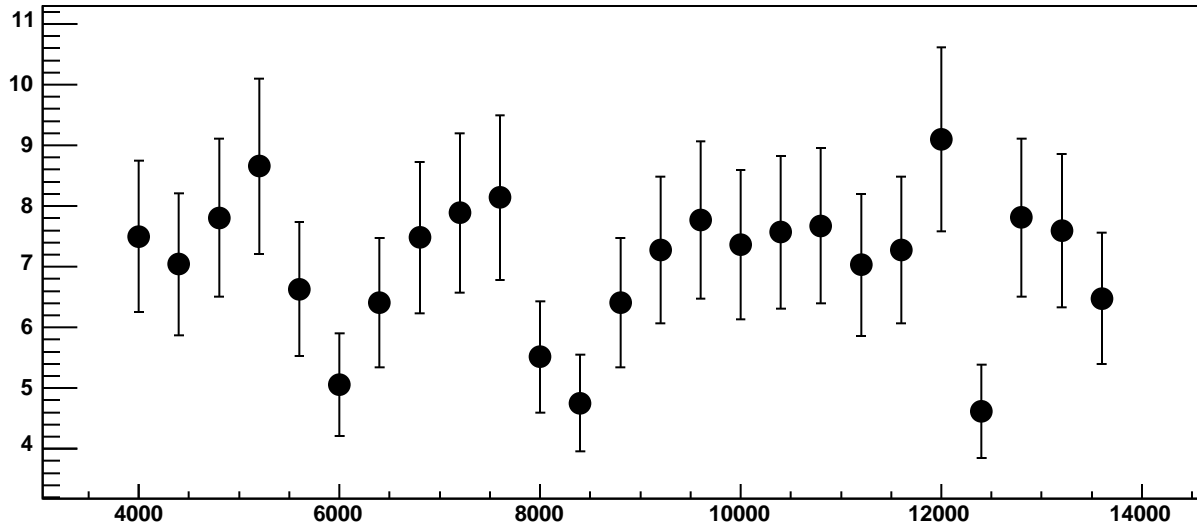


Chip 4, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

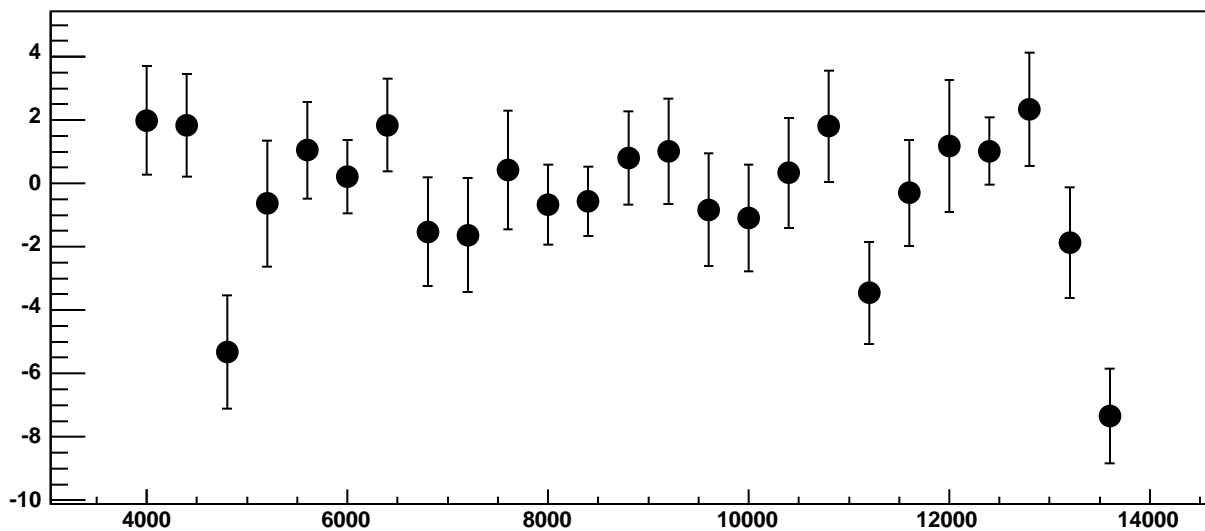


χ^2 / ndf 18.24 / 16
p0 336.8 ± 1.449
p1 0.01539 ± 0.0001893

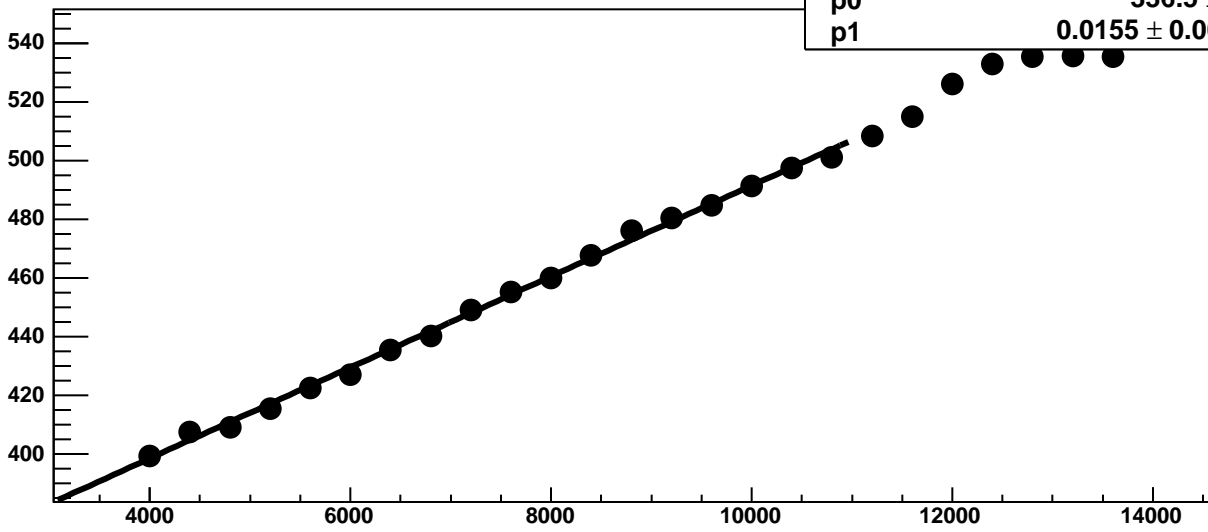
Chip 4, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

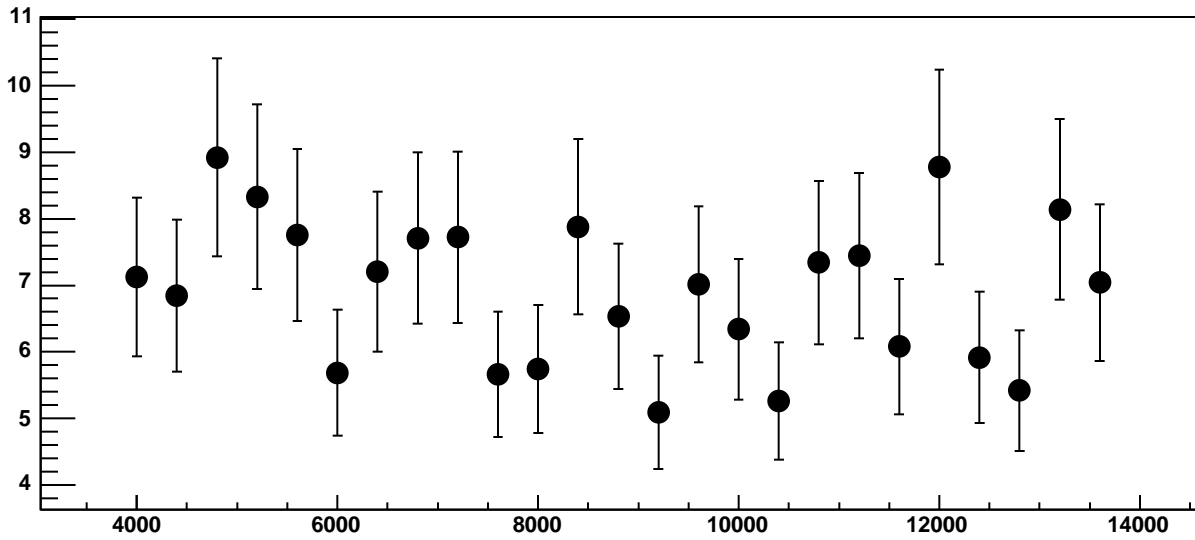


Chip 4, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

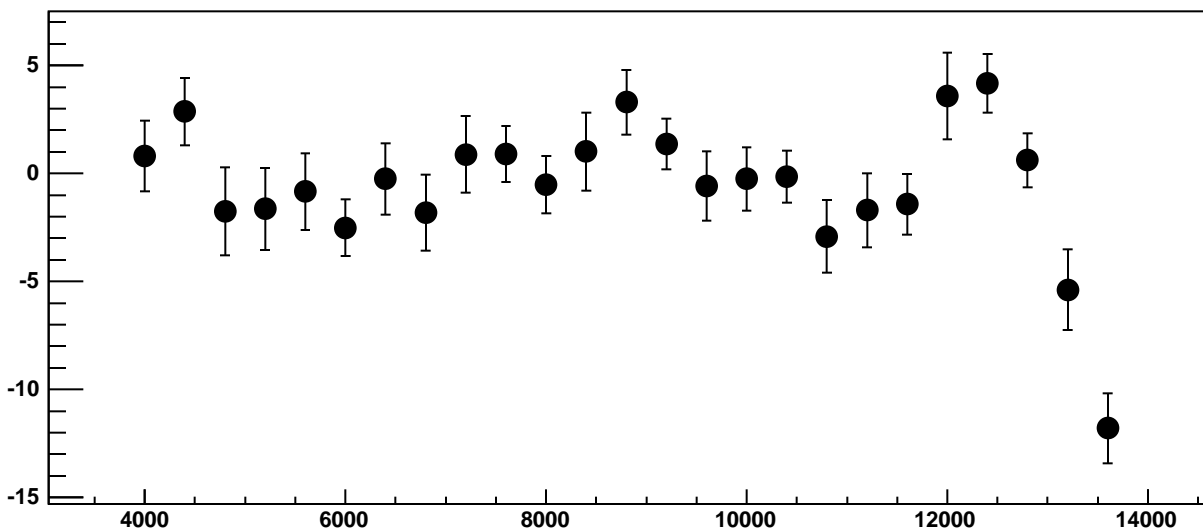


χ^2 / ndf 20.6 / 16
p0 336.5 ± 1.408
p1 0.0155 ± 0.0001768

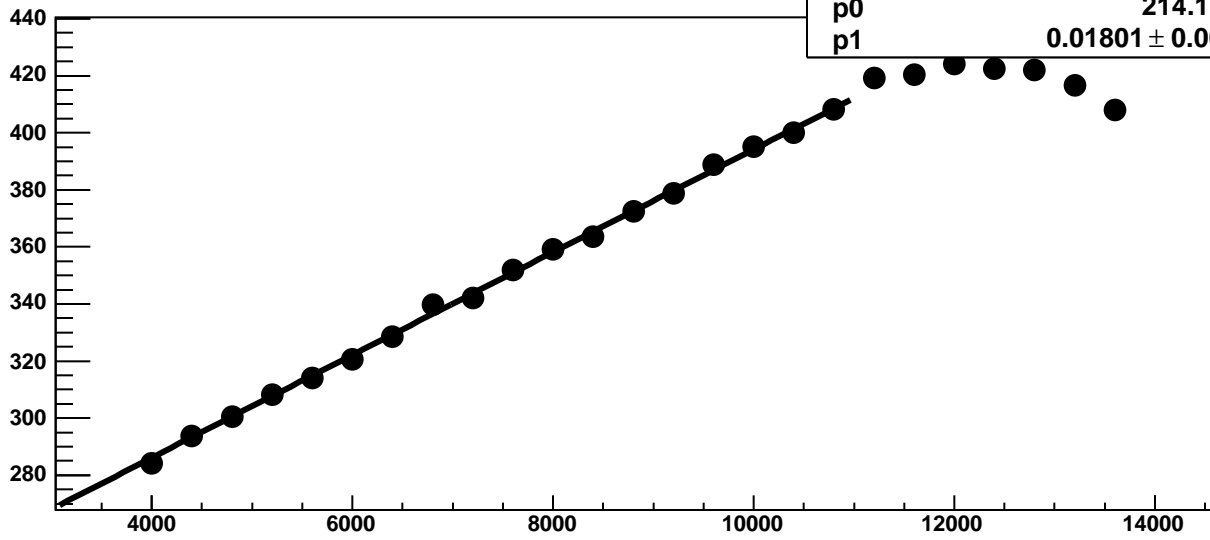
Chip 4, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



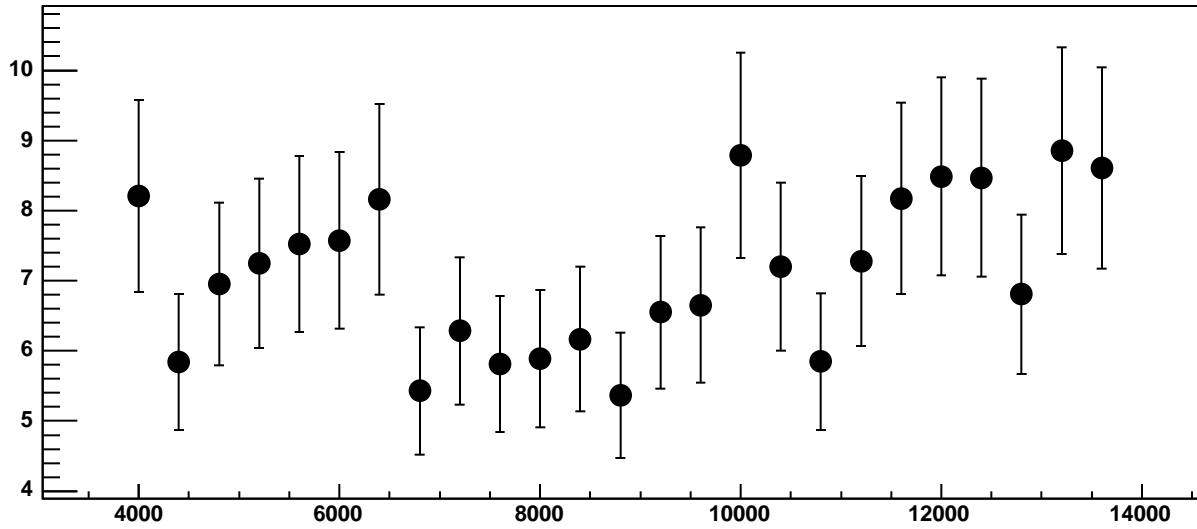
Chip 4, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



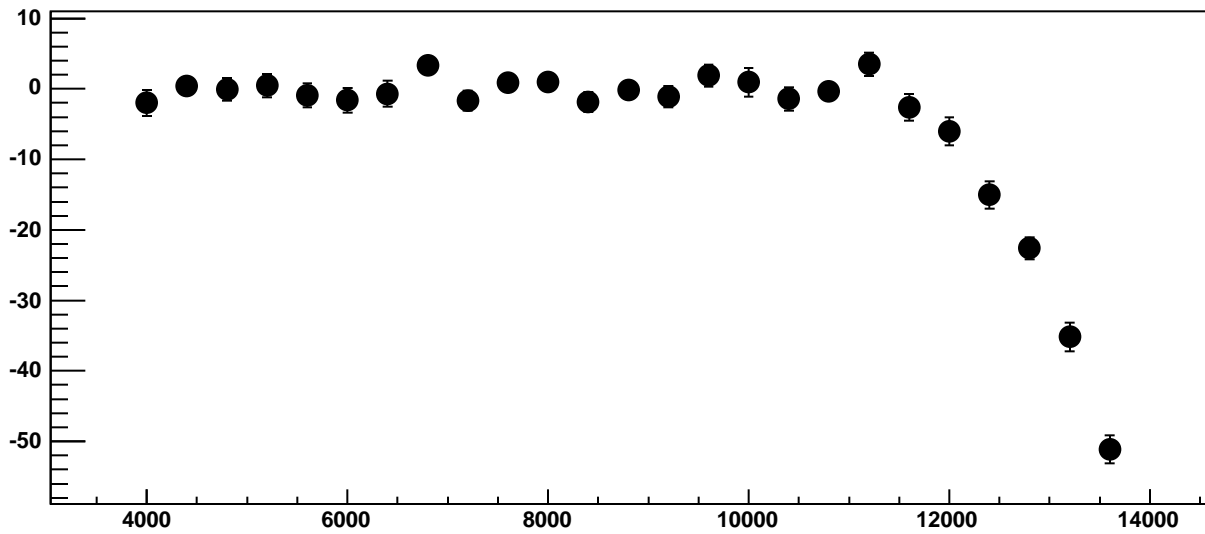
Chip 4, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



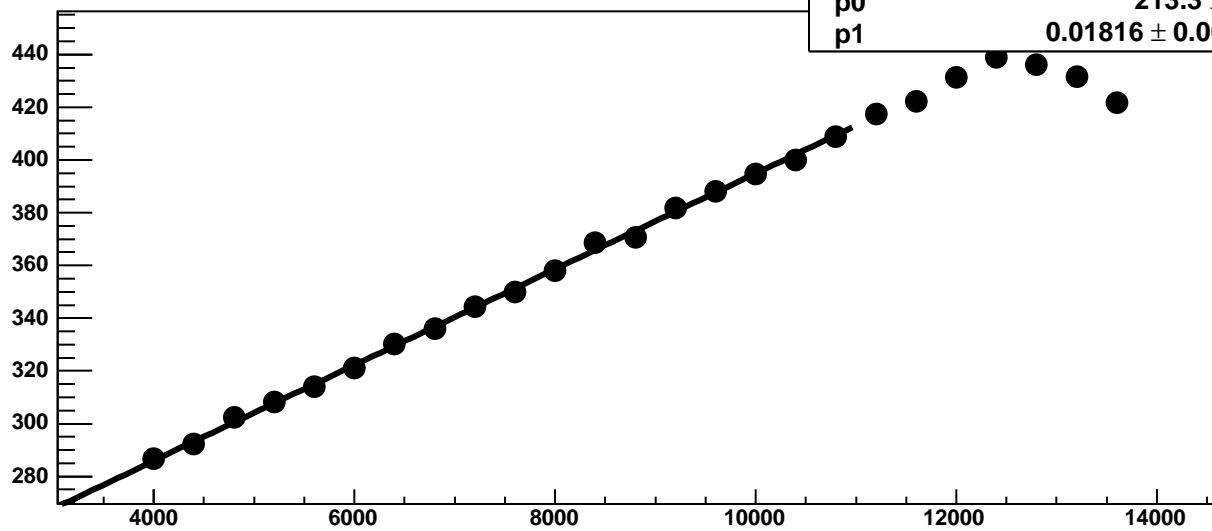
Chip 4, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



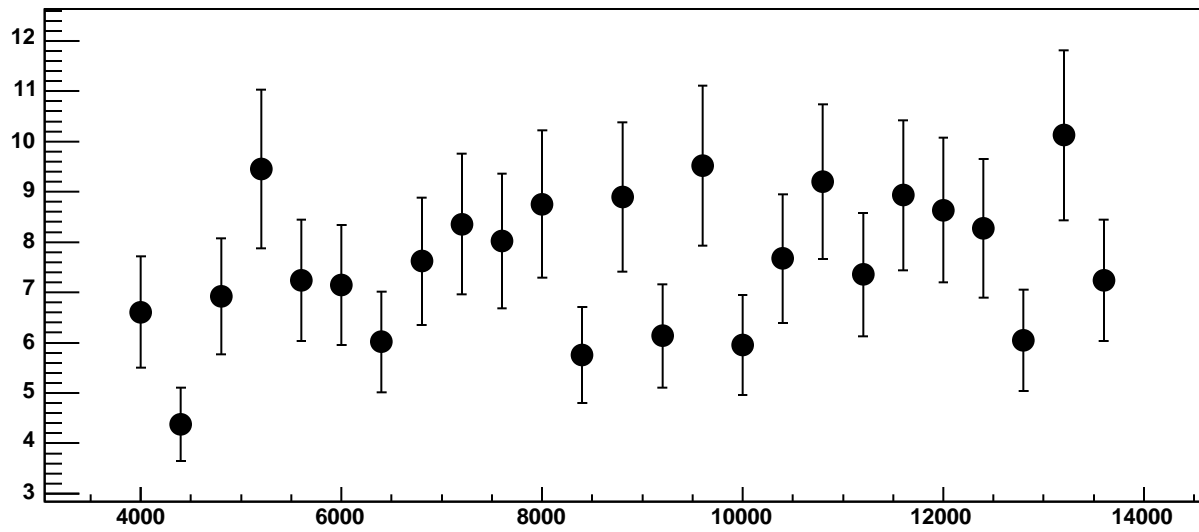
Chip 4, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



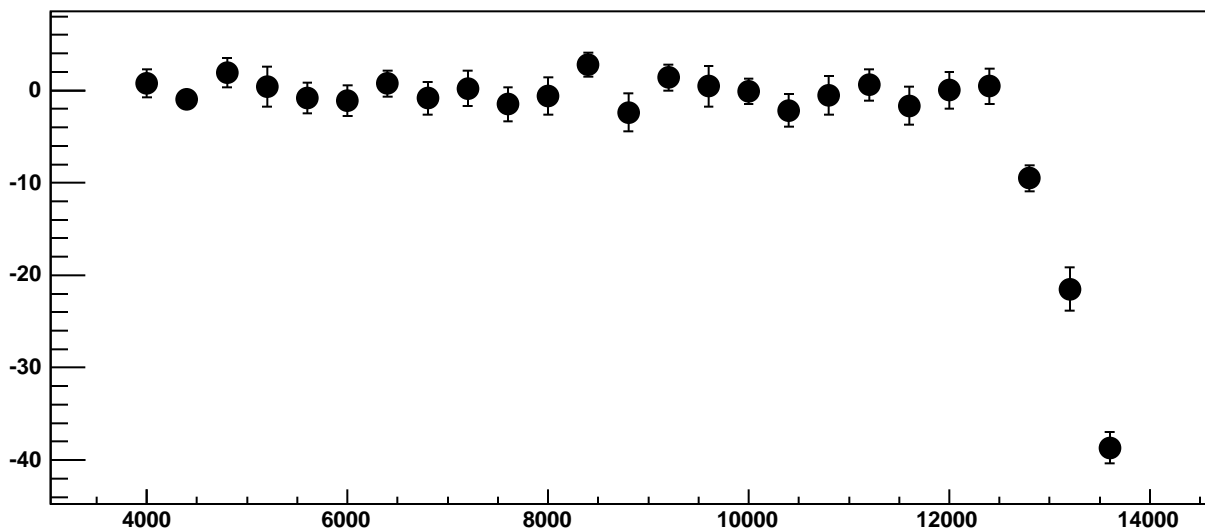
Chip 4, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



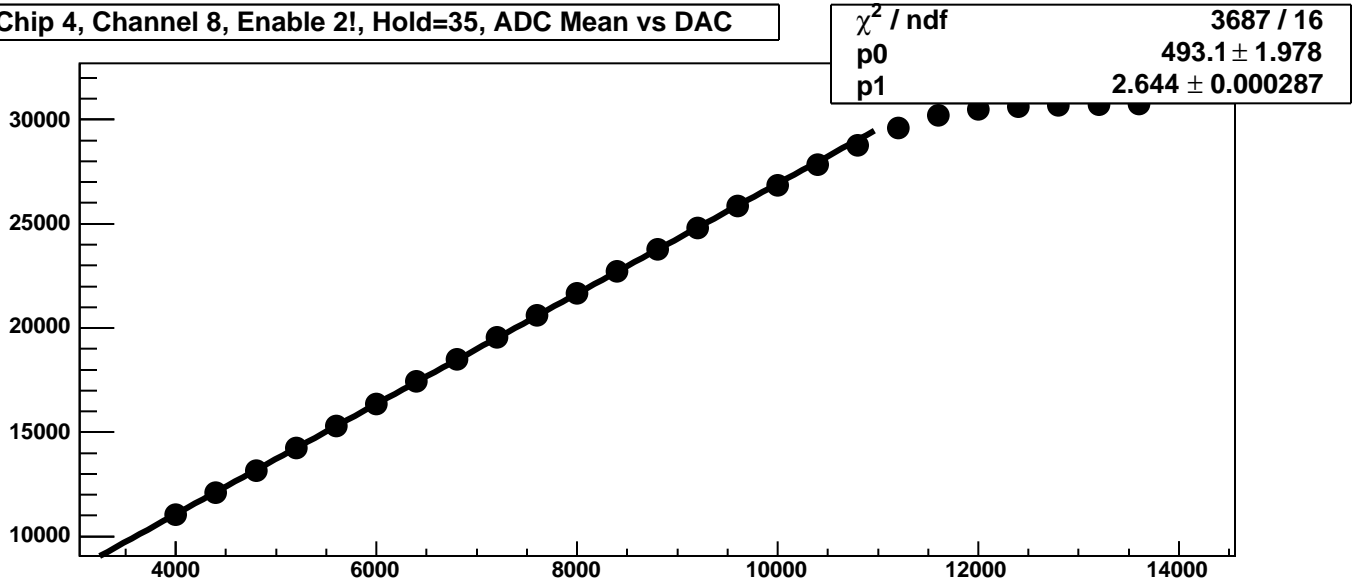
Chip 4, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



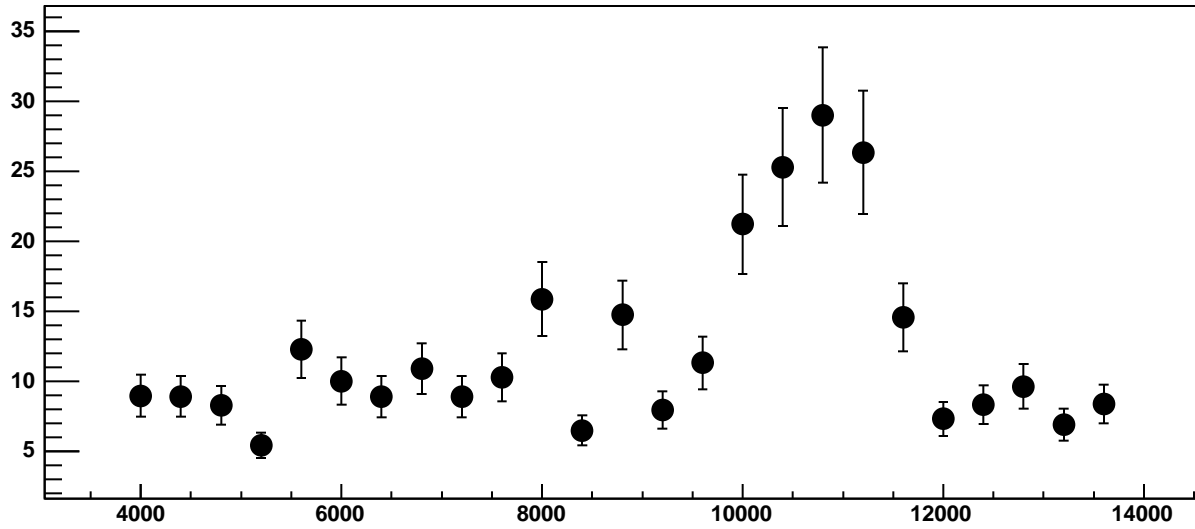
Chip 4, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



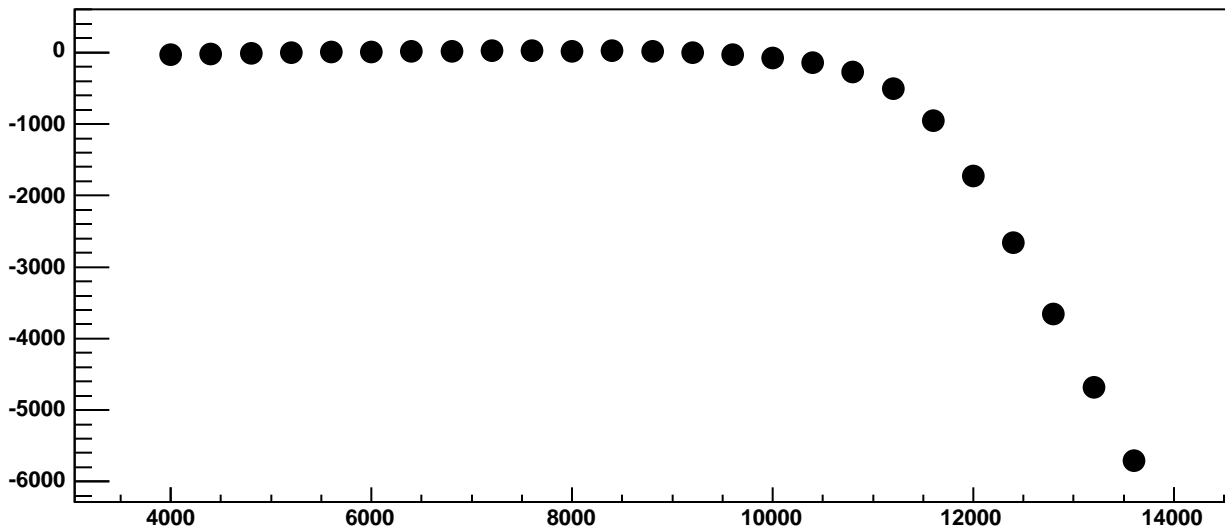
Chip 4, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC



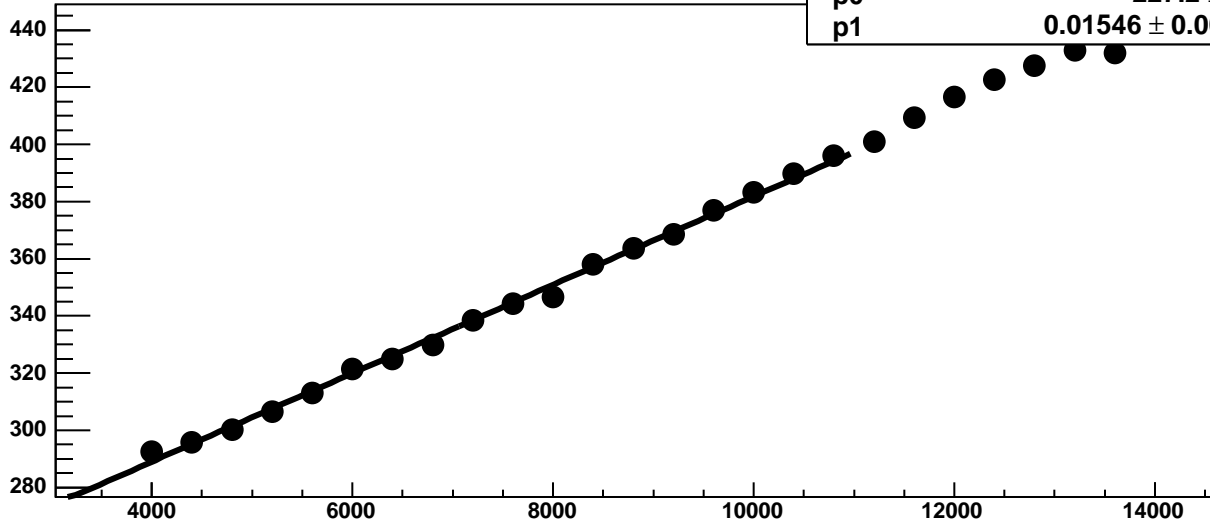
Chip 4, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



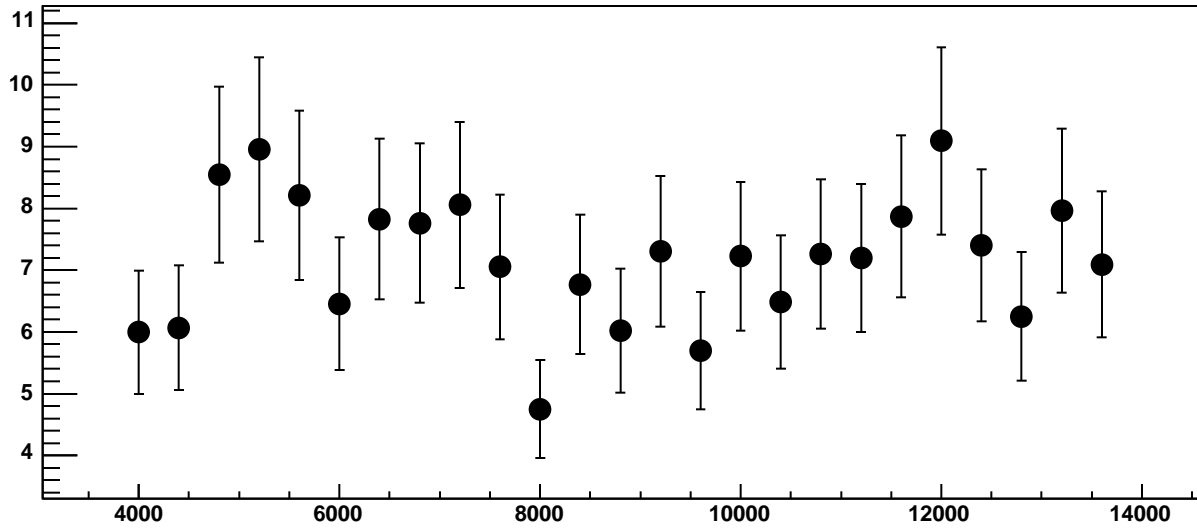
Chip 4, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC



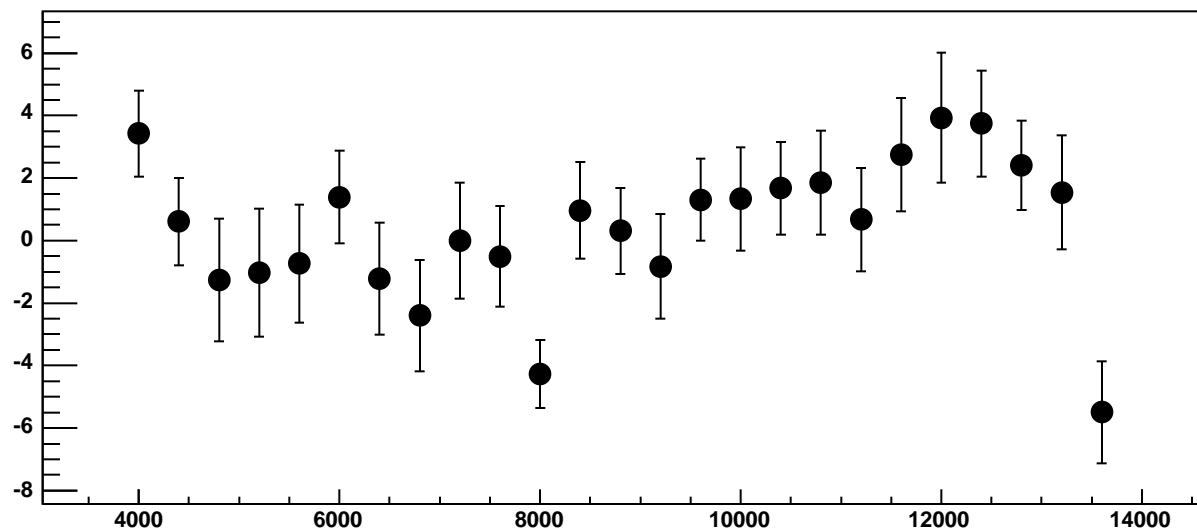
Chip 4, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



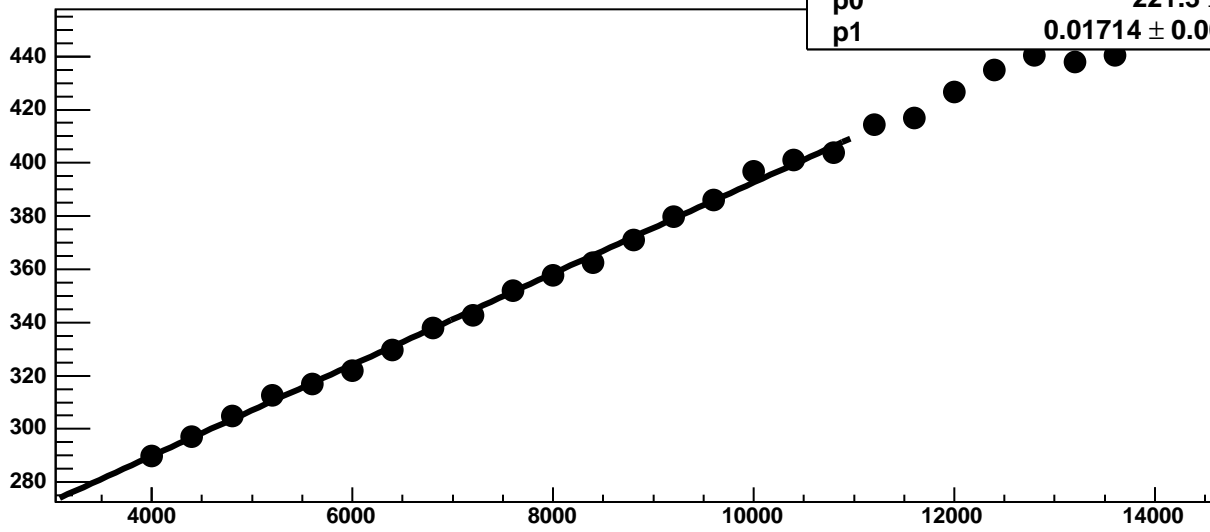
Chip 4, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

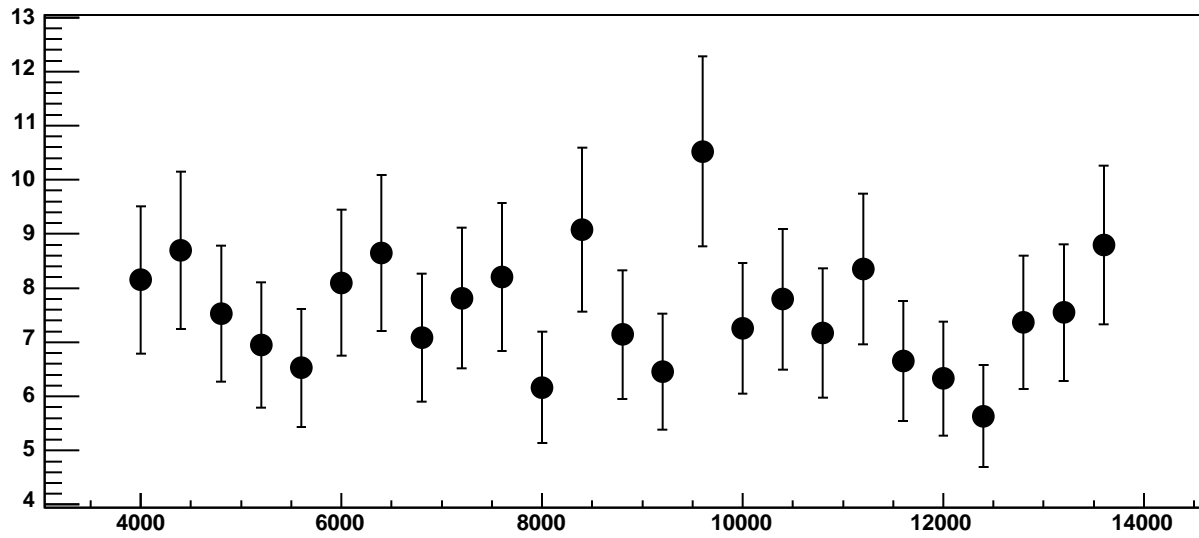


Chip 4, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

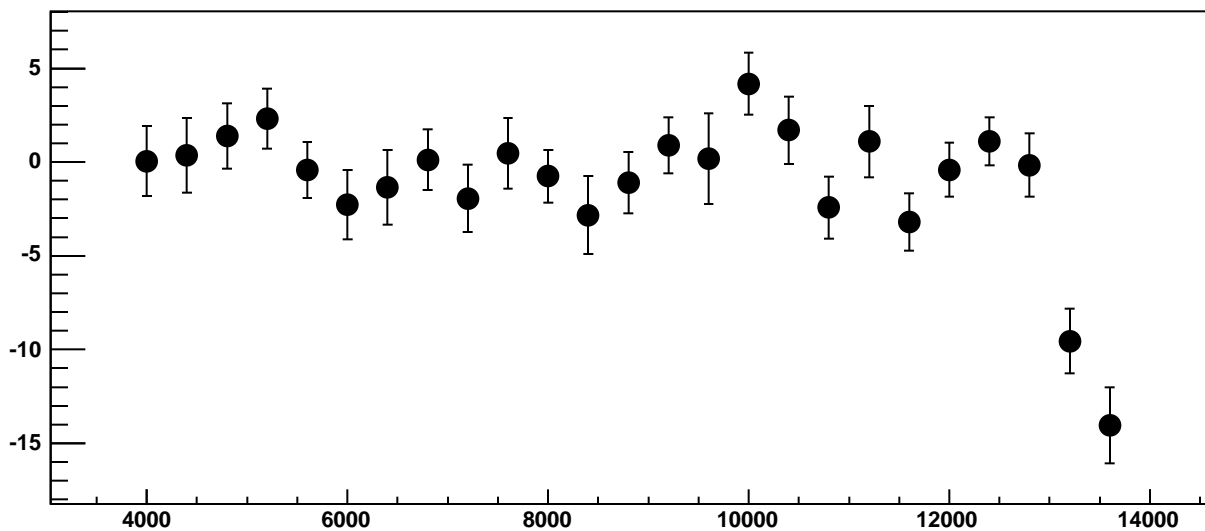


χ^2 / ndf 18.41 / 16
p0 221.3 ± 1.539
p1 0.01714 ± 0.0001995

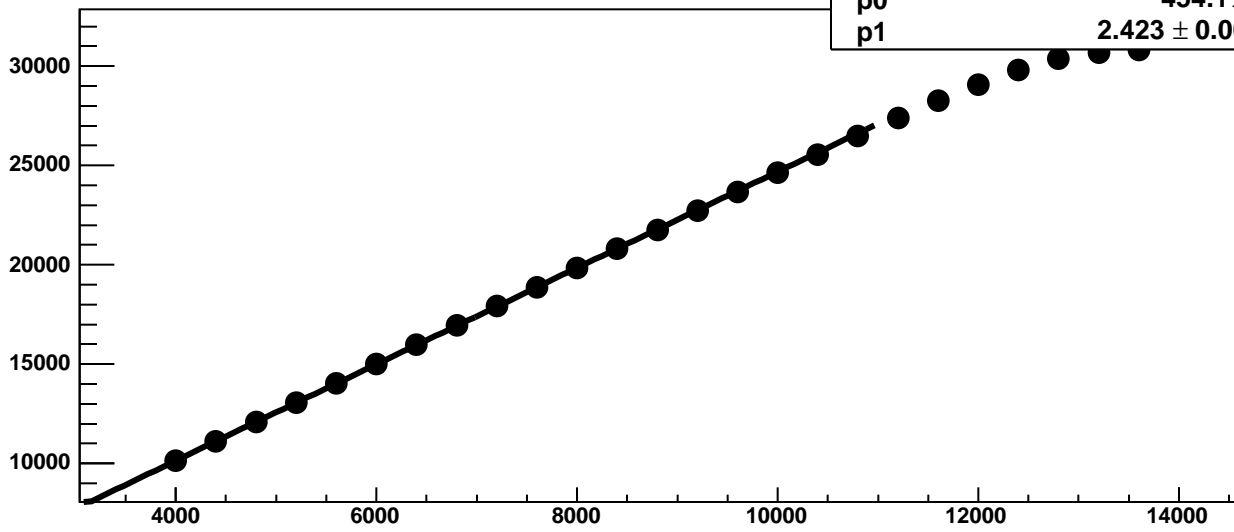
Chip 4, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

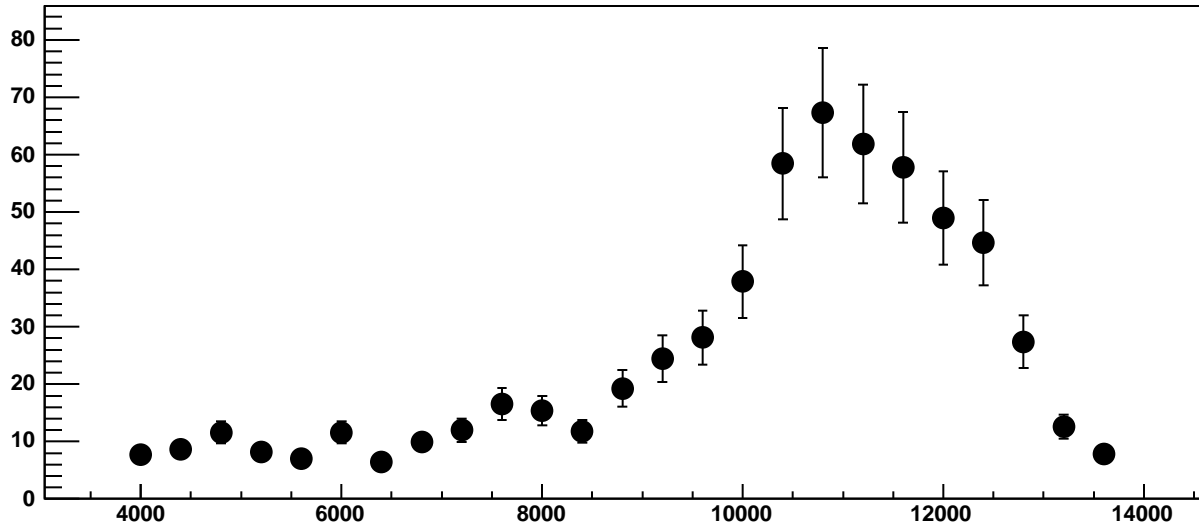


Chip 4, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC

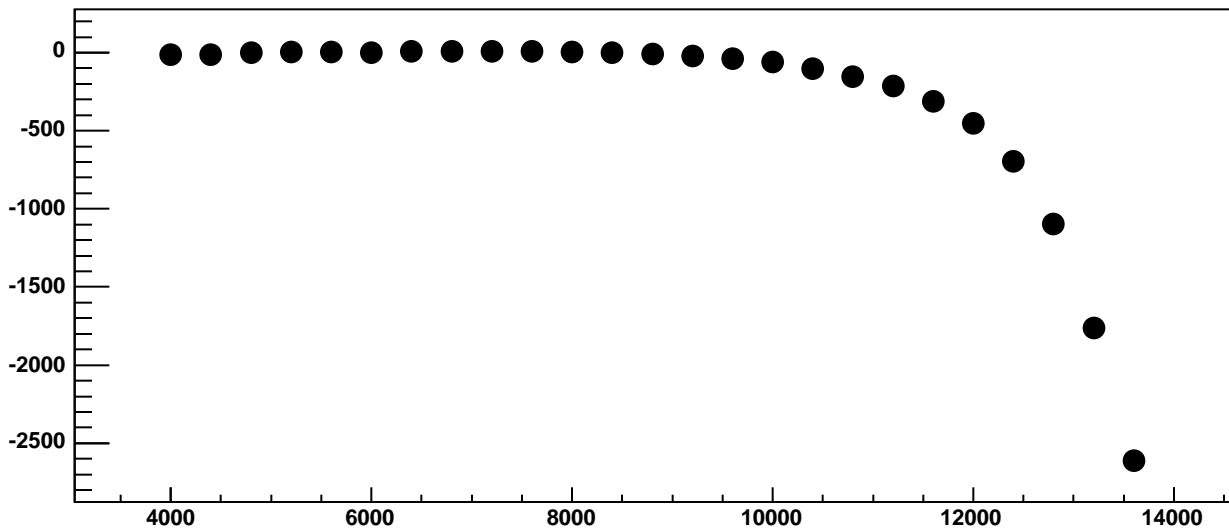


χ^2 / ndf 406.9 / 16
p0 454.1 ± 2.624
p1 2.423 ± 0.0004272

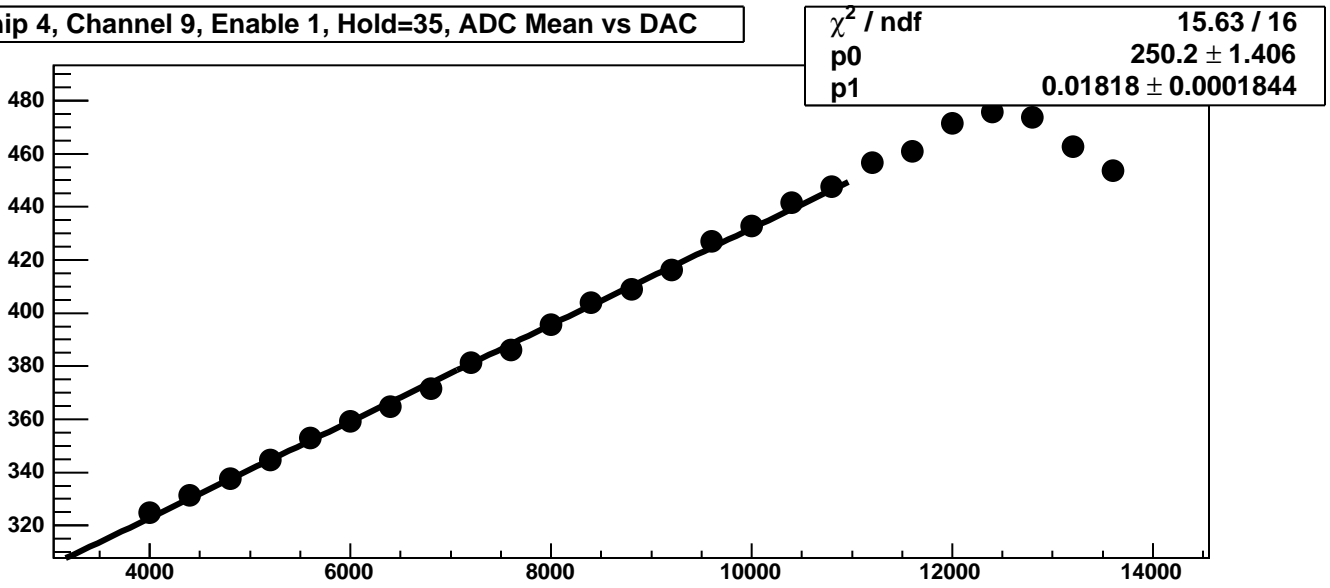
Chip 4, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



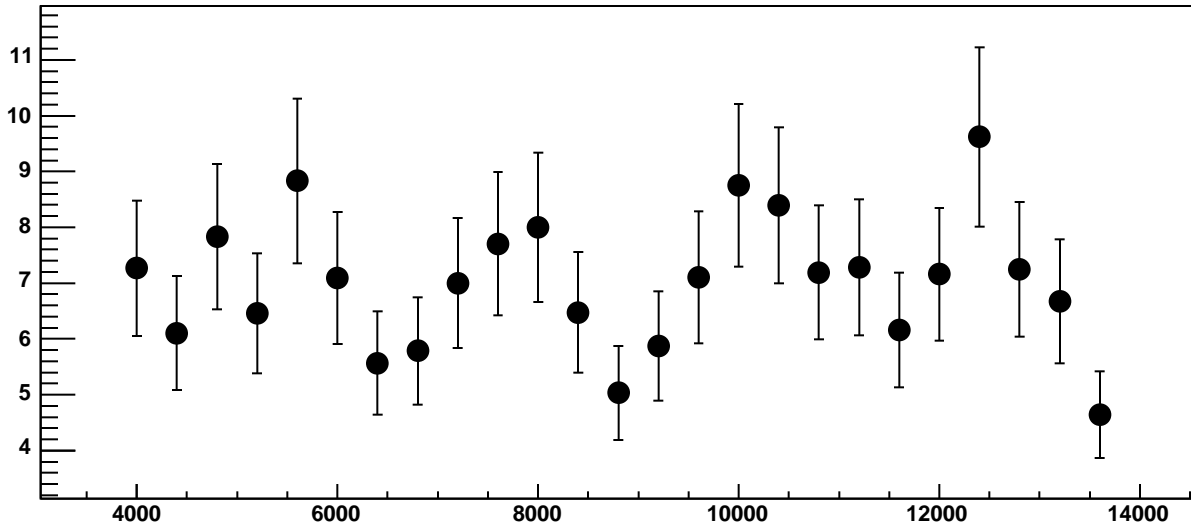
Chip 4, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC



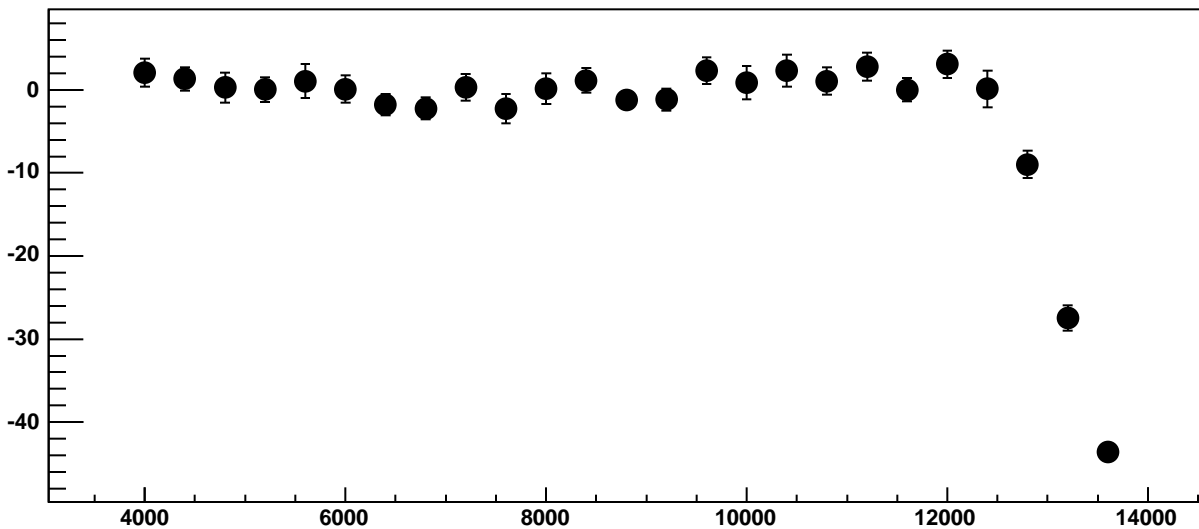
Chip 4, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



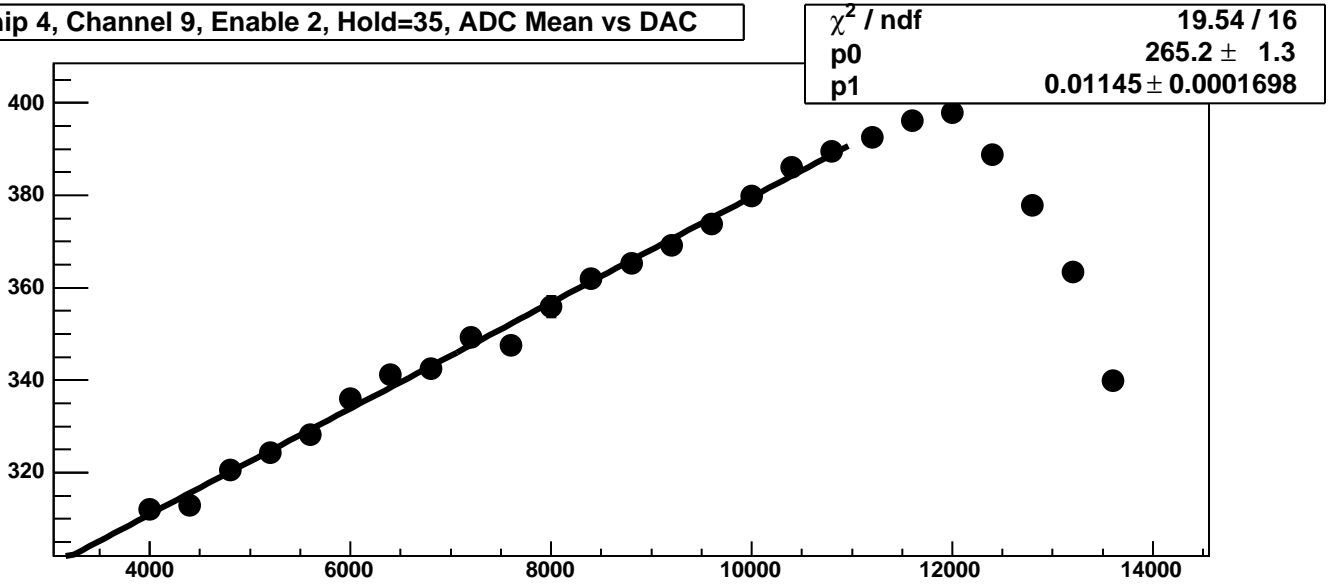
Chip 4, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



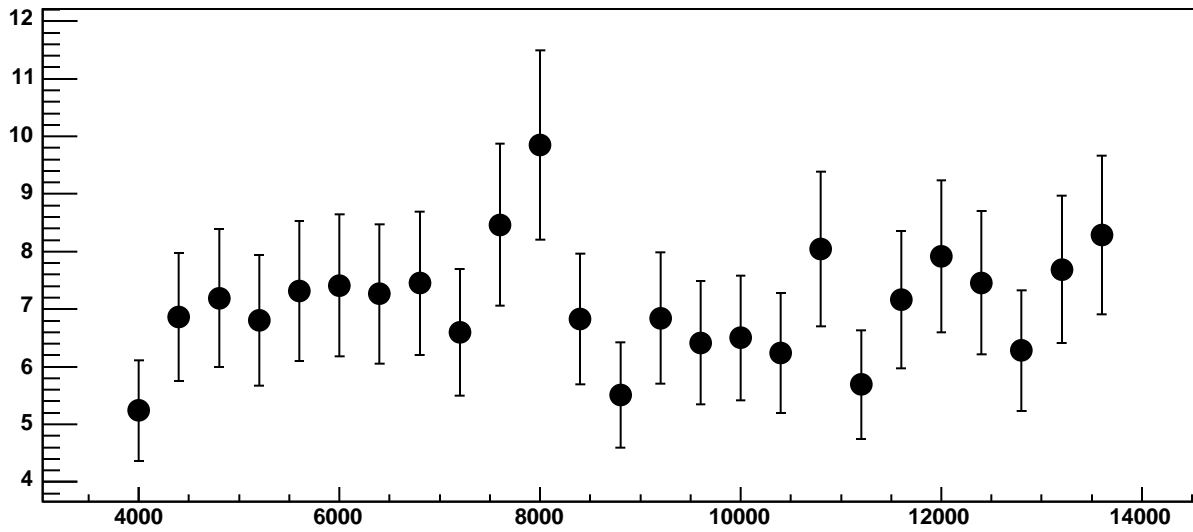
Chip 4, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



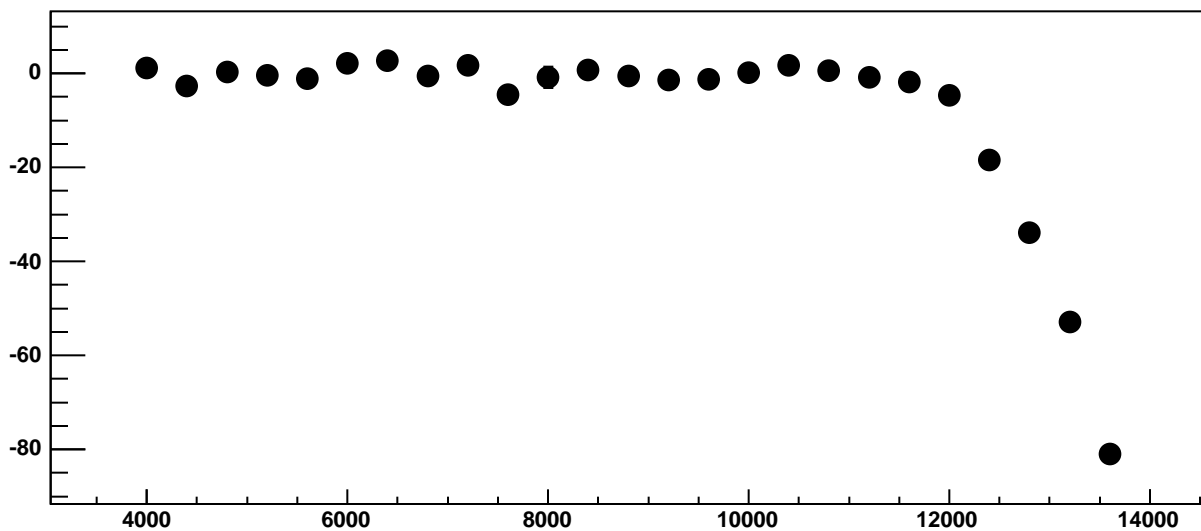
Chip 4, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



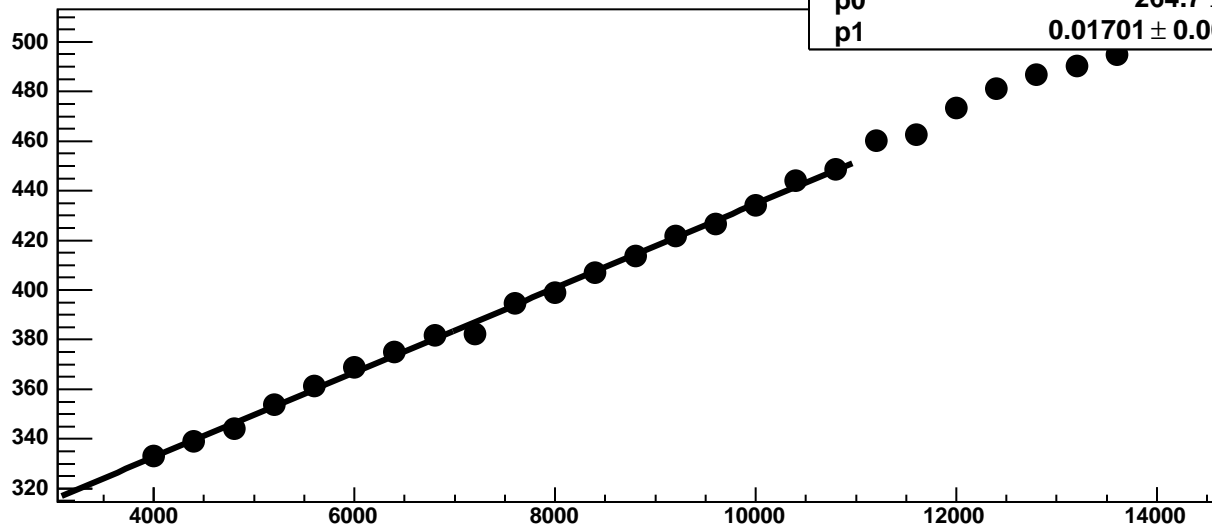
Chip 4, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

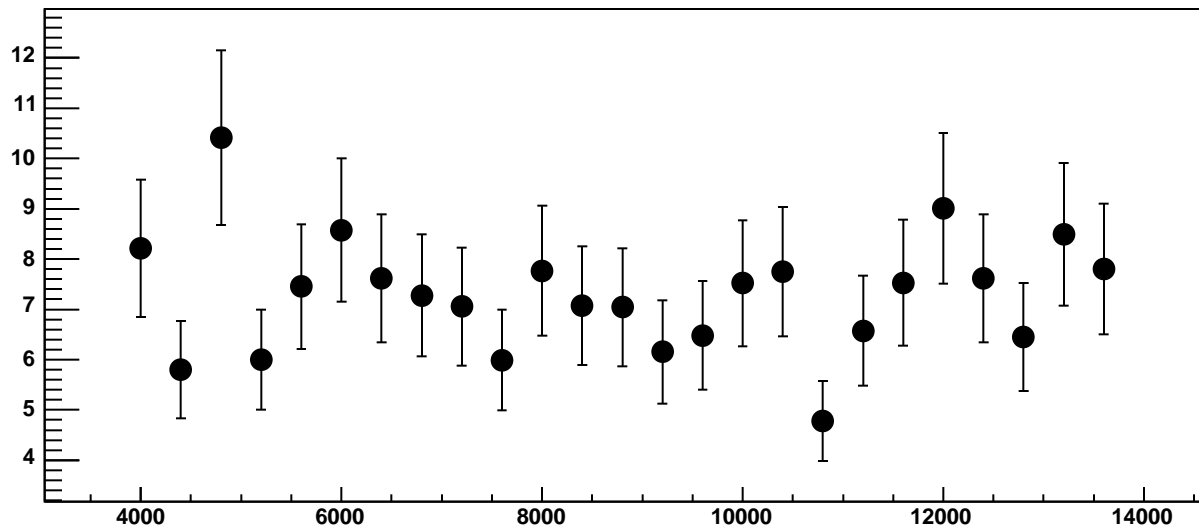


Chip 4, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

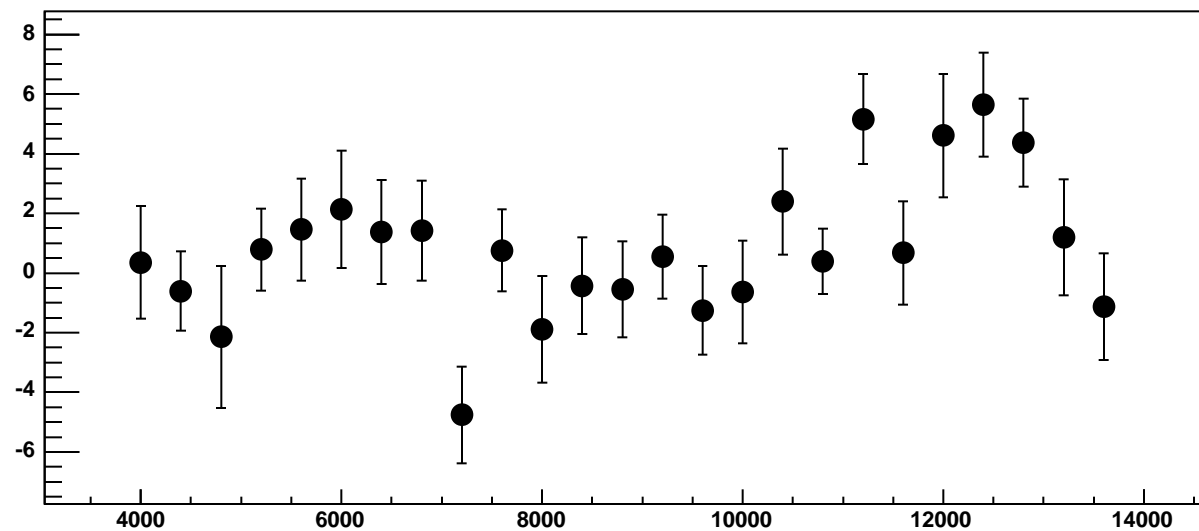


χ^2 / ndf 17.8 / 16
p0 264.7 ± 1.375
p1 0.01701 ± 0.0001729

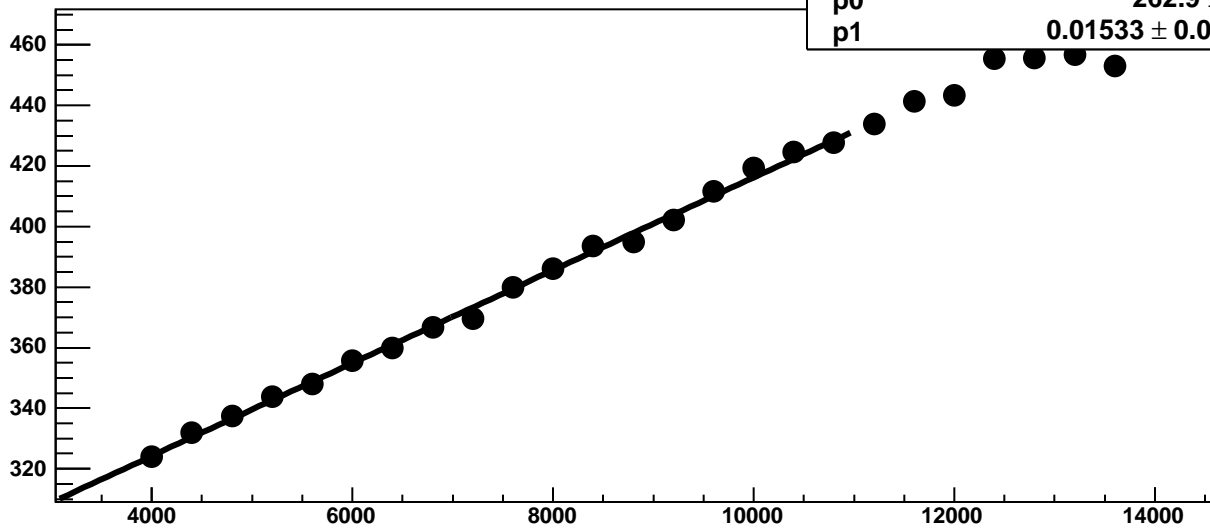
Chip 4, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



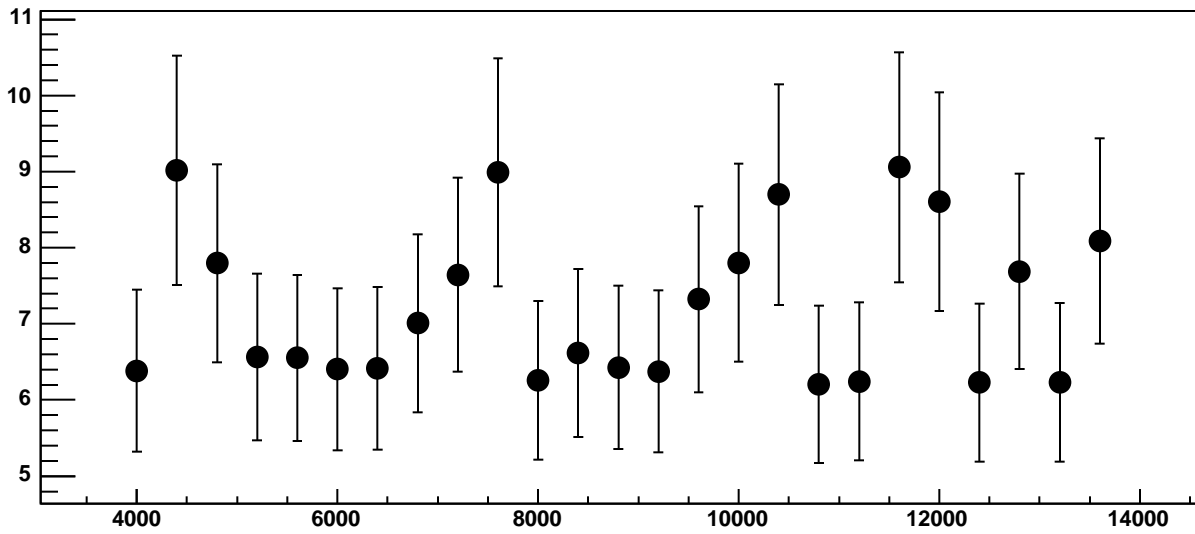
Chip 4, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC



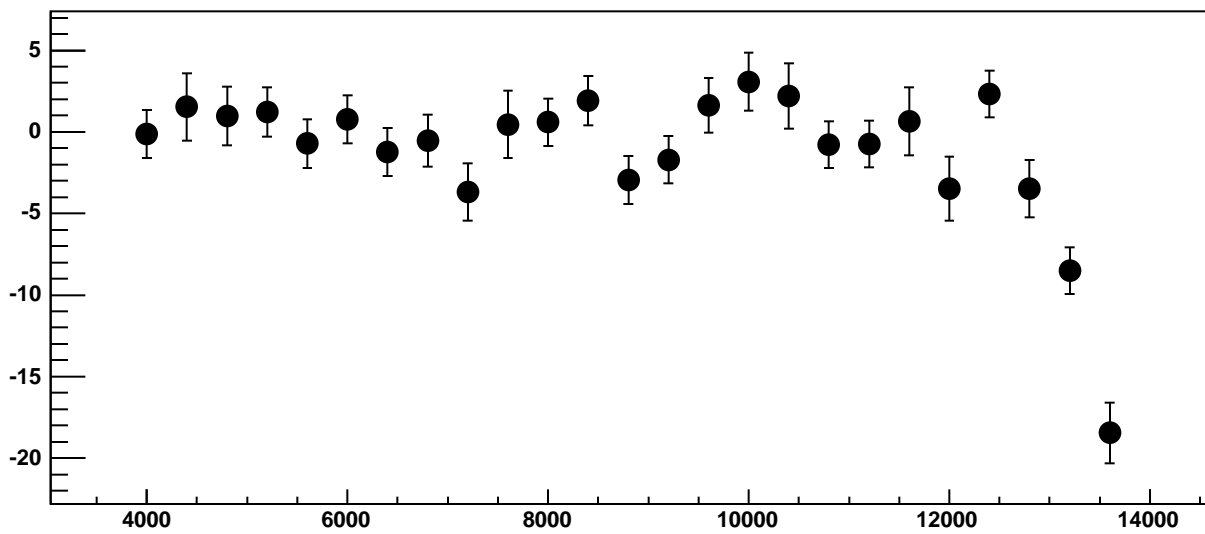
Chip 4, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



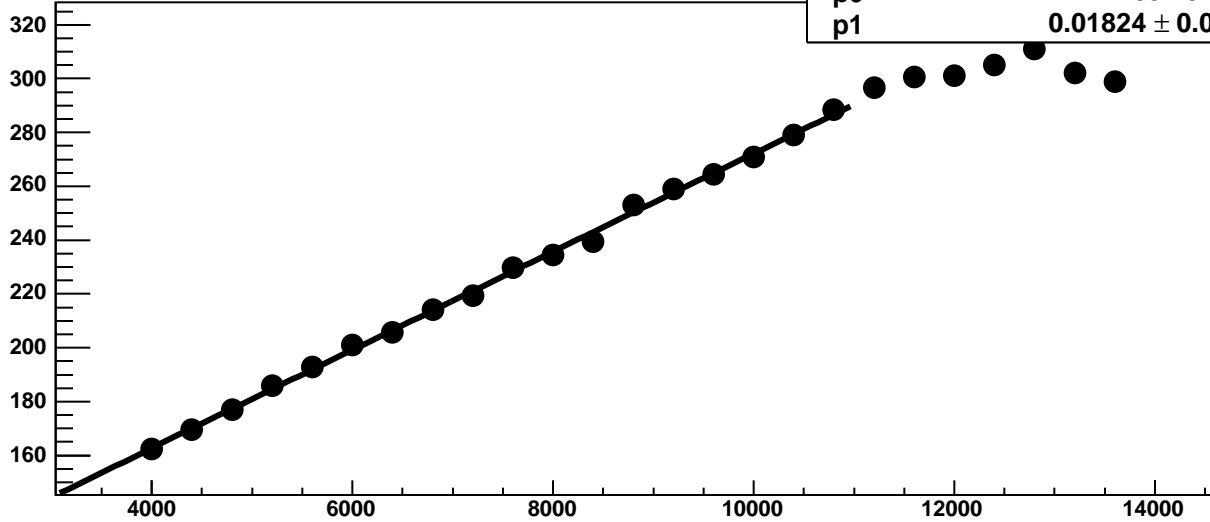
Chip 4, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



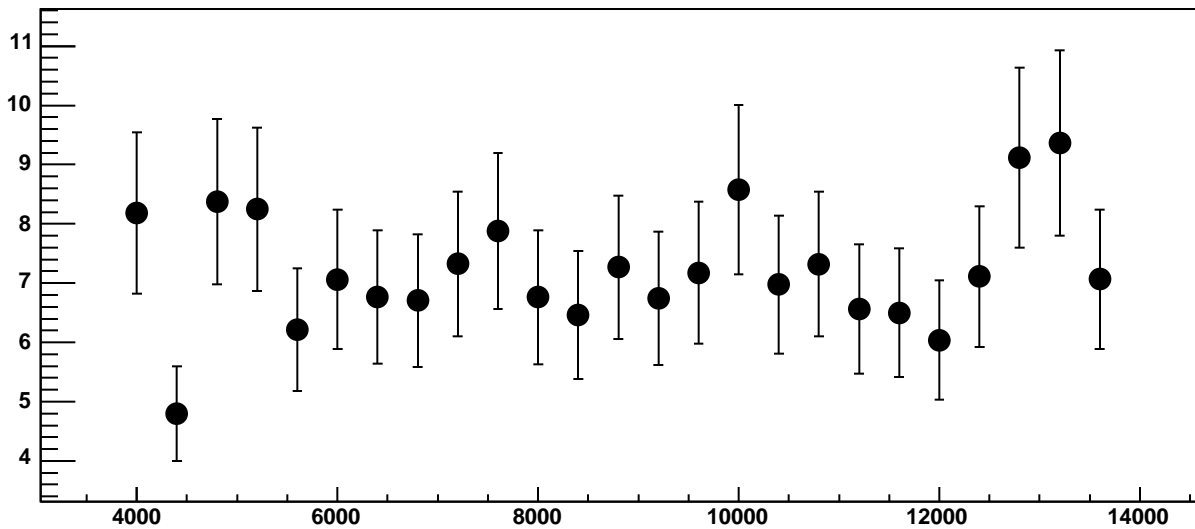
Chip 4, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



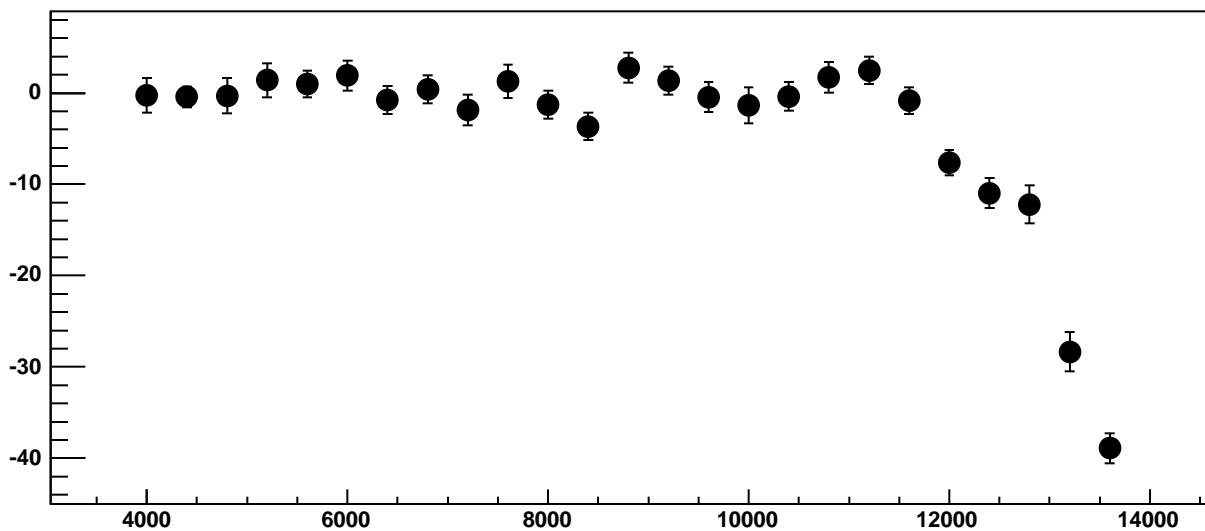
Chip 4, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC



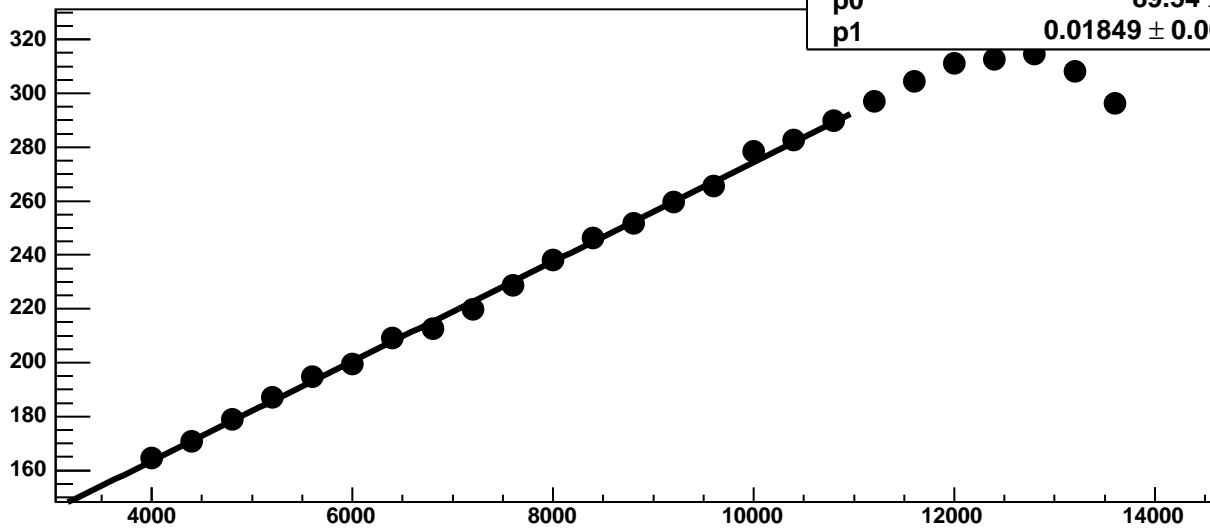
Chip 4, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC

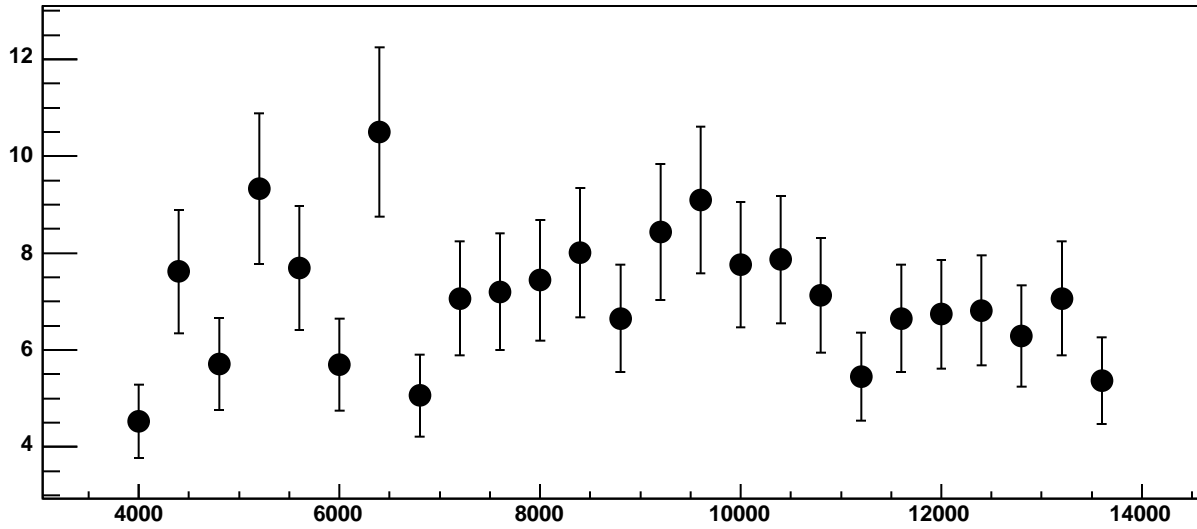


Chip 4, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

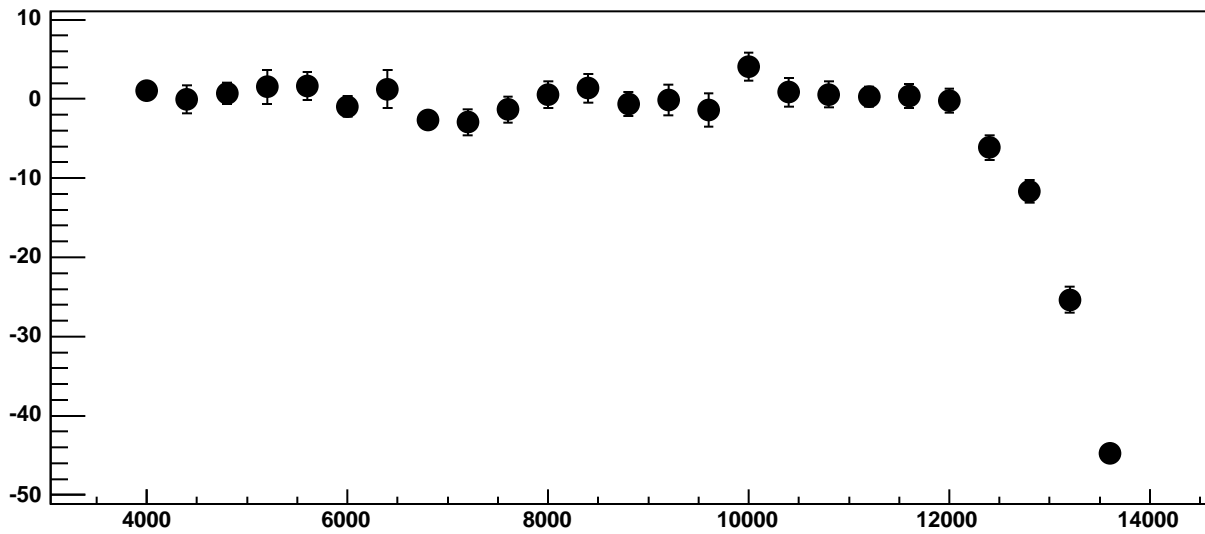


χ^2 / ndf 19.73 / 16
p0 89.54 ± 1.279
p1 0.01849 ± 0.0001754

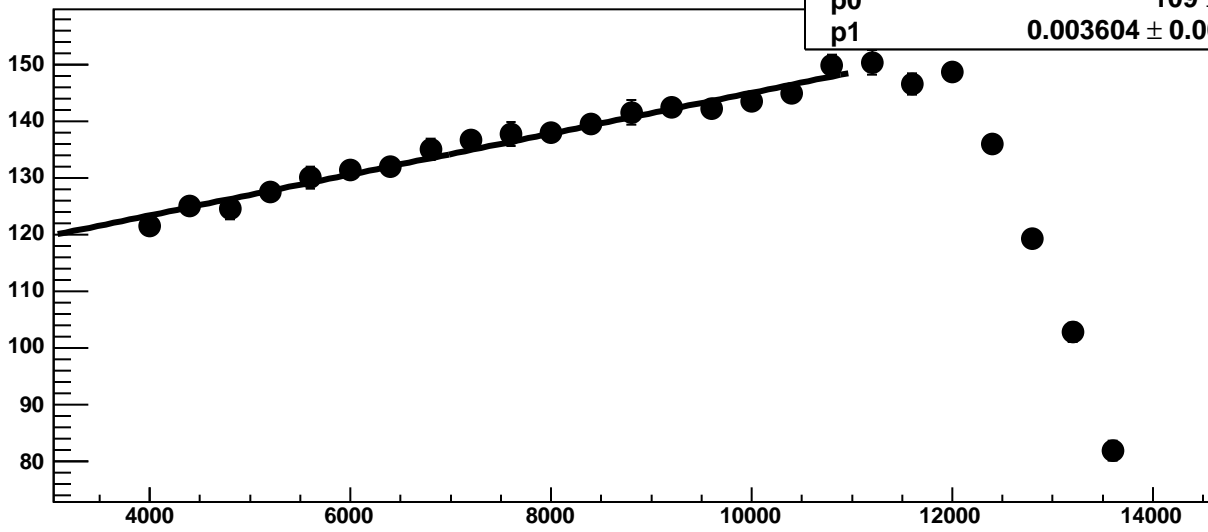
Chip 4, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



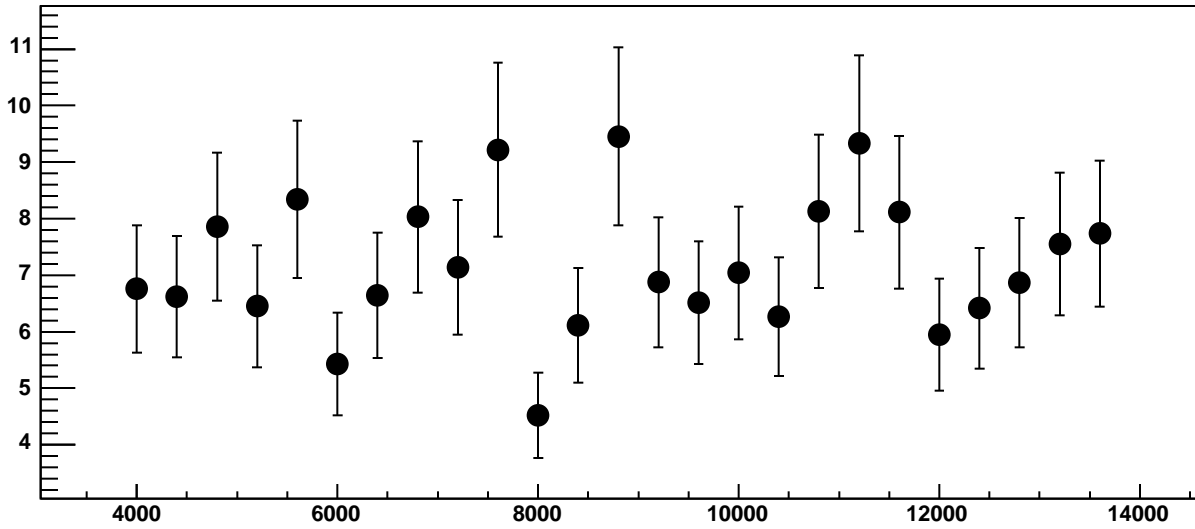
Chip 4, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



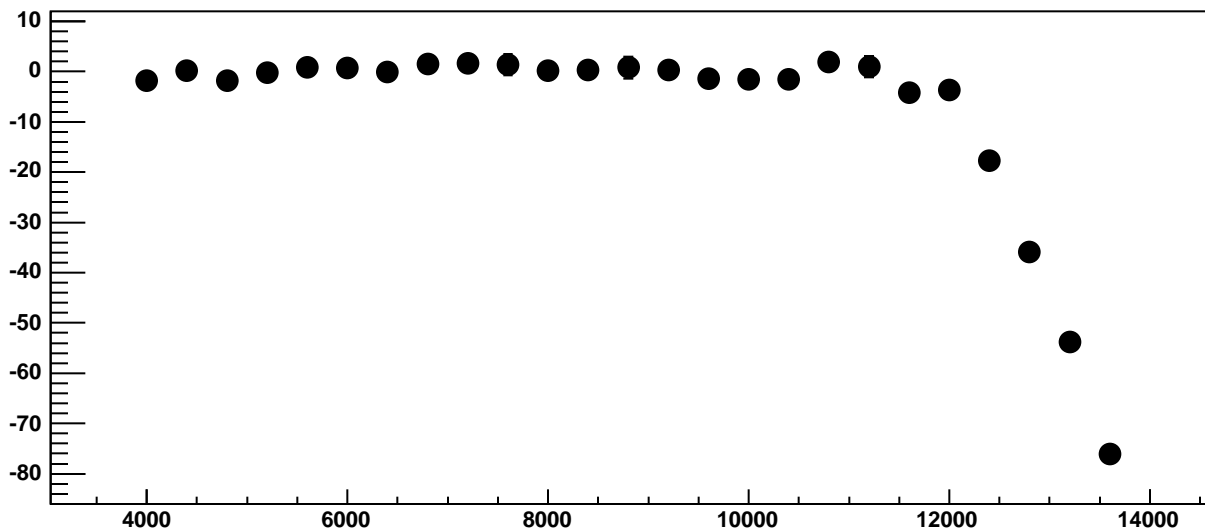
Chip 4, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



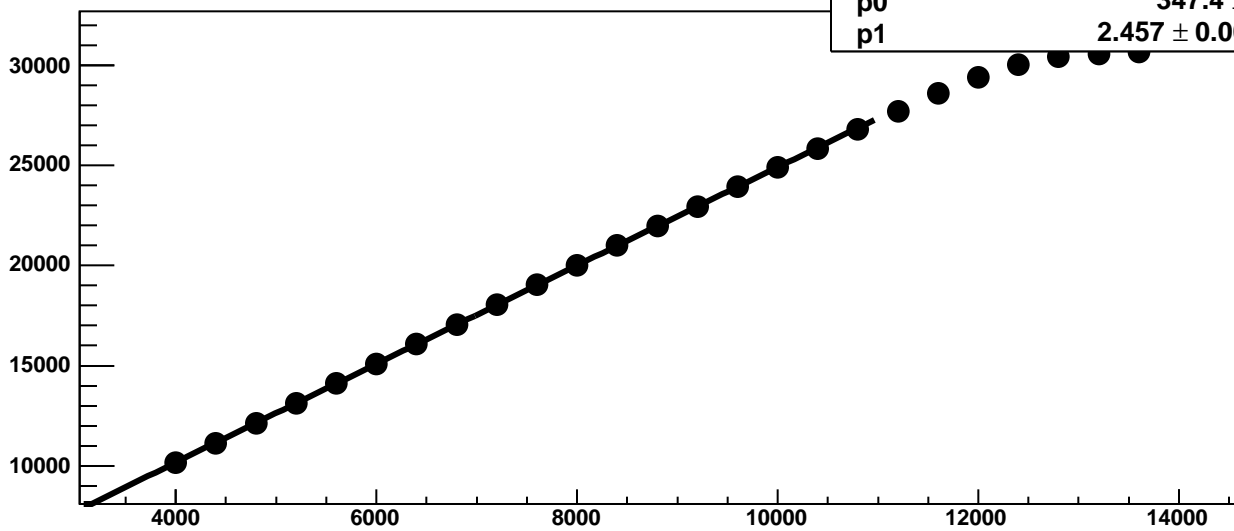
Chip 4, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

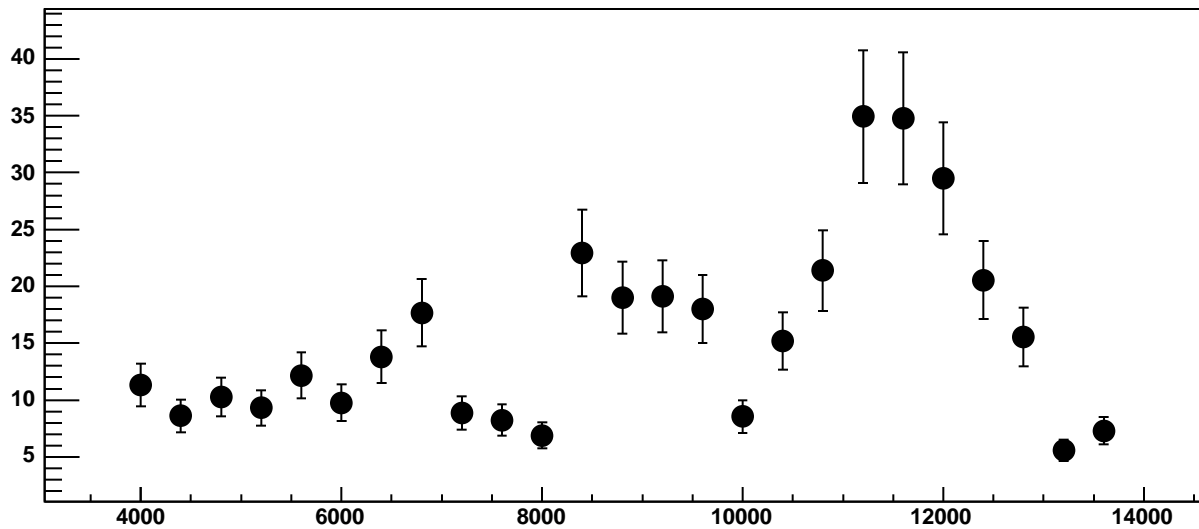


Chip 4, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC

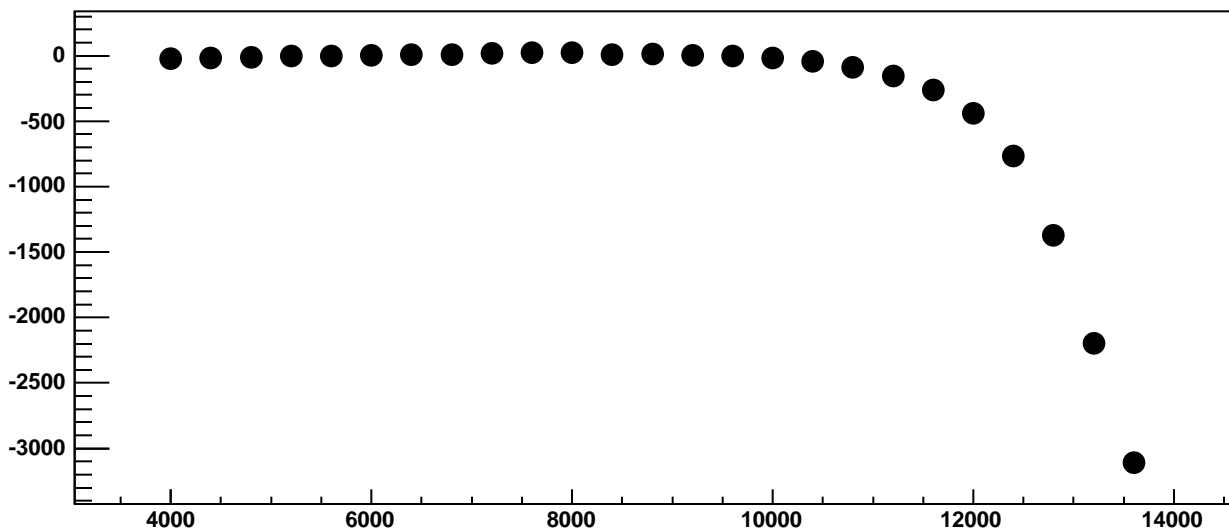


χ^2 / ndf 1177 / 16
p0 347.4 ± 2.237
p1 2.457 ± 0.0003096

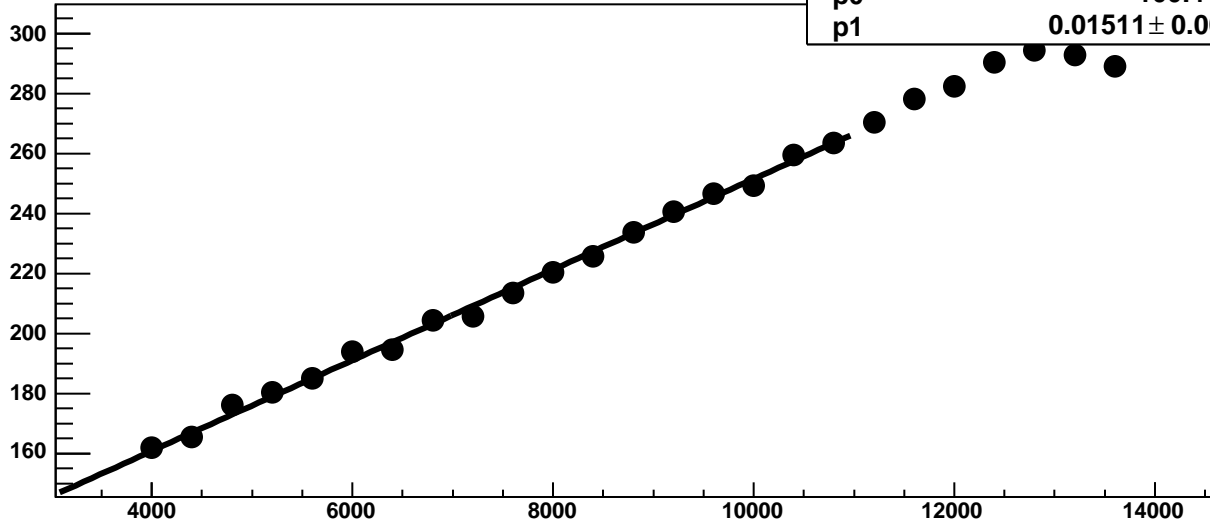
Chip 4, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



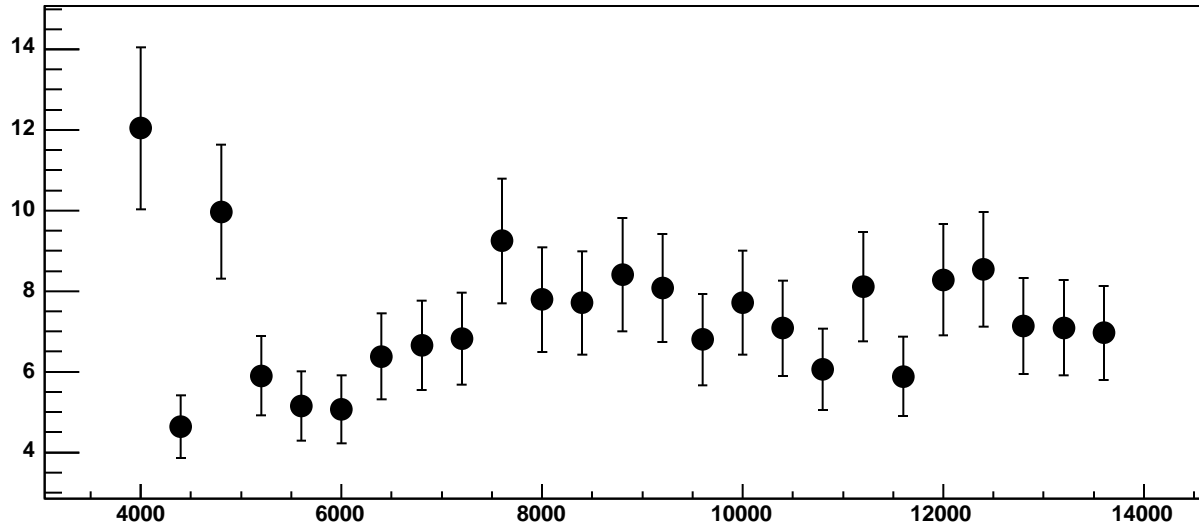
Chip 4, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC



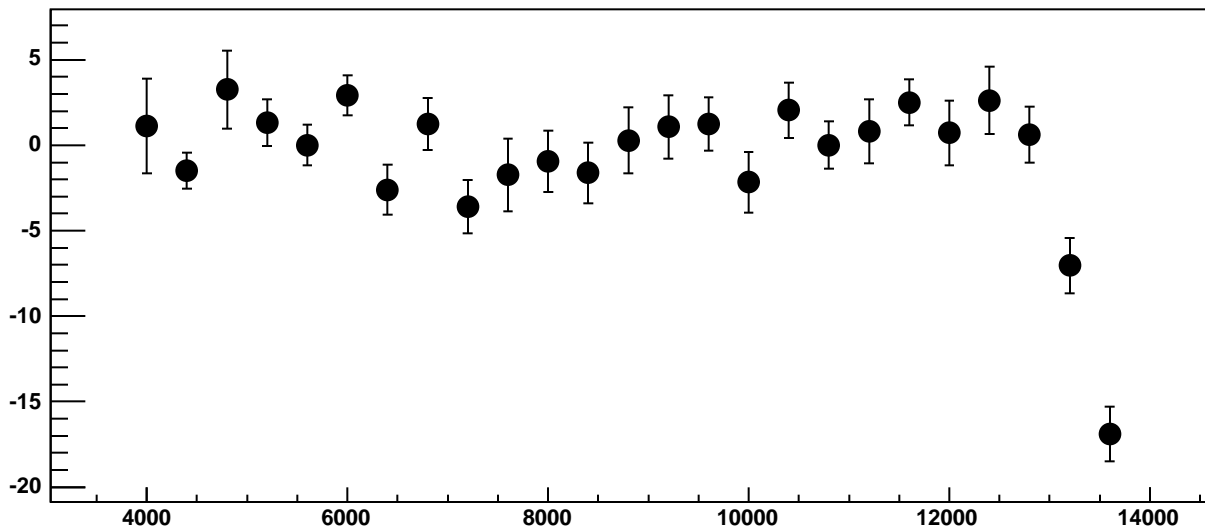
Chip 4, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



Chip 4, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

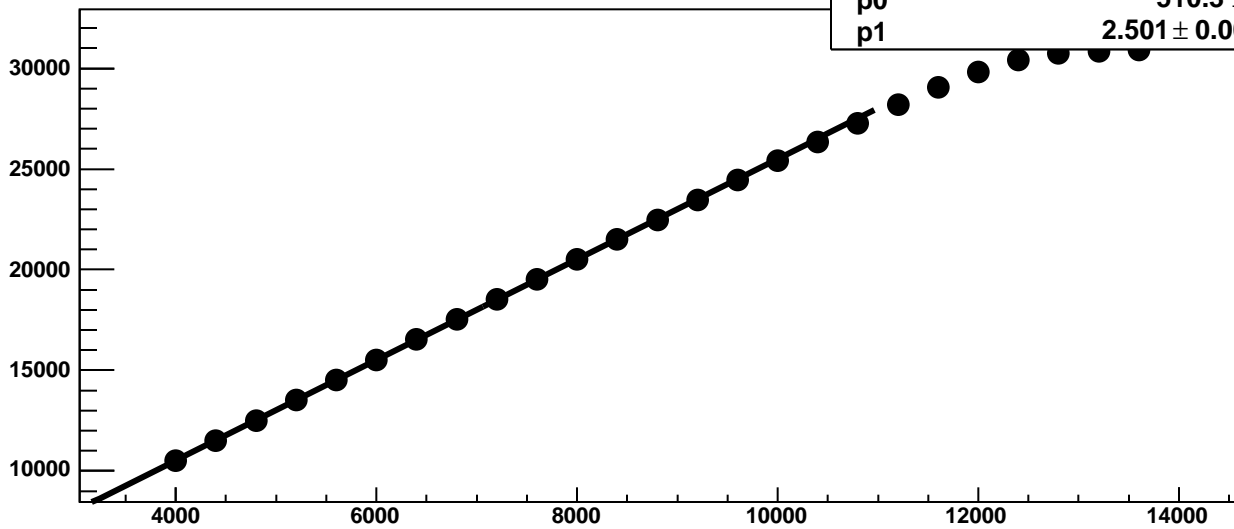


Chip 4, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

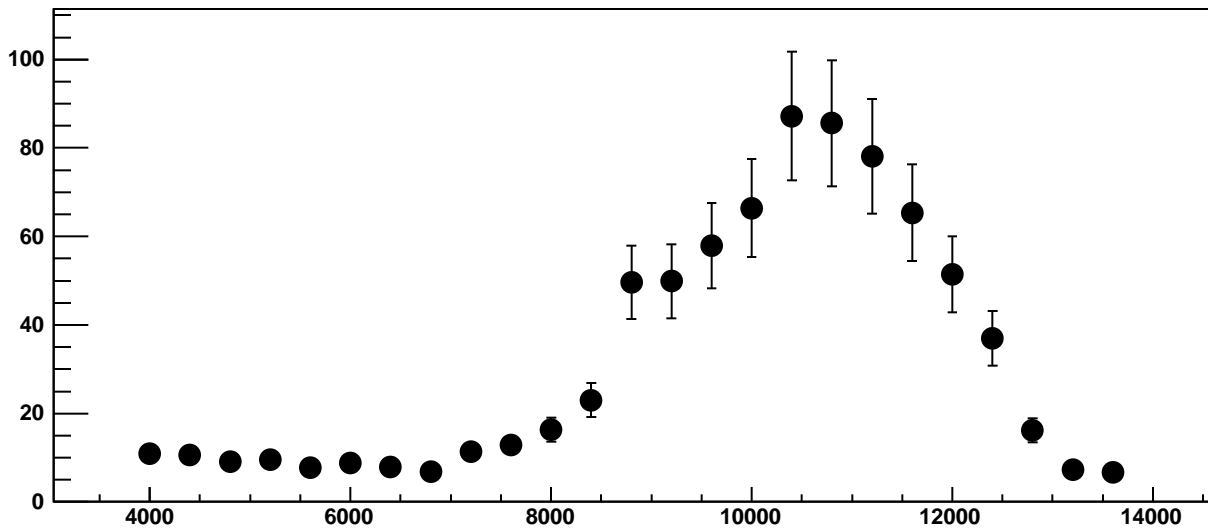


Chip 4, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC

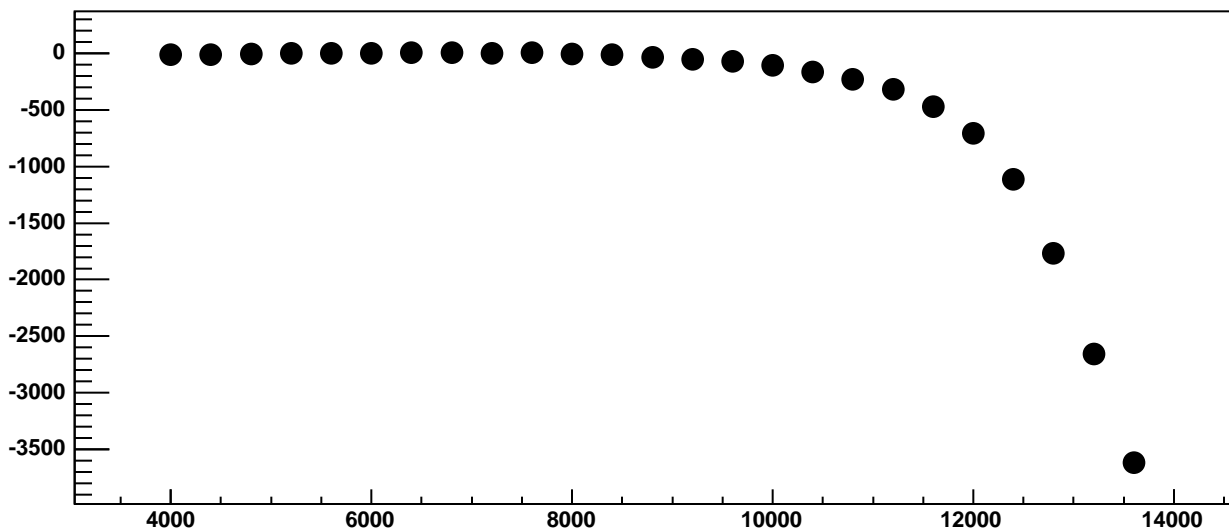
χ^2 / ndf 393.4 / 16
p0 510.3 ± 3.375
p1 2.501 ± 0.0005527



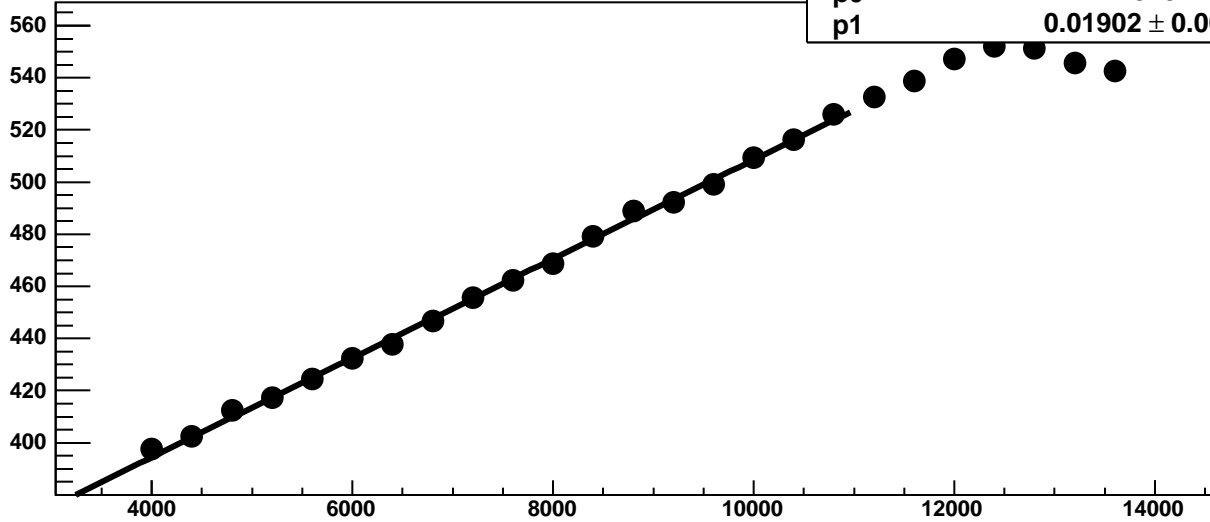
Chip 4, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



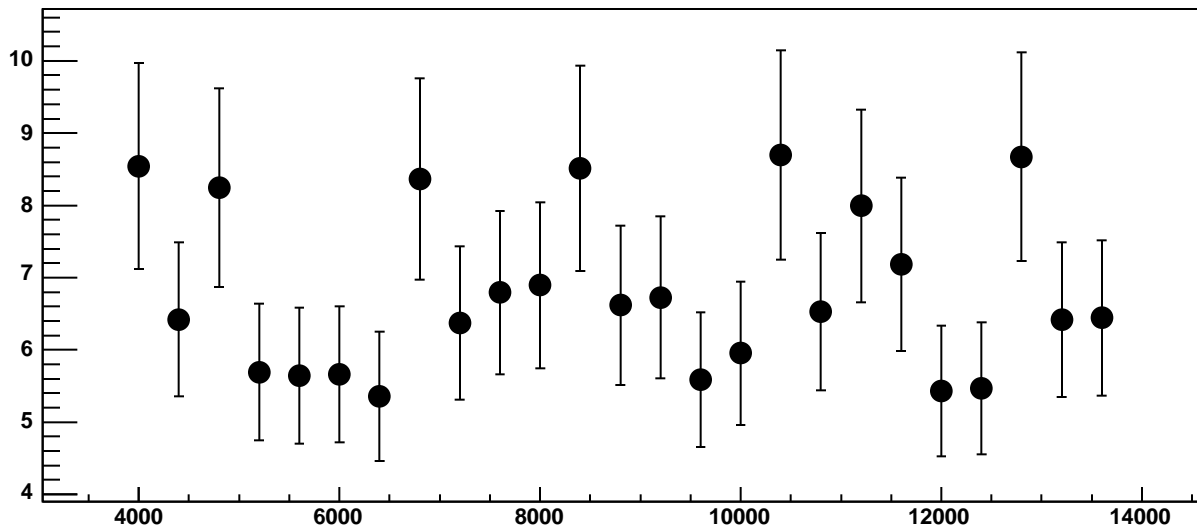
Chip 4, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC



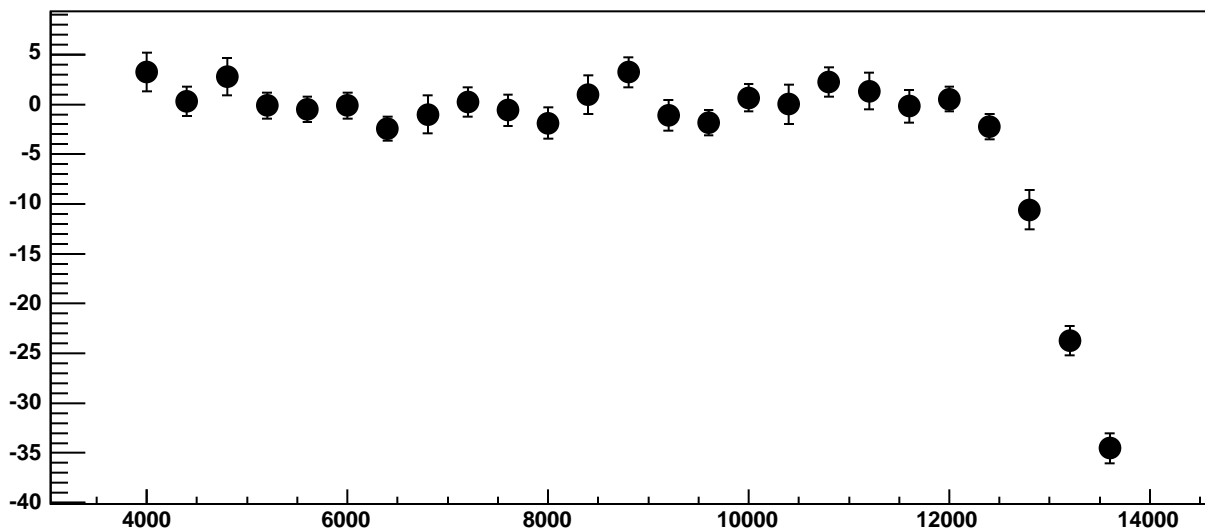
Chip 4, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



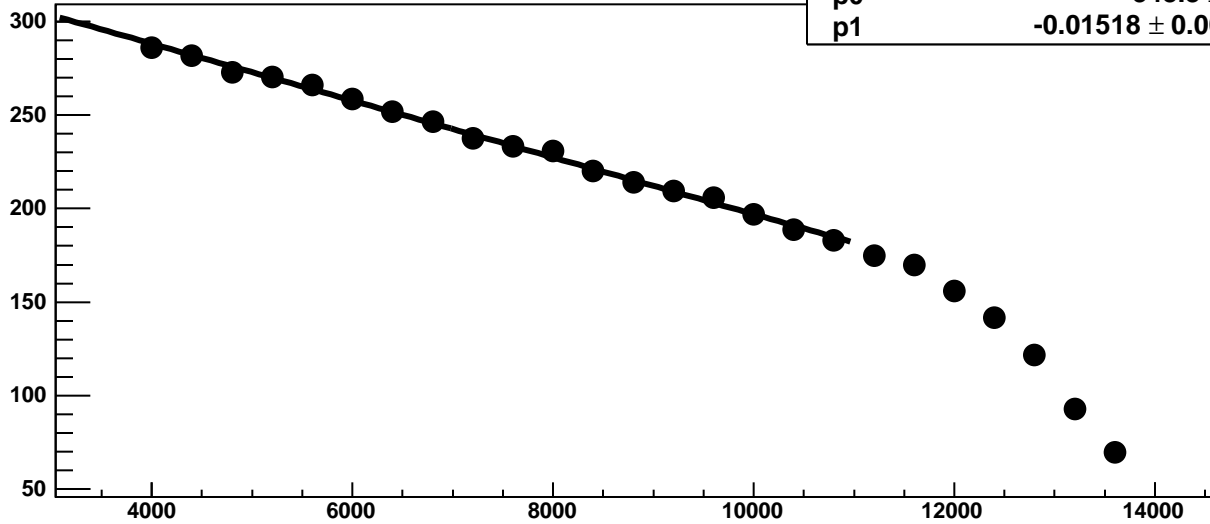
Chip 4, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

21.28 / 16

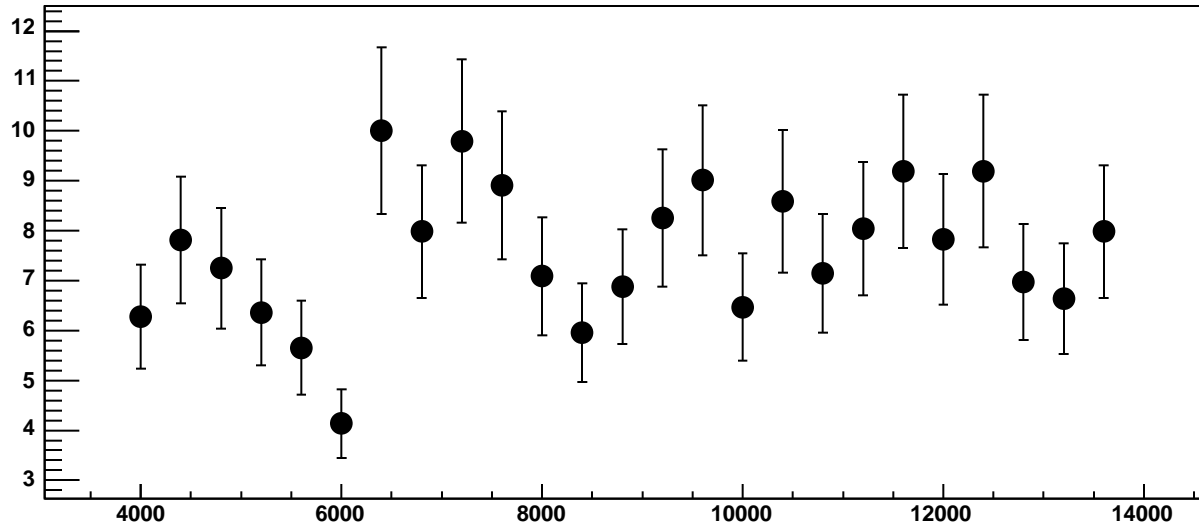
p0

348.8 ± 1.347

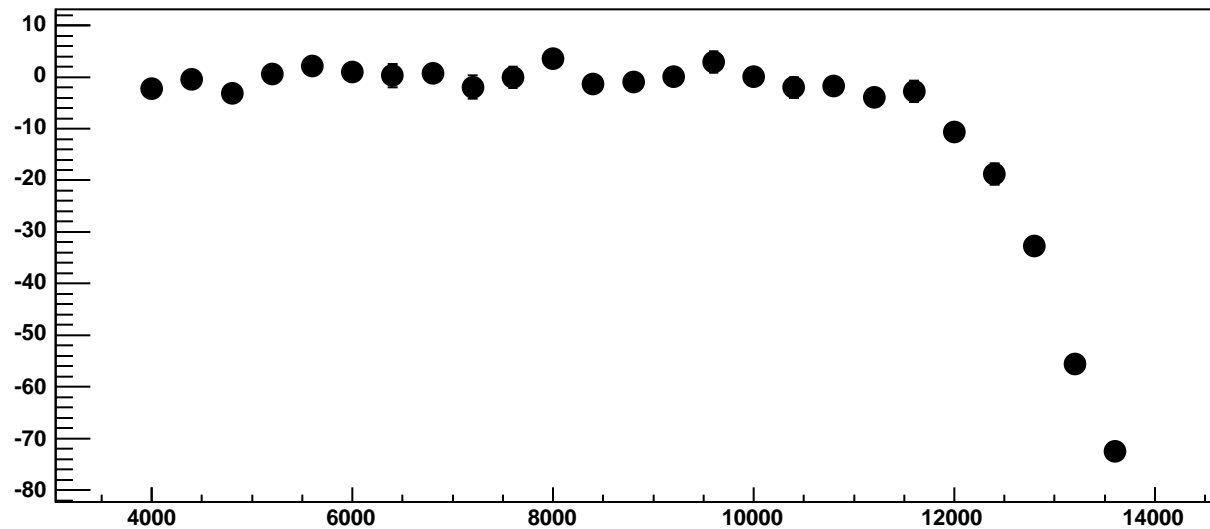
p1

-0.01518 ± 0.0001818

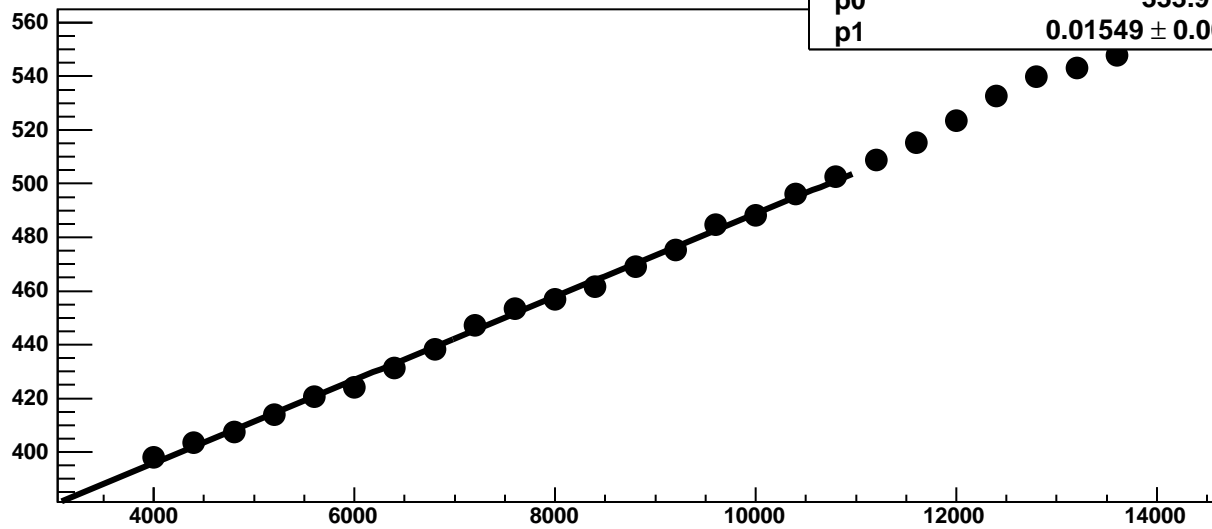
Chip 4, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

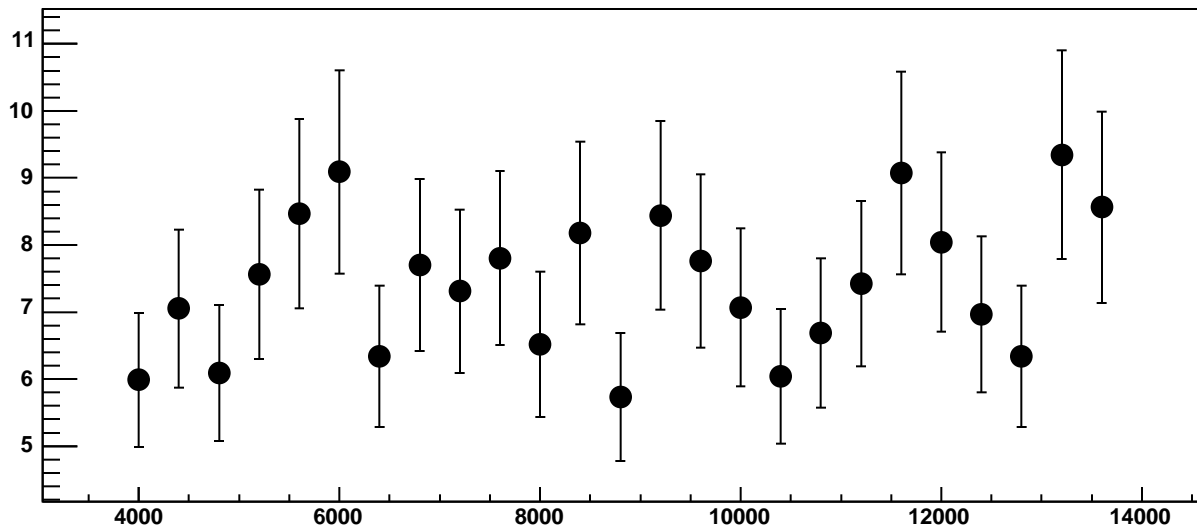


Chip 4, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

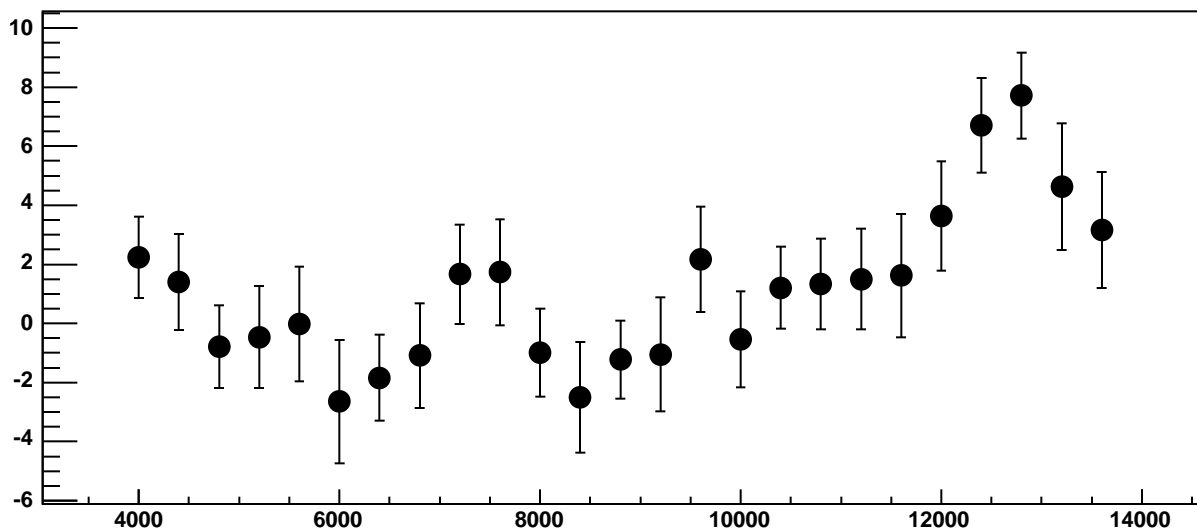


χ^2 / ndf 15.77 / 16
p0 333.9 ± 1.35
p1 0.01549 ± 0.0001749

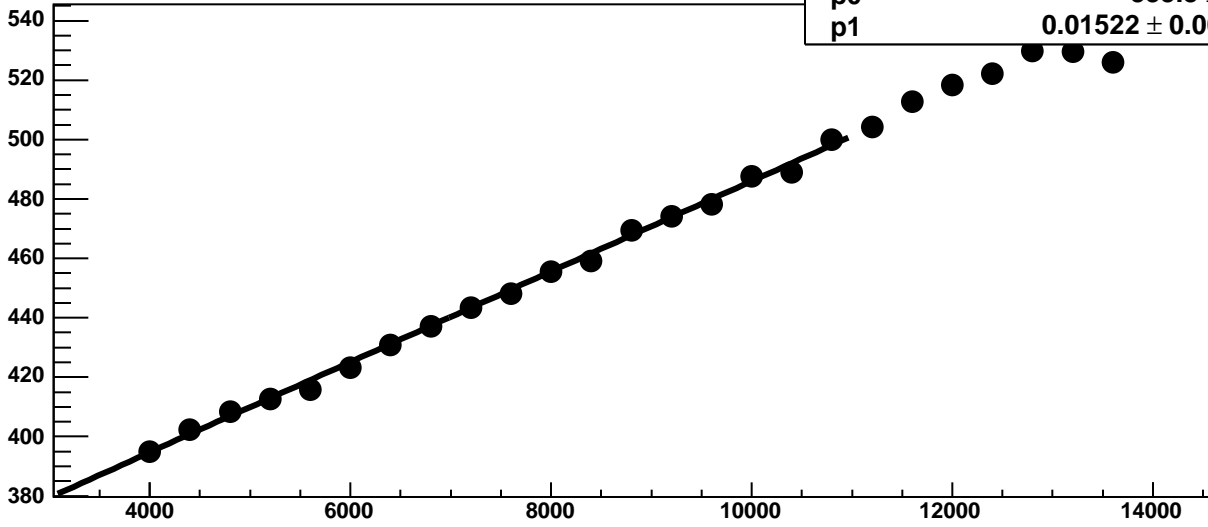
Chip 4, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



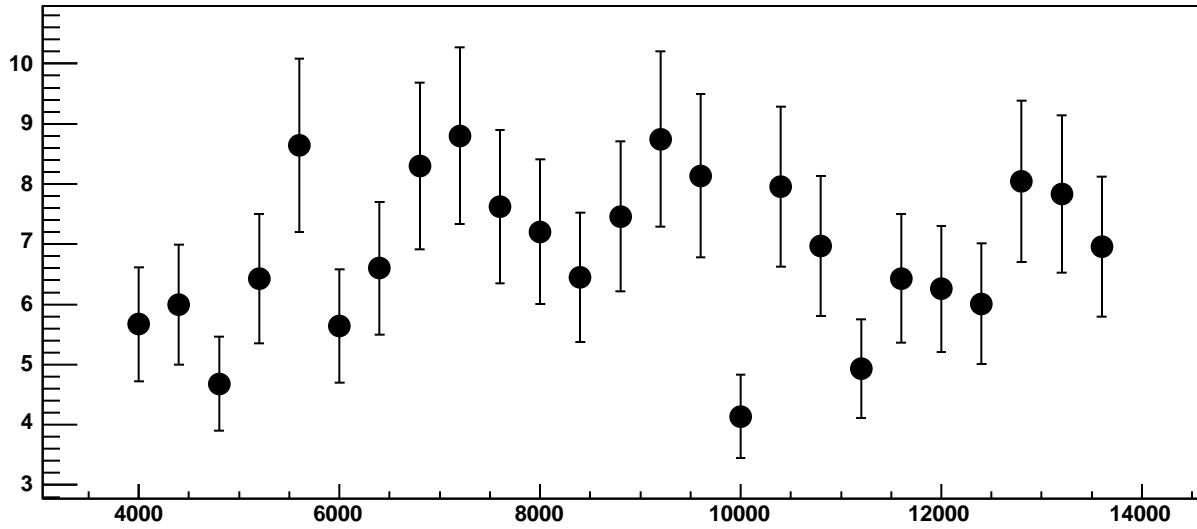
Chip 4, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC



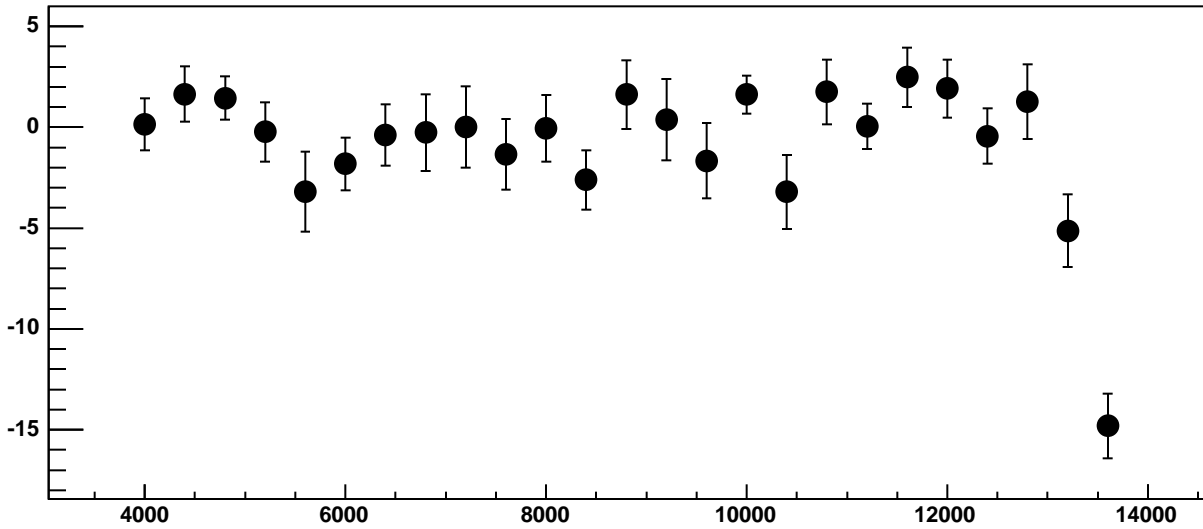
Chip 4, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



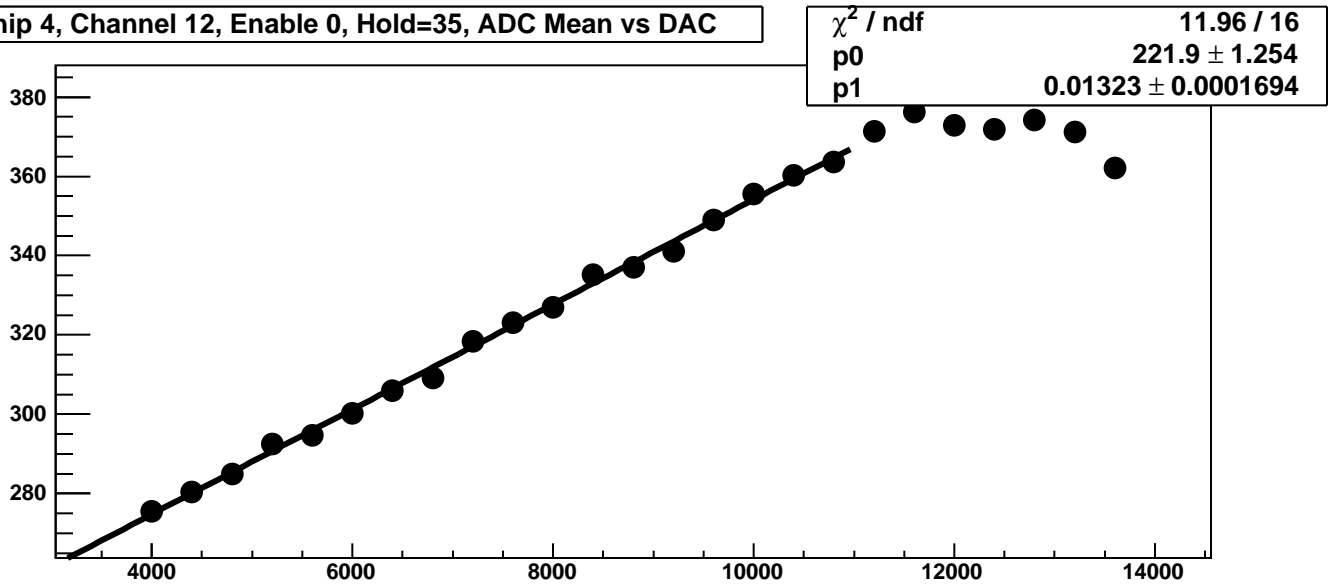
Chip 4, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



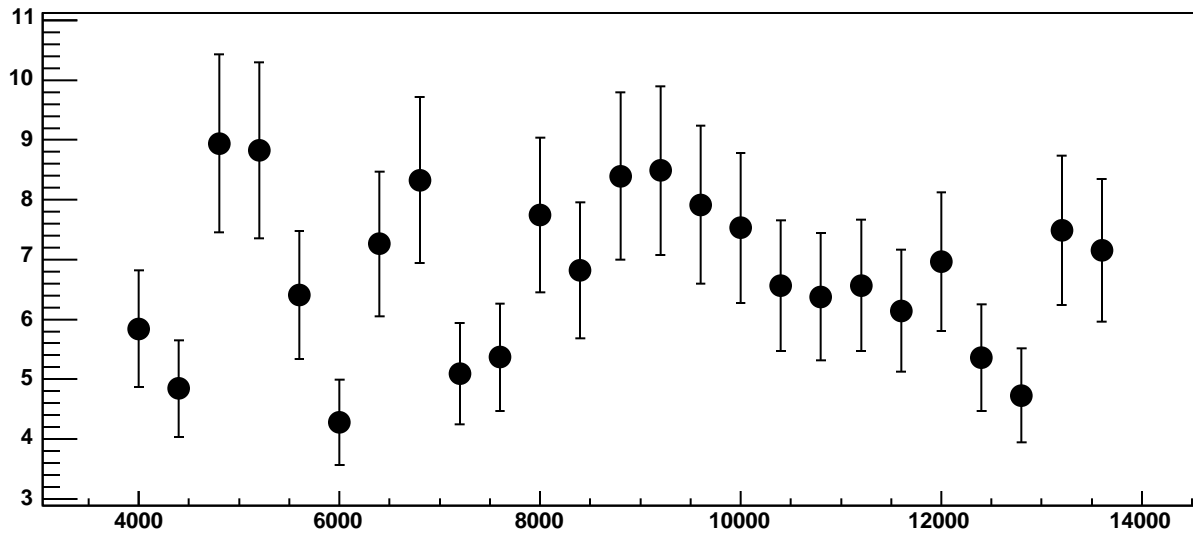
Chip 4, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



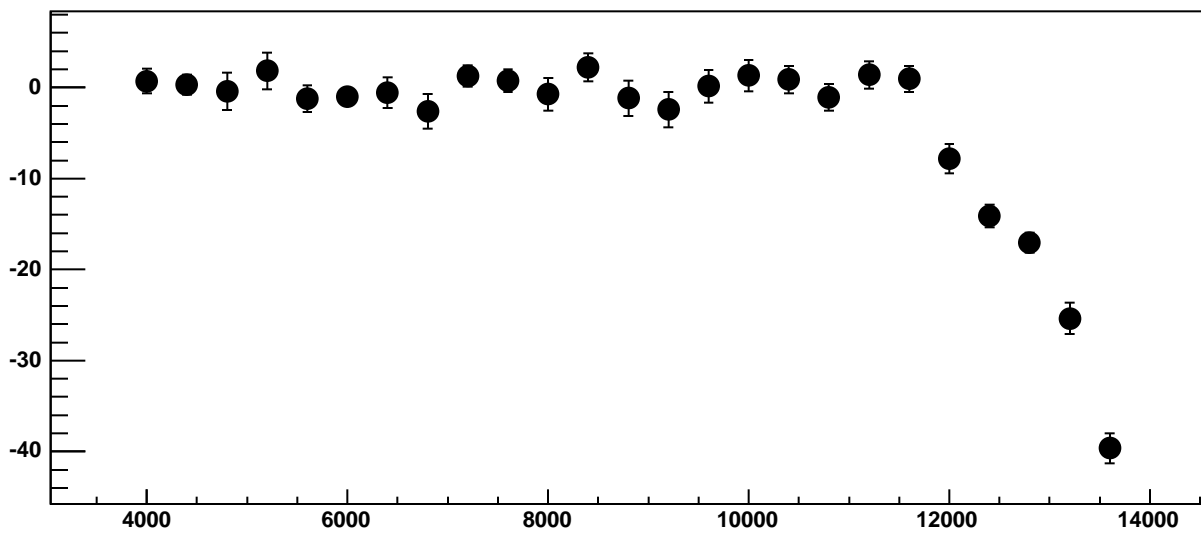
Chip 4, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



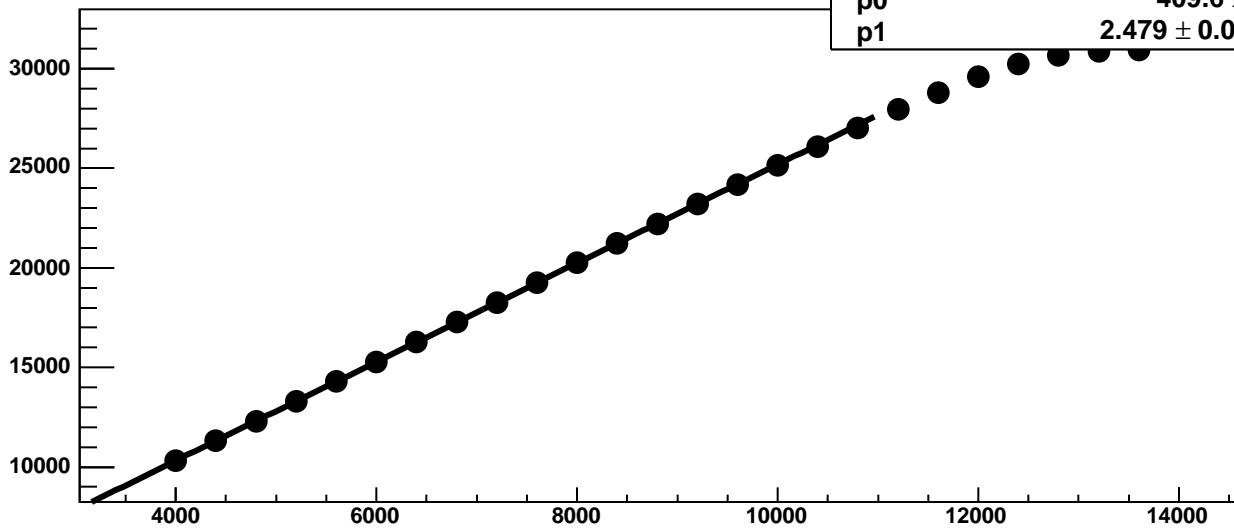
Chip 4, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



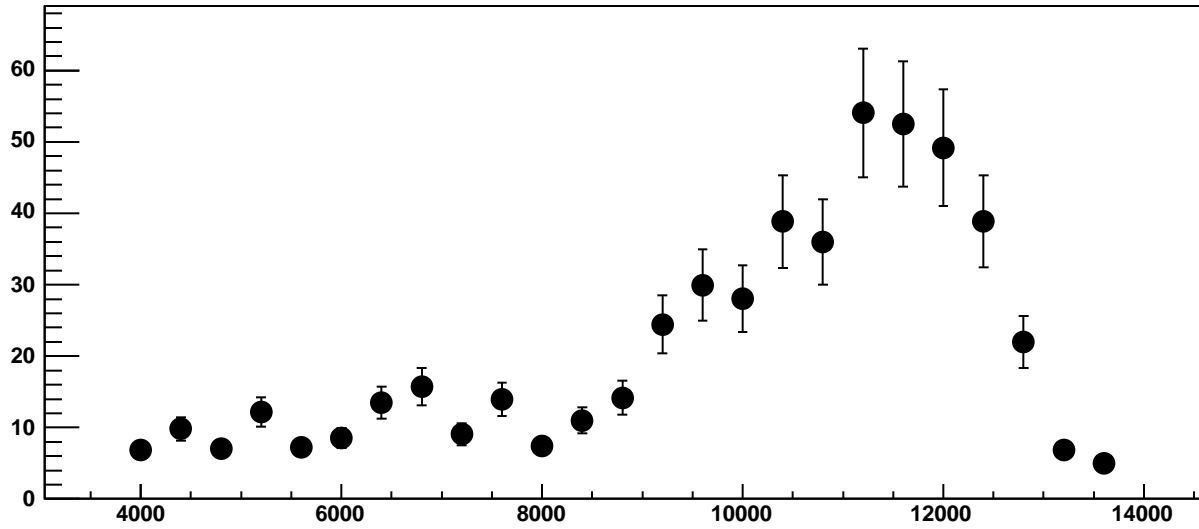
Chip 4, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



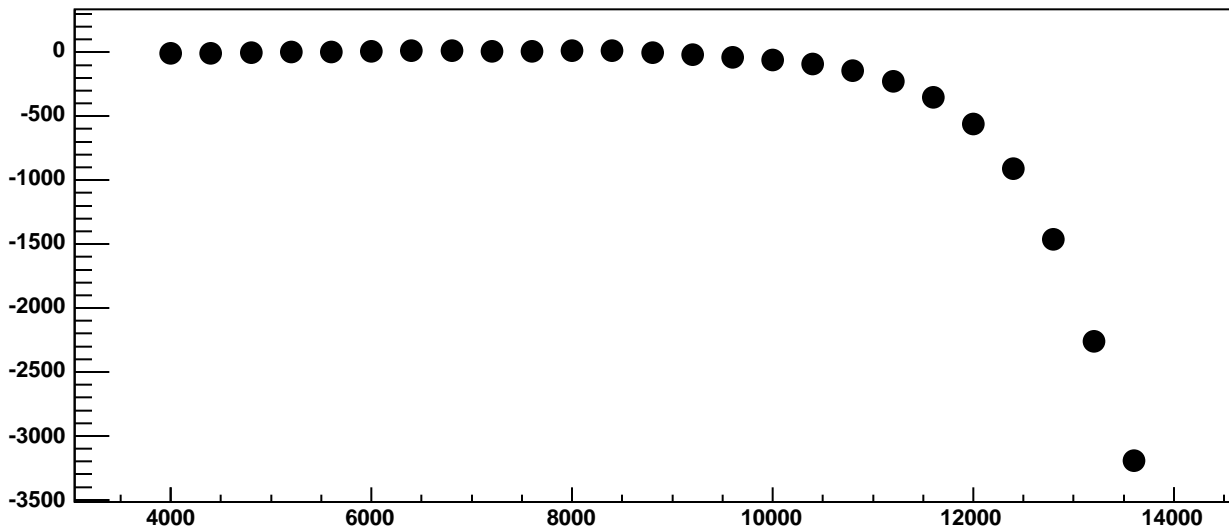
Chip 4, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC



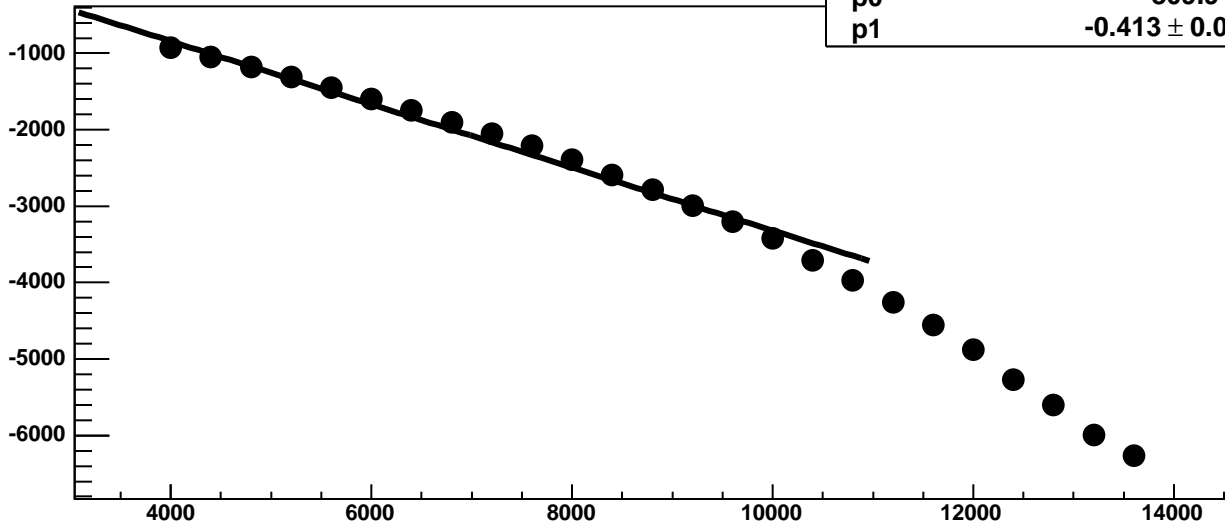
Chip 4, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

6955 / 16

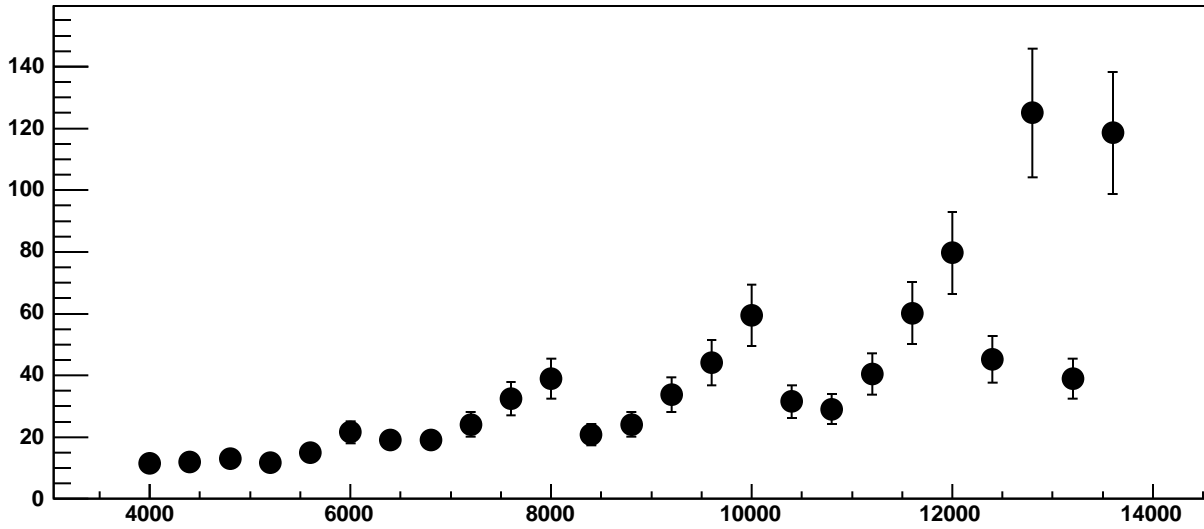
p0

809.9 ± 3.453

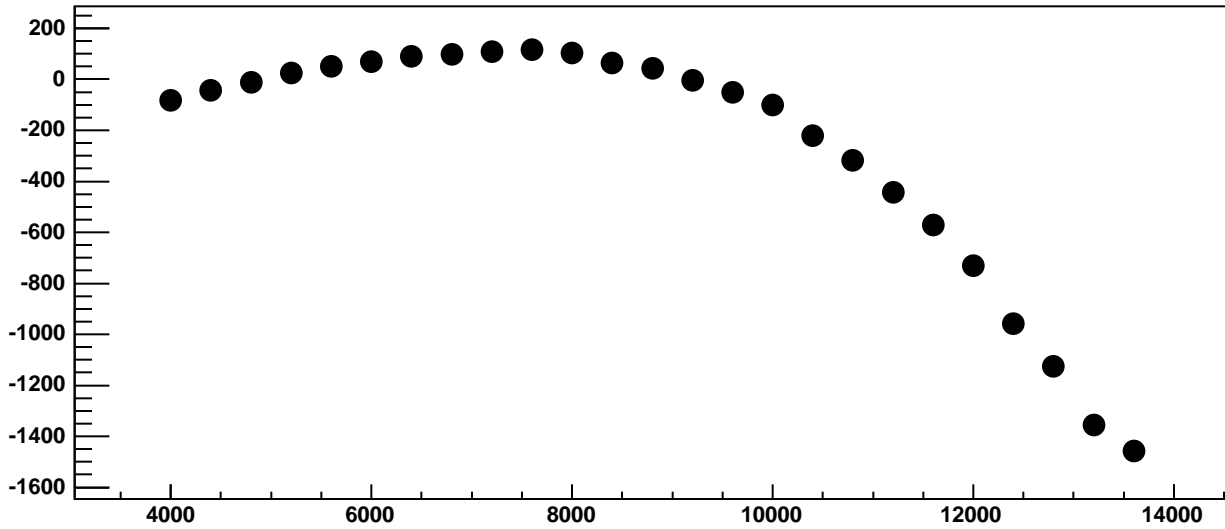
p1

-0.413 ± 0.0005638

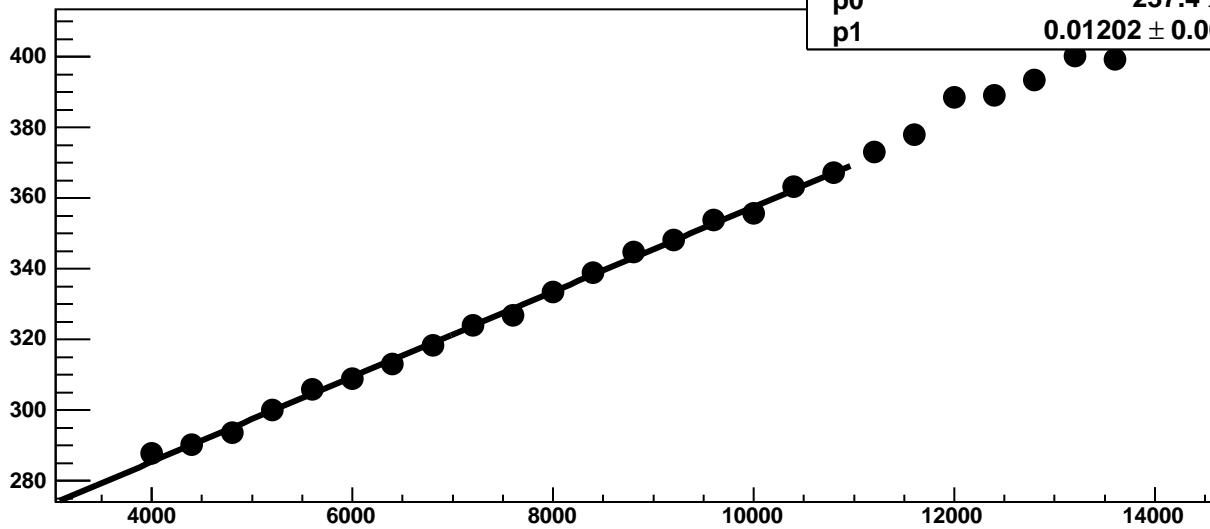
Chip 4, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



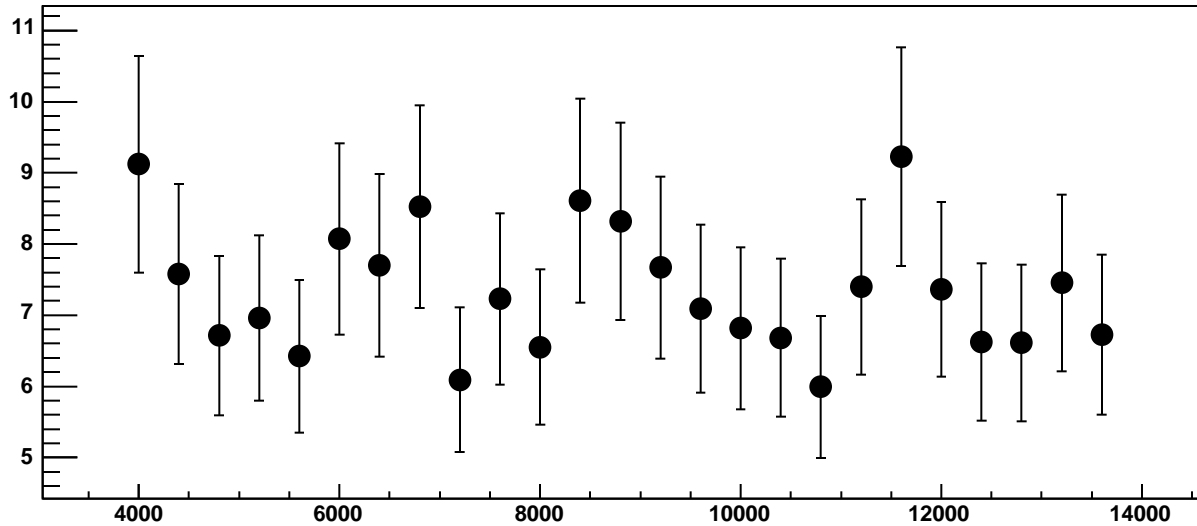
Chip 4, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



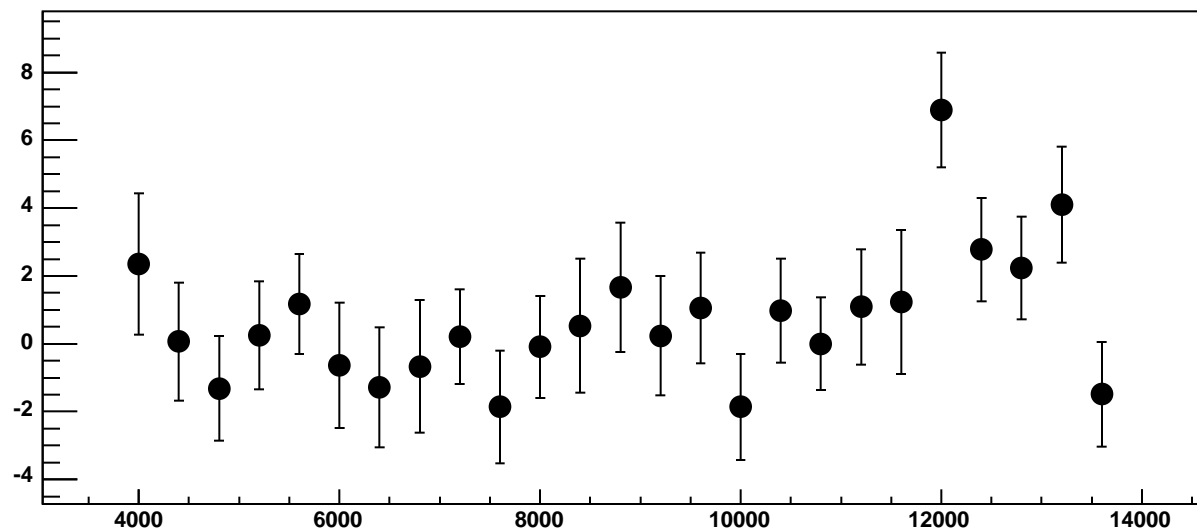
Chip 4, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



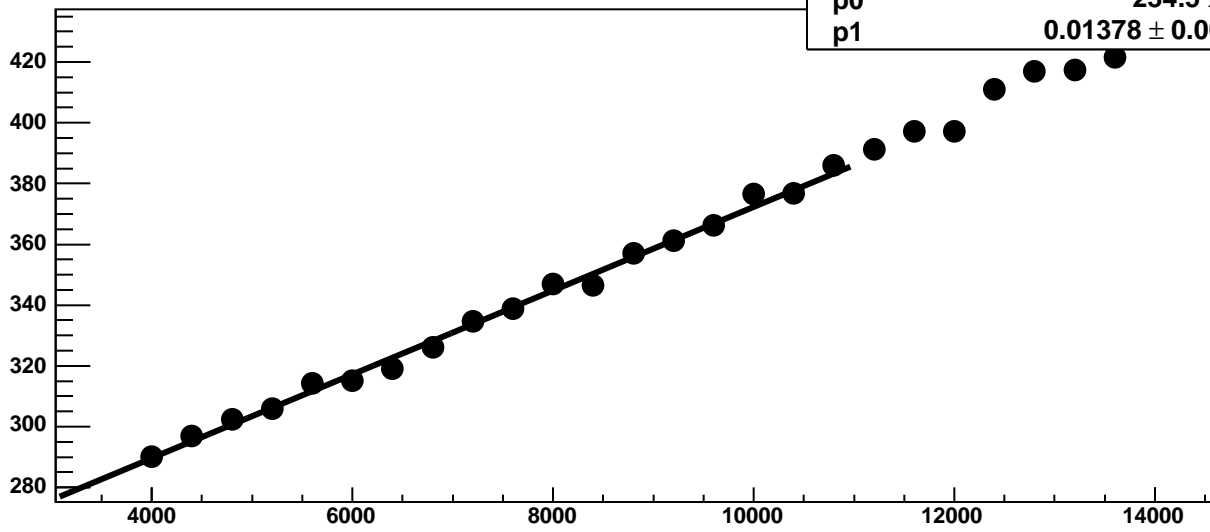
Chip 4, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



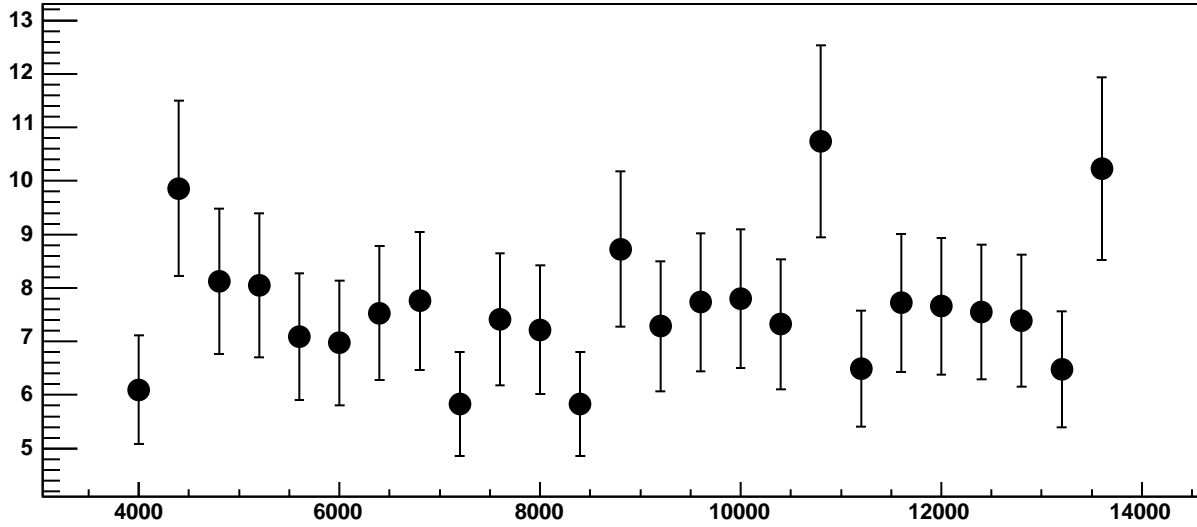
Chip 4, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



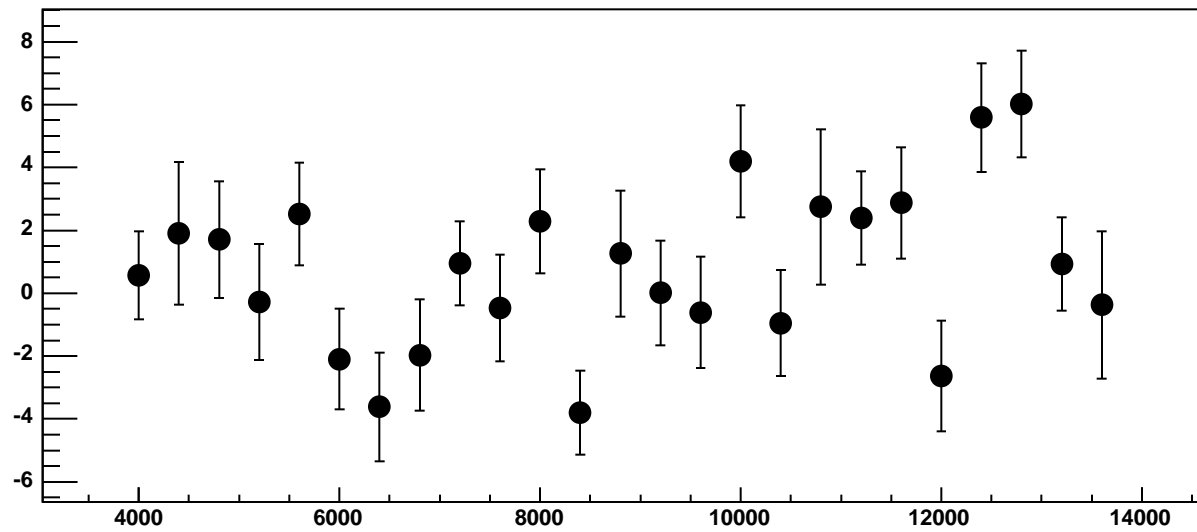
Chip 4, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC



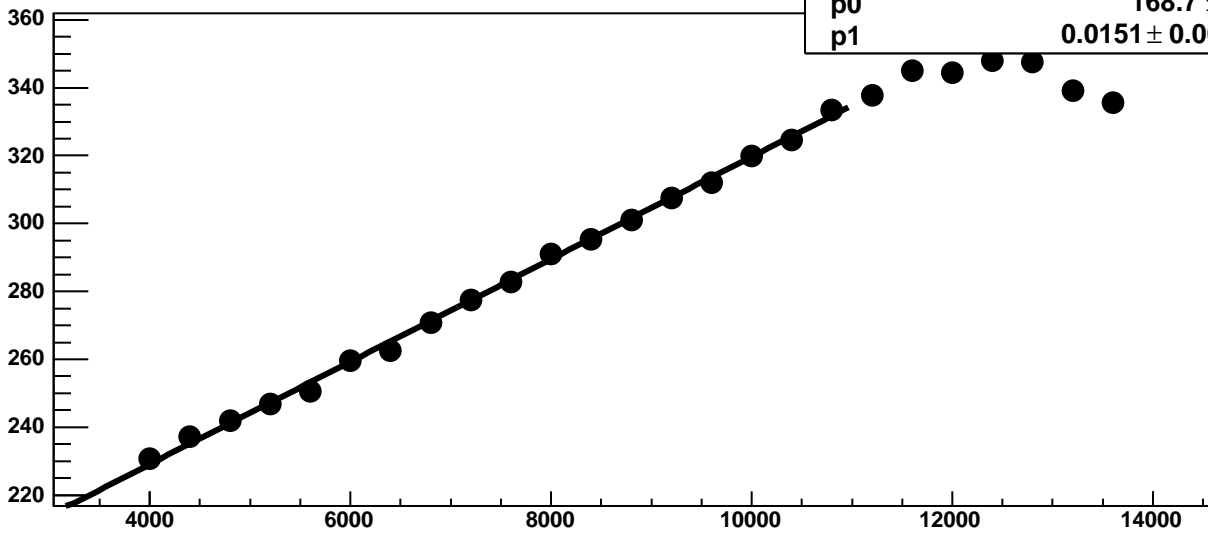
Chip 4, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



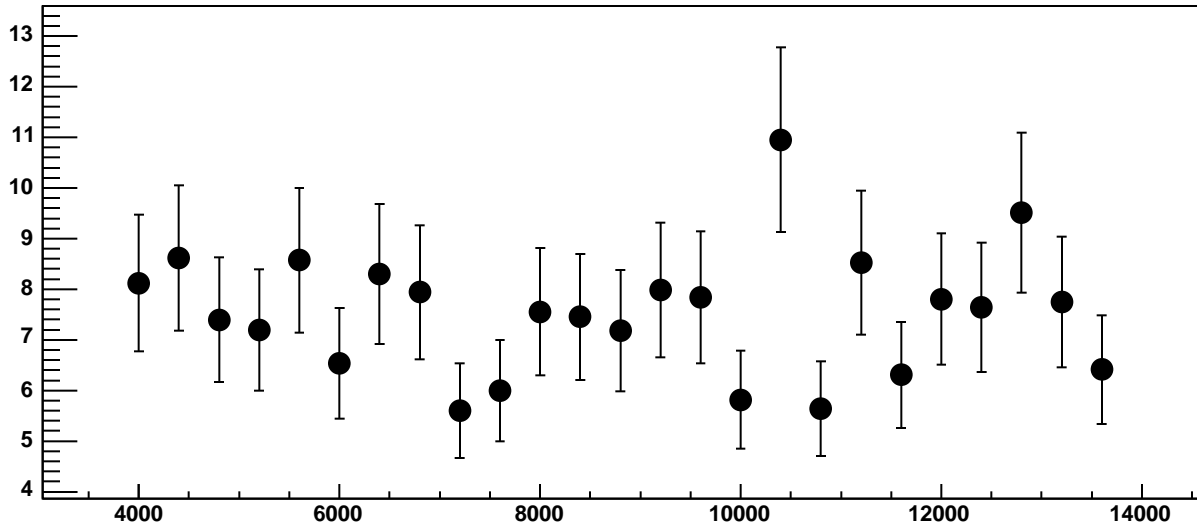
Chip 4, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC



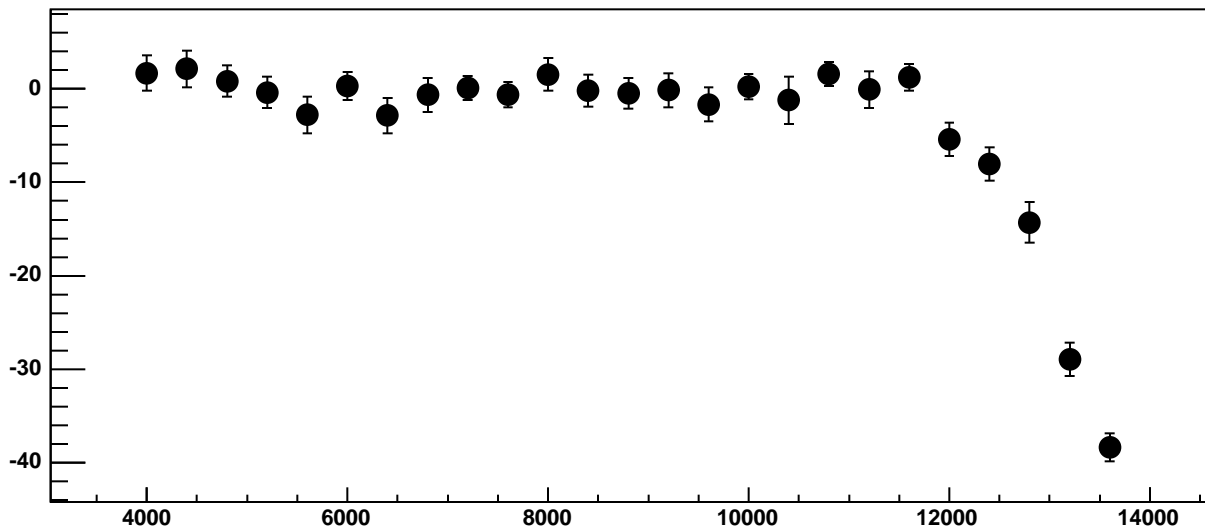
Chip 4, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



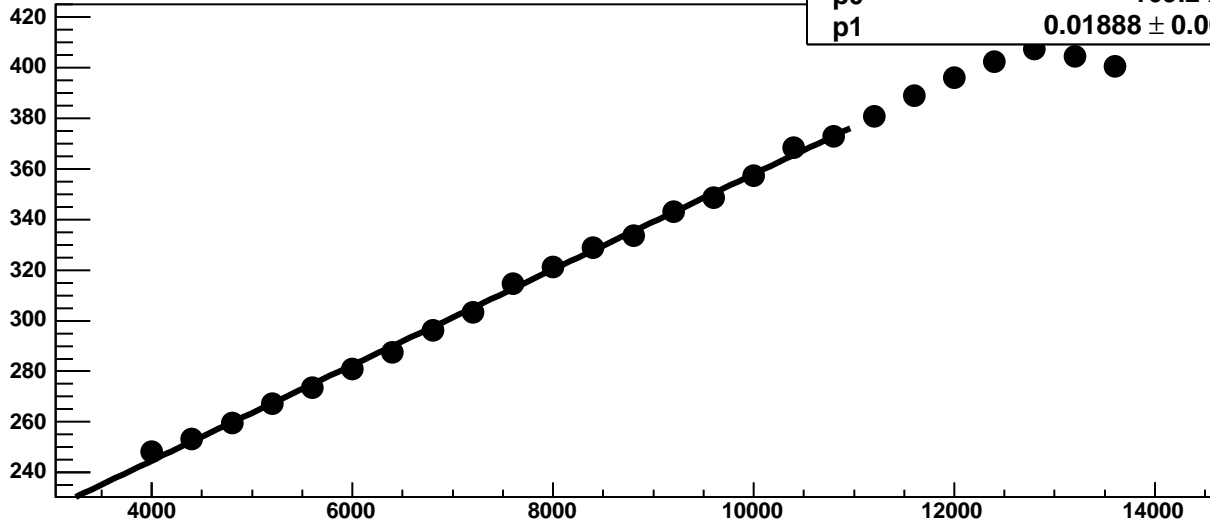
Chip 4, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



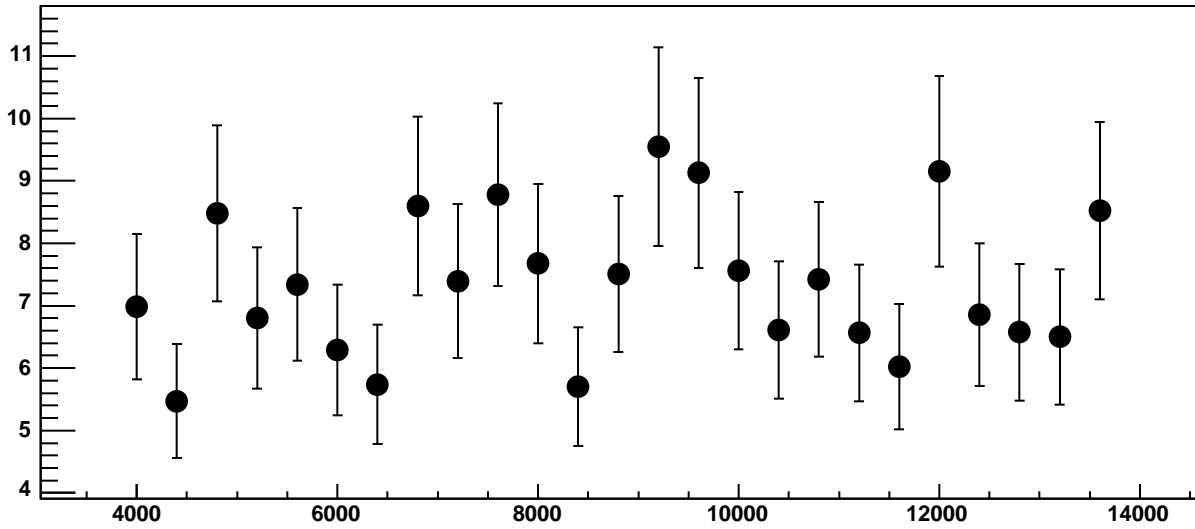
Chip 4, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



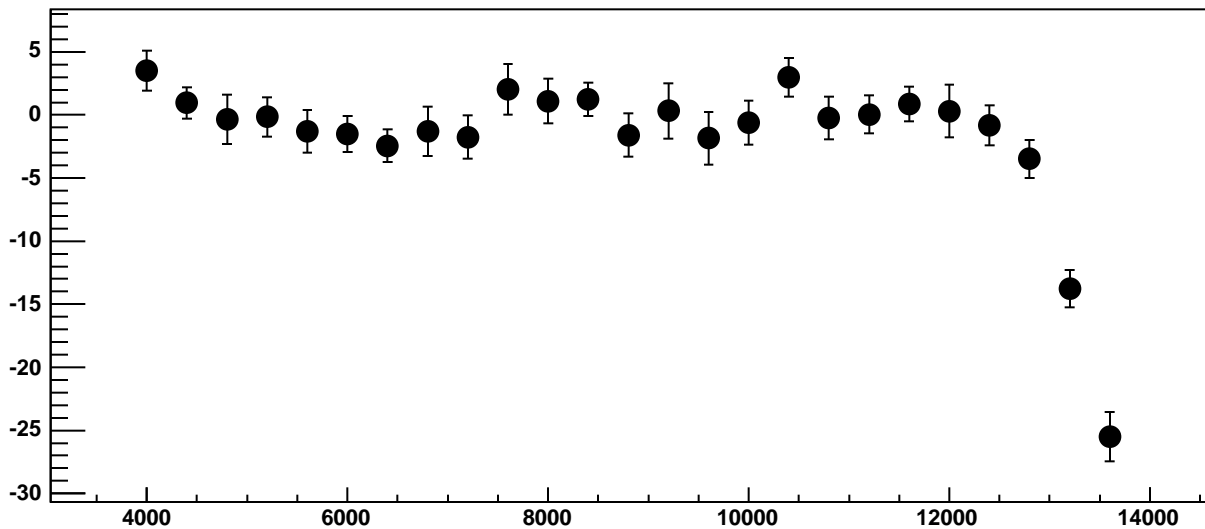
Chip 4, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC



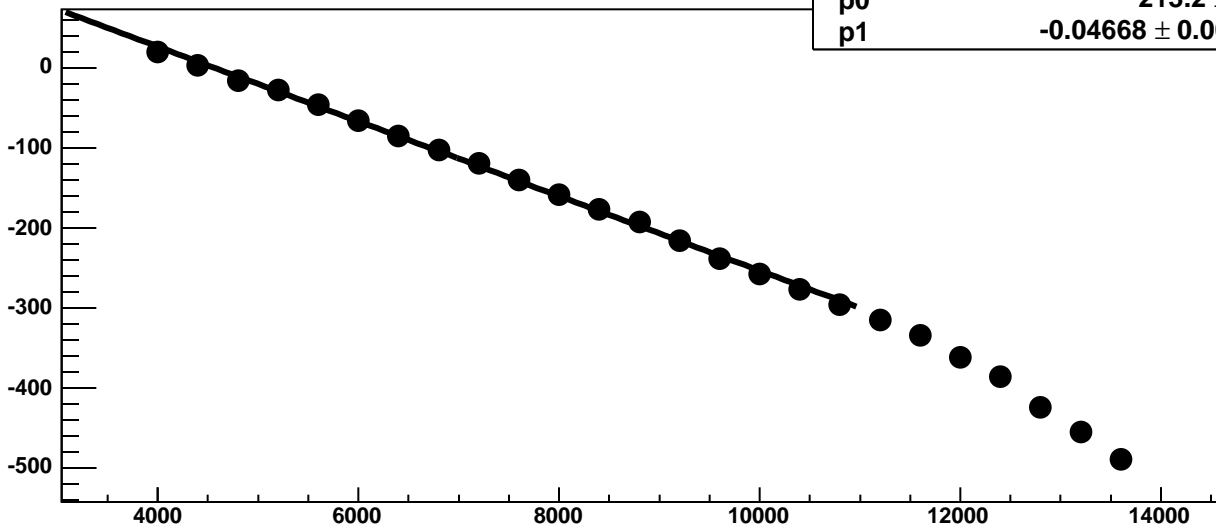
Chip 4, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC



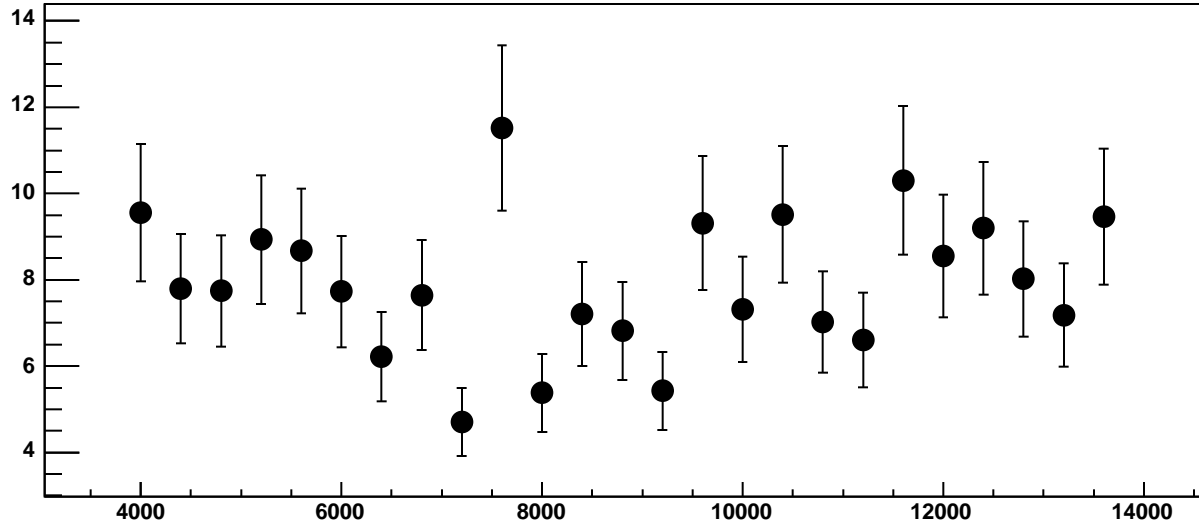
Chip 4, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC



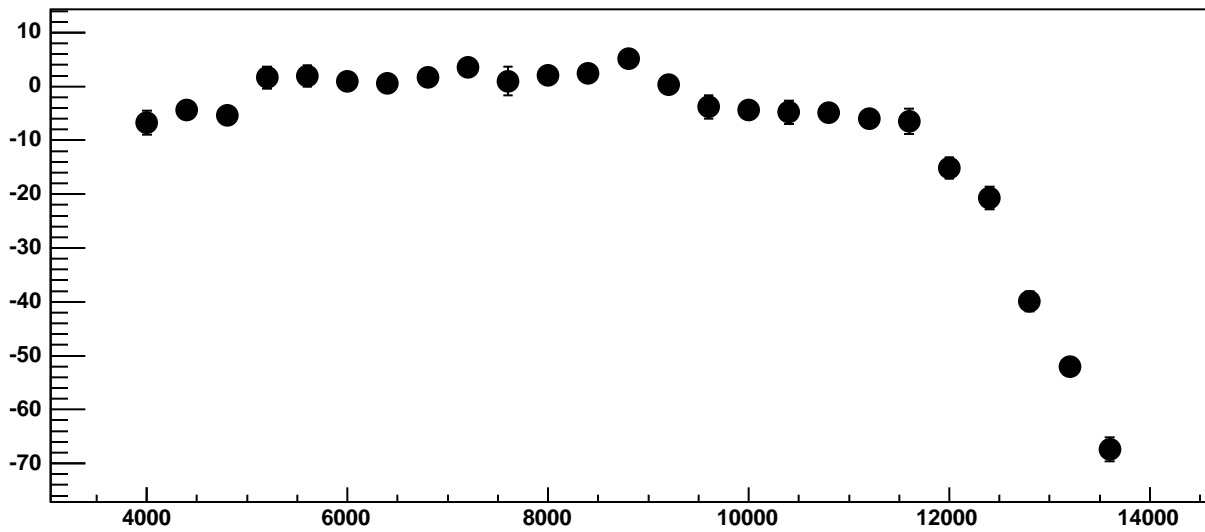
Chip 4, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



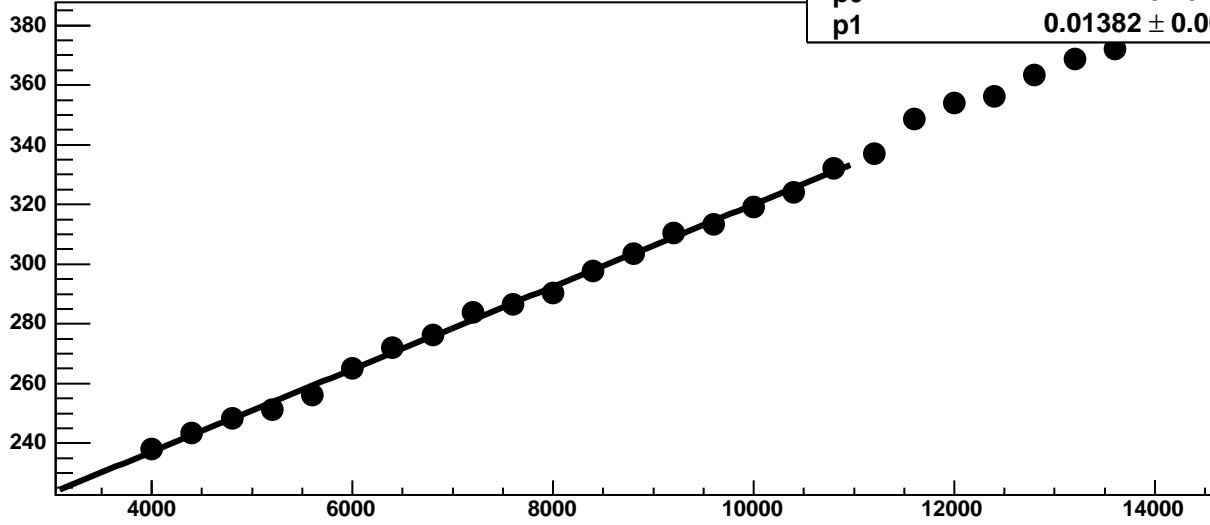
Chip 4, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



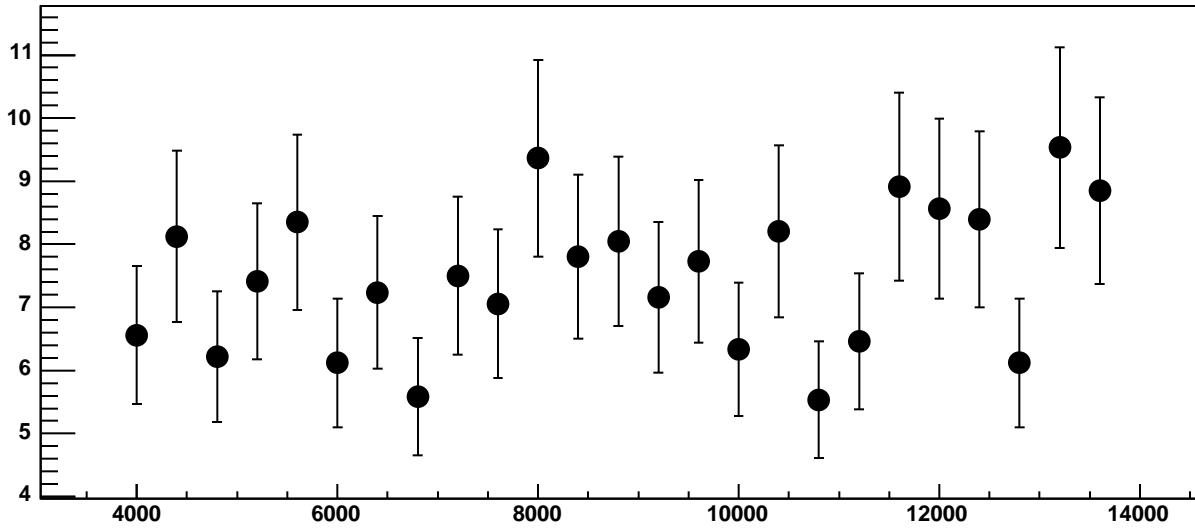
Chip 4, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



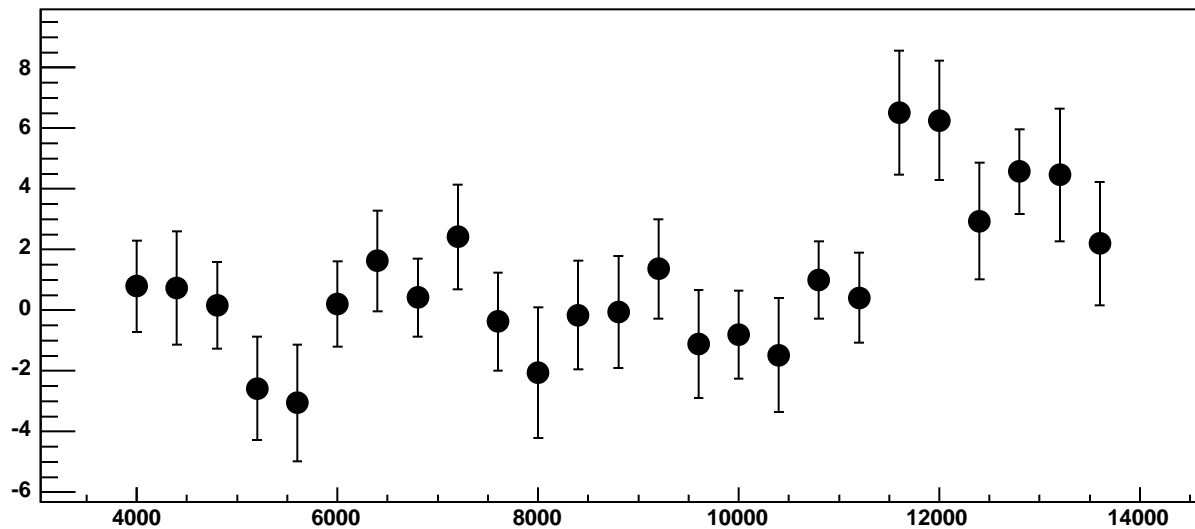
Chip 4, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC



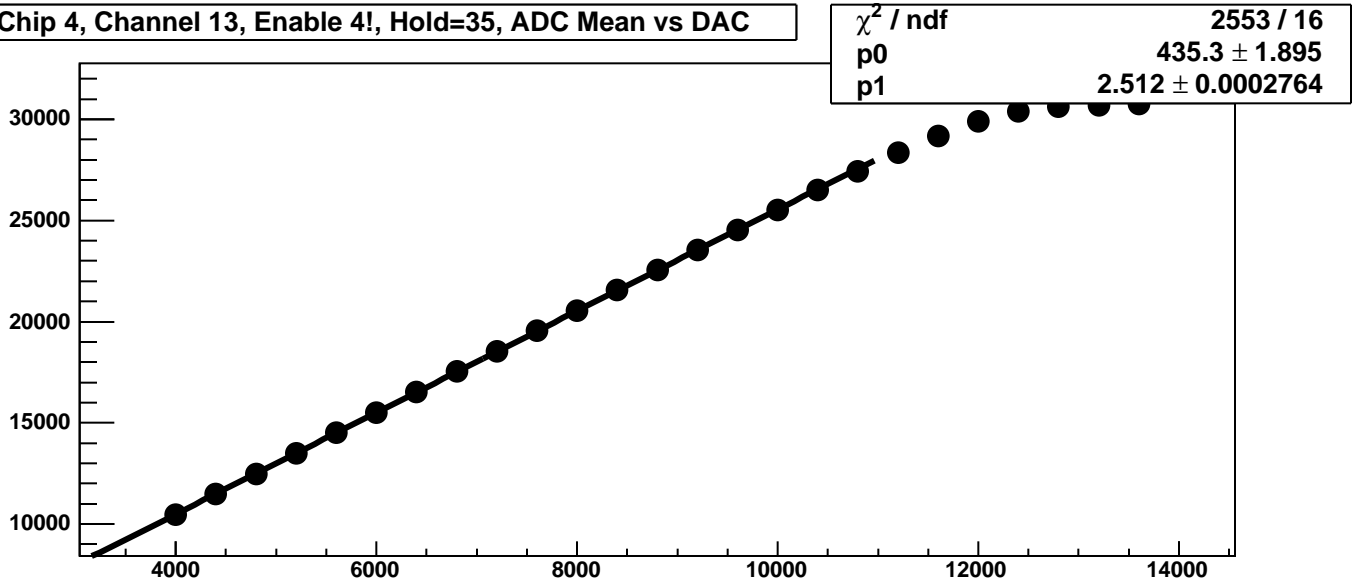
Chip 4, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



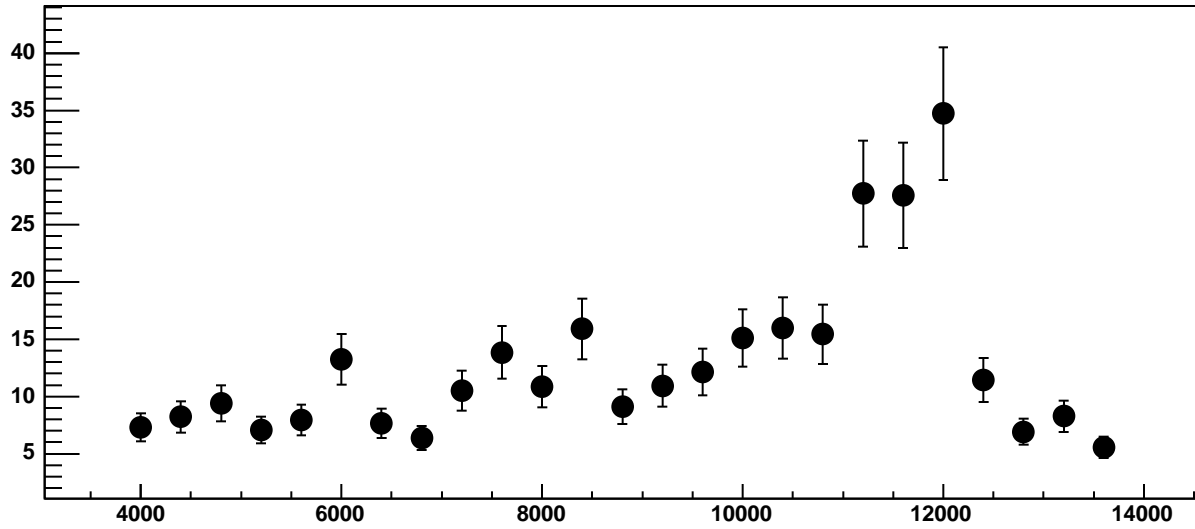
Chip 4, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



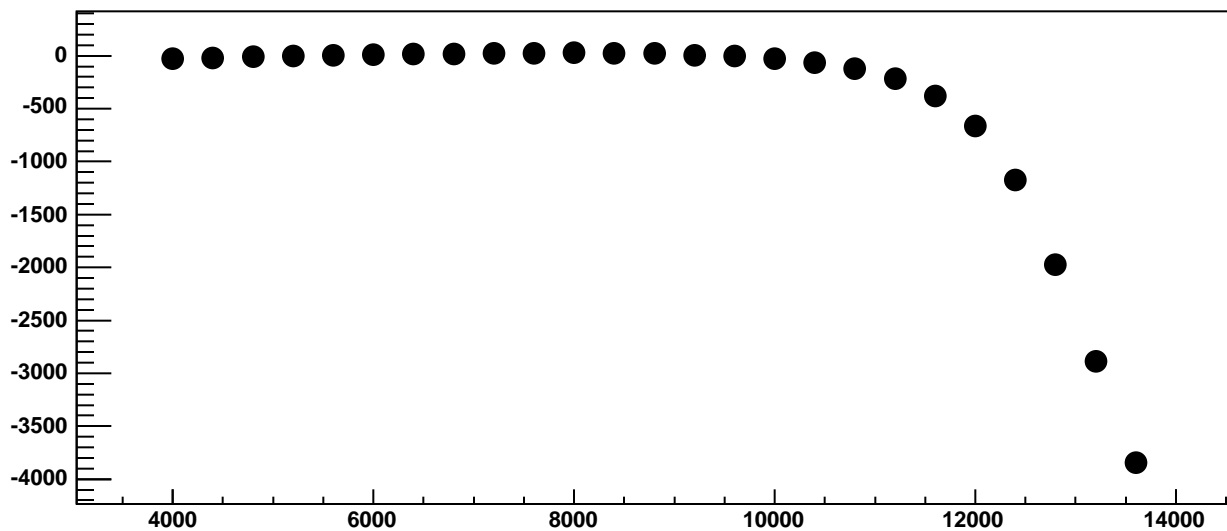
Chip 4, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC



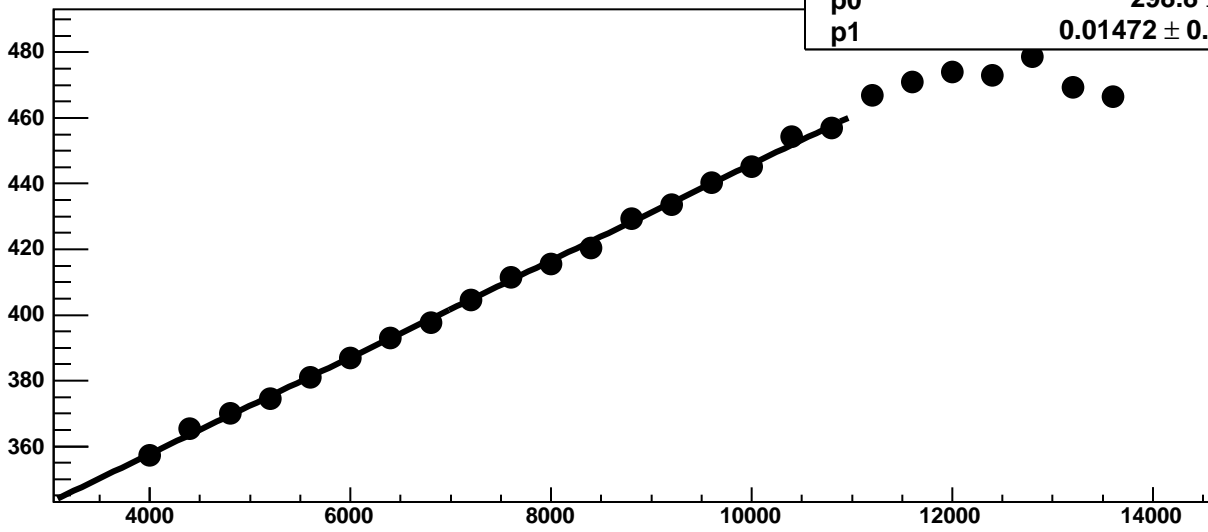
Chip 4, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC



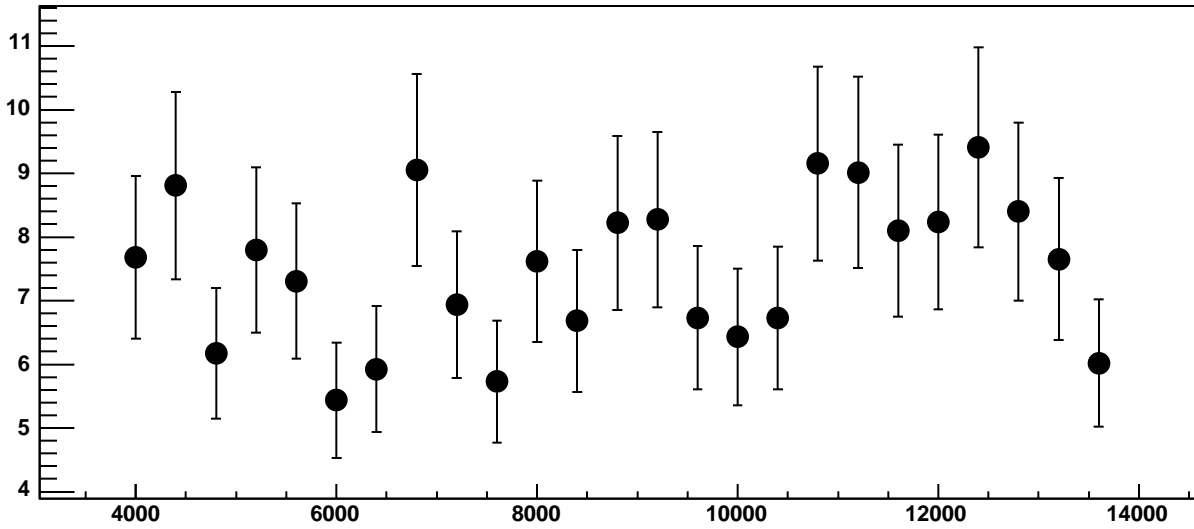
Chip 4, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC



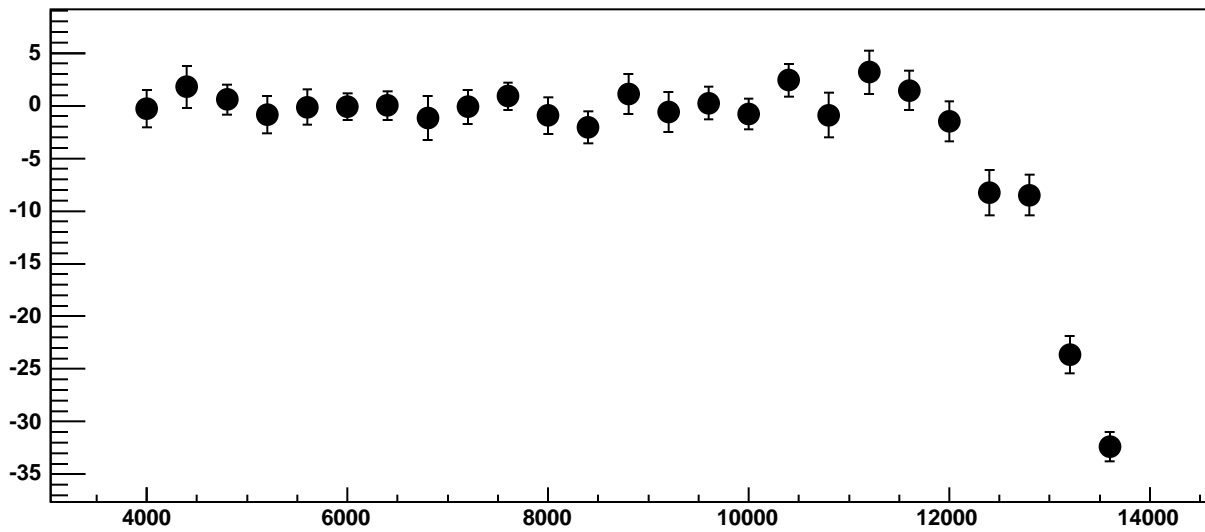
Chip 4, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



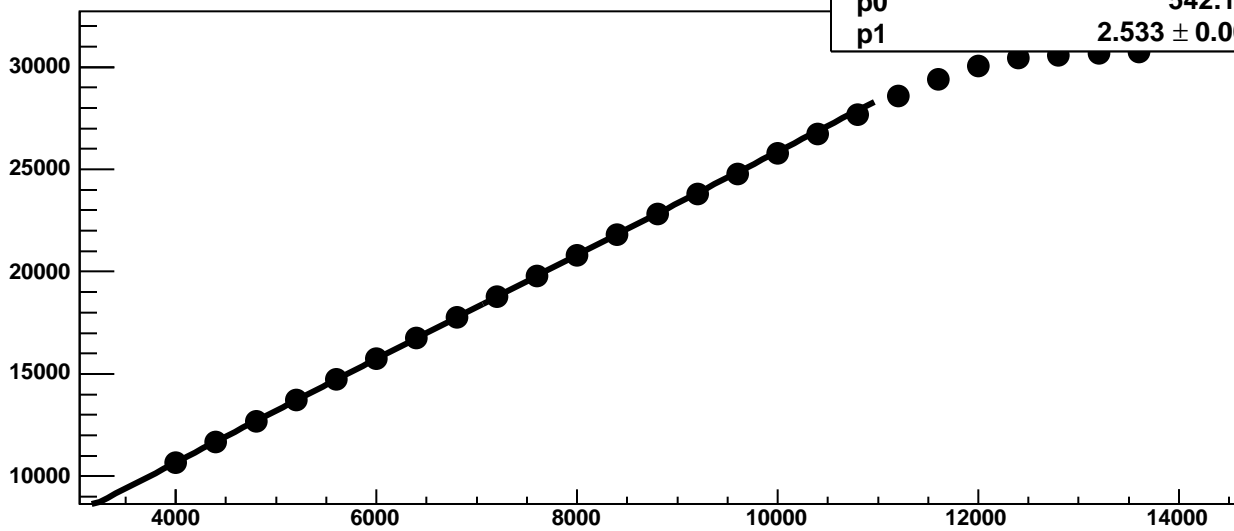
Chip 4, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



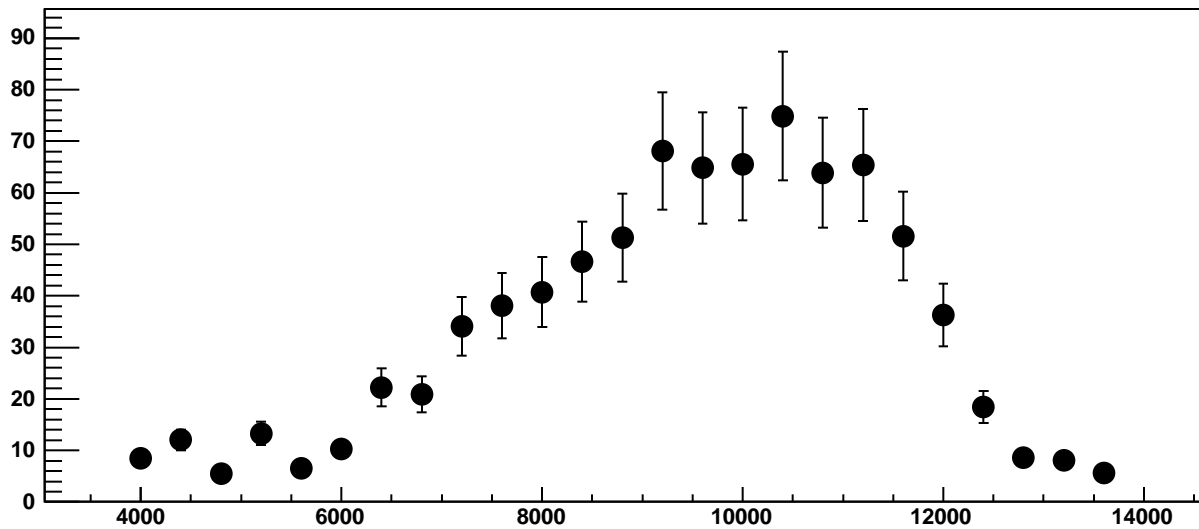
Chip 4, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



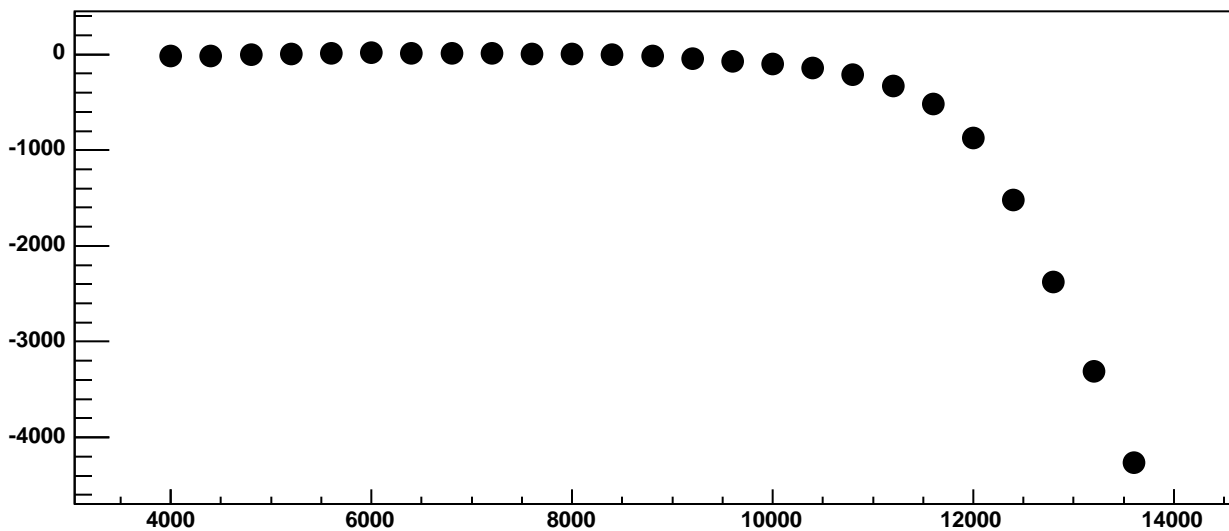
Chip 4, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC



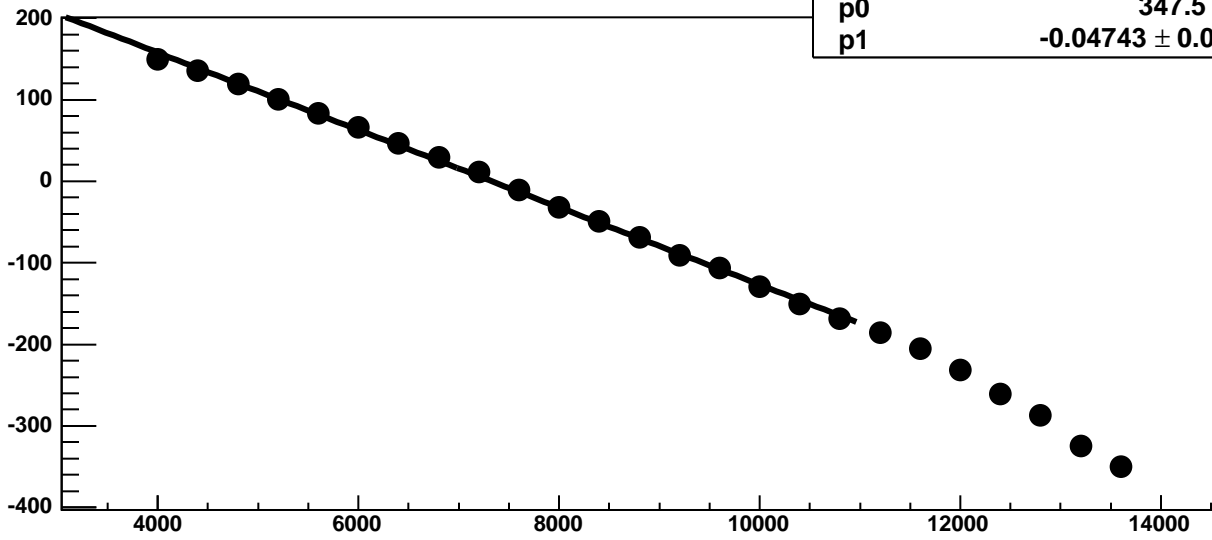
Chip 4, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



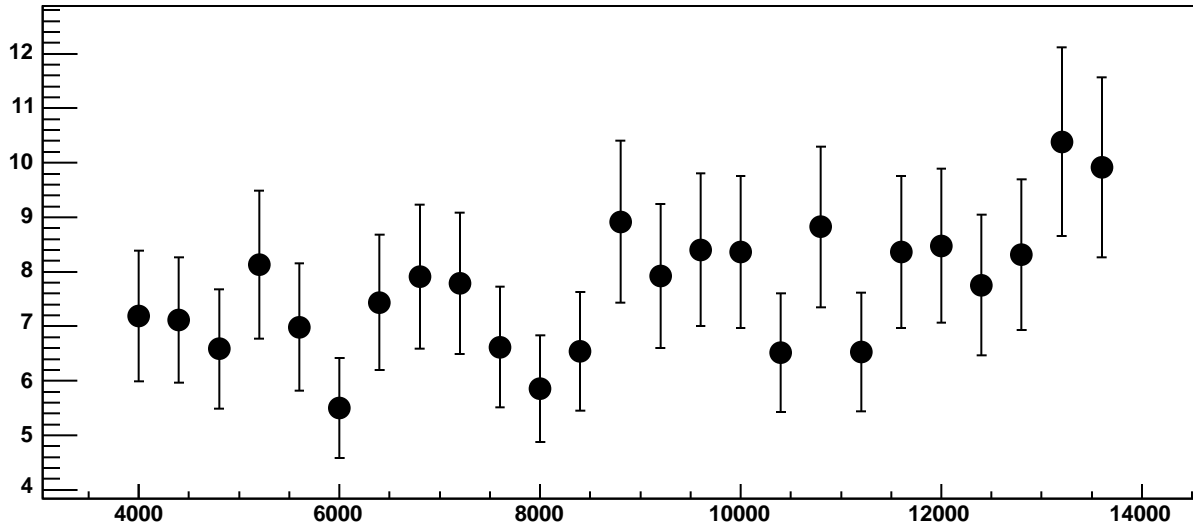
Chip 4, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC



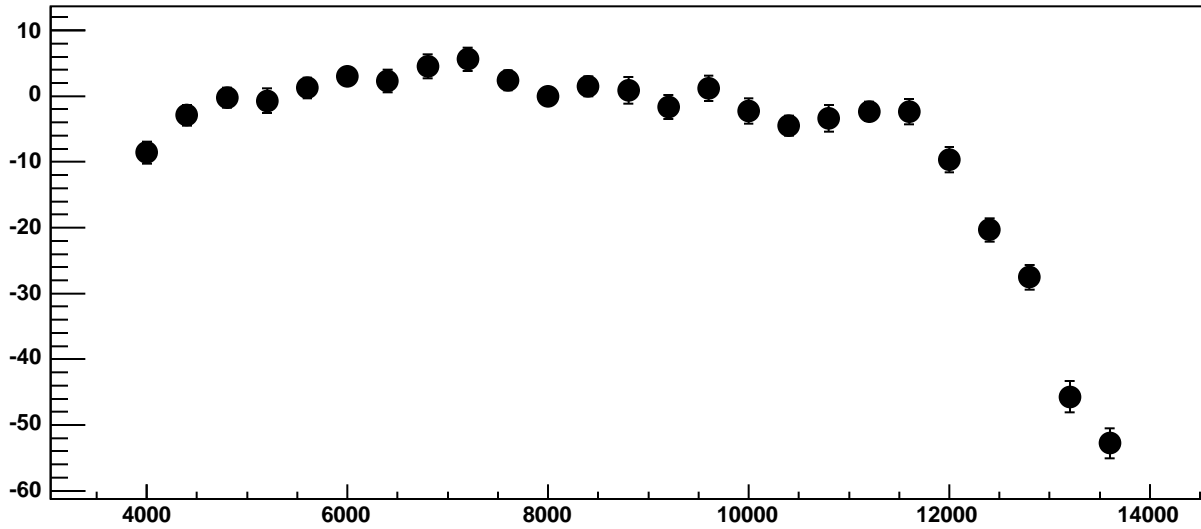
Chip 4, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



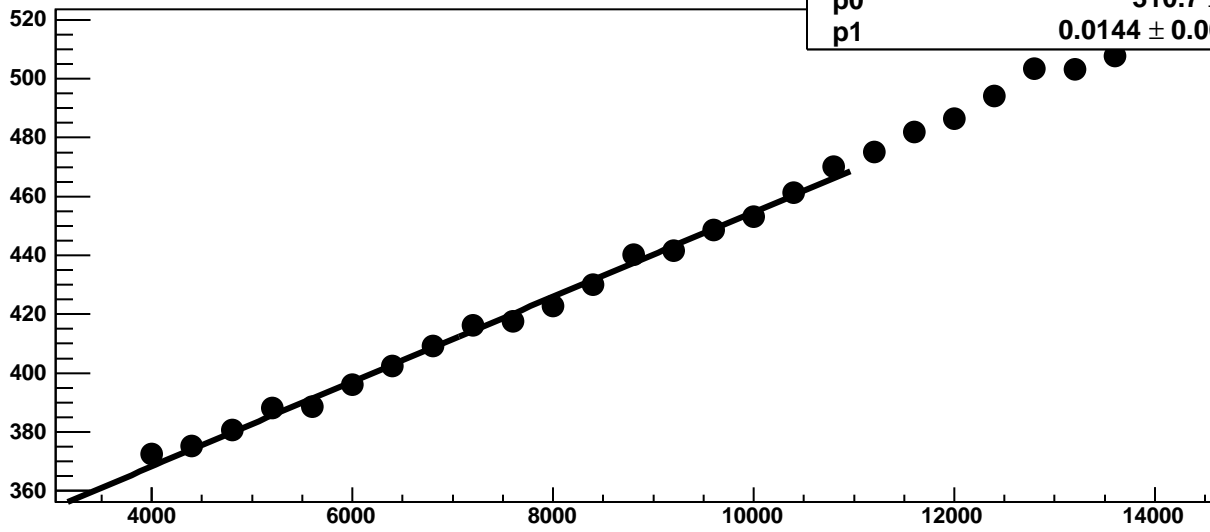
Chip 4, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

32.74 / 16

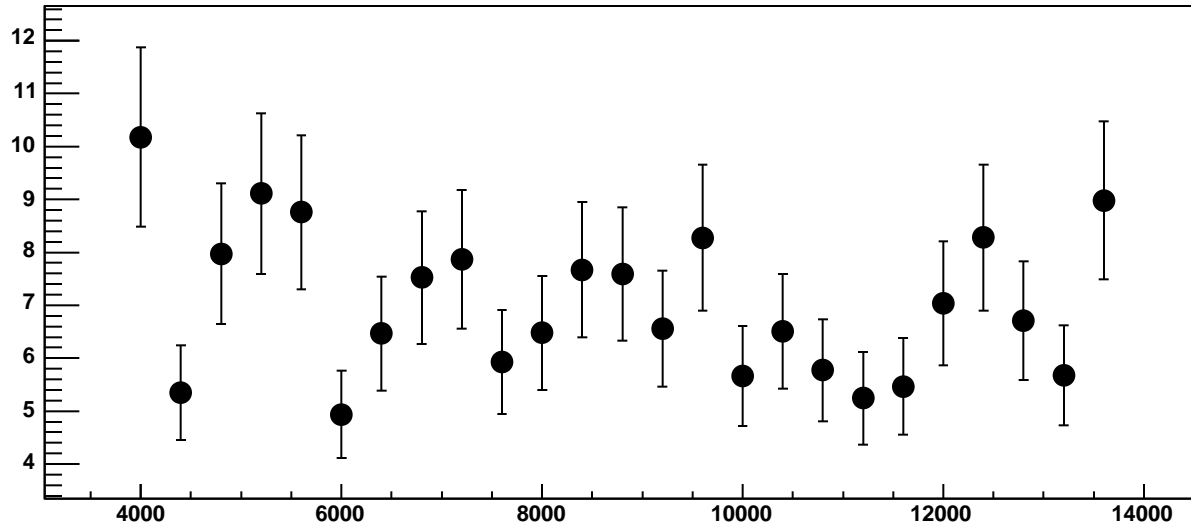
p0

310.7 ± 1.377

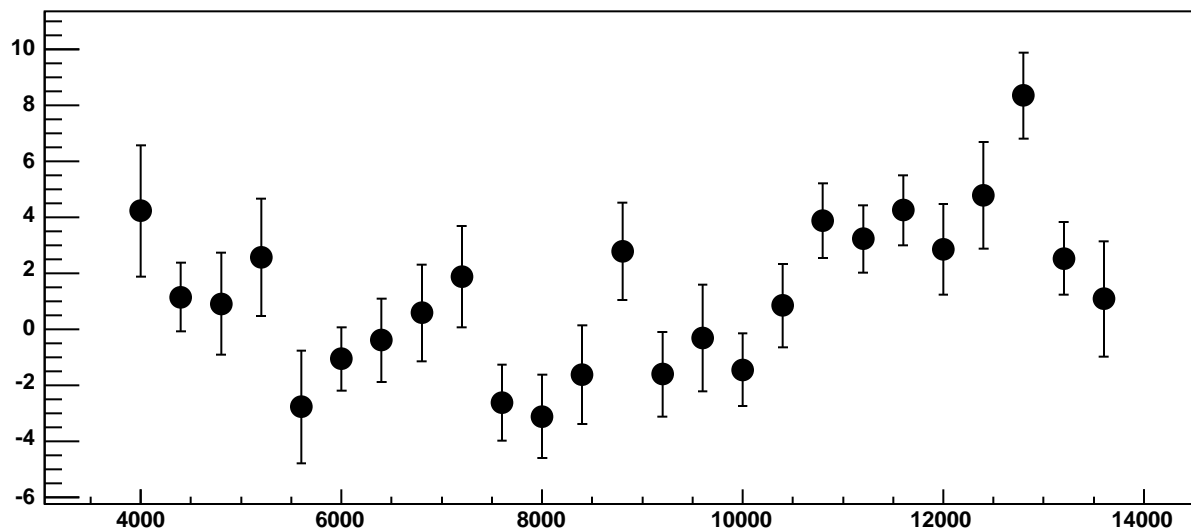
p1

0.0144 ± 0.0001754

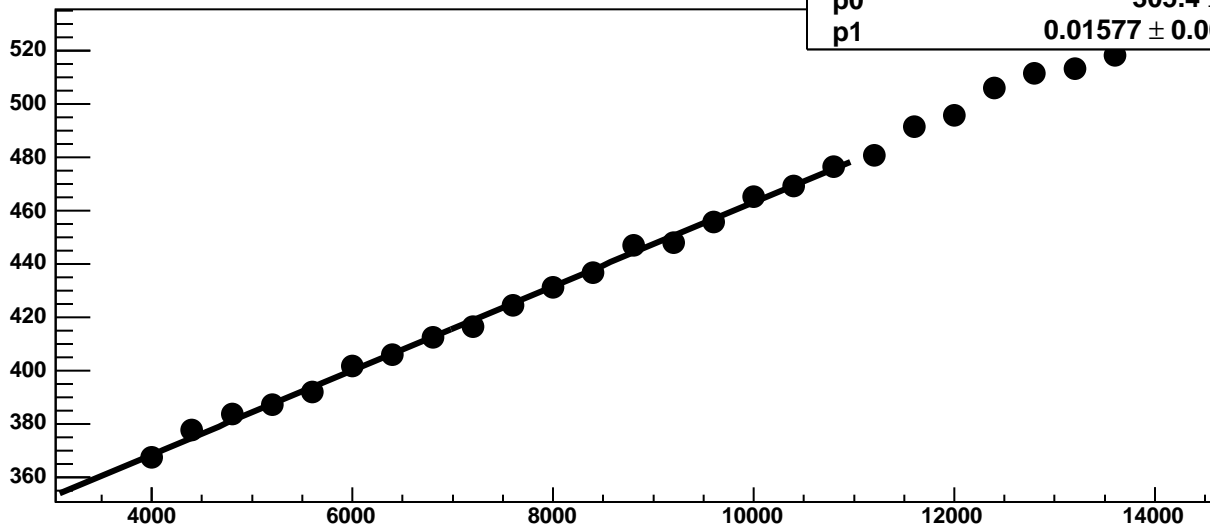
Chip 4, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



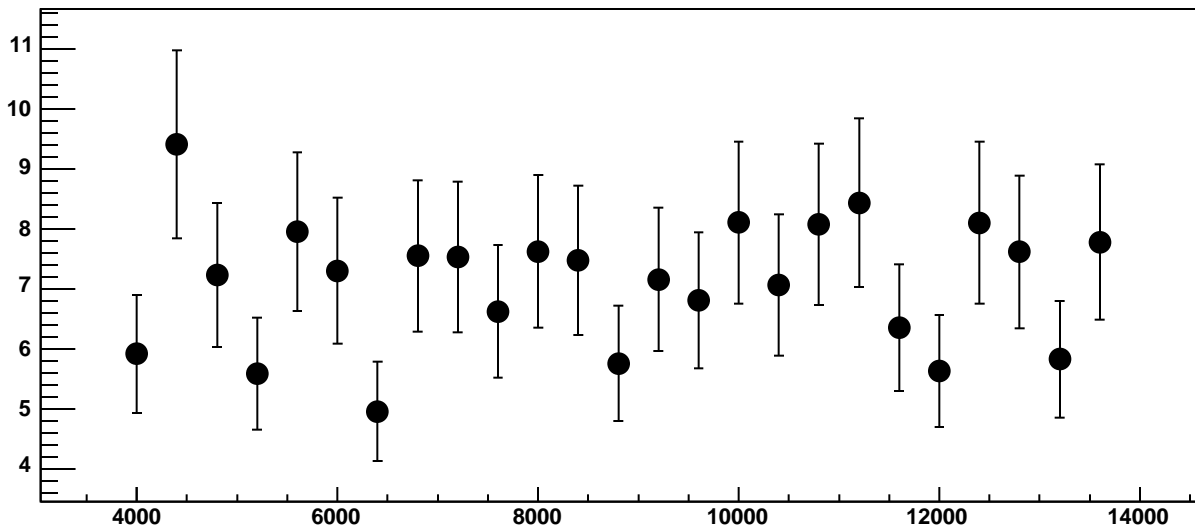
Chip 4, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



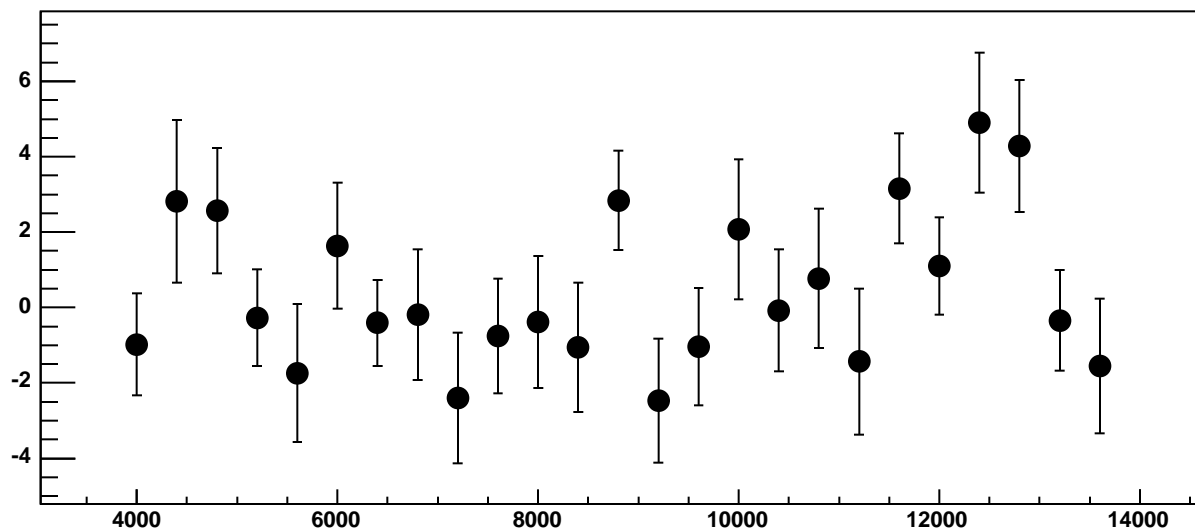
Chip 4, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC



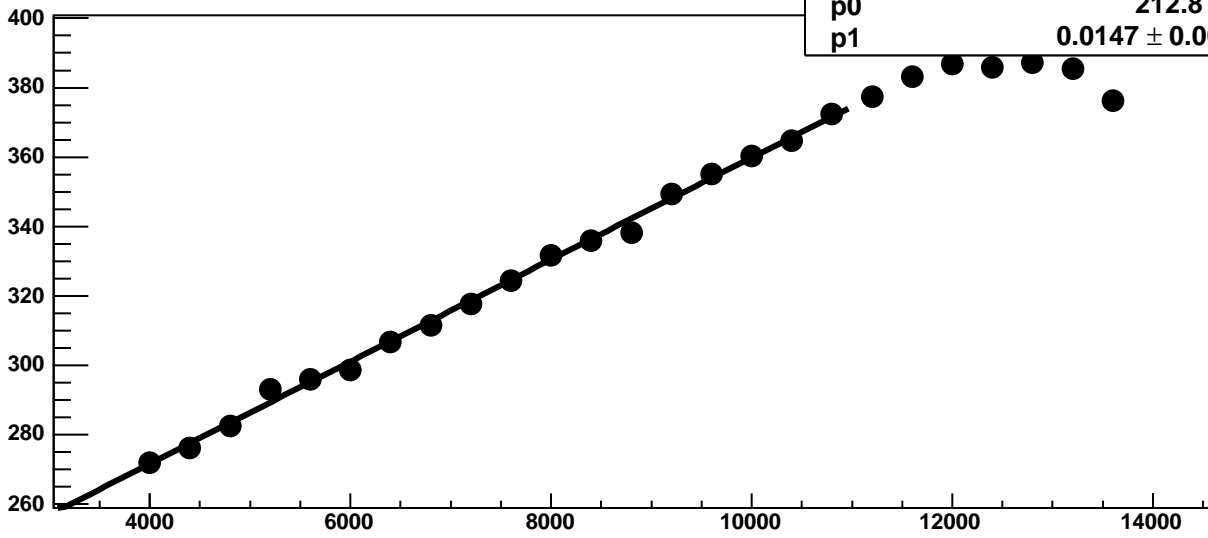
Chip 4, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



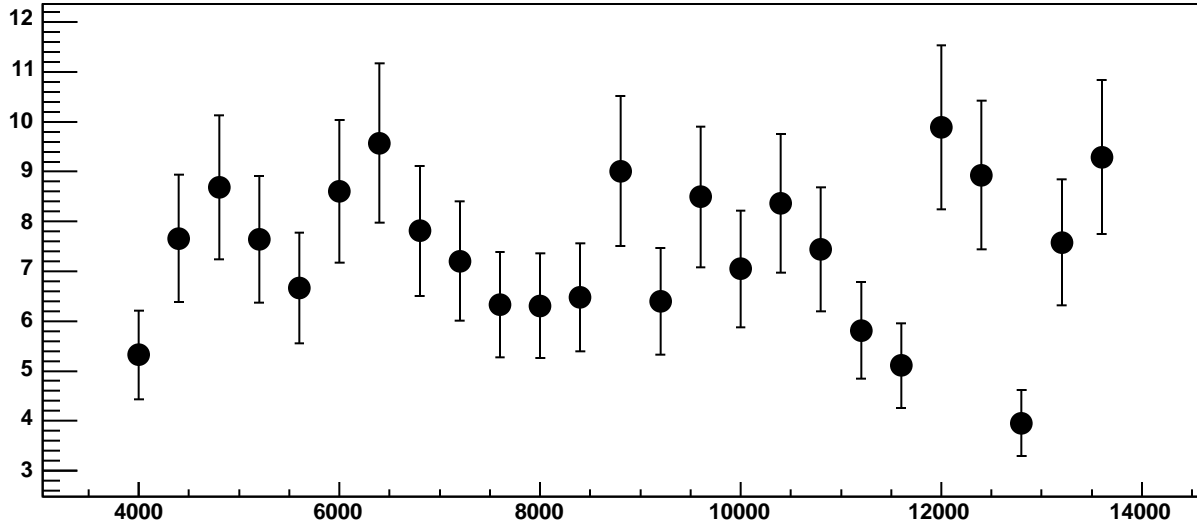
Chip 4, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC



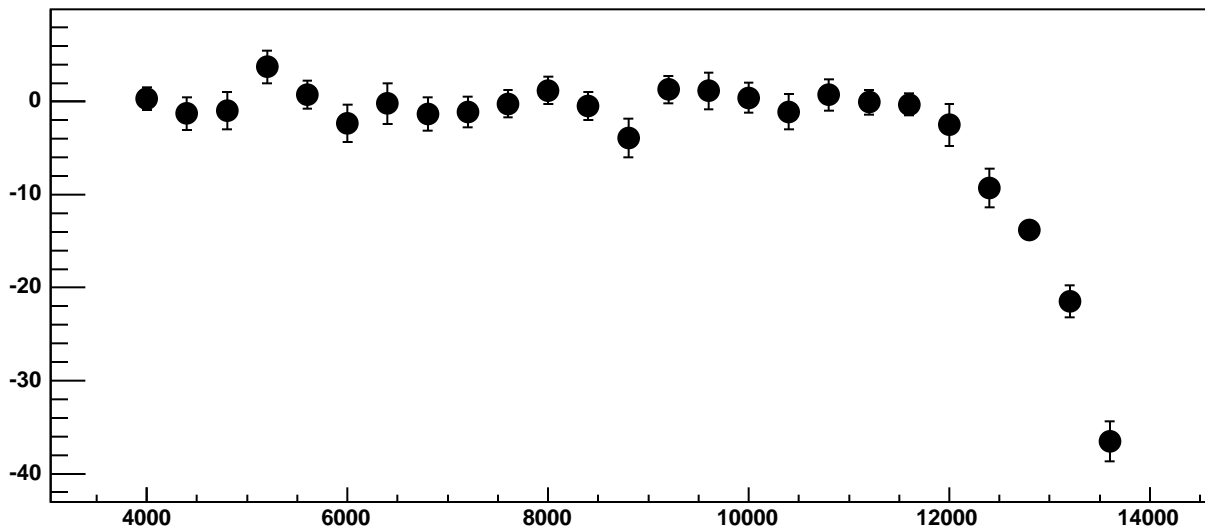
Chip 4, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



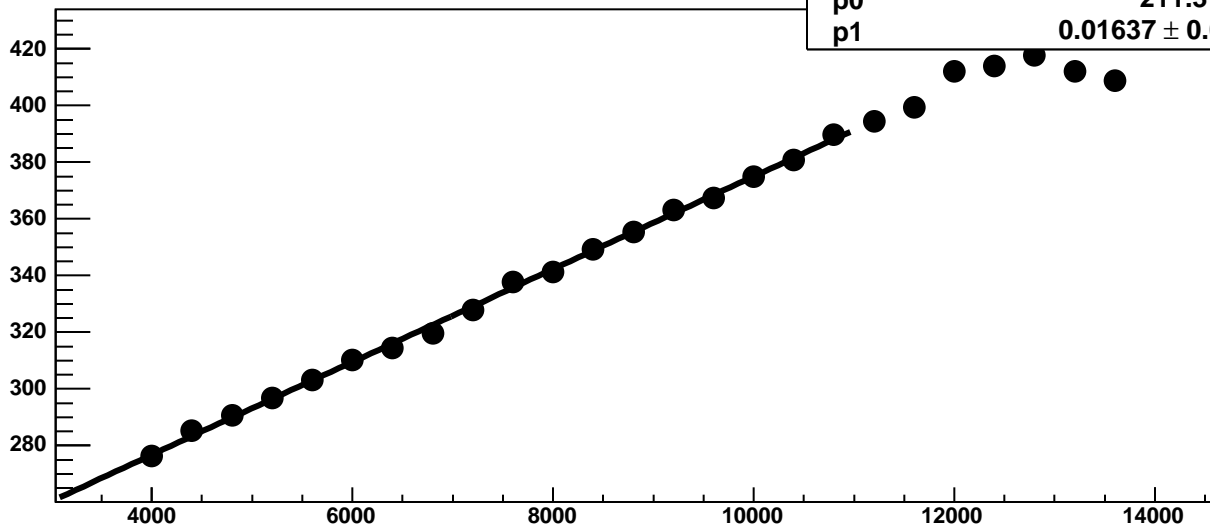
Chip 4, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



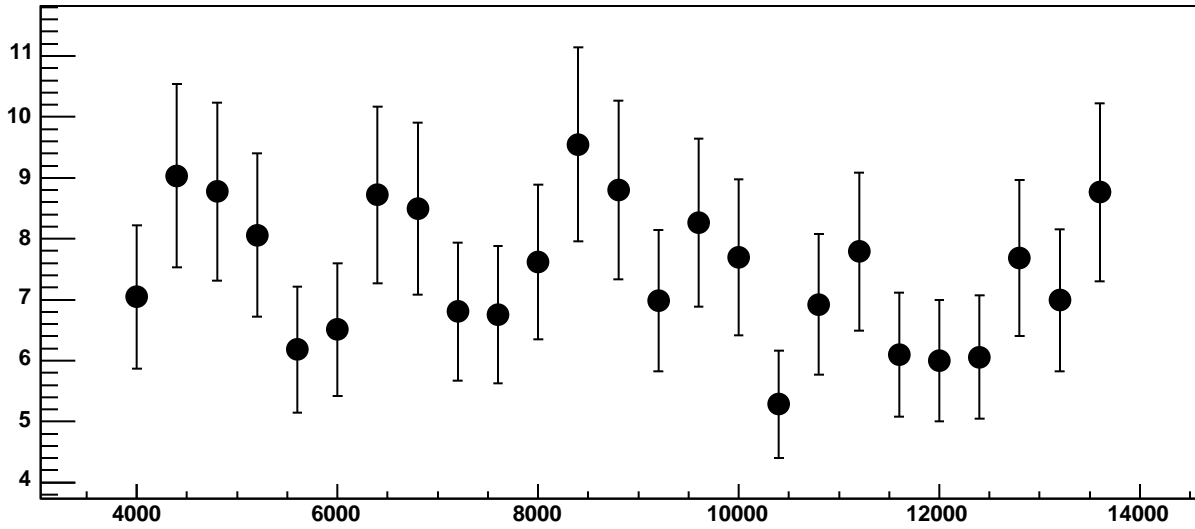
Chip 4, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



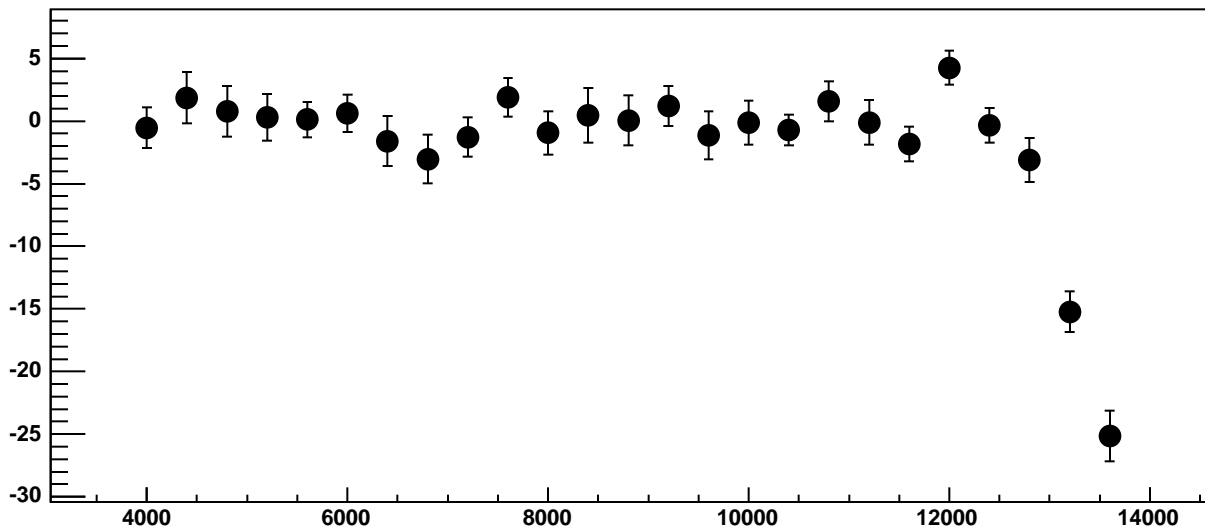
Chip 4, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



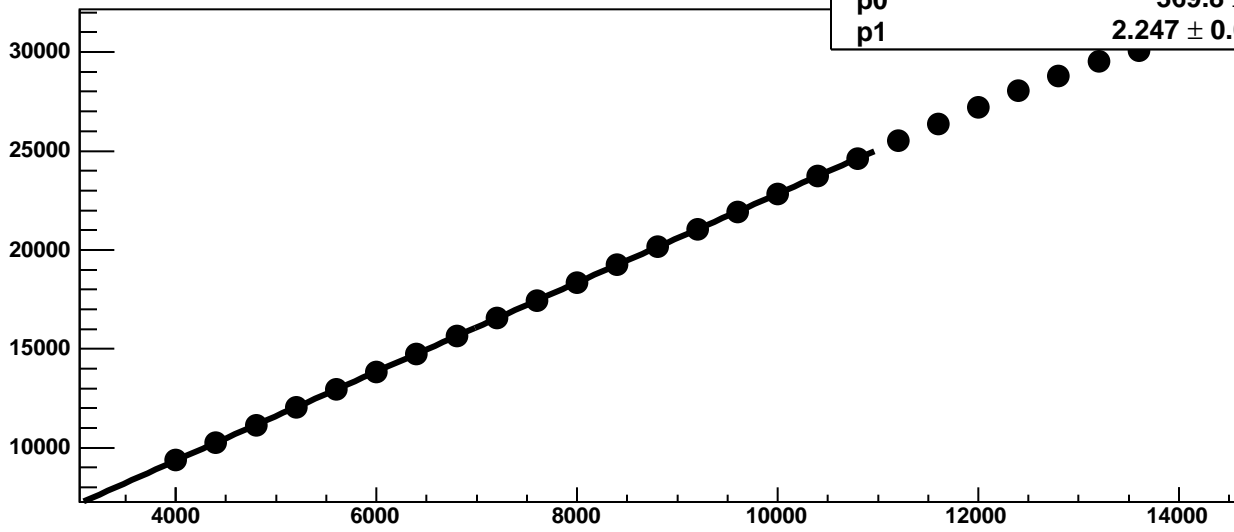
Chip 4, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

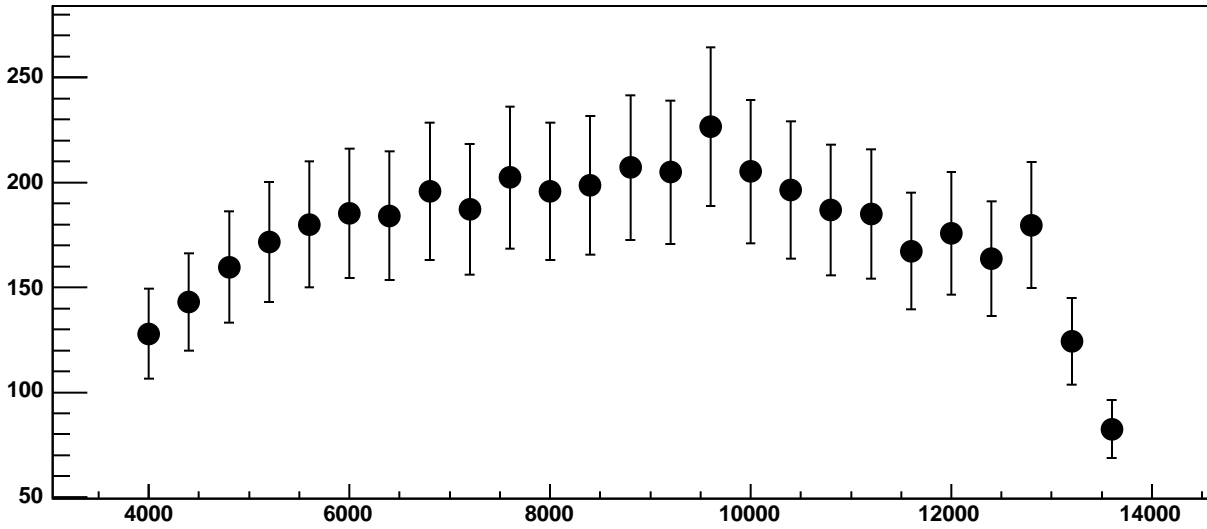


Chip 4, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC

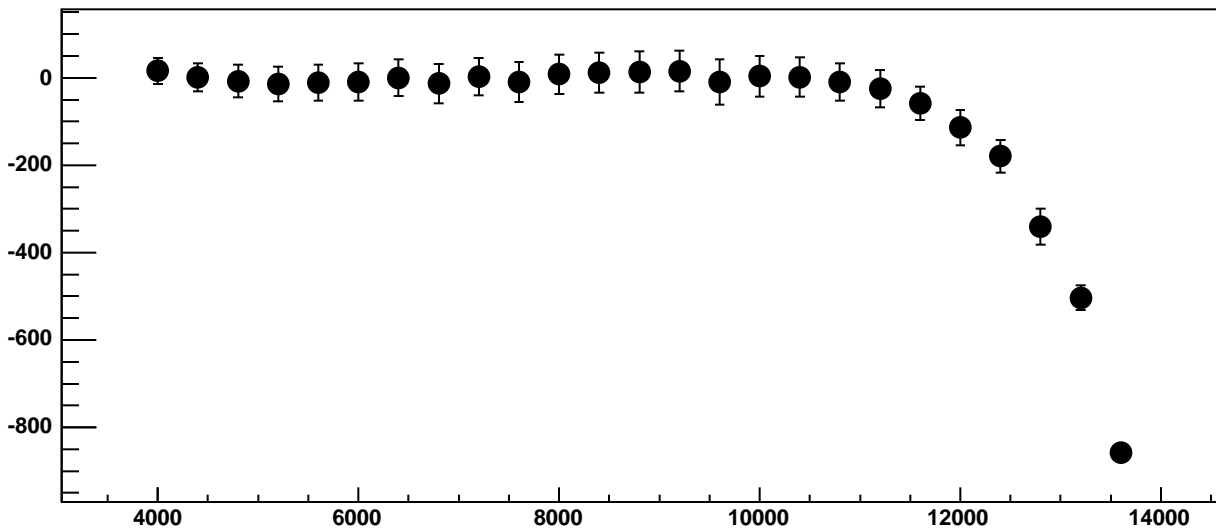


χ^2 / ndf 1.093 / 16
p0 369.8 ± 32.34
p1 2.247 ± 0.004478

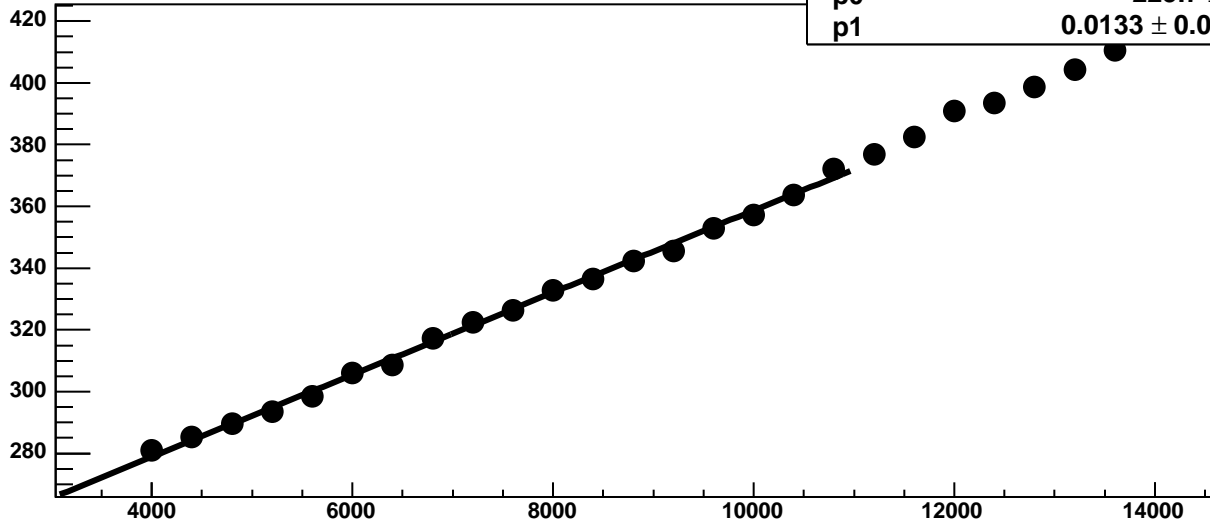
Chip 4, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

13.81 / 16

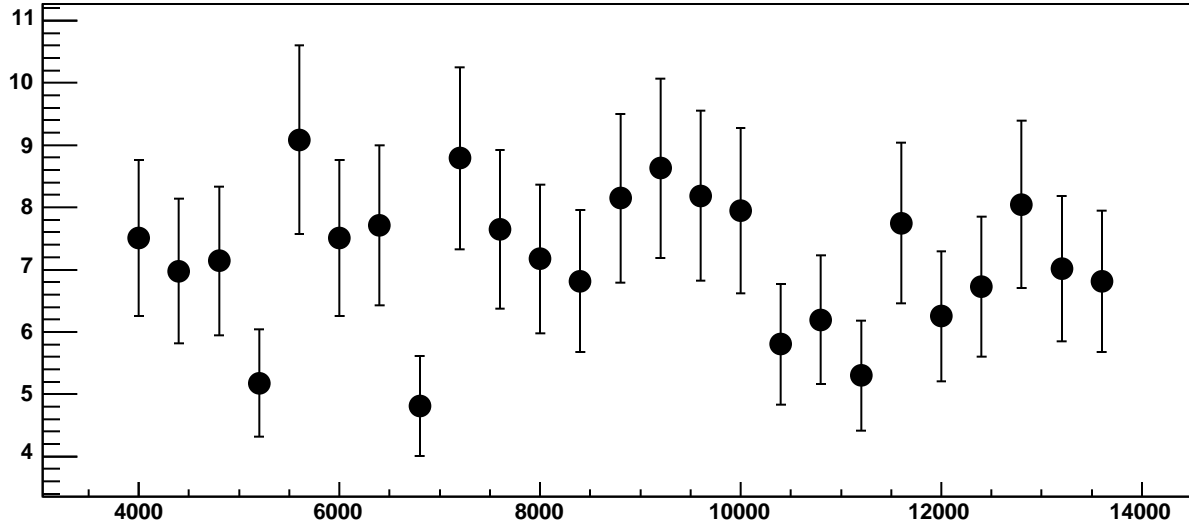
p0

225.7 ± 1.364

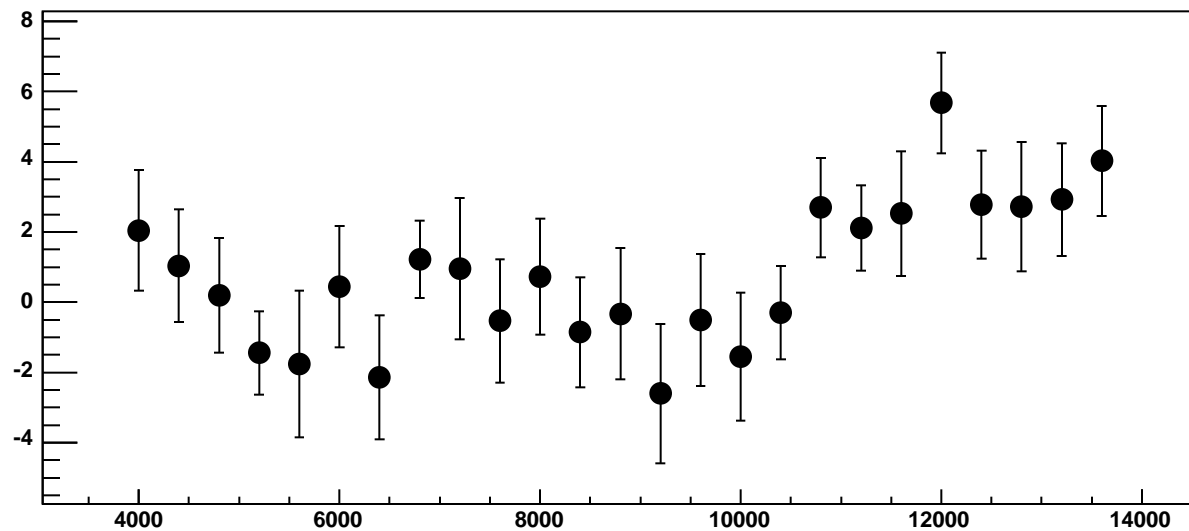
p1

0.0133 ± 0.0001781

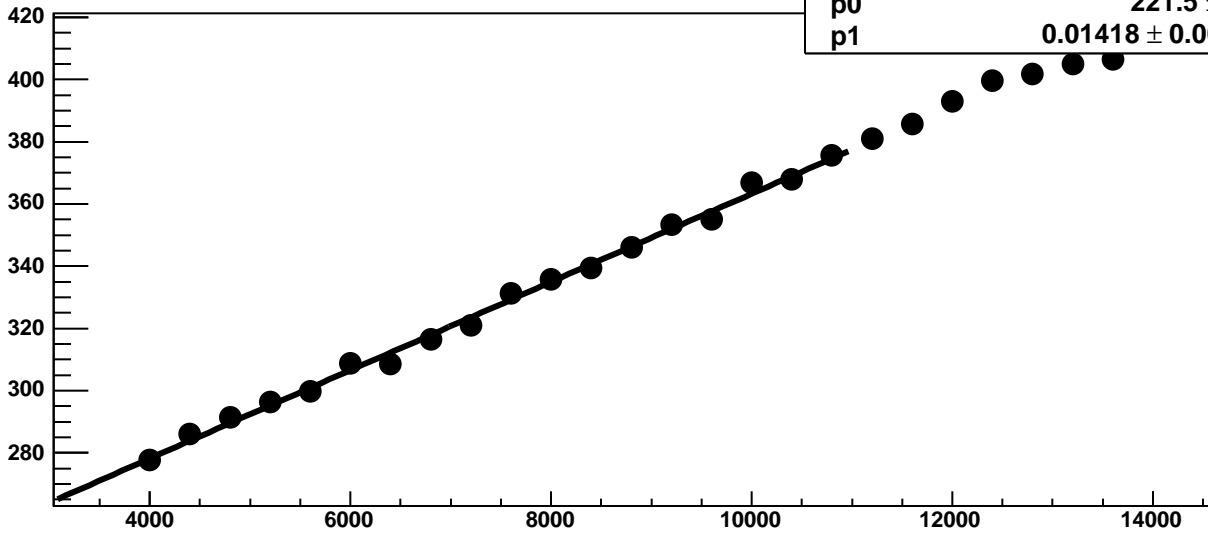
Chip 4, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

24.68 / 16

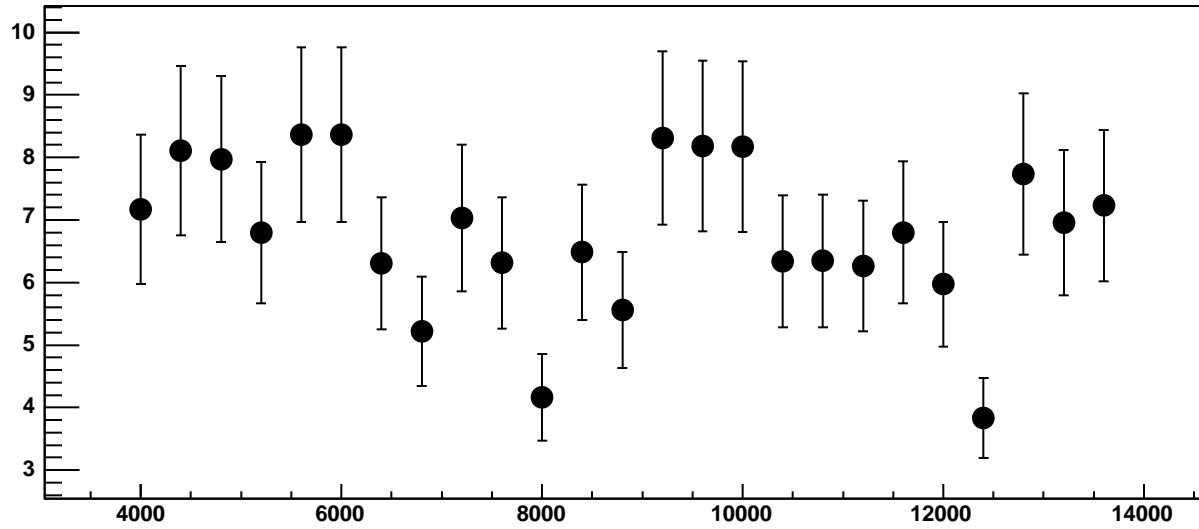
p0

221.5 ± 1.454

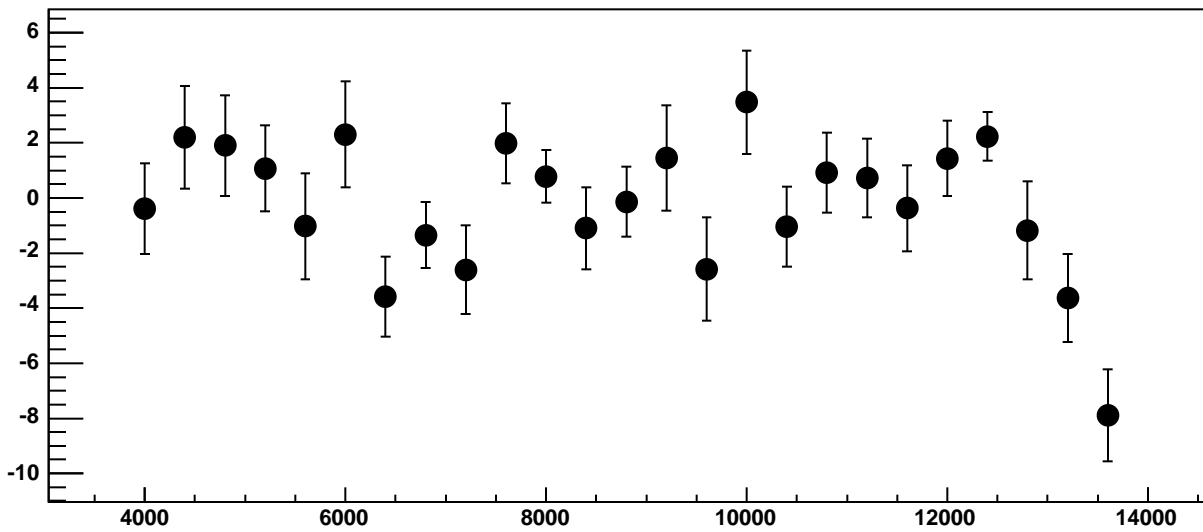
p1

0.01418 ± 0.0001864

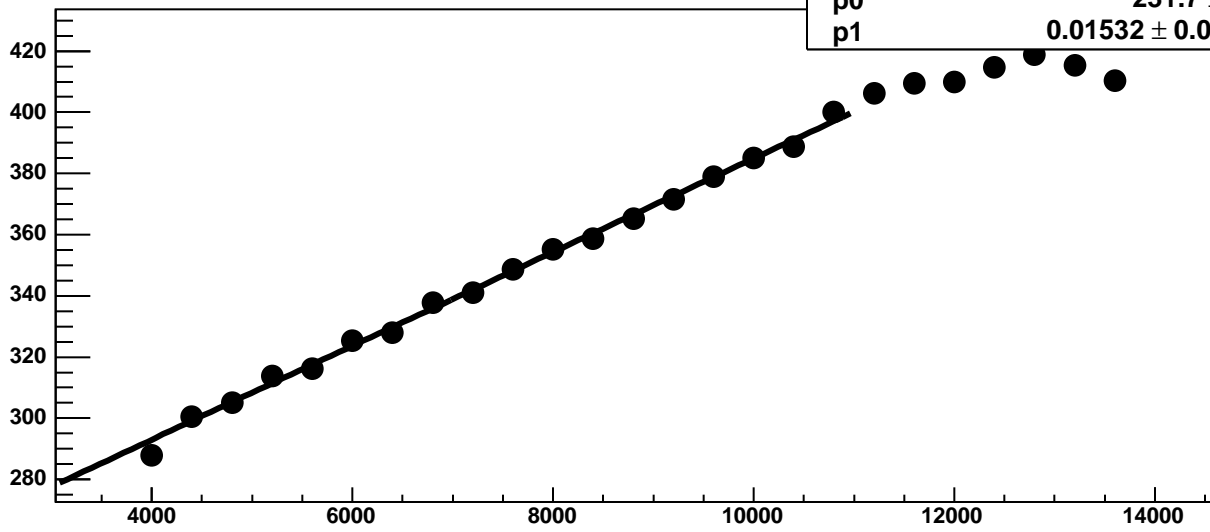
Chip 4, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



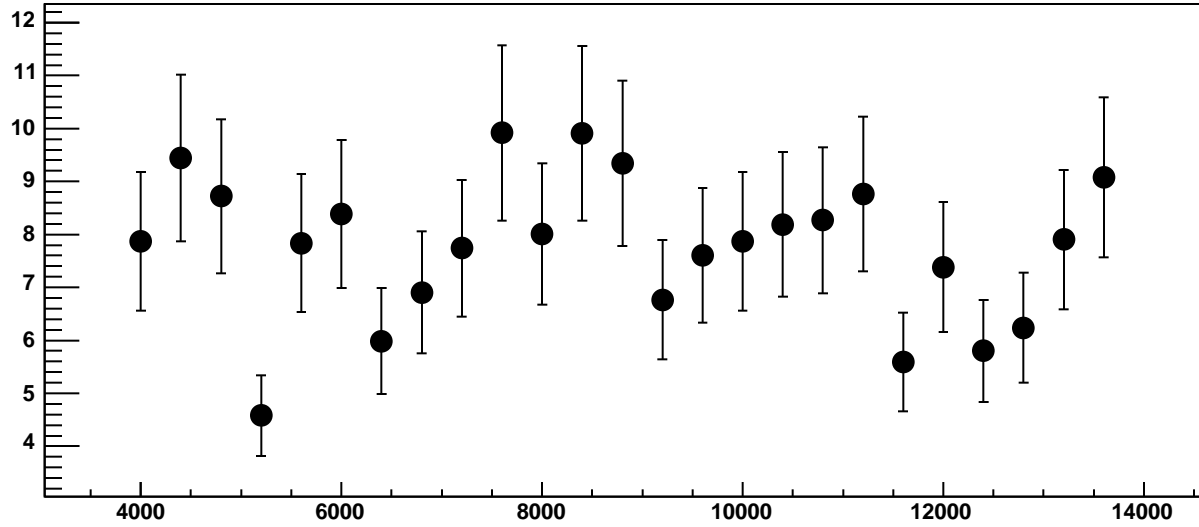
Chip 4, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



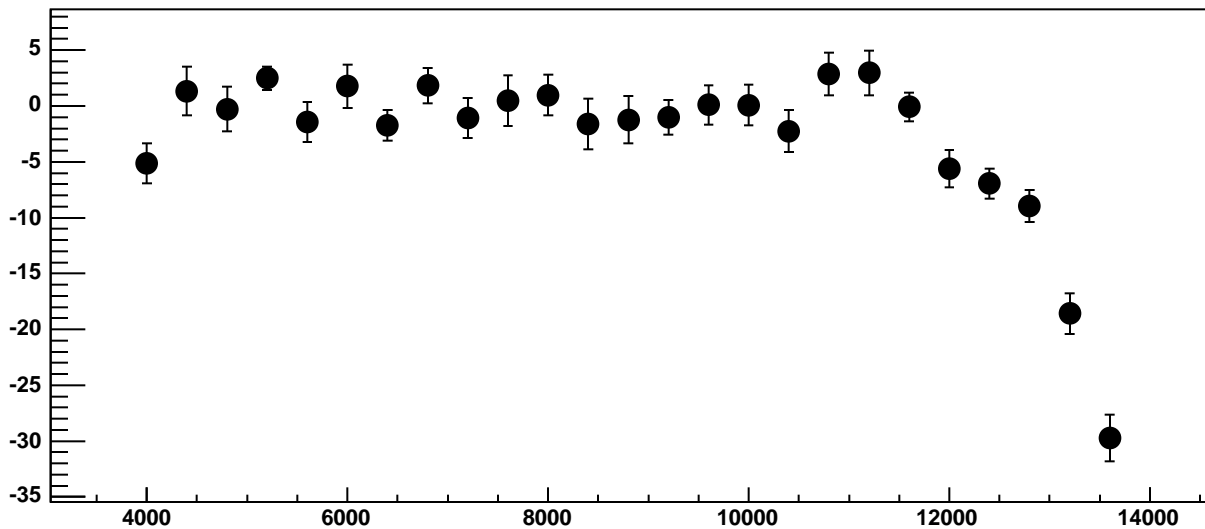
Chip 4, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



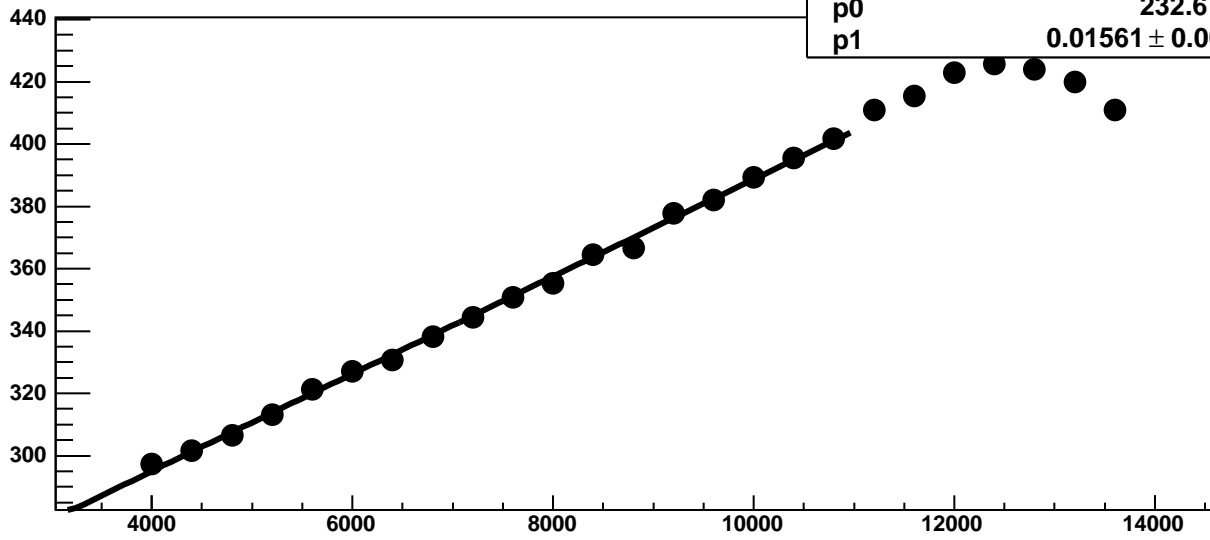
Chip 4, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

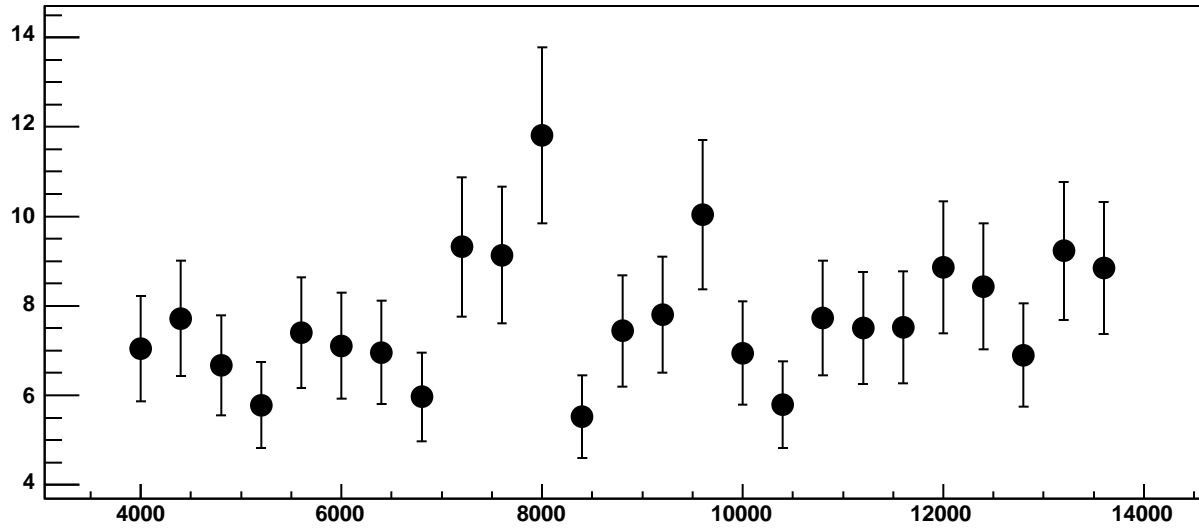


Chip 4, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

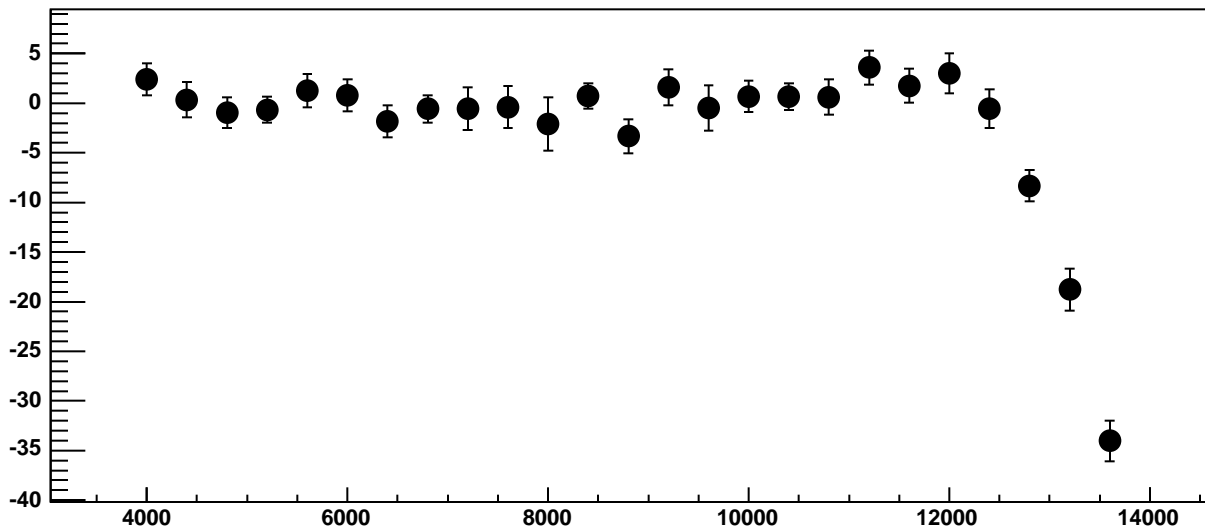


χ^2 / ndf 11.29 / 16
p0 232.6 ± 1.39
p1 0.01561 ± 0.0001824

Chip 4, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC

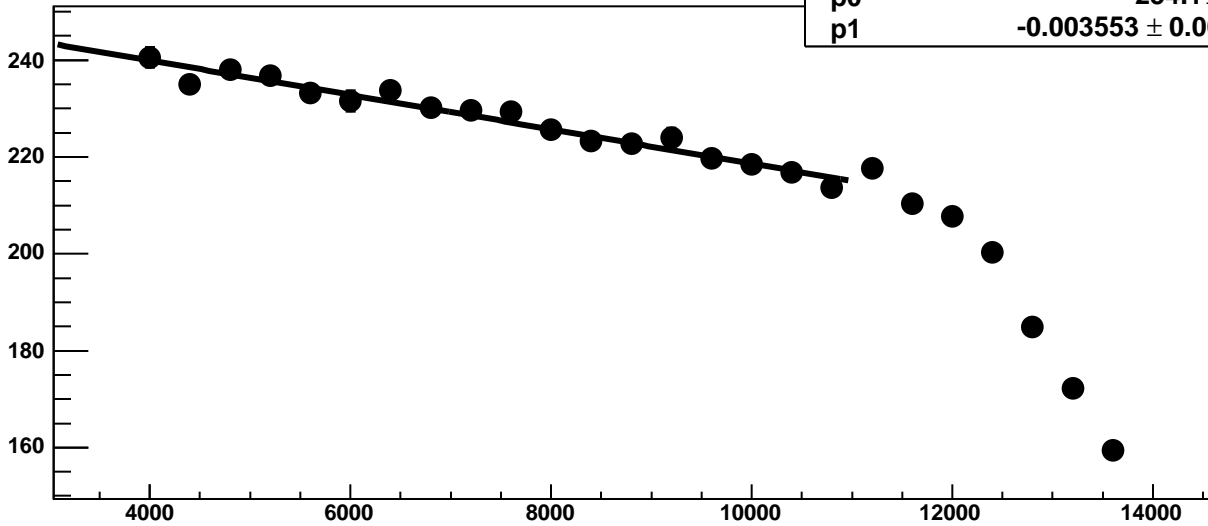


Chip 4, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

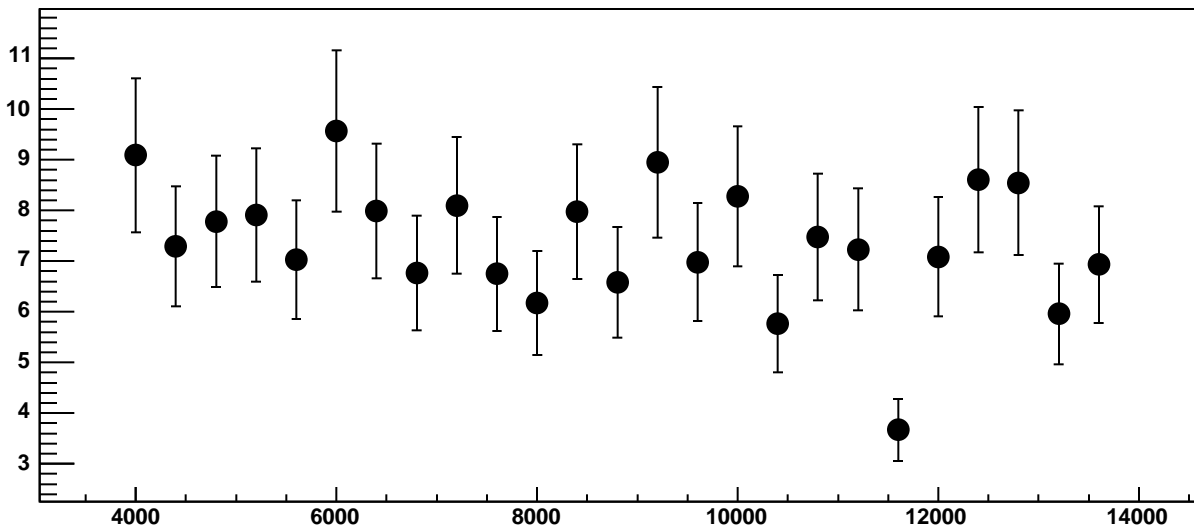


Chip 4, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

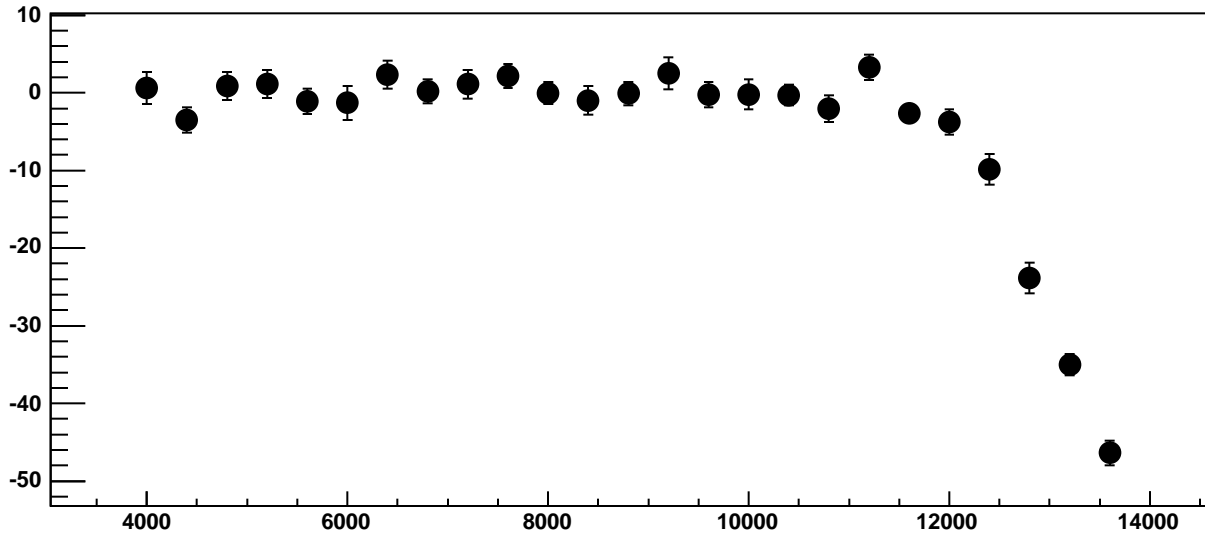
χ^2 / ndf 13.47 / 16
p0 254.1 ± 1.527
p1 -0.003553 ± 0.0001943



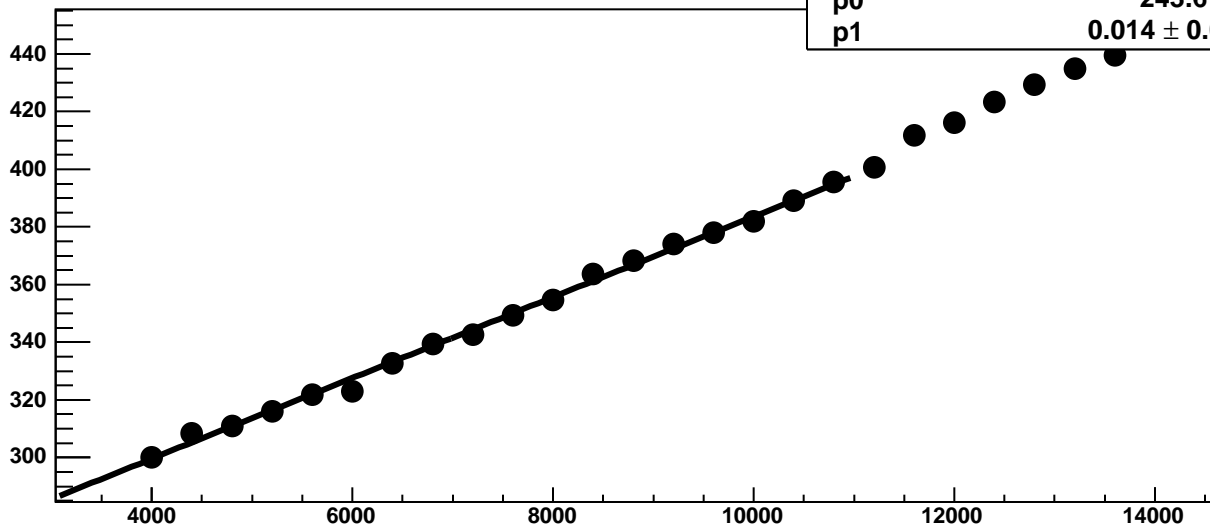
Chip 4, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



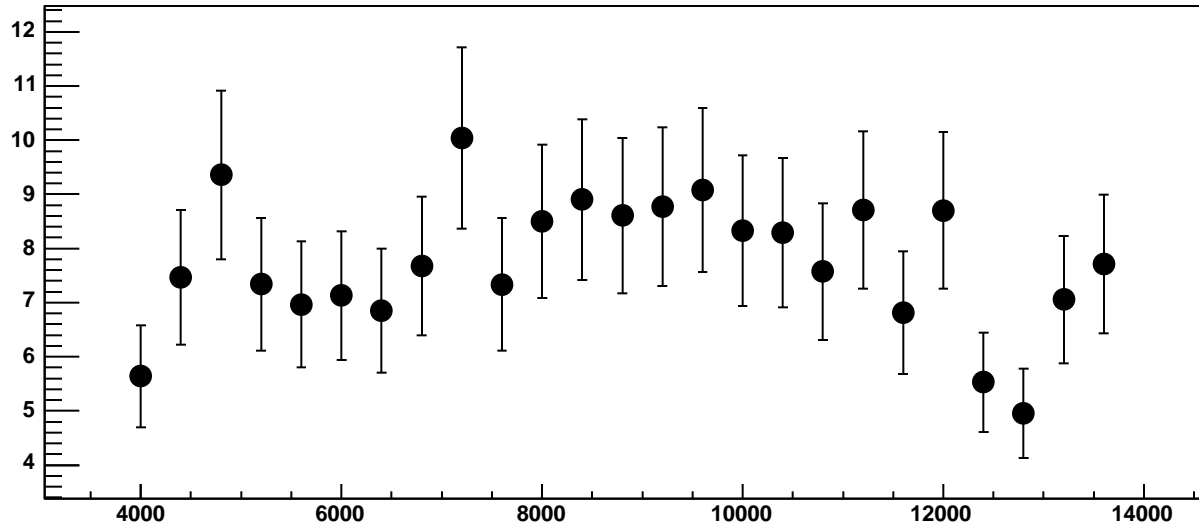
Chip 4, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC



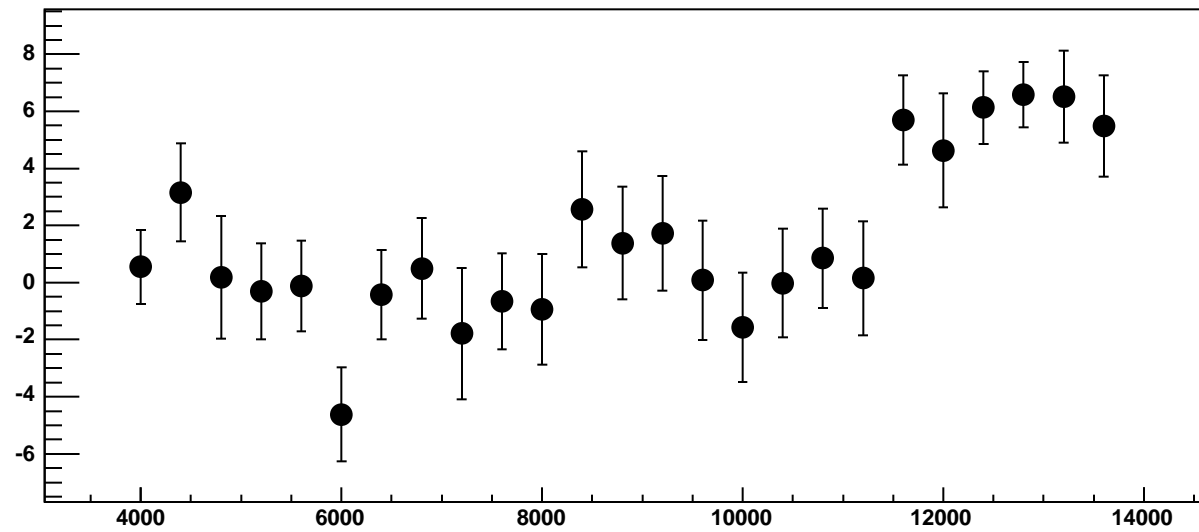
Chip 4, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



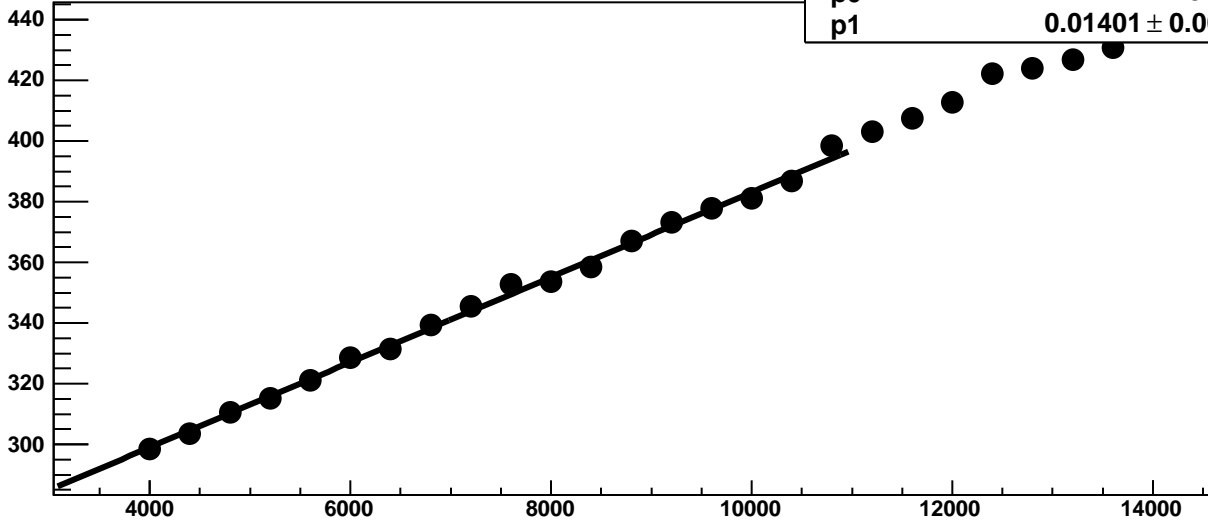
Chip 4, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



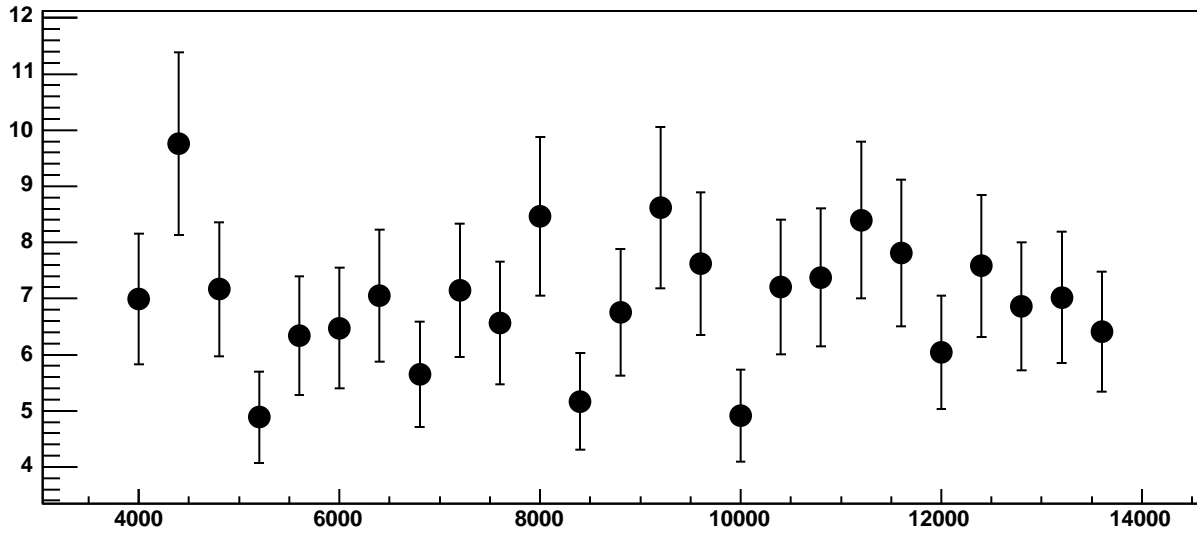
Chip 4, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



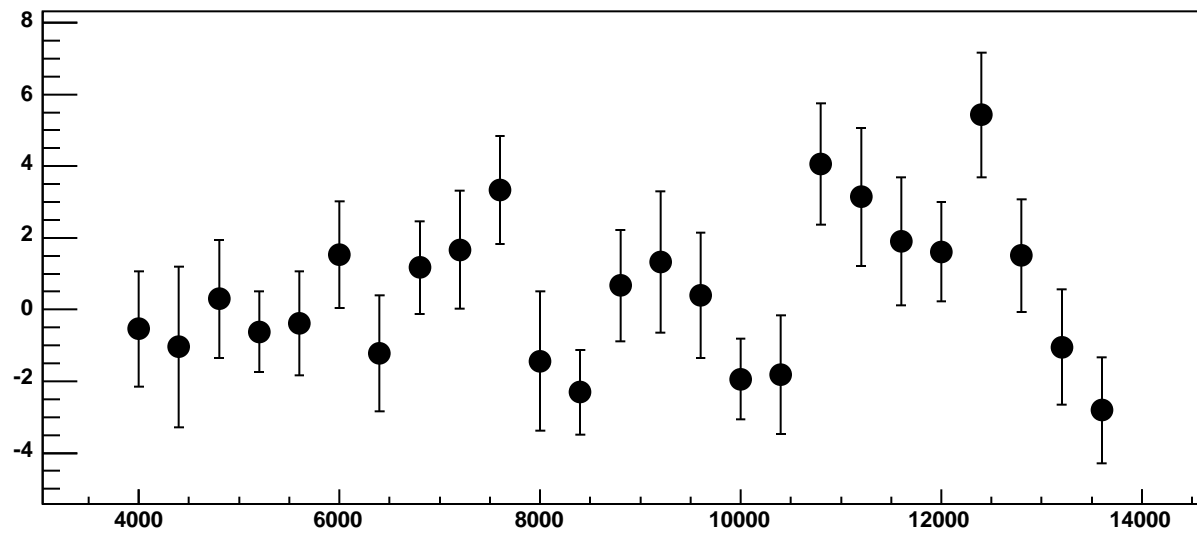
Chip 4, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



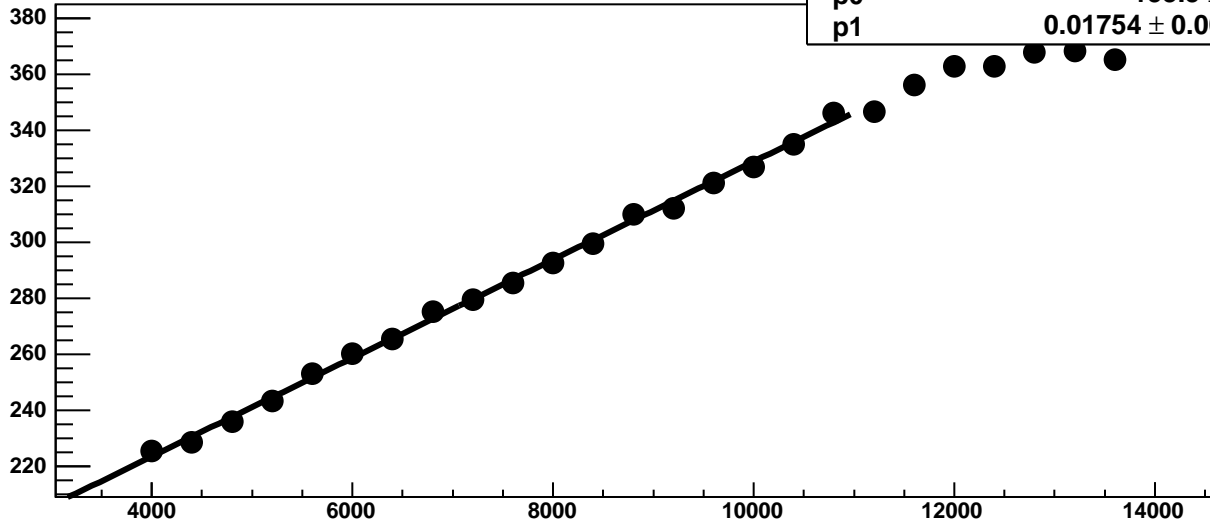
Chip 4, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



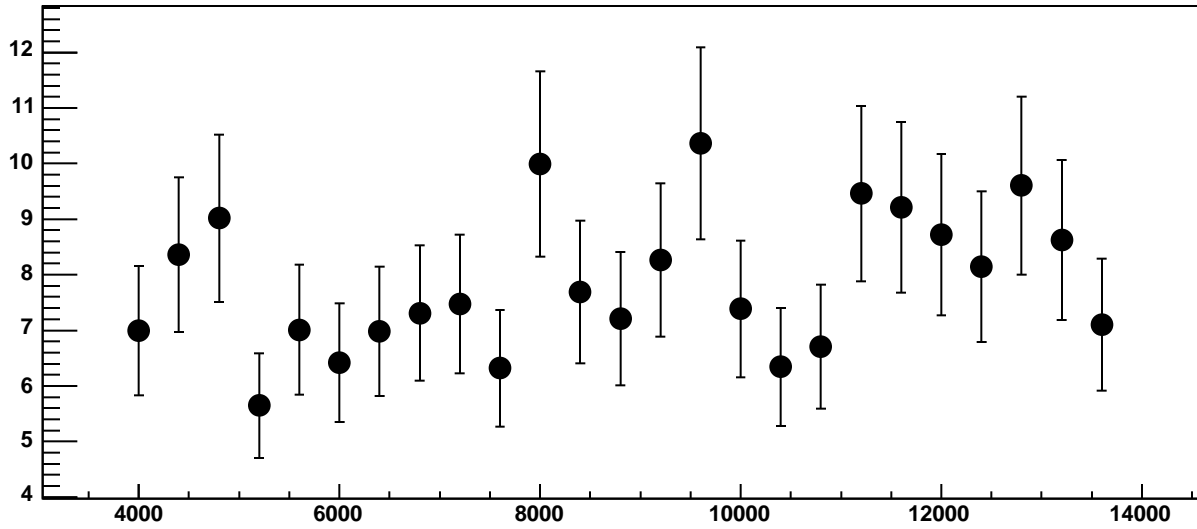
Chip 4, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



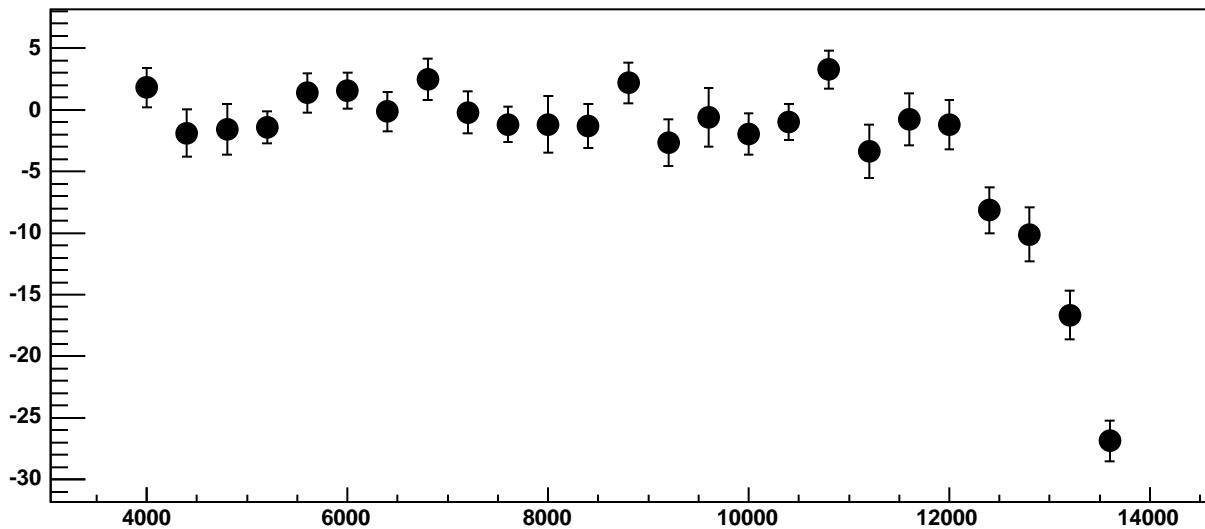
Chip 4, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



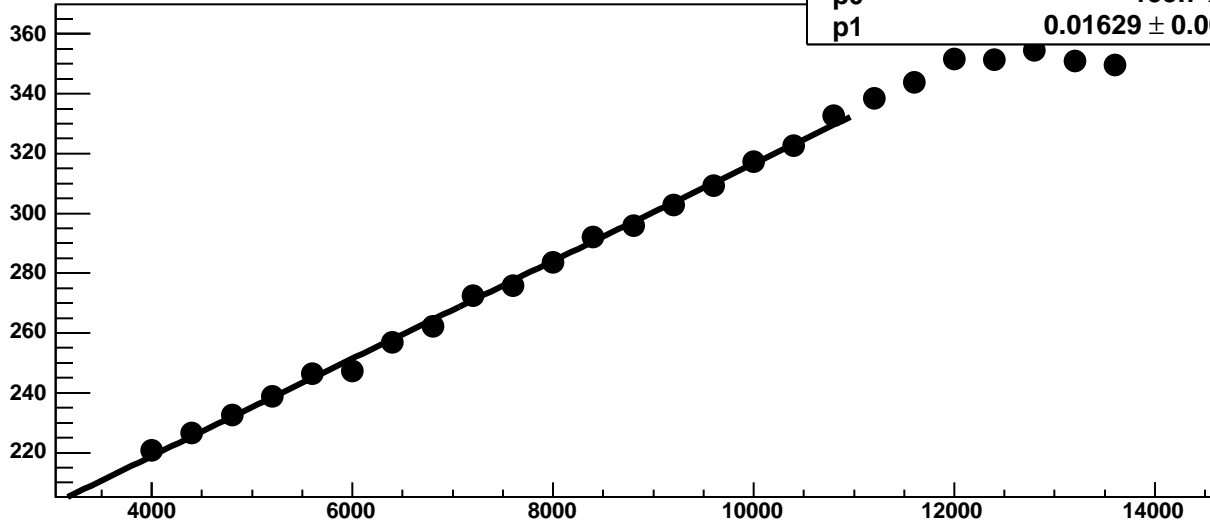
Chip 4, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



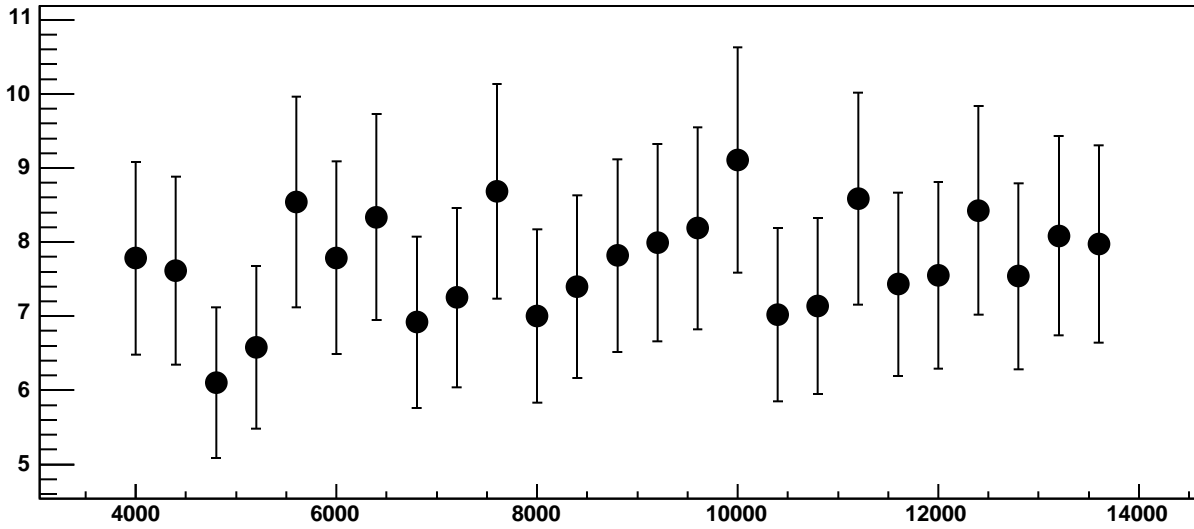
Chip 4, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



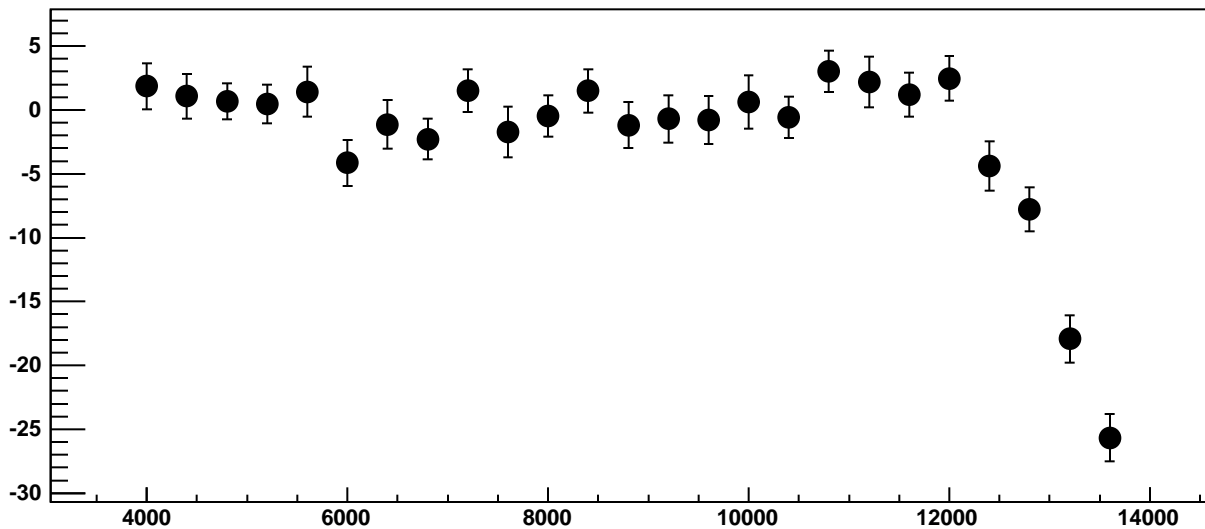
Chip 4, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



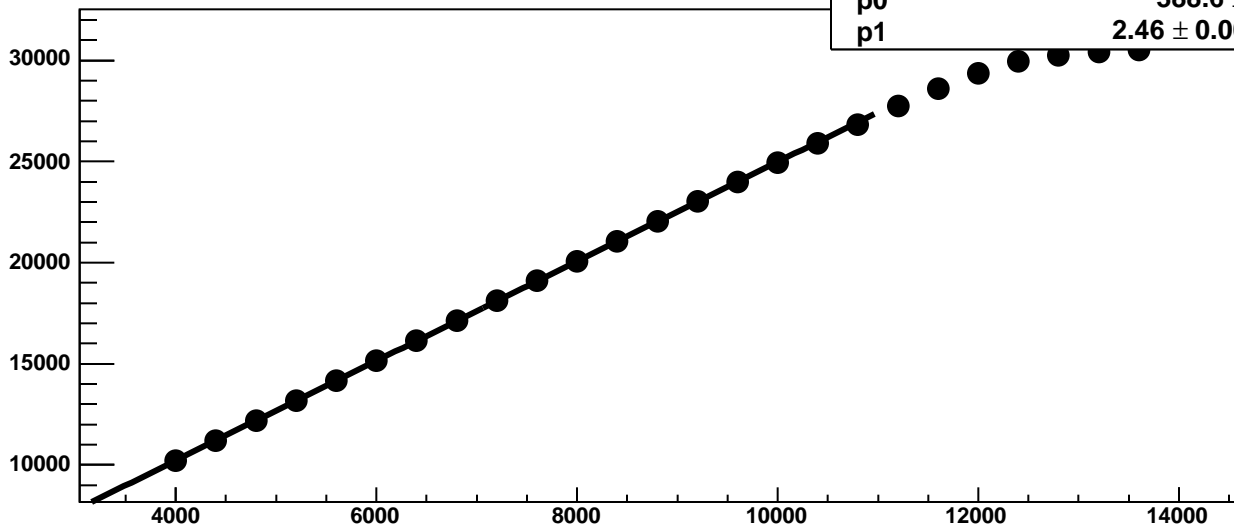
Chip 4, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



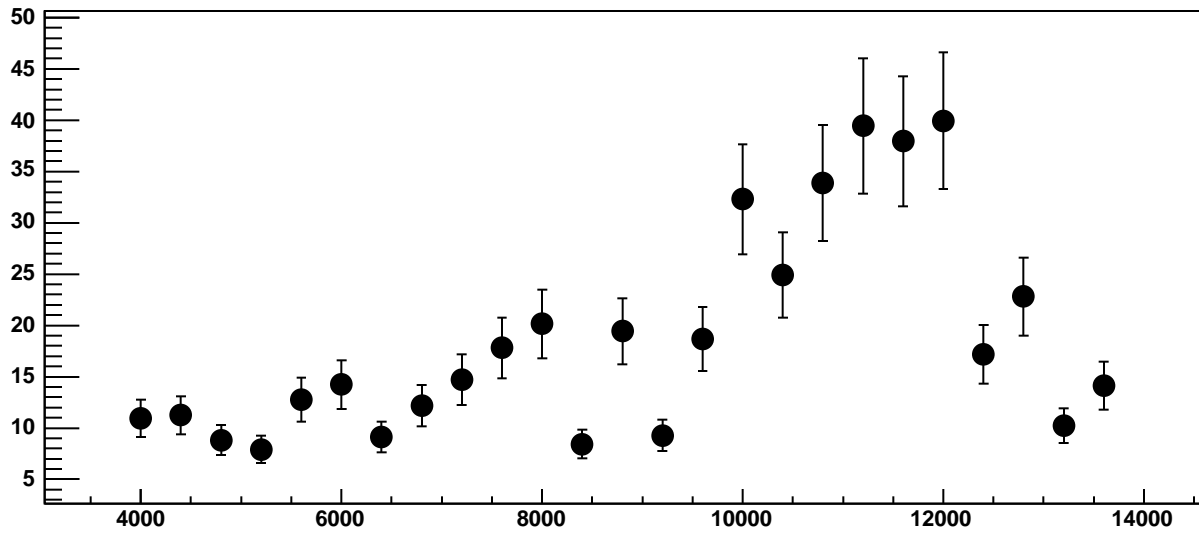
Chip 4, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



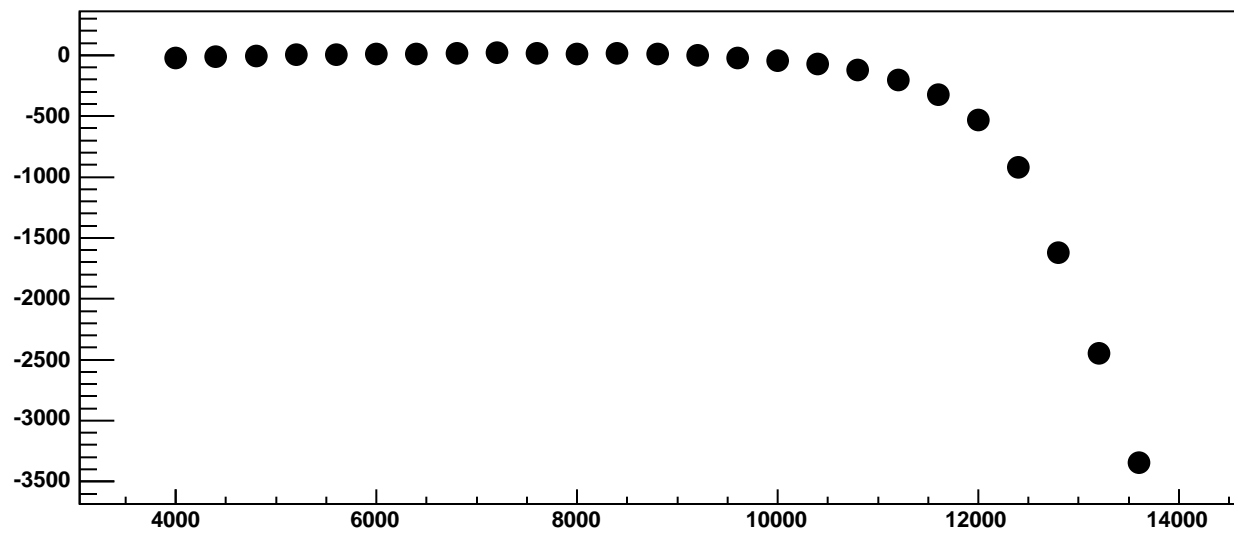
Chip 4, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC



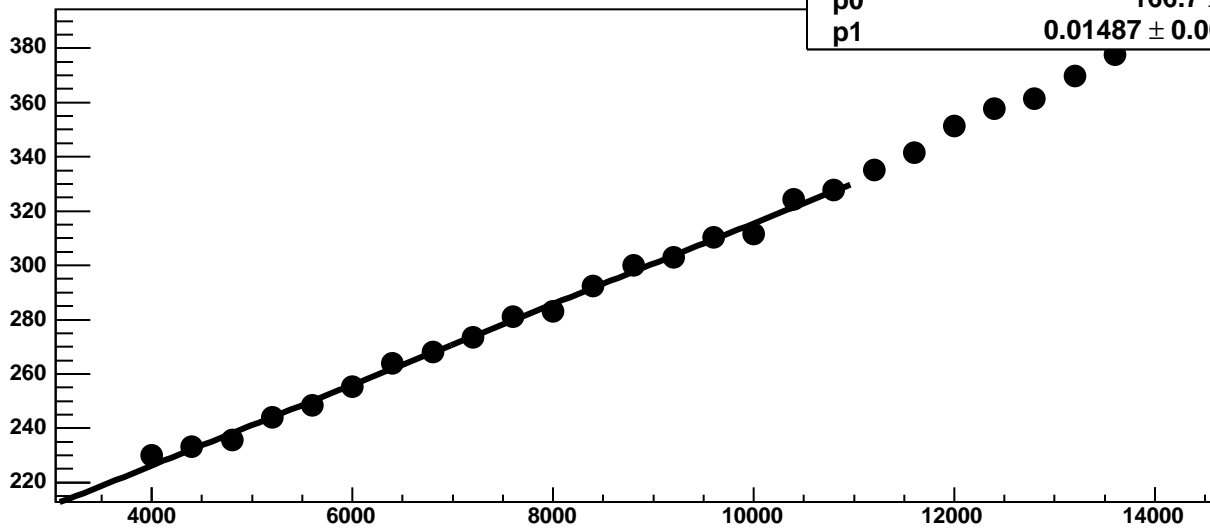
Chip 4, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



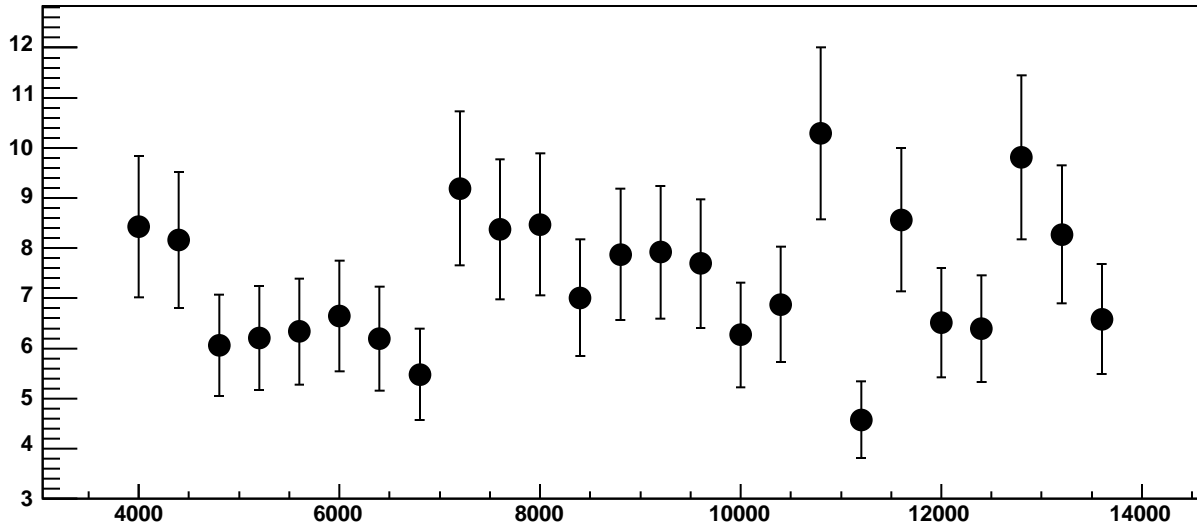
Chip 4, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC



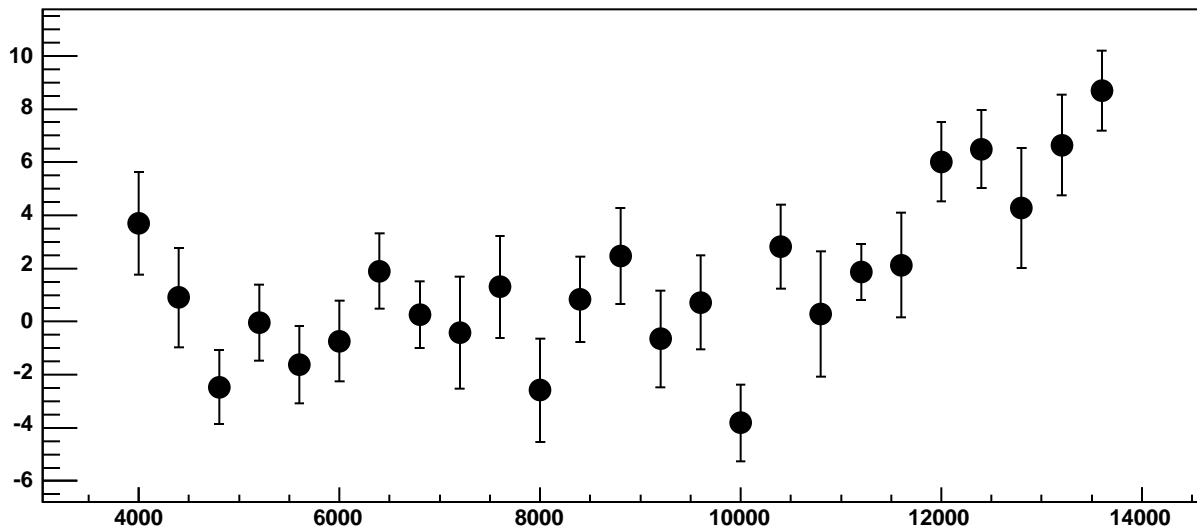
Chip 4, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



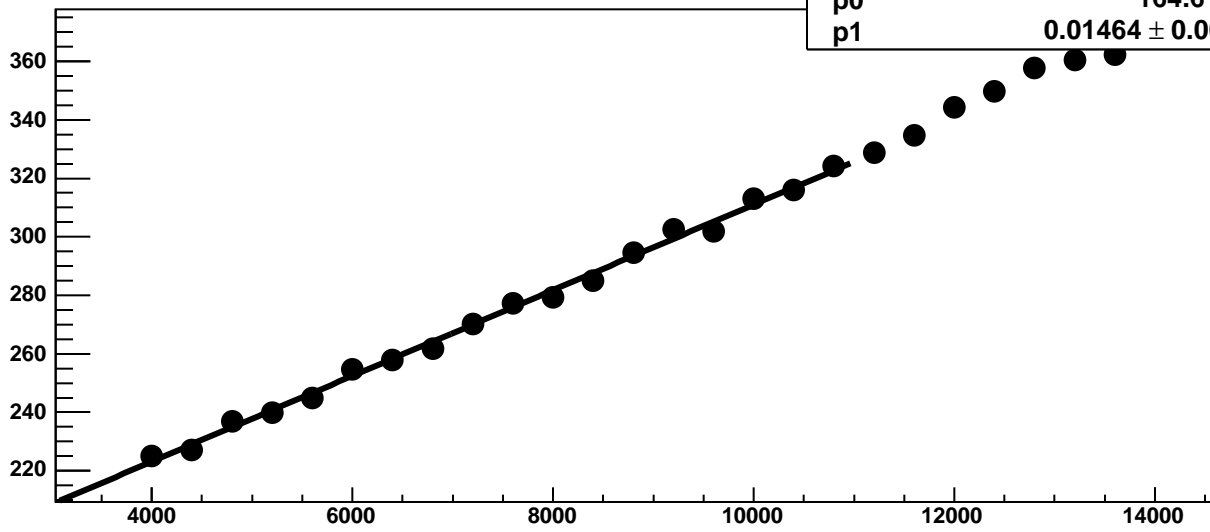
Chip 4, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

22.71 / 16

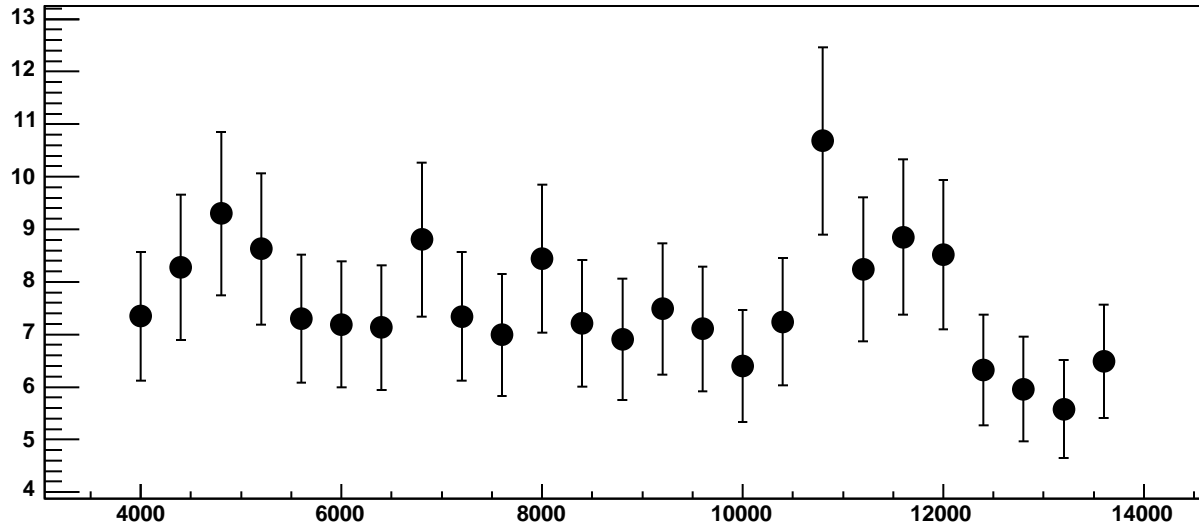
p0

164.6 ± 1.581

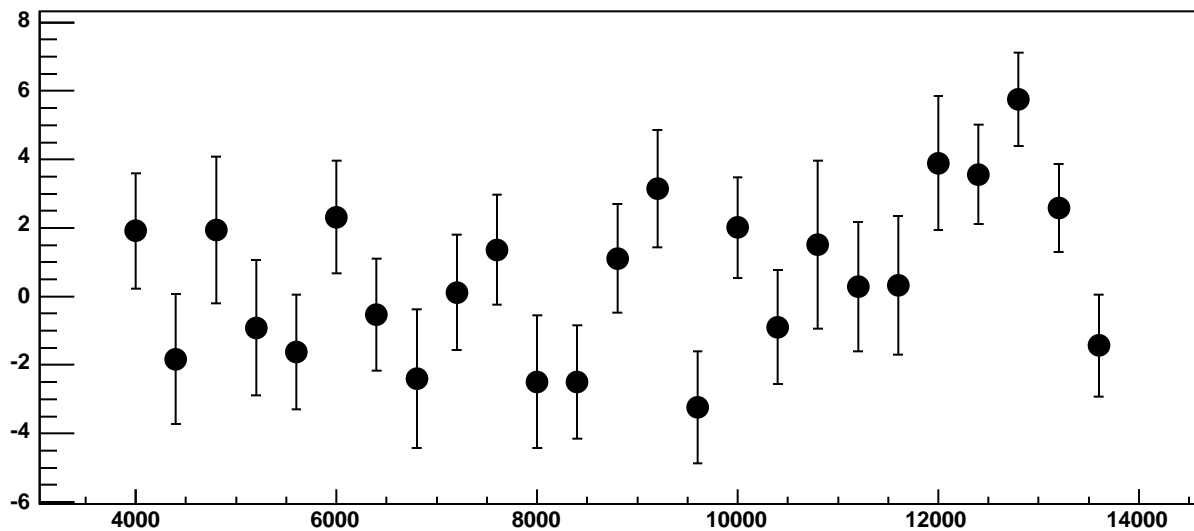
p1

0.01464 ± 0.0002038

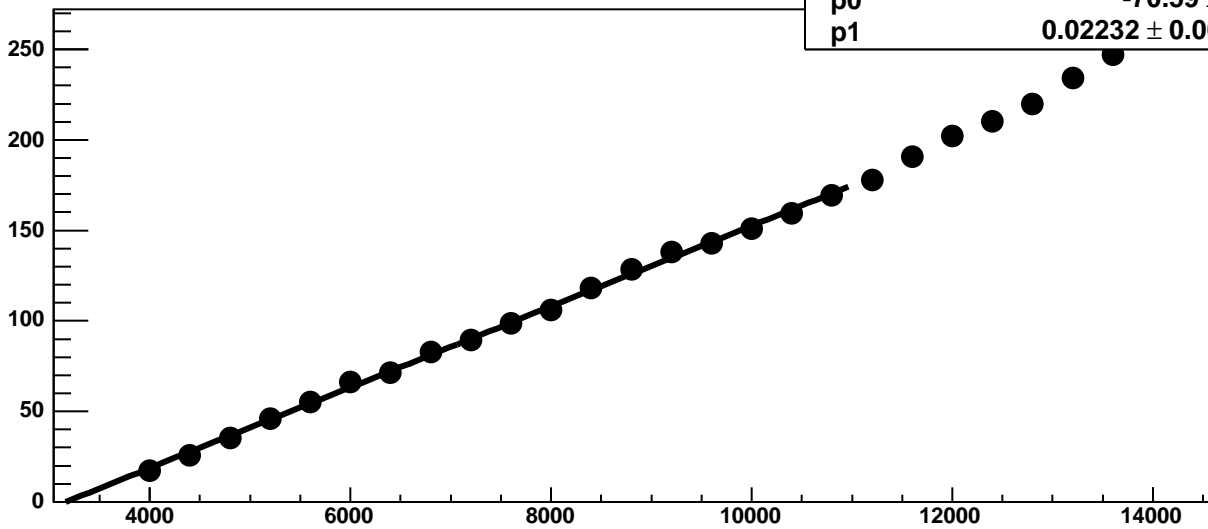
Chip 4, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

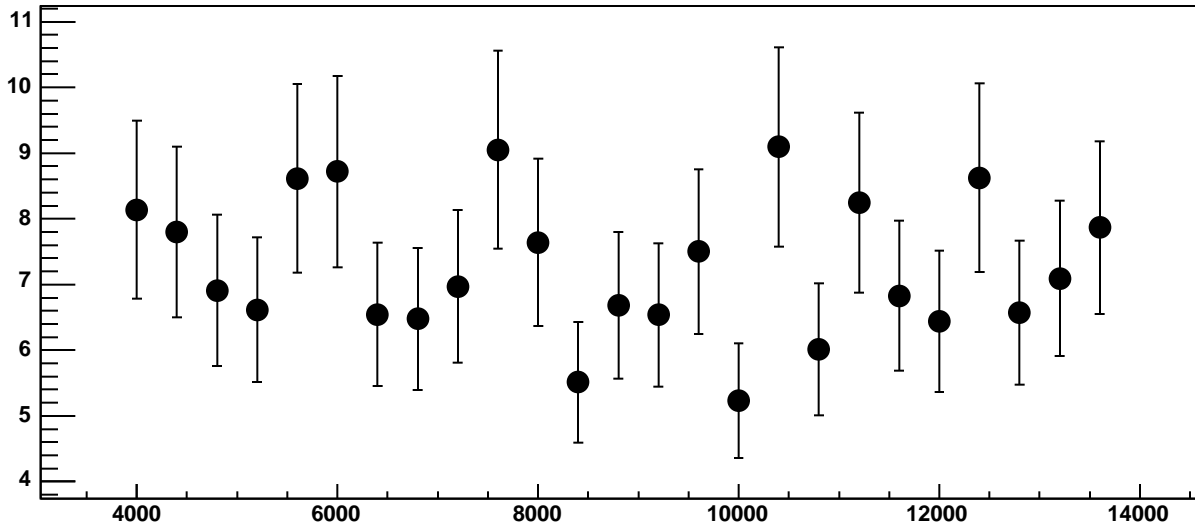


Chip 5, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

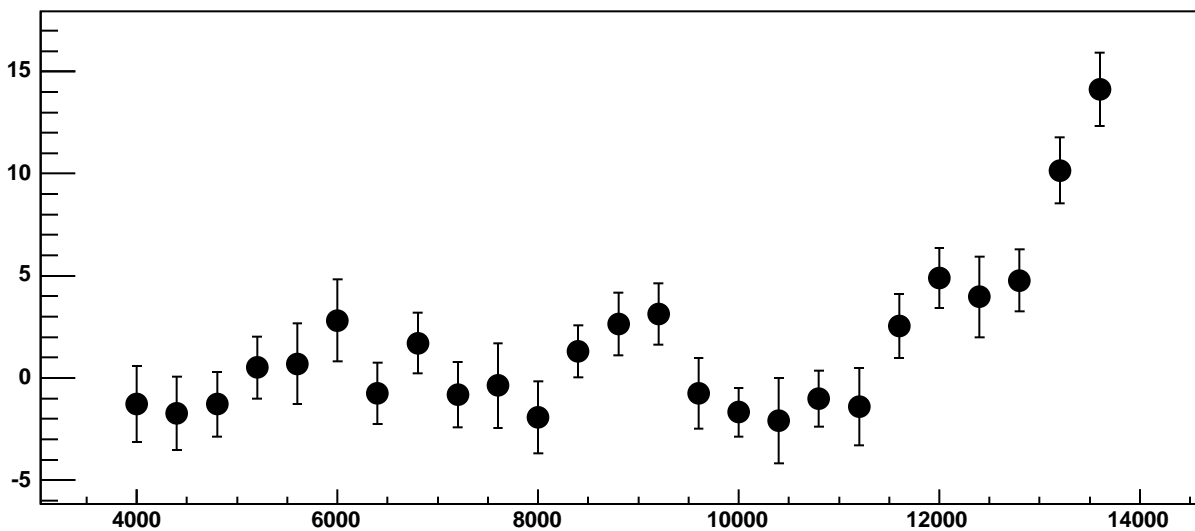


χ^2 / ndf 19.36 / 16
p0 -70.59 ± 1.449
p1 0.02232 ± 0.0001829

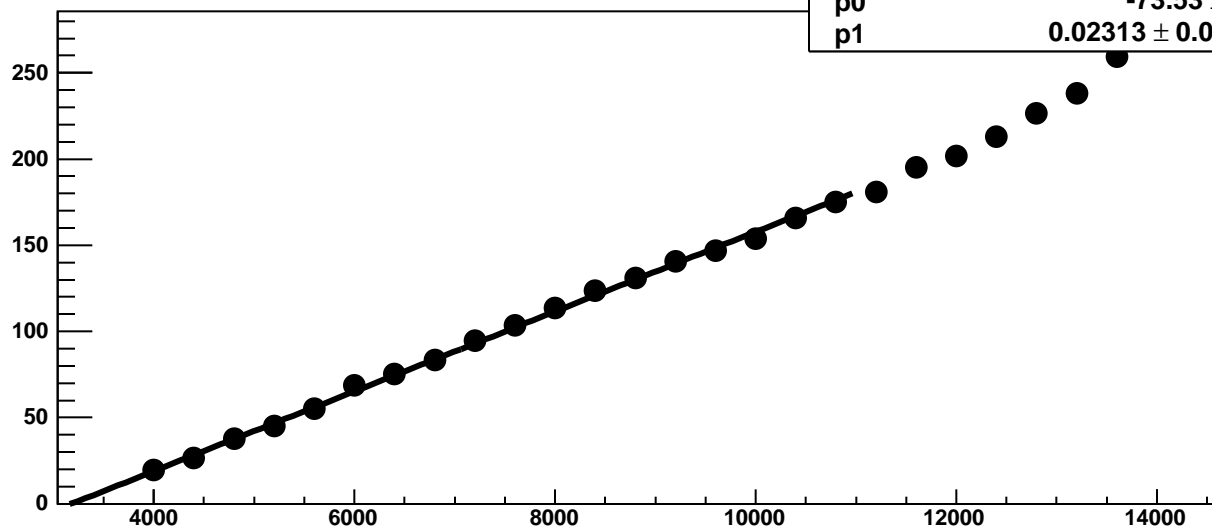
Chip 5, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

23.69 / 16

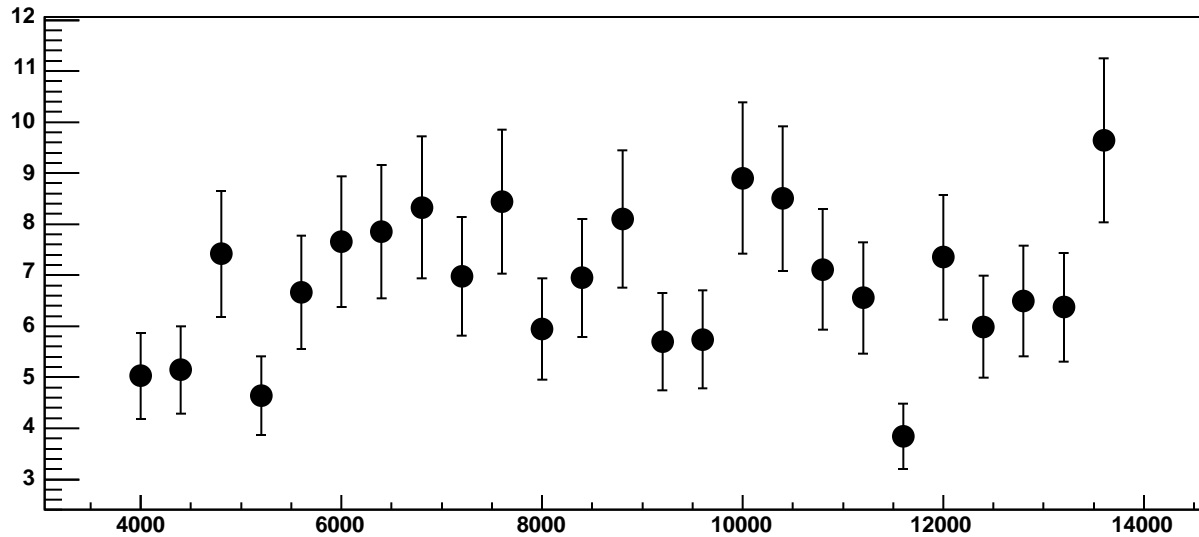
p0

-73.53 ± 1.203

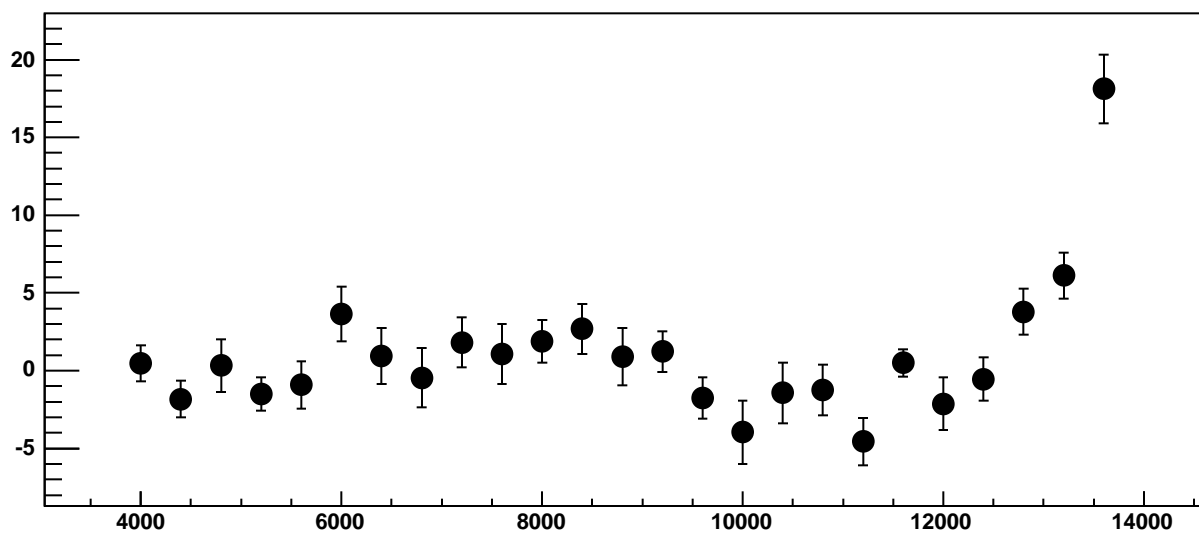
p1

0.02313 ± 0.0001641

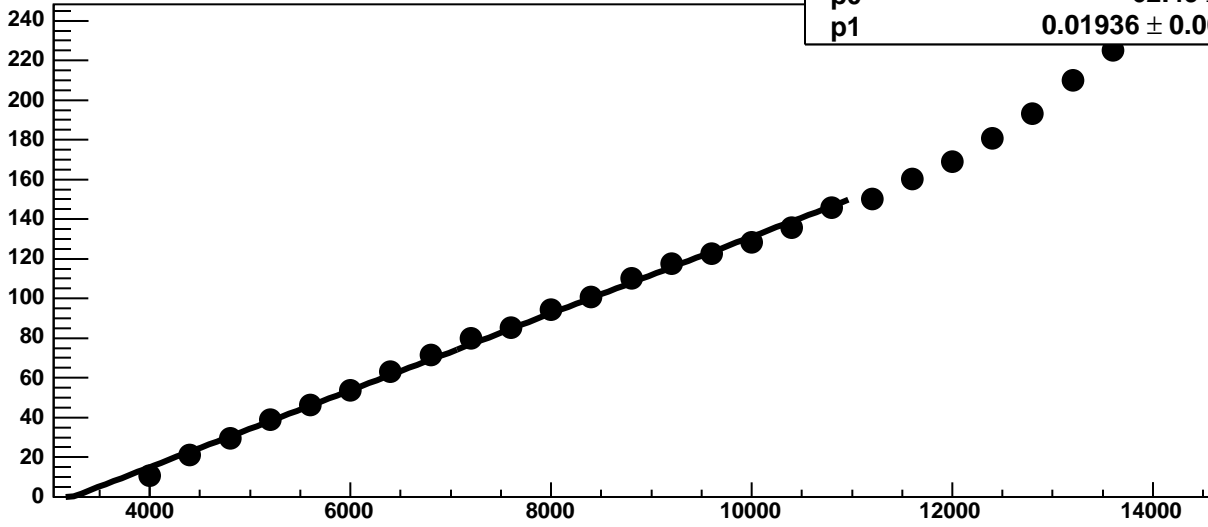
Chip 5, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

32.39 / 16

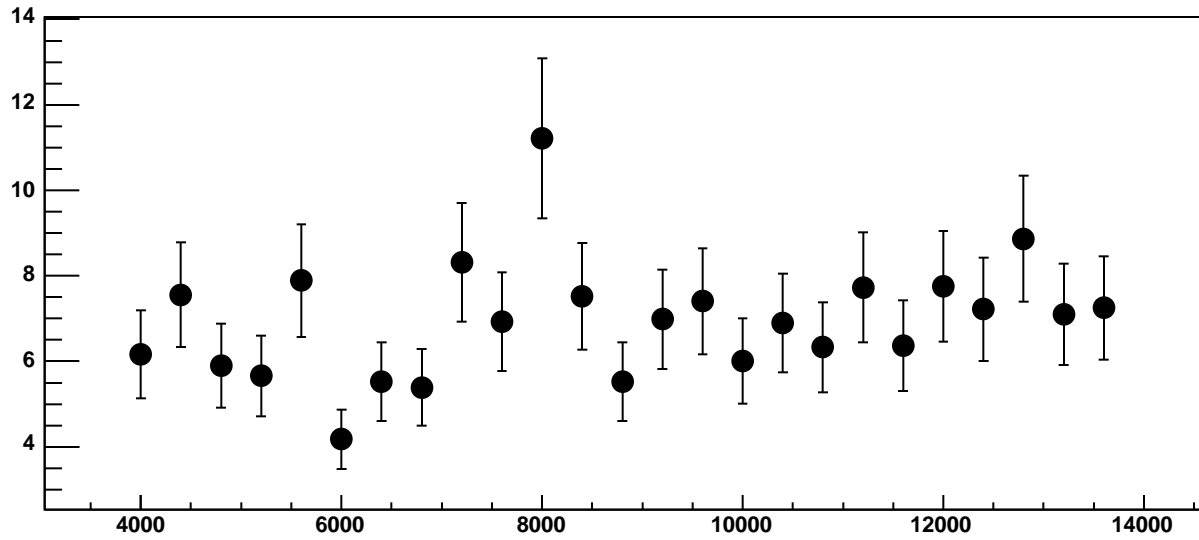
p0

-62.49 ± 1.243

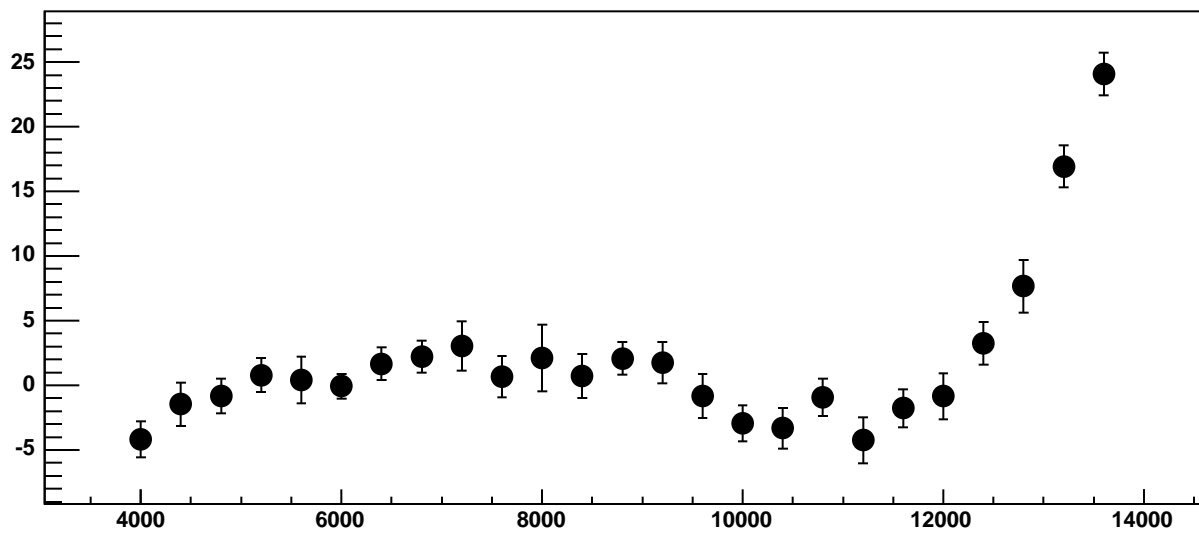
p1

0.01936 ± 0.0001658

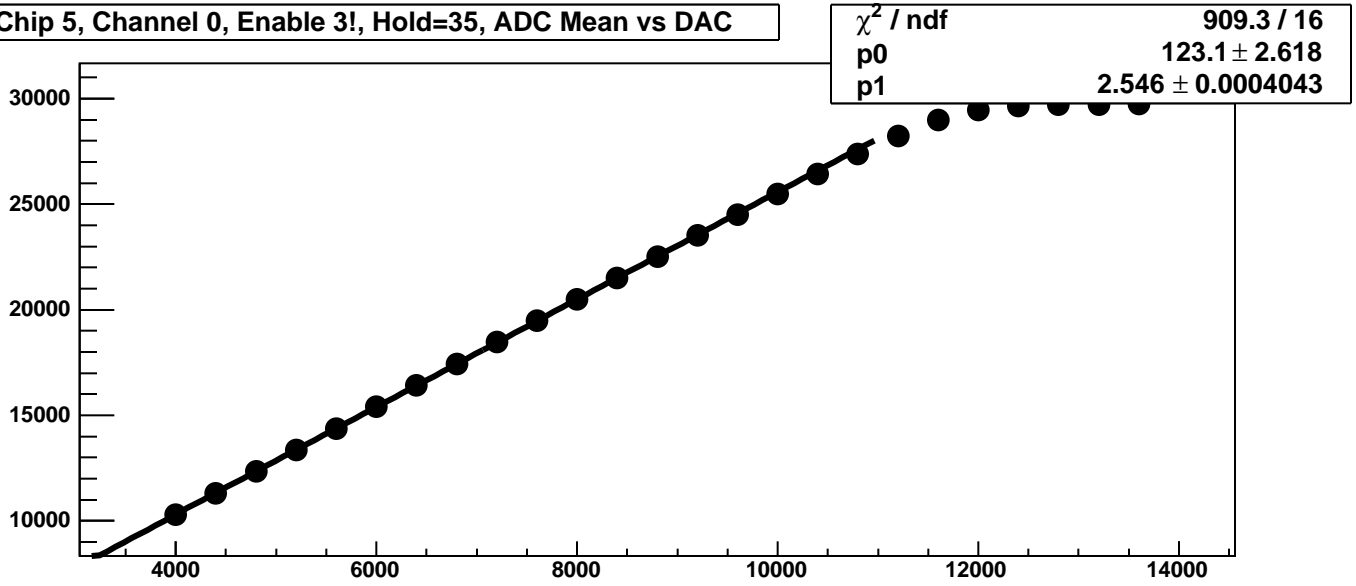
Chip 5, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



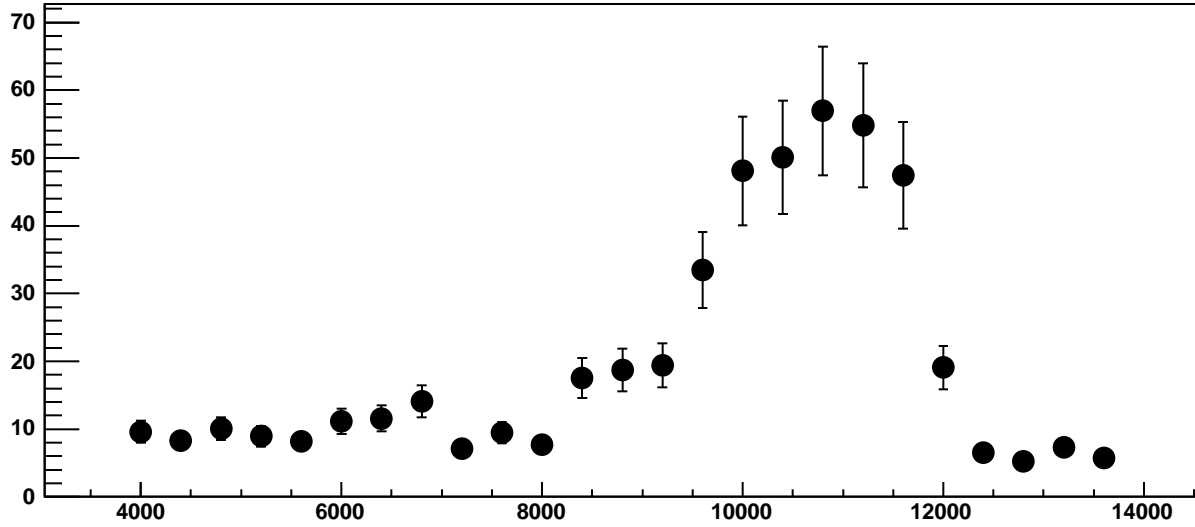
Chip 5, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



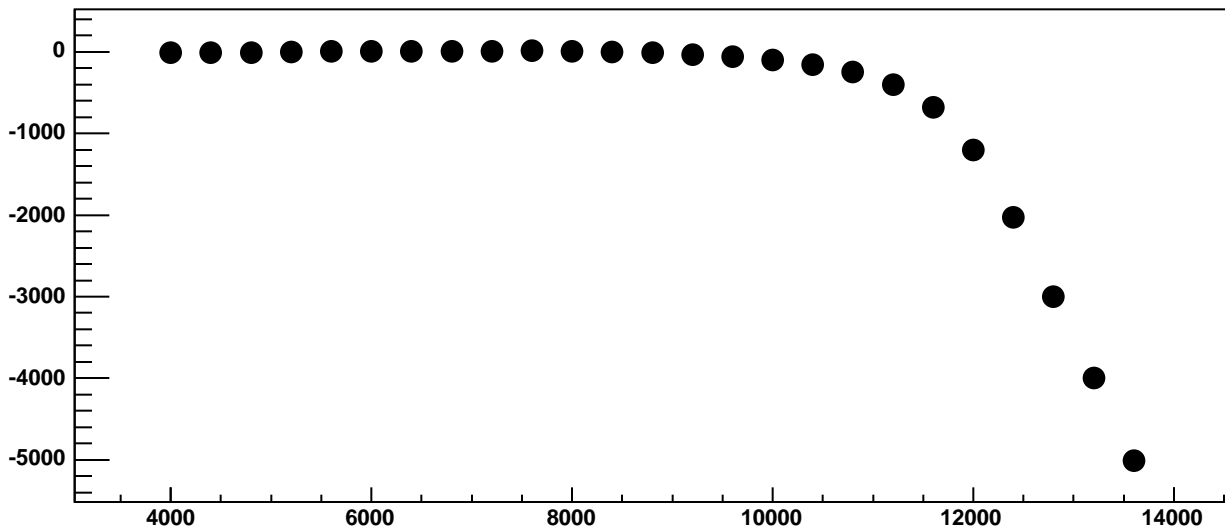
Chip 5, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC



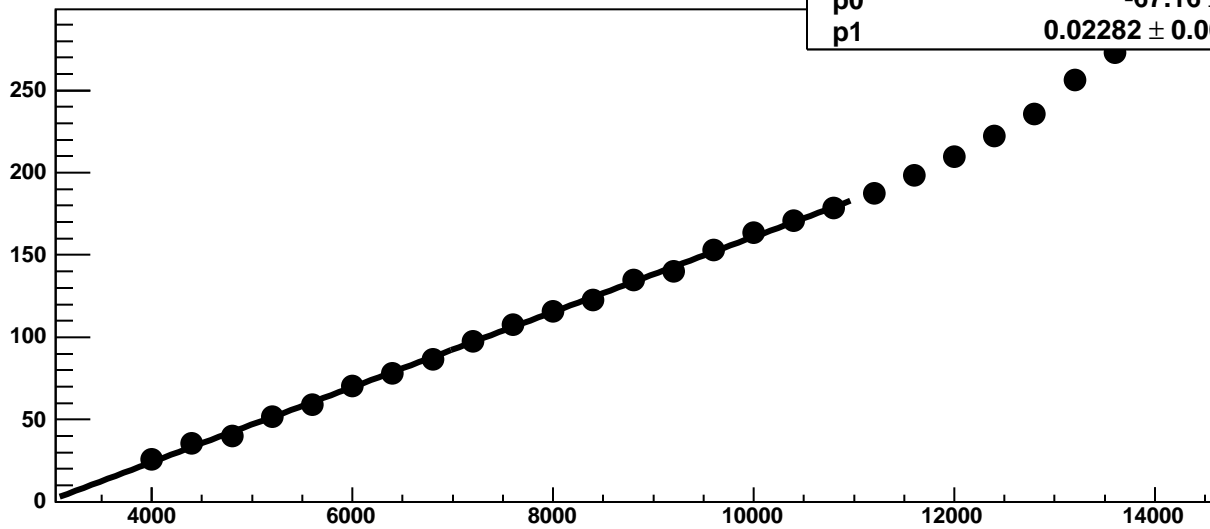
Chip 5, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC

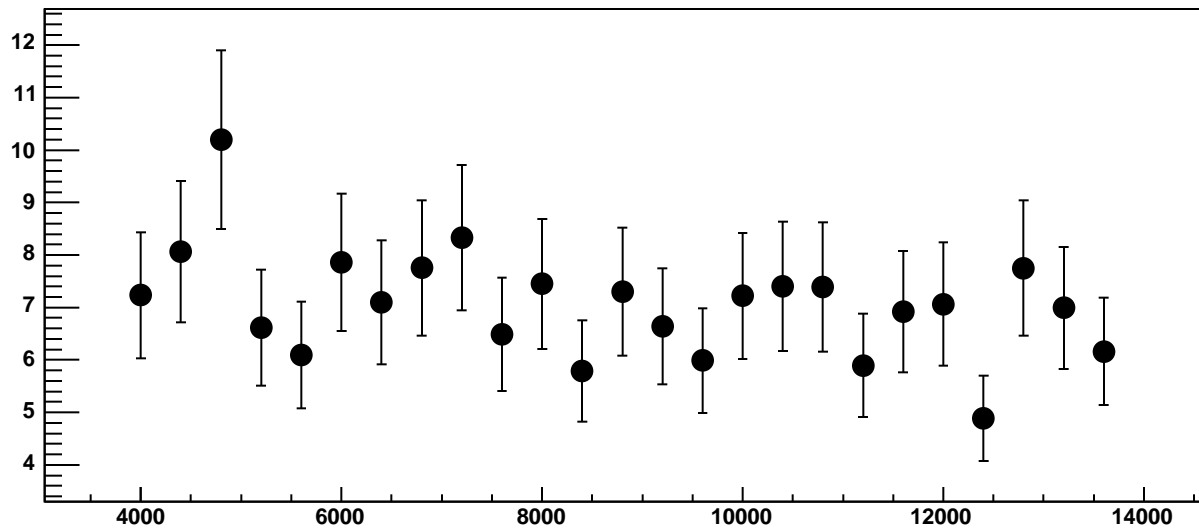


Chip 5, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

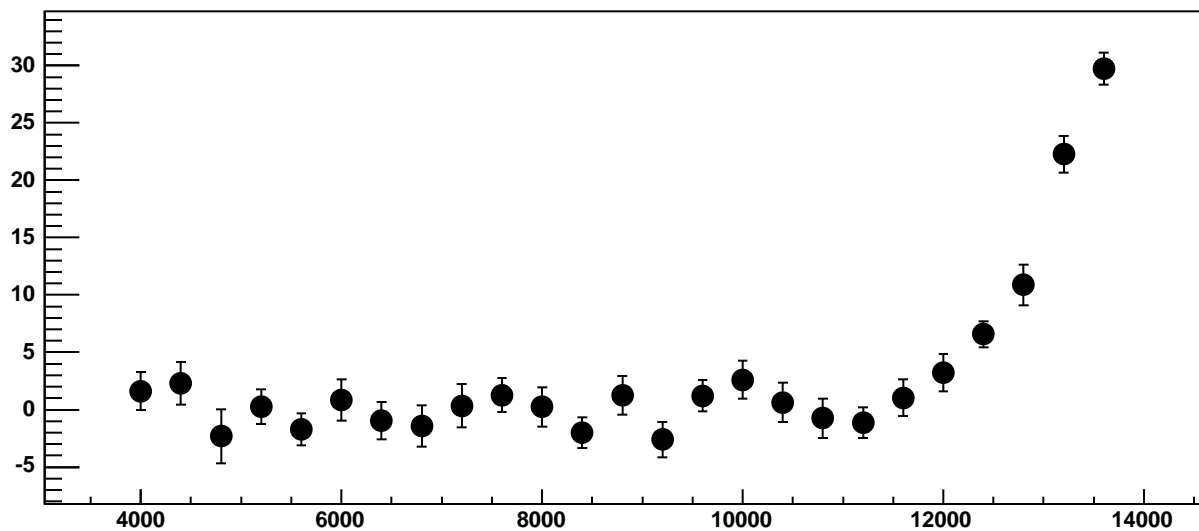


χ^2 / ndf 16.34 / 16
p0 -67.16 ± 1.482
p1 0.02282 ± 0.0001898

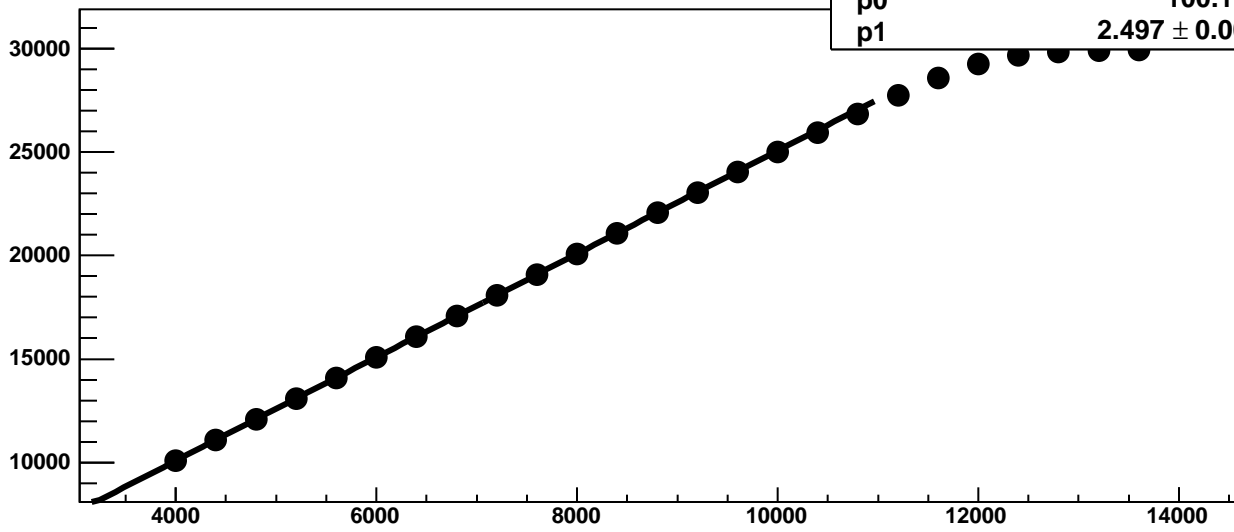
Chip 5, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



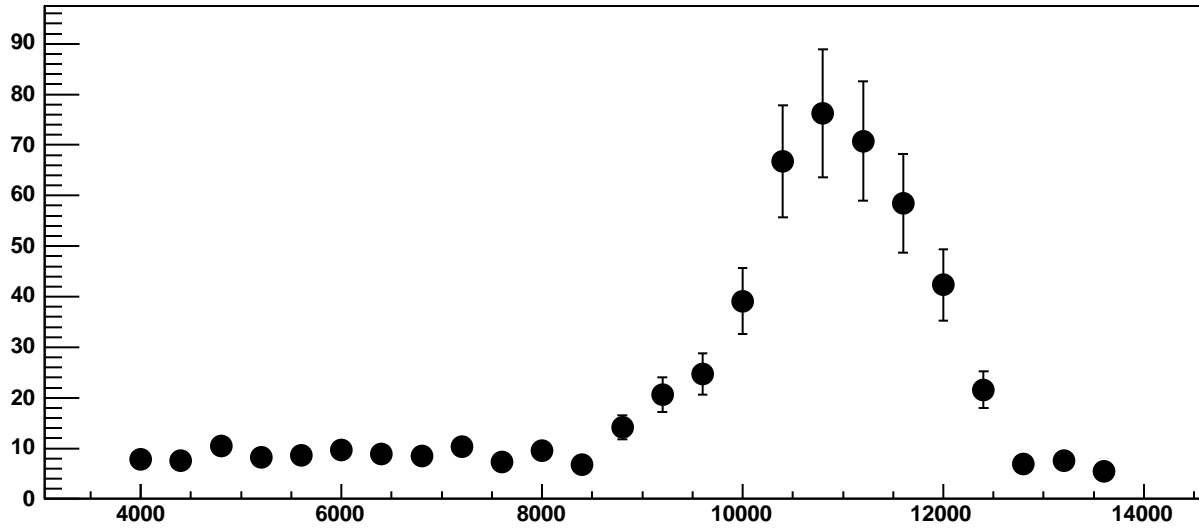
Chip 5, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



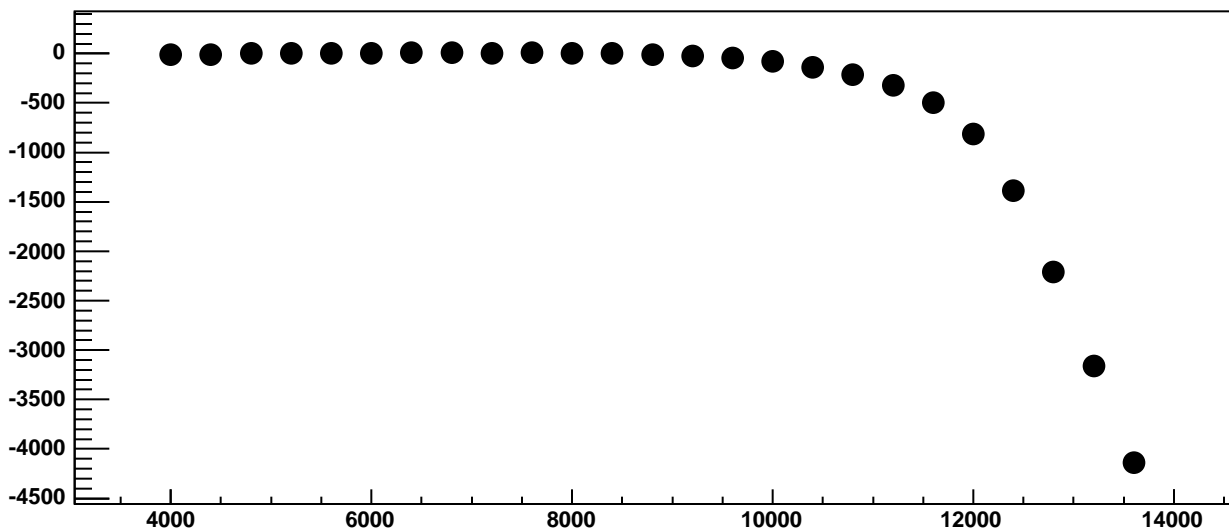
Chip 5, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



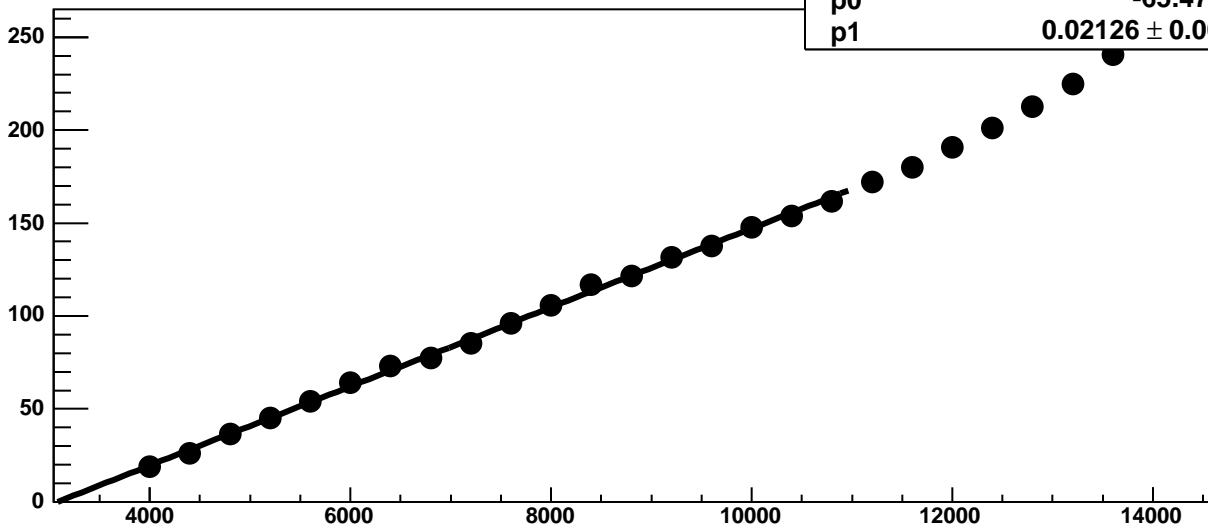
Chip 5, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

18.92 / 16

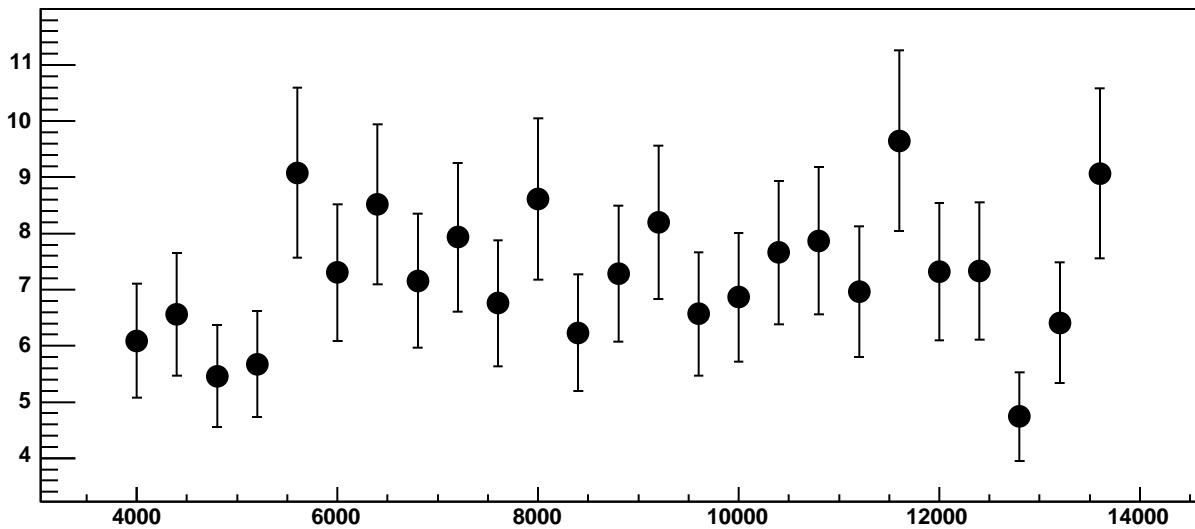
p0

-65.47 ± 1.32

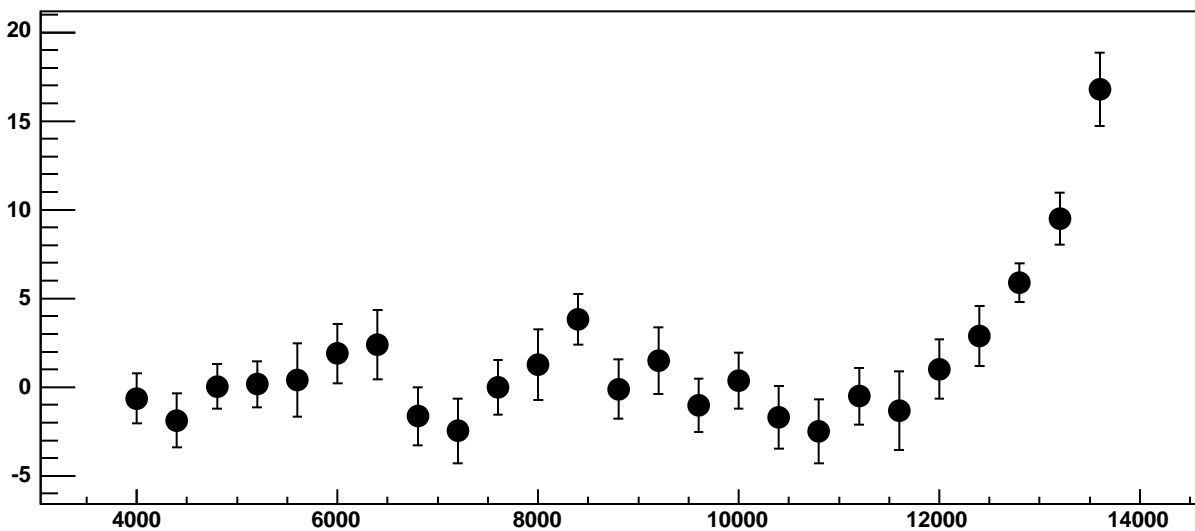
p1

0.02126 ± 0.0001767

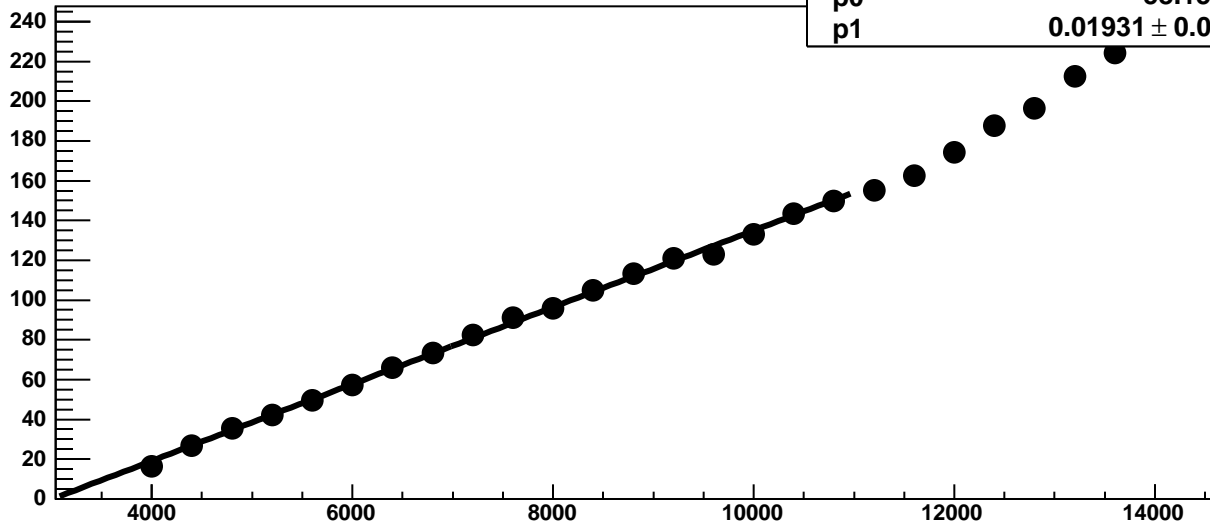
Chip 5, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

18.39 / 16

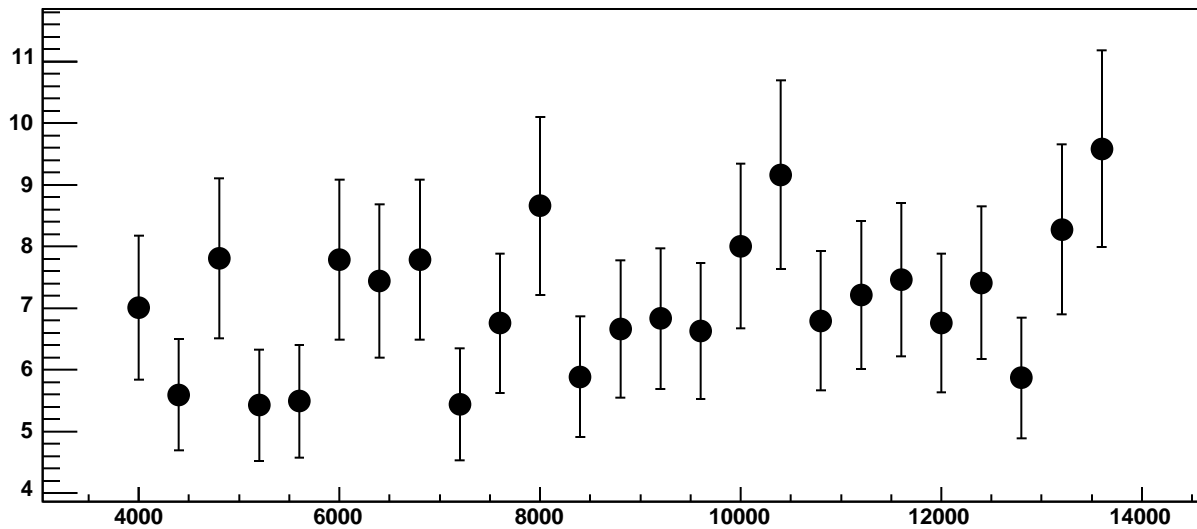
p0

-58.15 ± 1.32

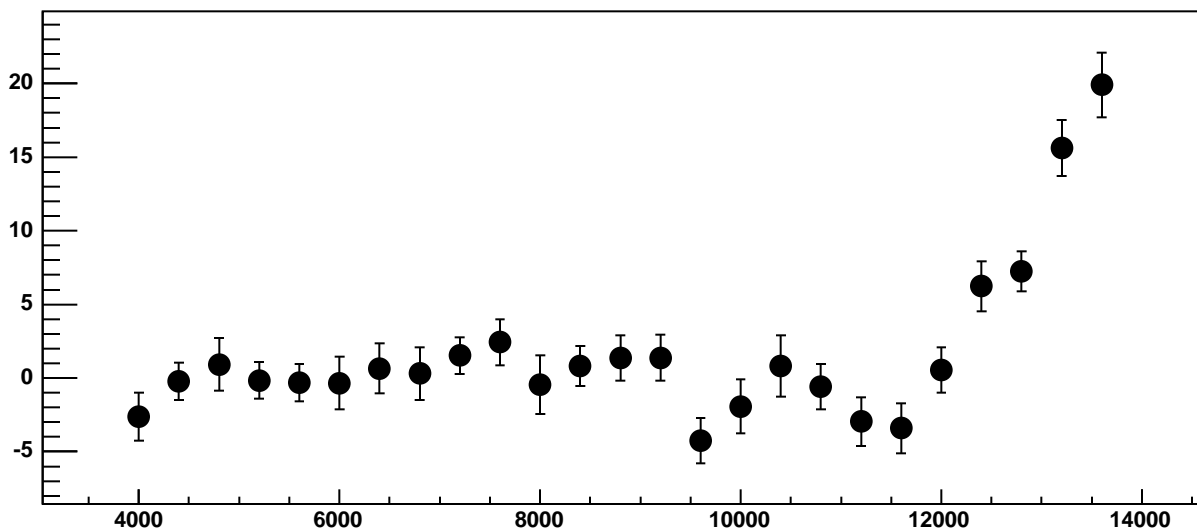
p1

0.01931 ± 0.0001771

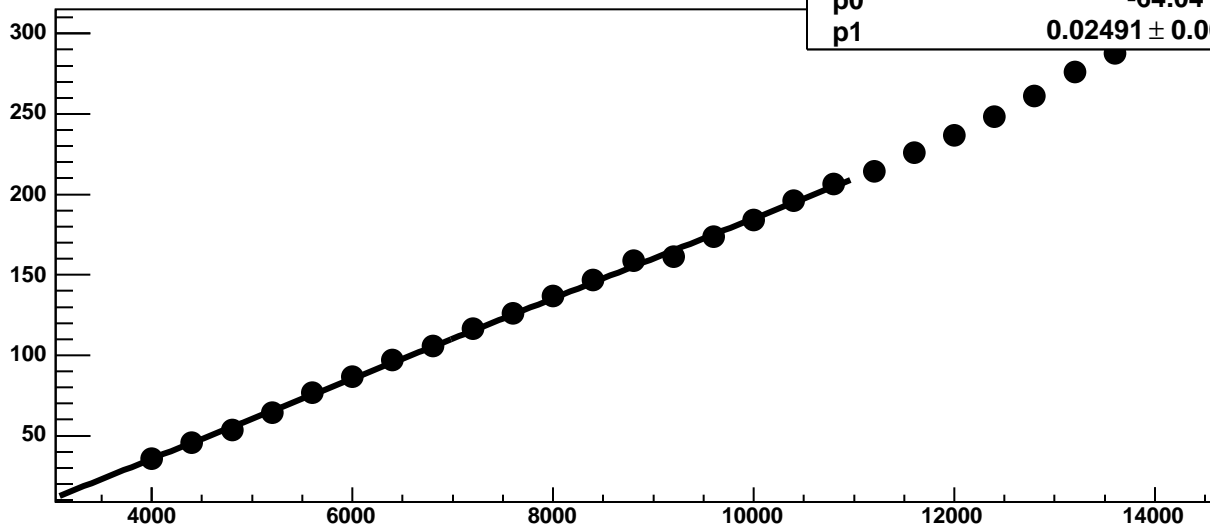
Chip 5, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

24.88 / 16

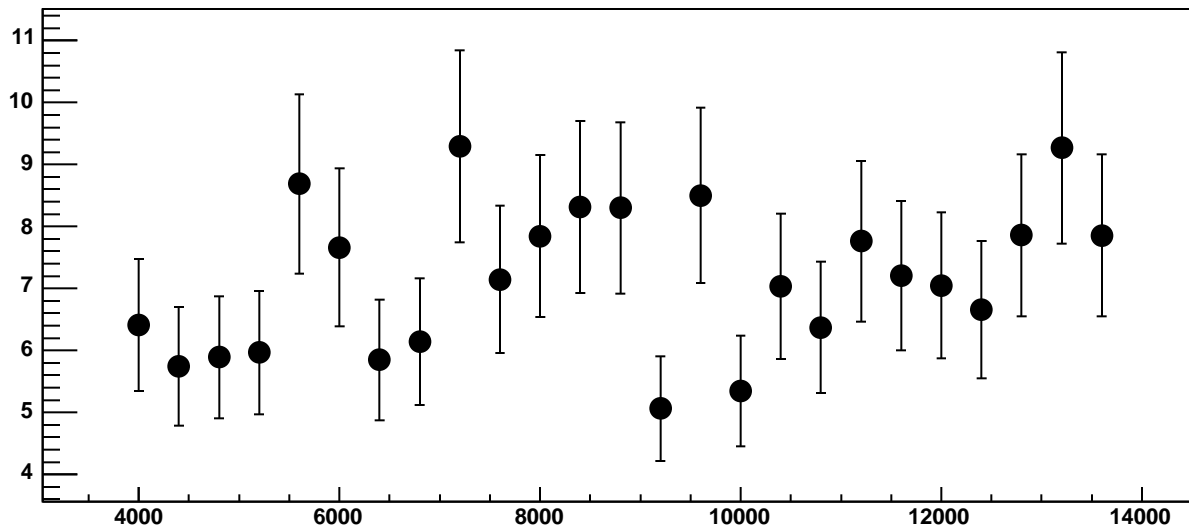
p0

-64.04 ± 1.261

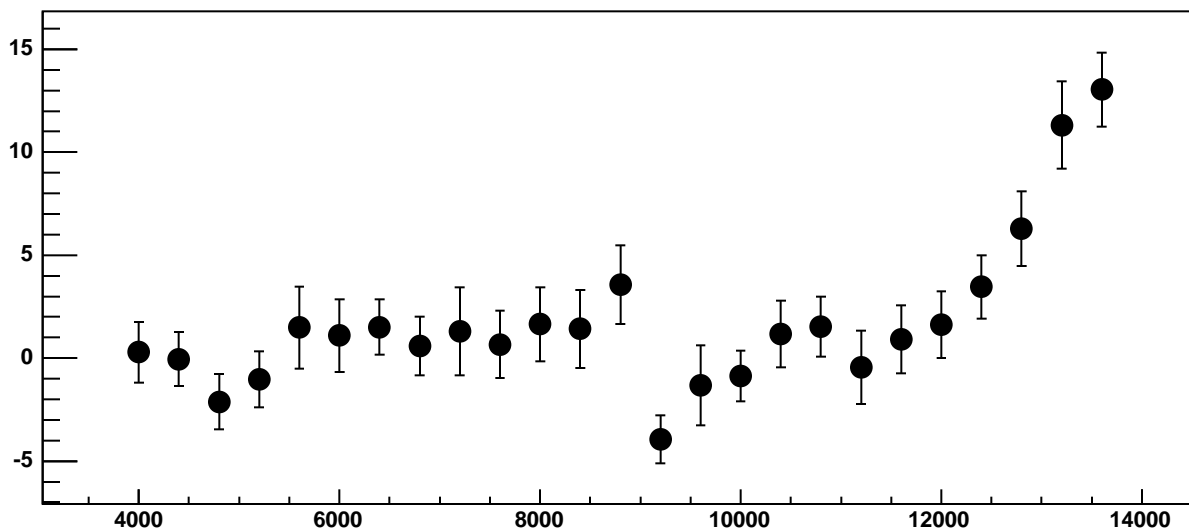
p1

0.02491 ± 0.0001645

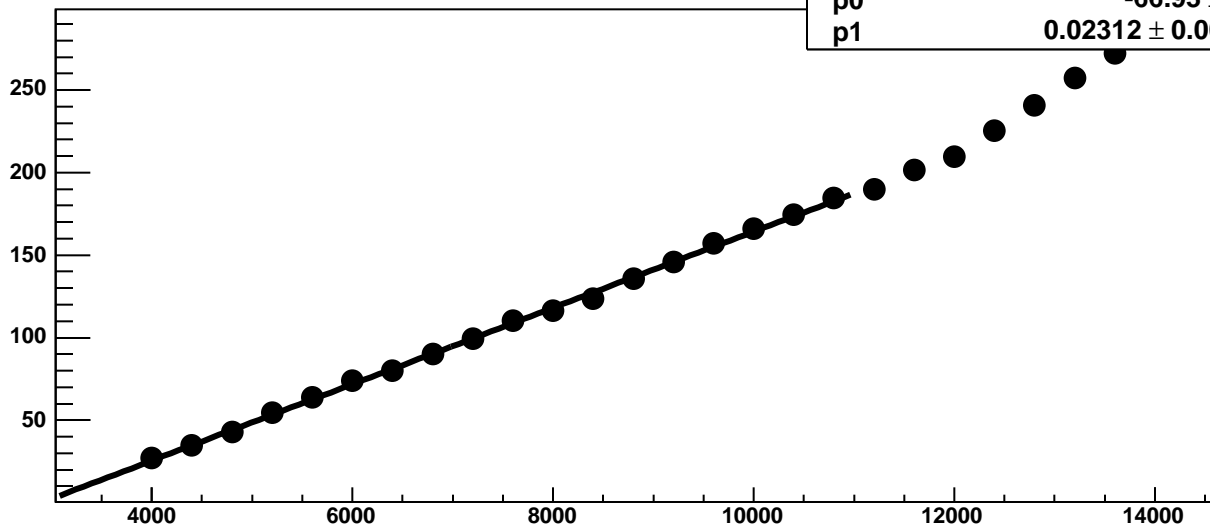
Chip 5, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



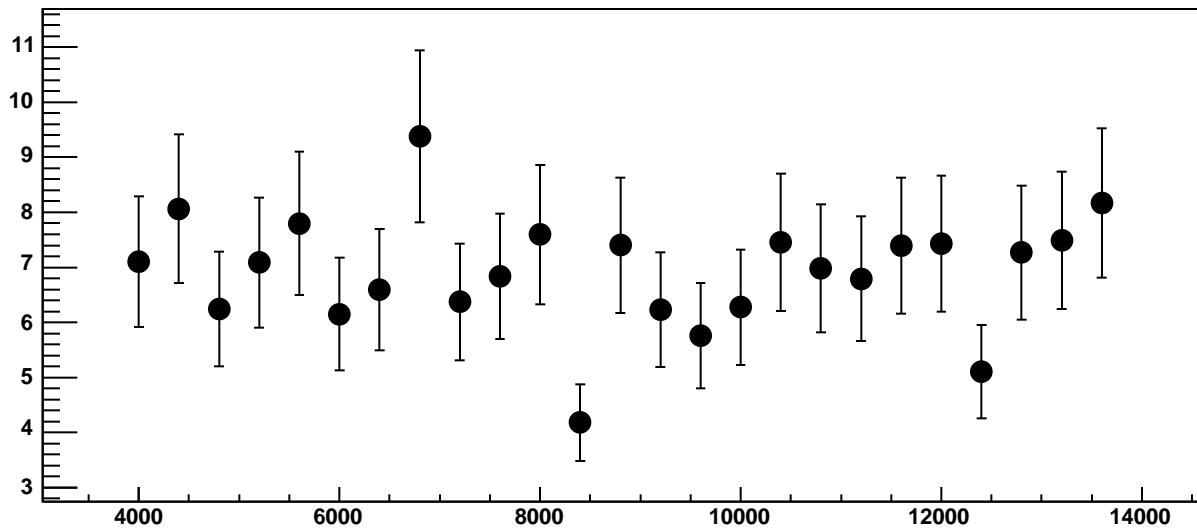
Chip 5, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC



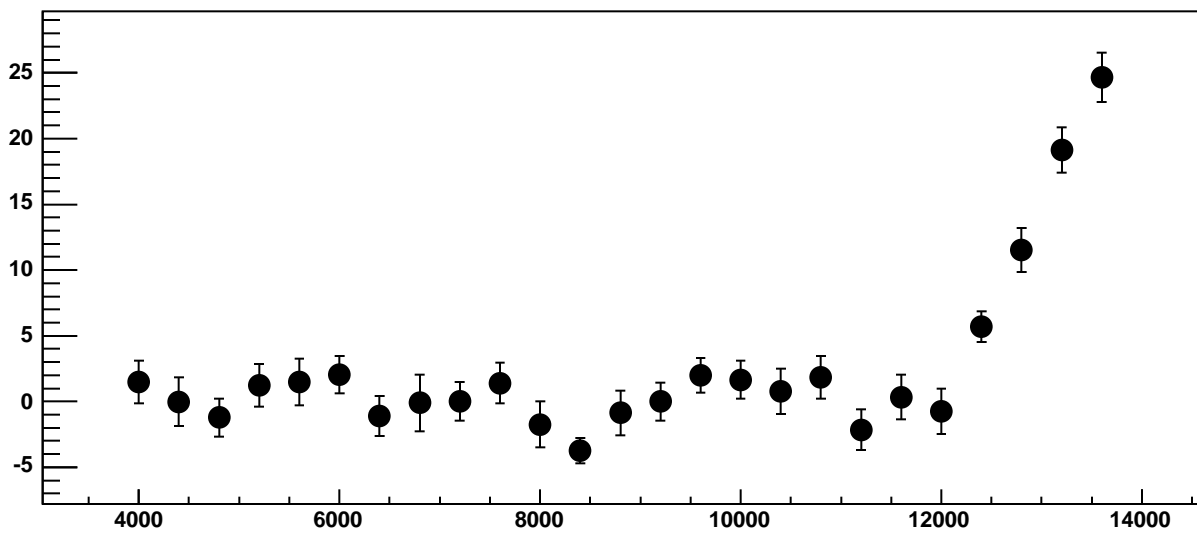
Chip 5, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



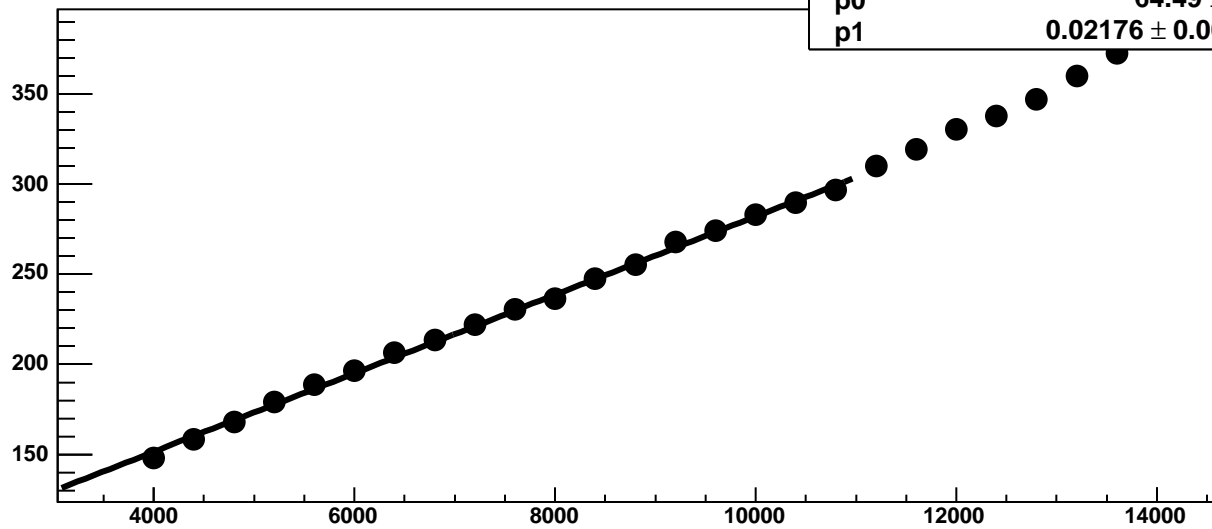
Chip 5, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

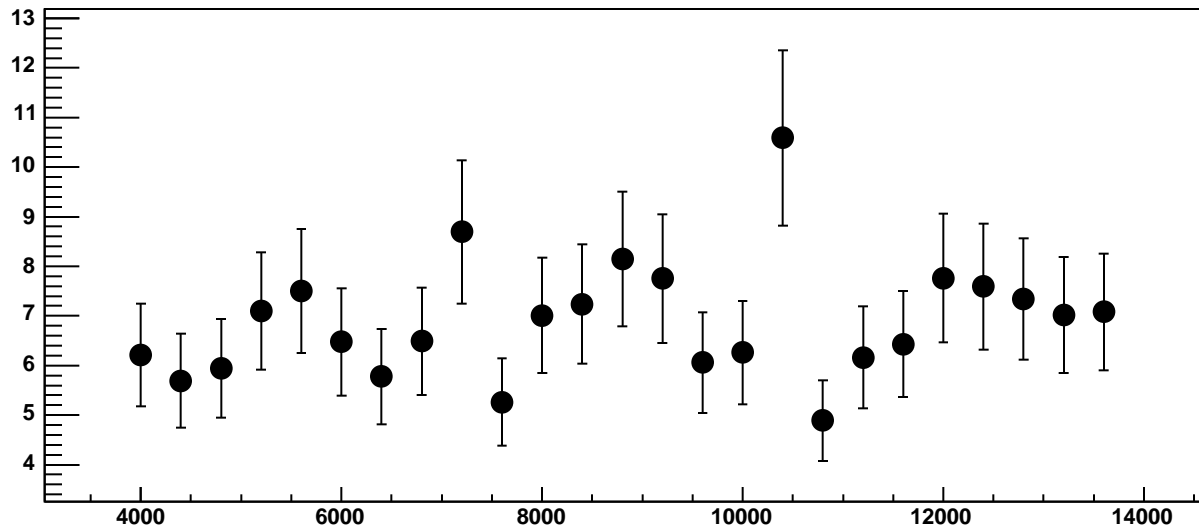


Chip 5, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC

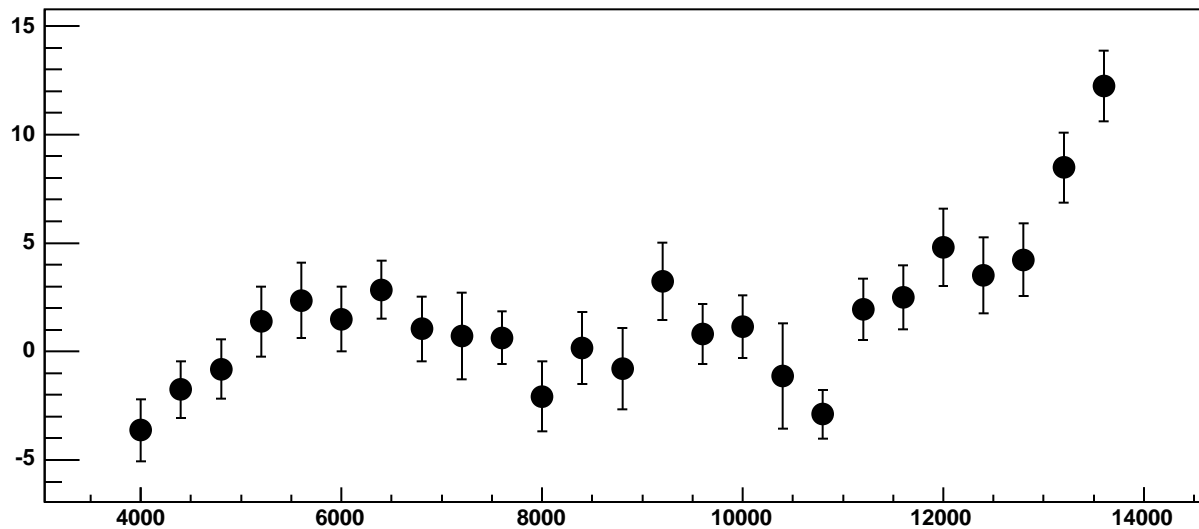


χ^2 / ndf 30.66 / 16
p0 64.49 ± 1.246
p1 0.02176 ± 0.0001628

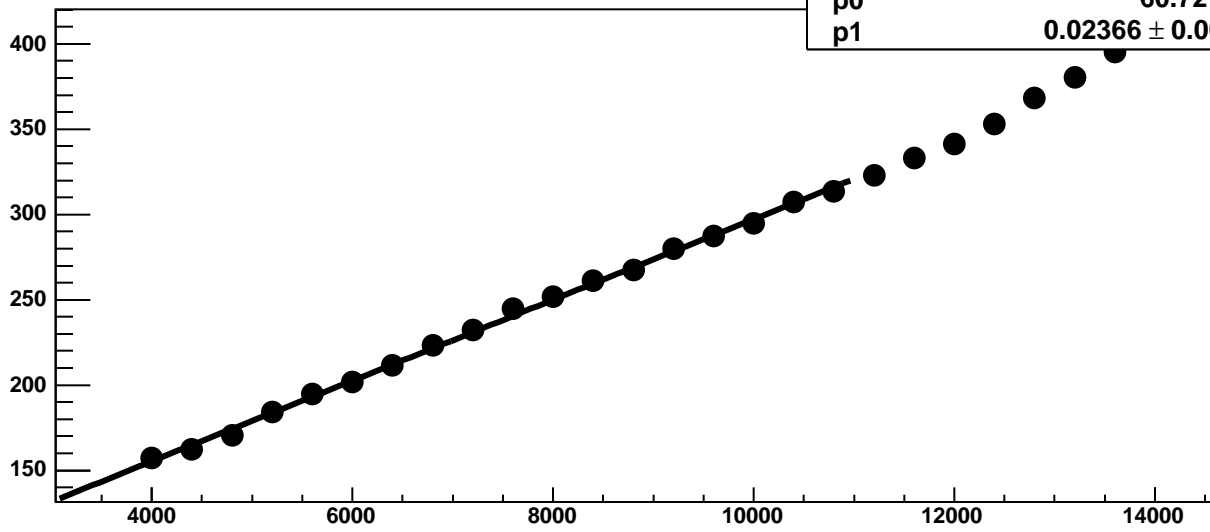
Chip 5, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

35.29 / 16

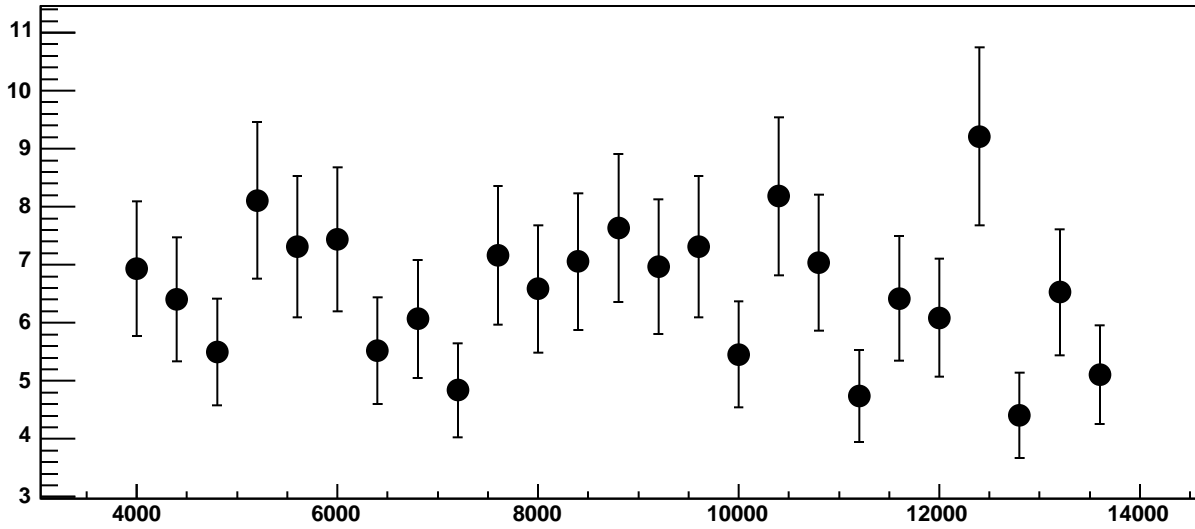
p0

60.72 ± 1.33

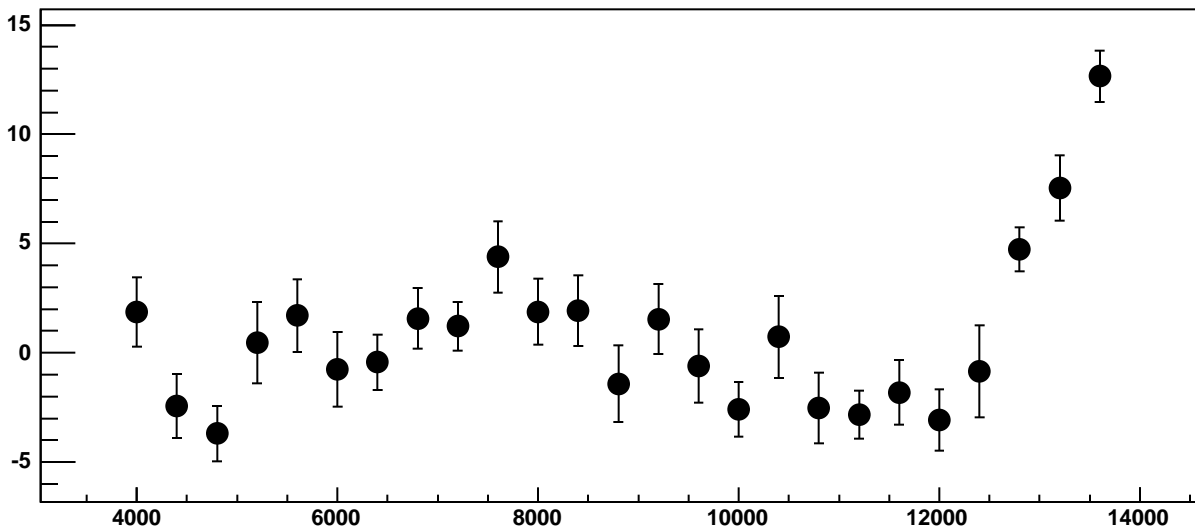
p1

0.02366 ± 0.0001755

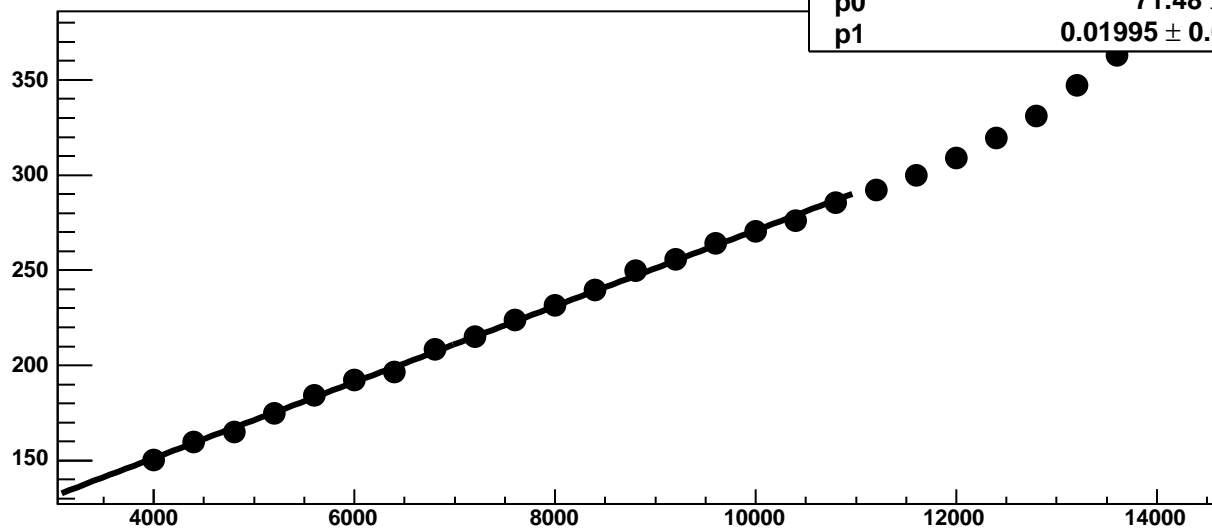
Chip 5, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

16.58 / 16

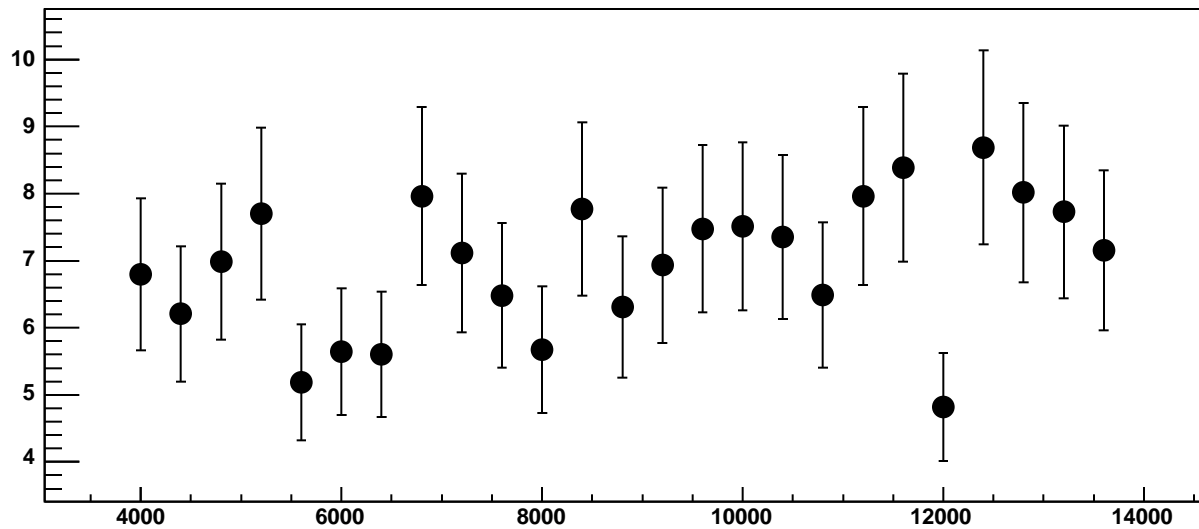
p0

71.48 ± 1.316

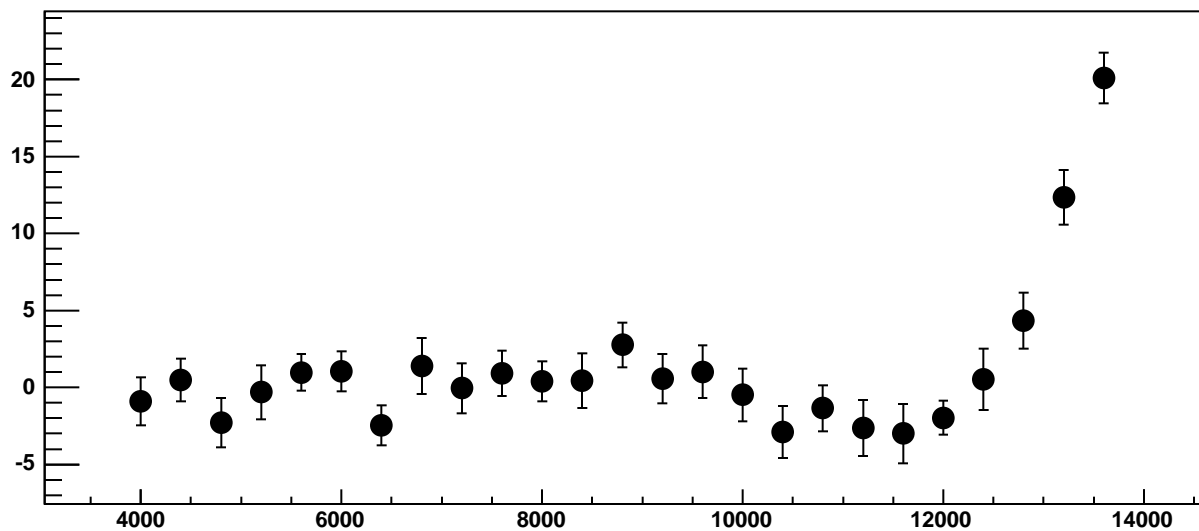
p1

0.01995 ± 0.000175

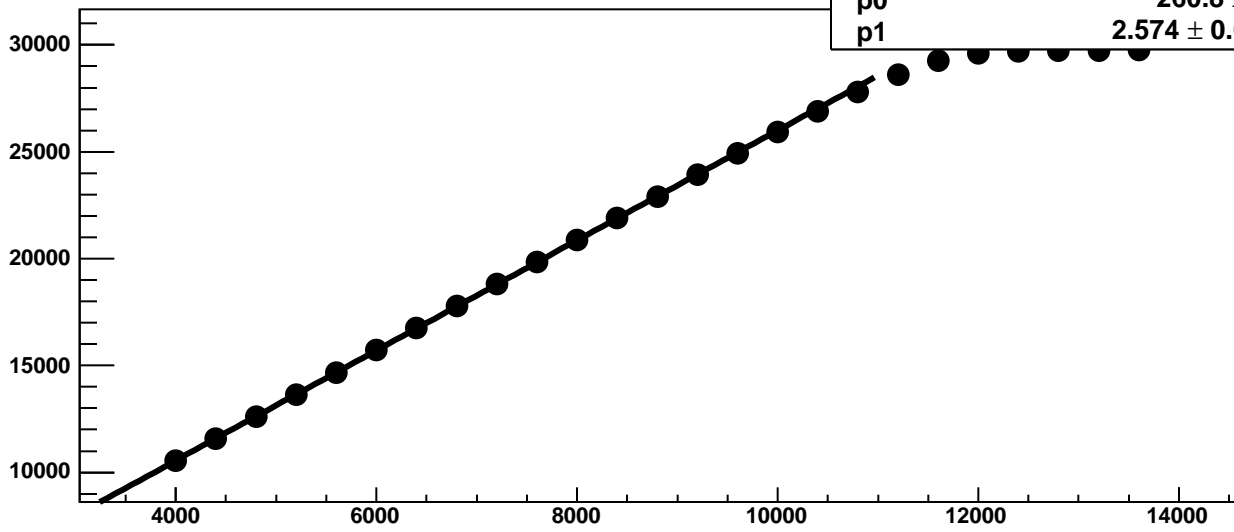
Chip 5, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



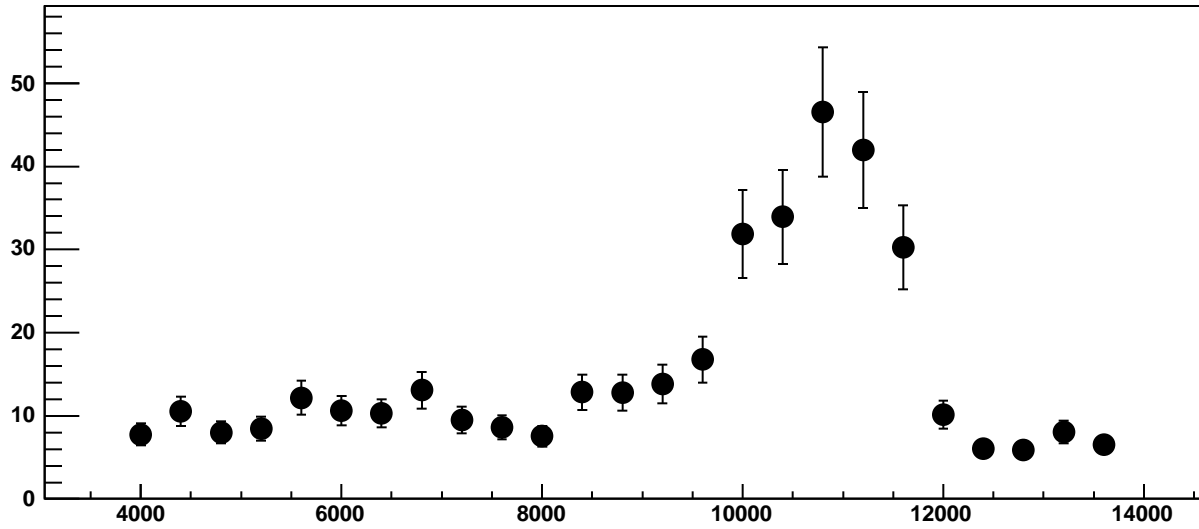
Chip 5, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



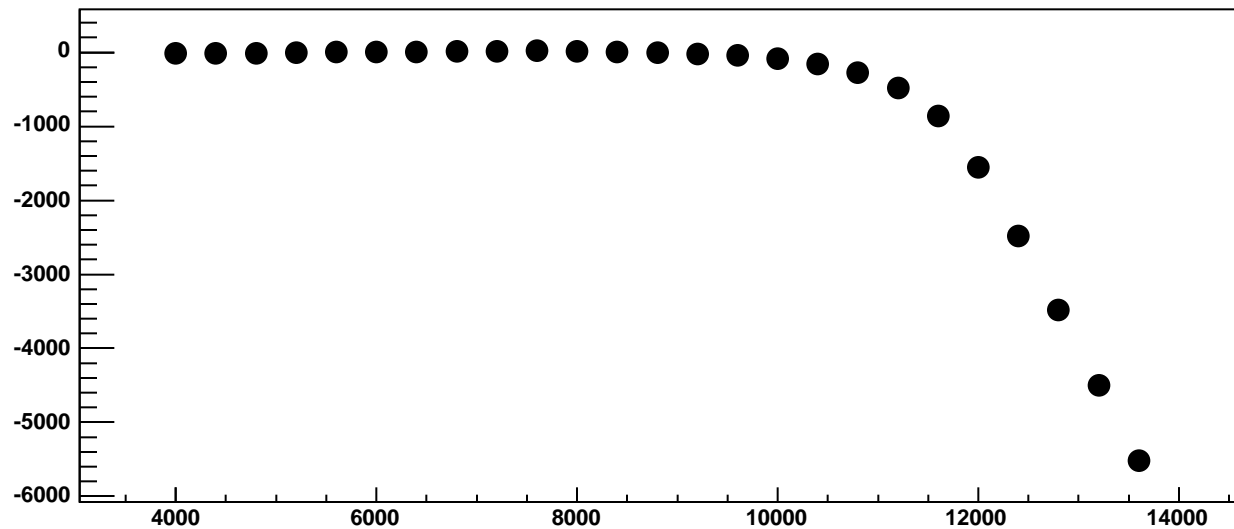
Chip 5, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC



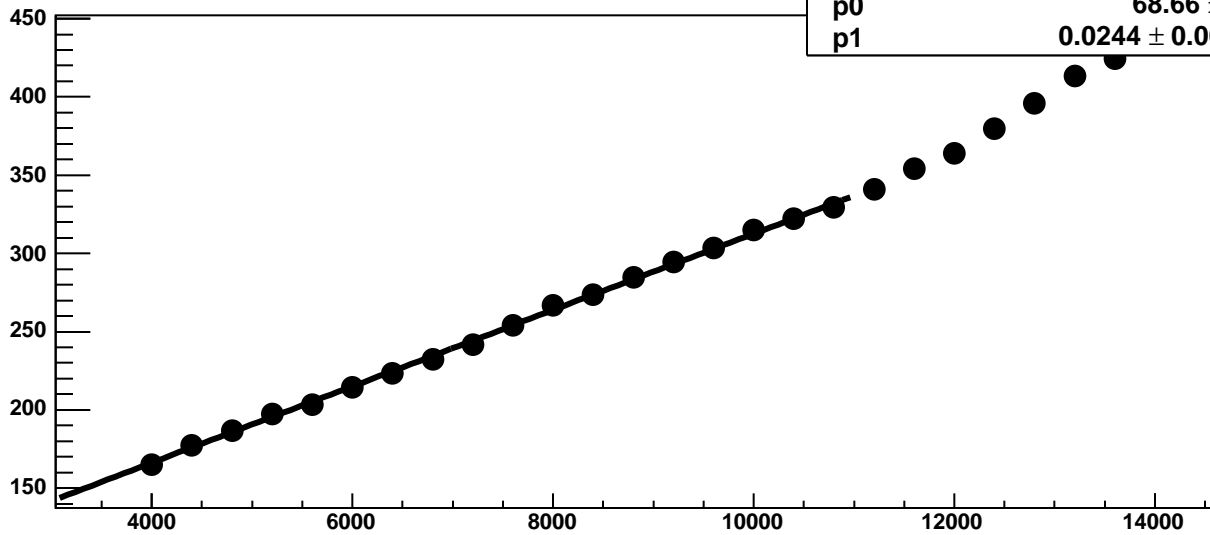
Chip 5, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



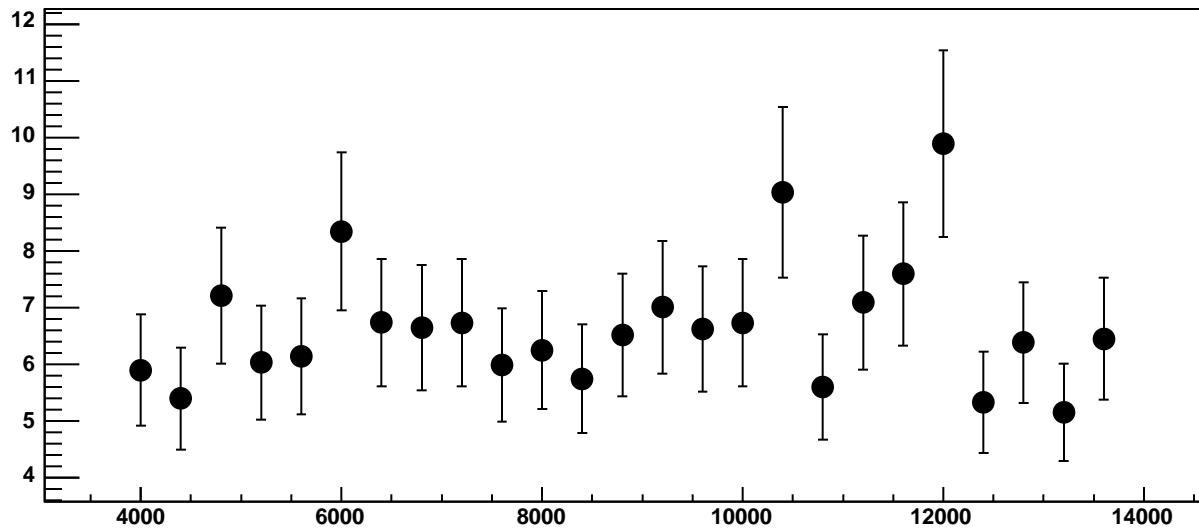
Chip 5, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC



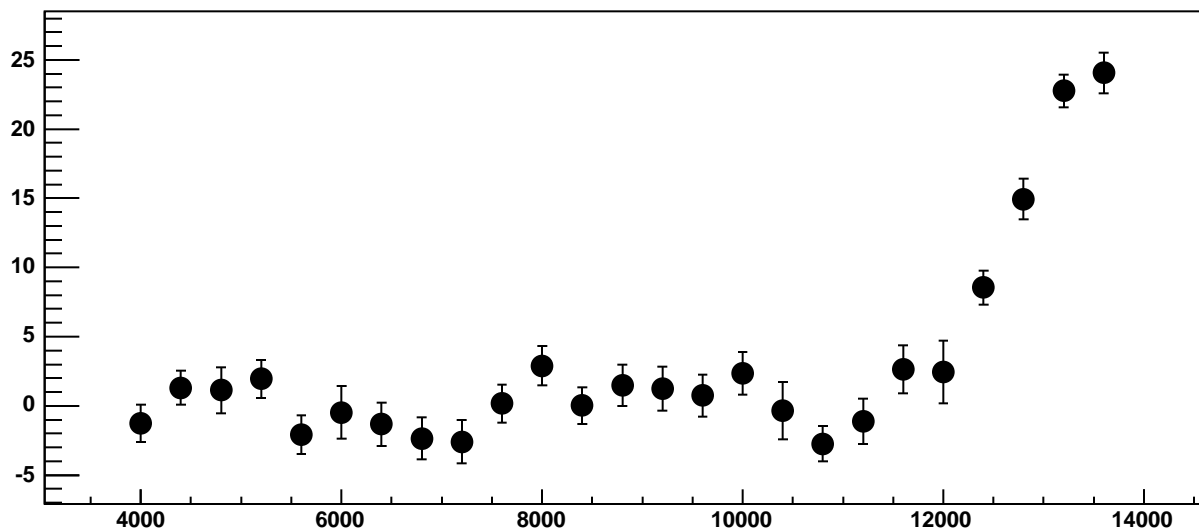
Chip 5, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC



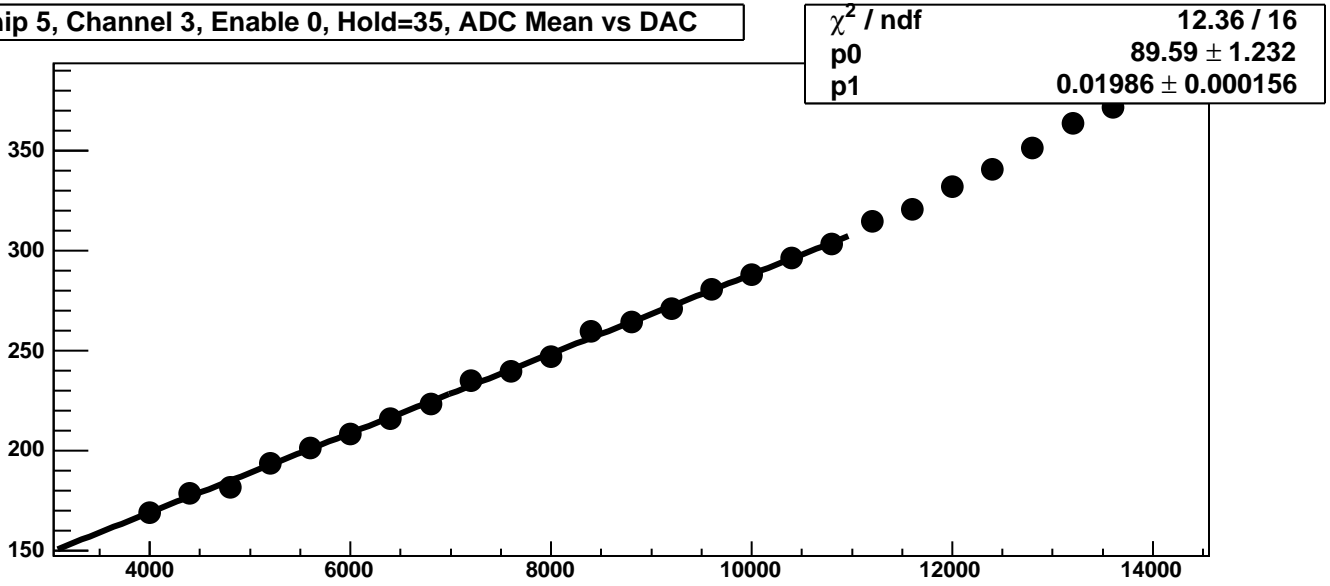
Chip 5, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



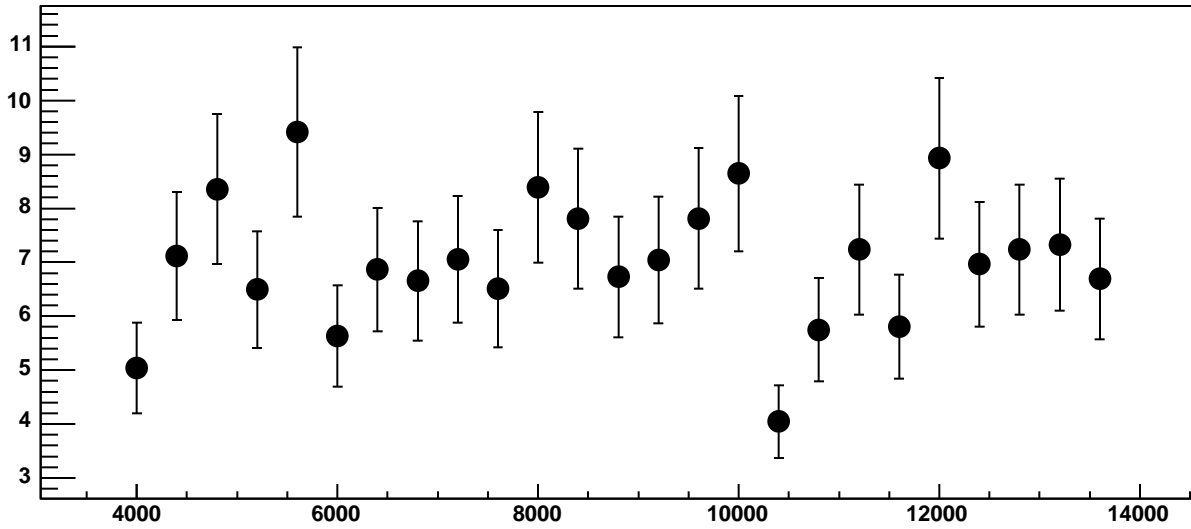
Chip 5, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



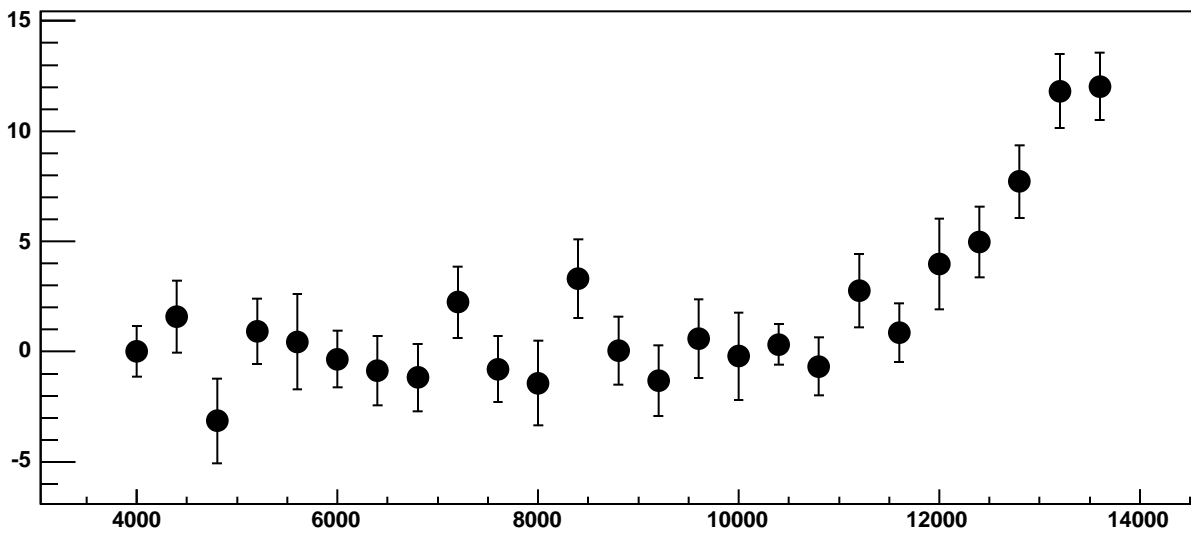
Chip 5, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



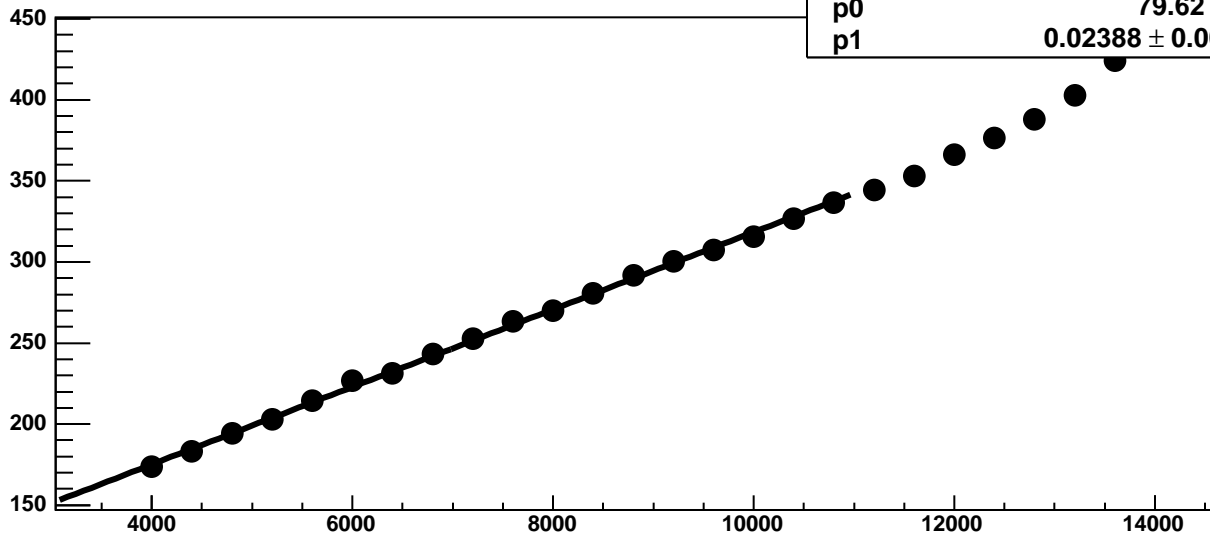
Chip 5, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

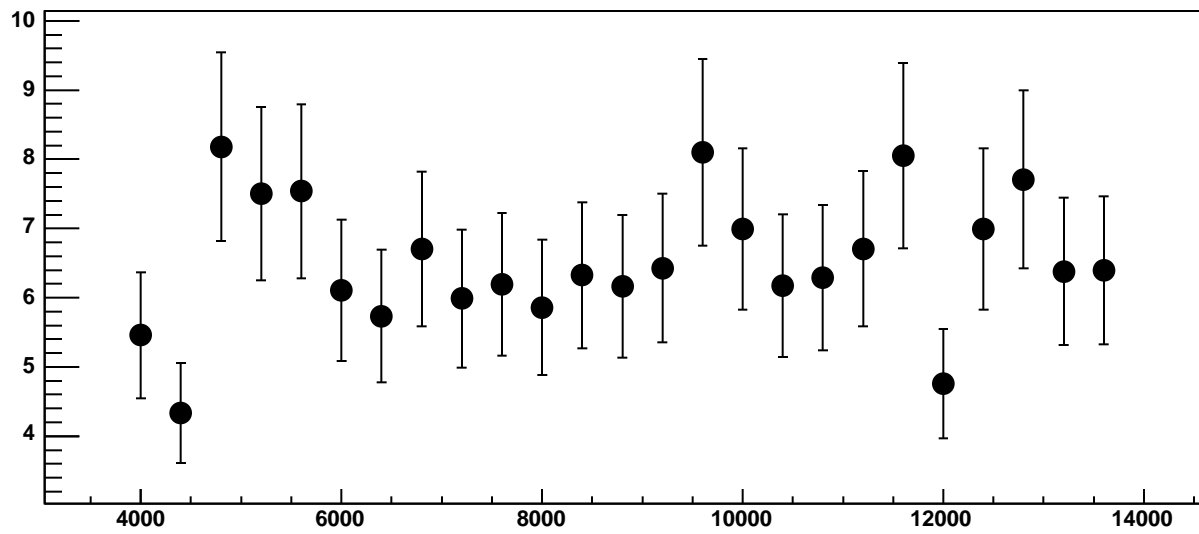


Chip 5, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC

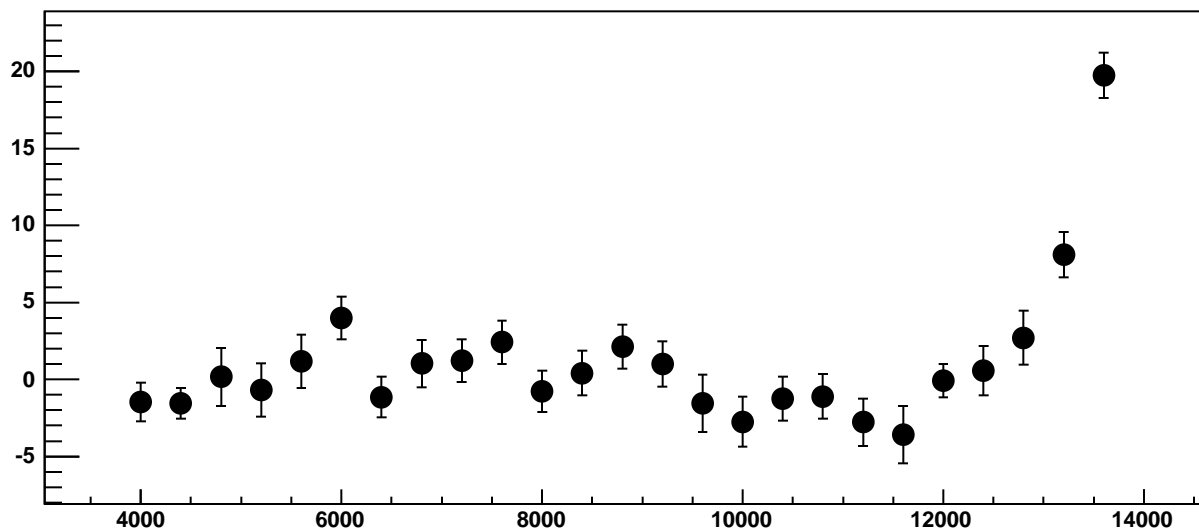


χ^2 / ndf 25.49 / 16
p0 79.62 ± 1.191
p1 0.02388 ± 0.0001586

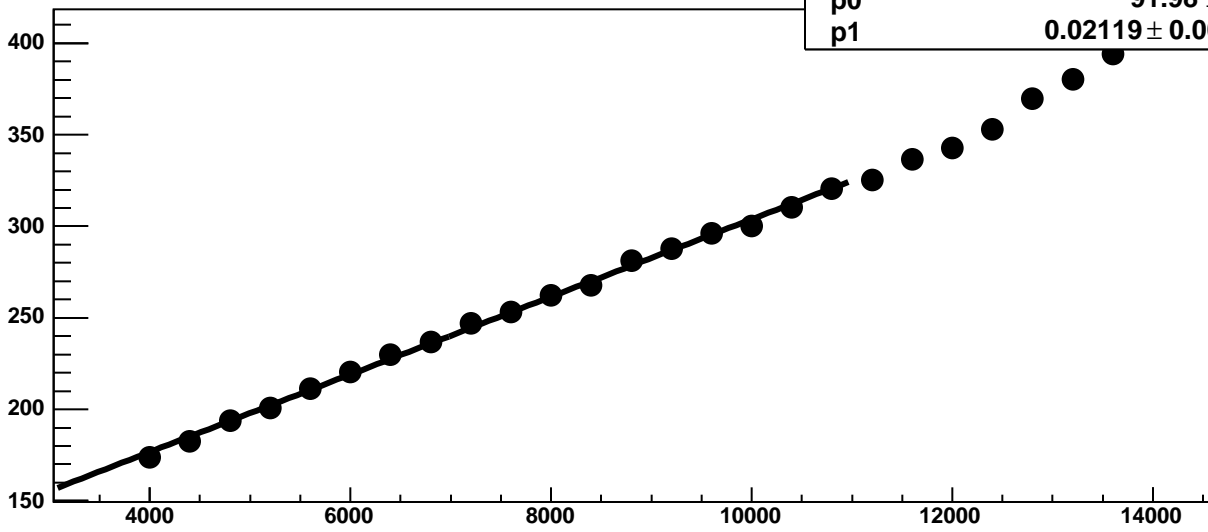
Chip 5, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



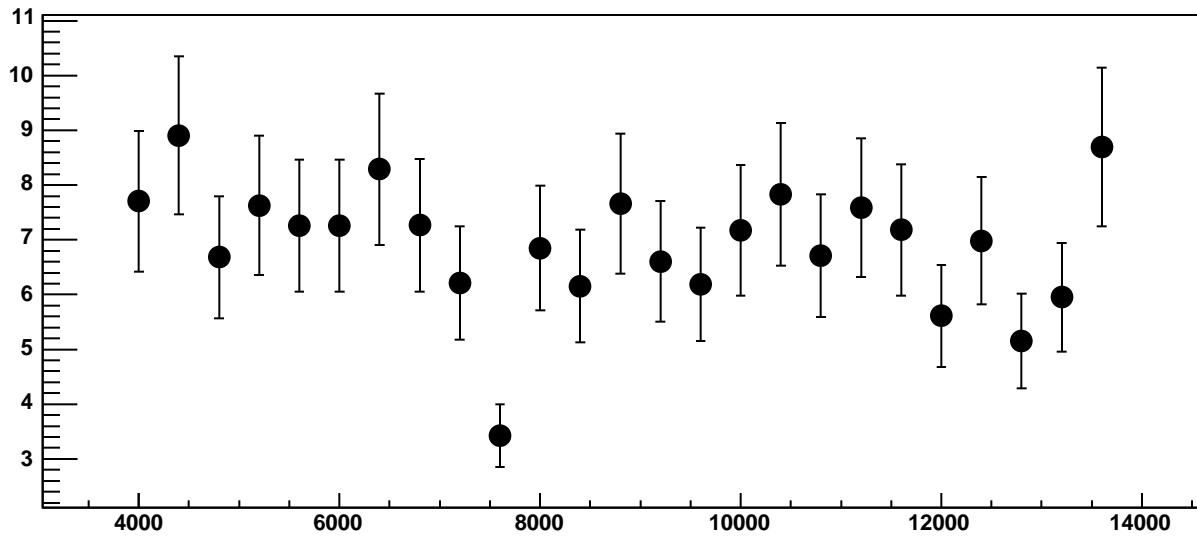
Chip 5, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC



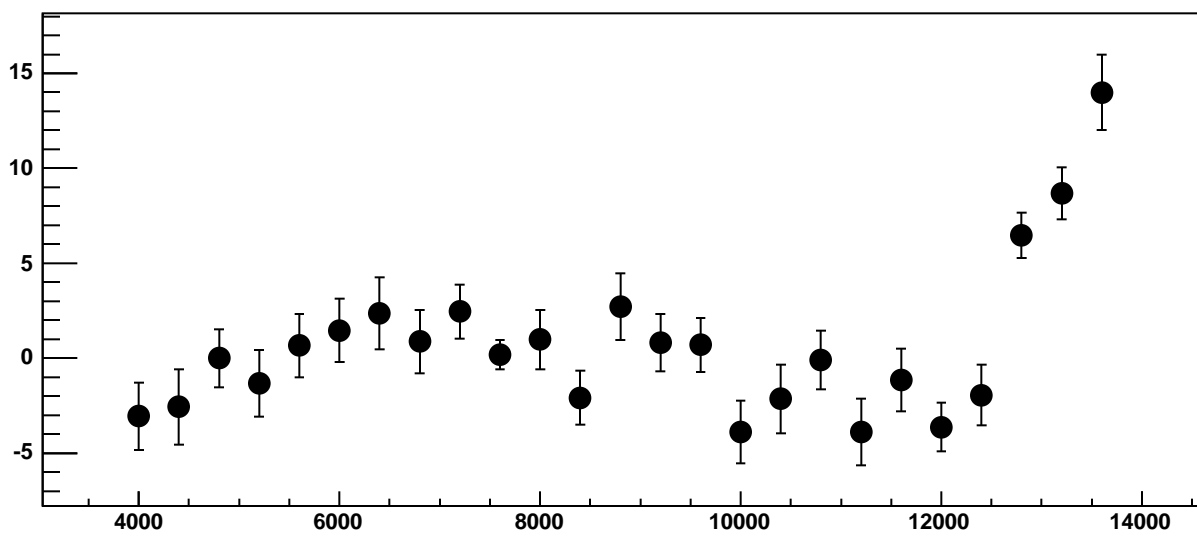
Chip 5, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



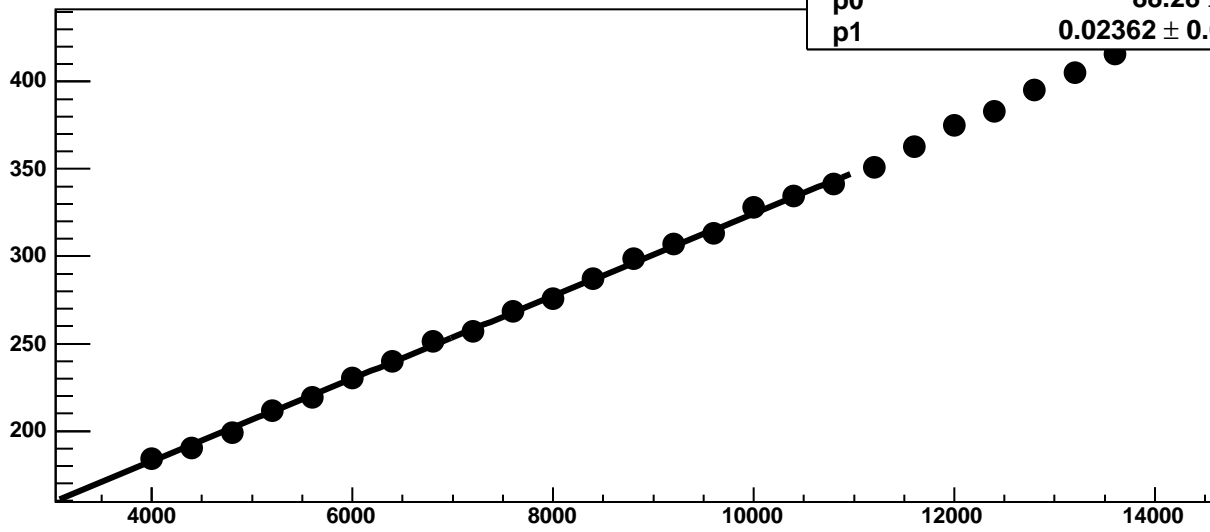
Chip 5, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

23 / 16

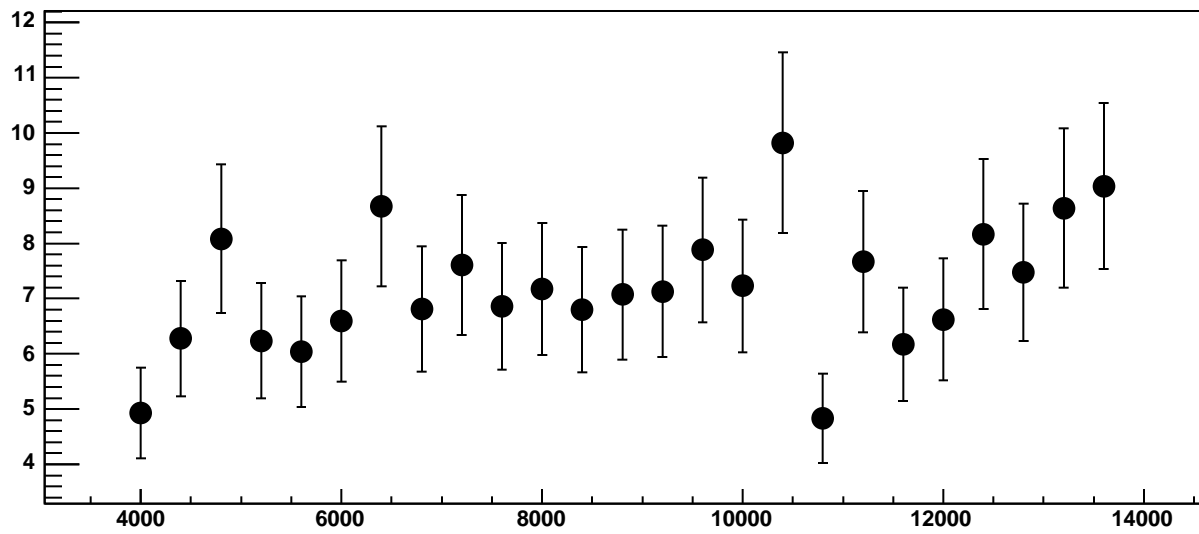
p0

88.28 ± 1.229

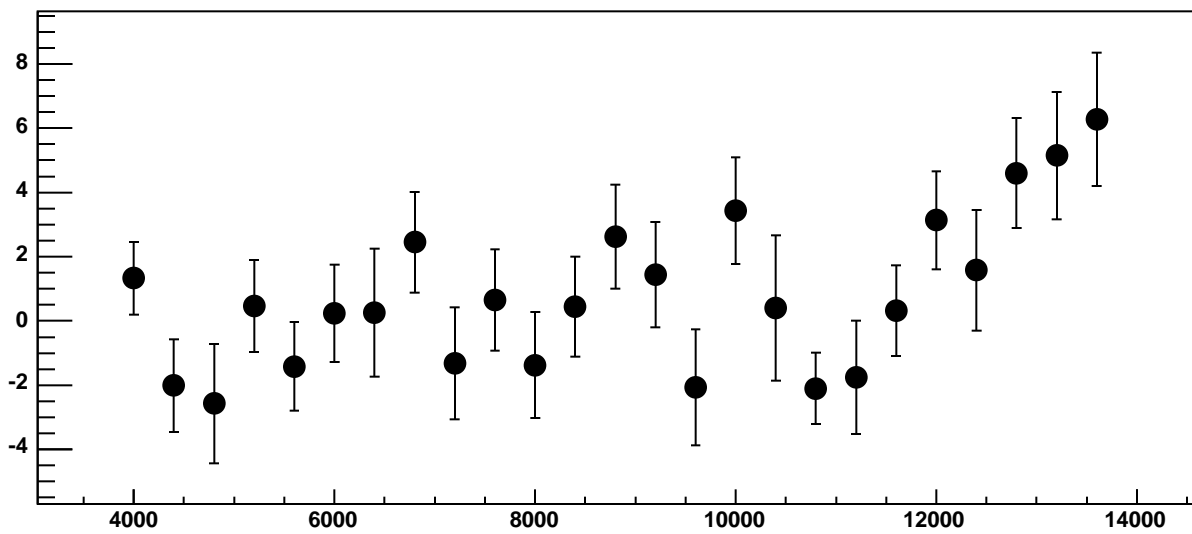
p1

0.02362 ± 0.000162

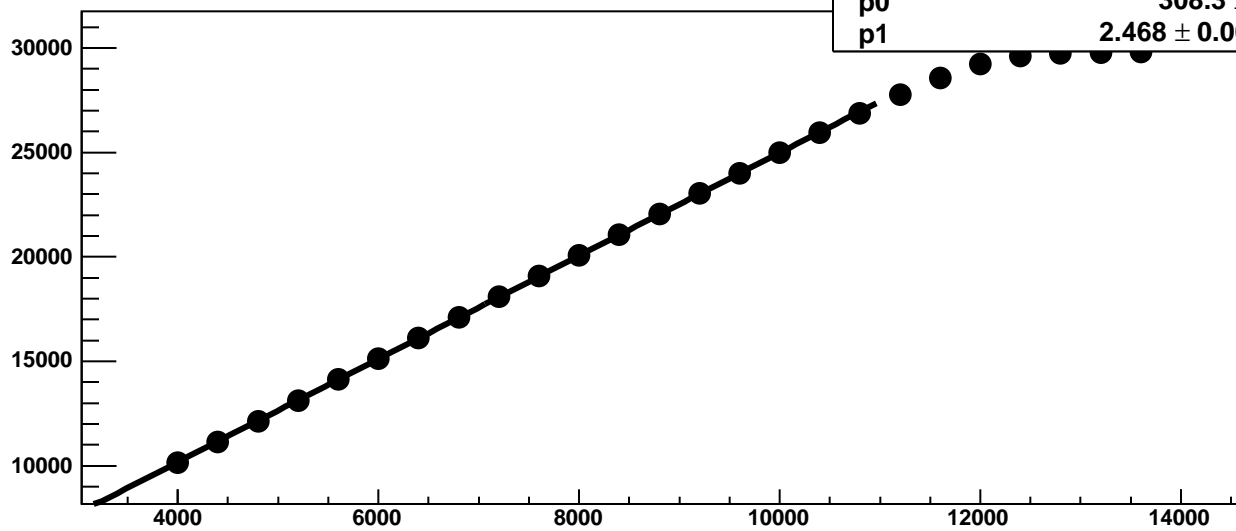
Chip 5, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

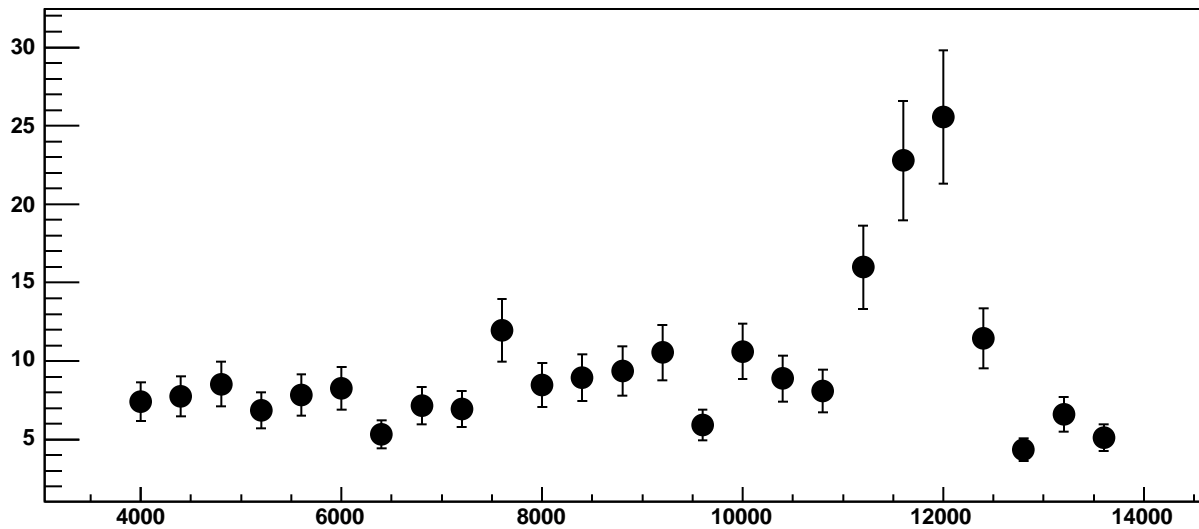


Chip 5, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC

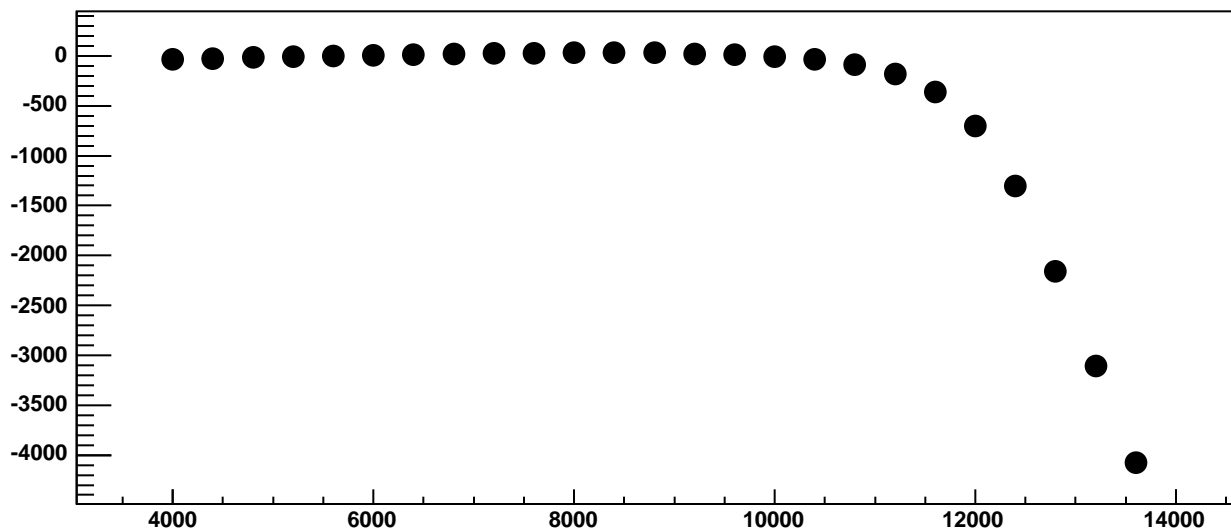


χ^2 / ndf 4818 / 16
p0 308.3 ± 1.547
p1 2.468 ± 0.0002072

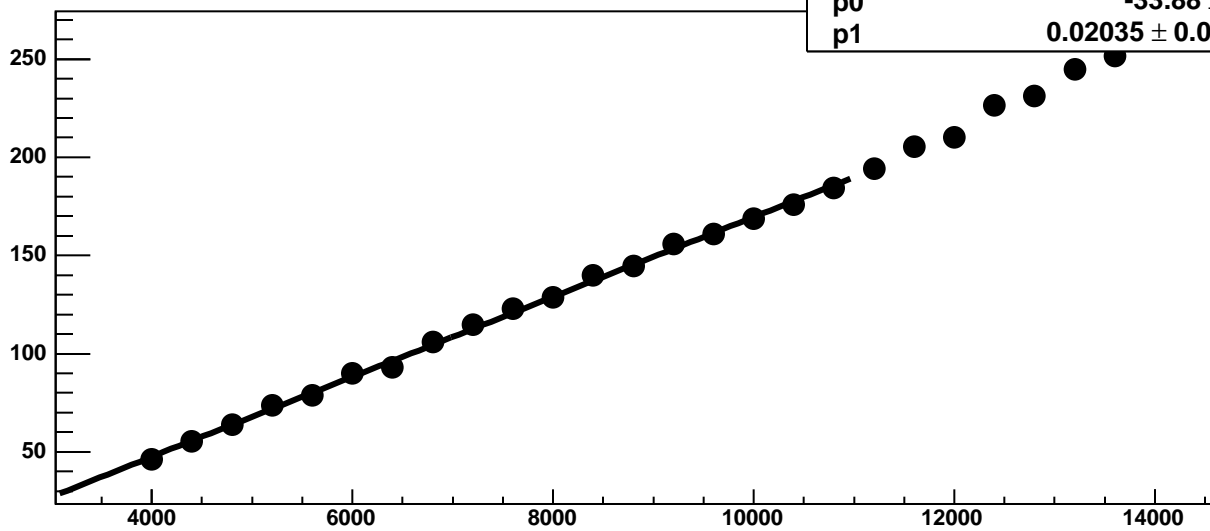
Chip 5, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

27.67 / 16

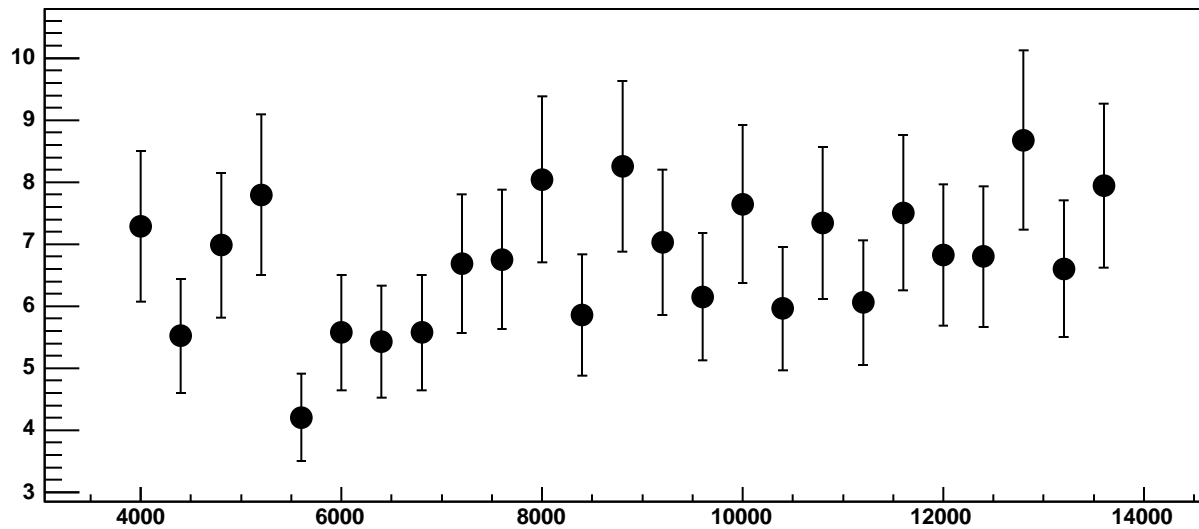
p0

-33.88 ± 1.259

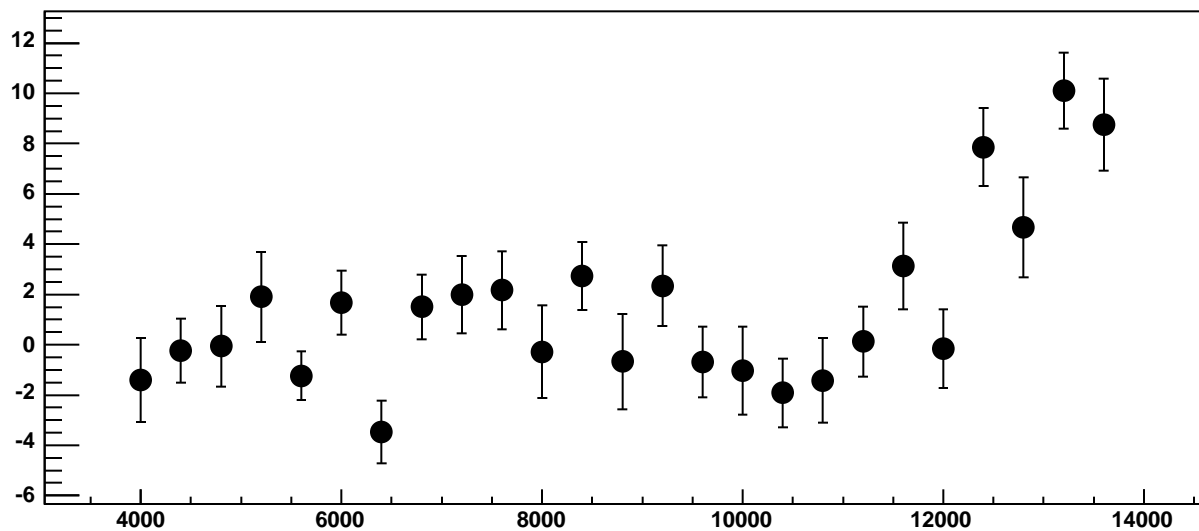
p1

0.02035 ± 0.0001691

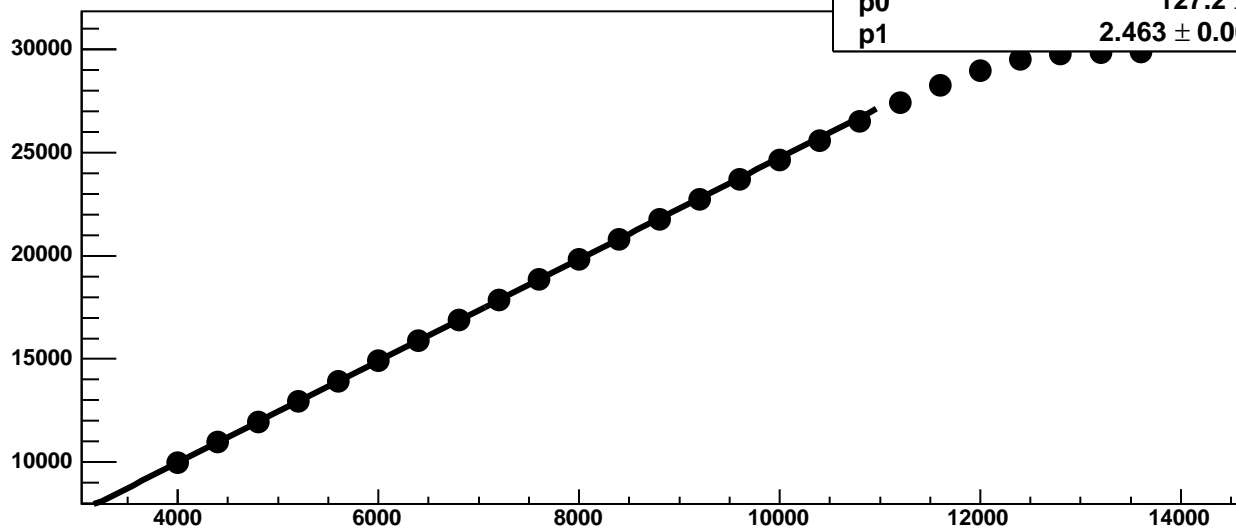
Chip 5, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



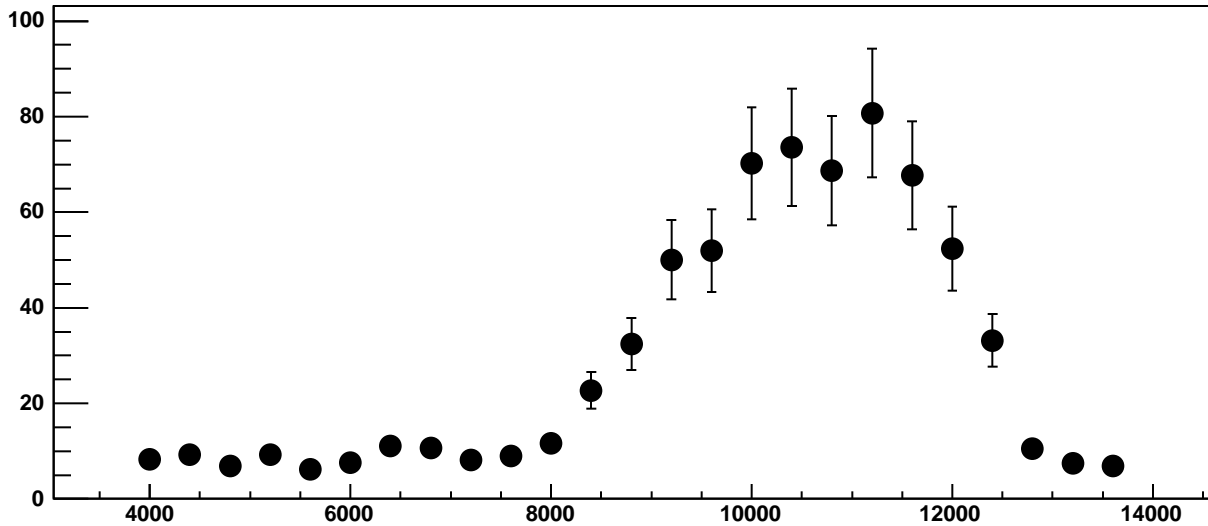
Chip 5, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



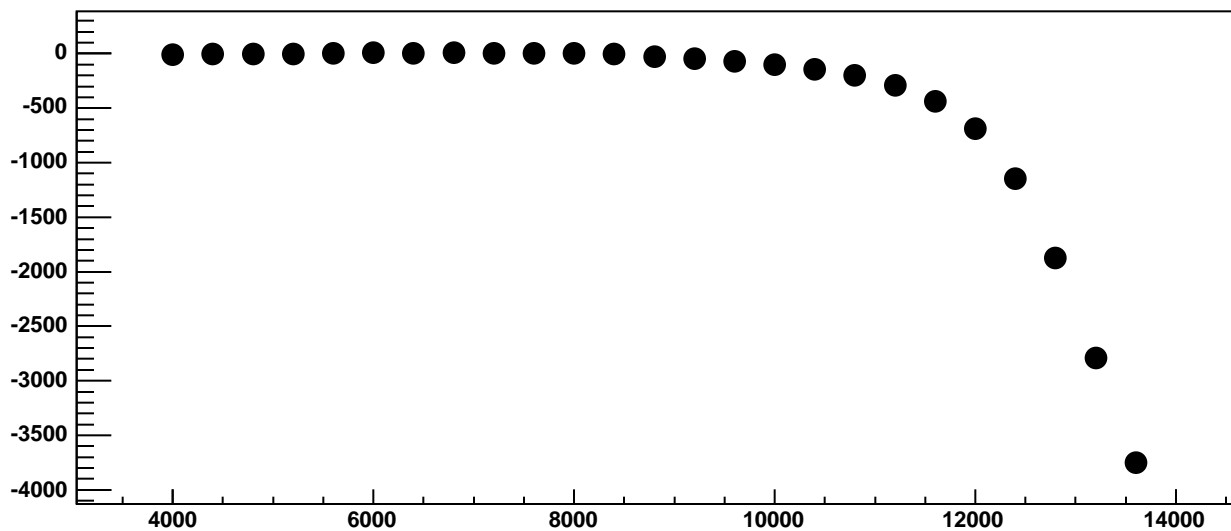
Chip 5, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC



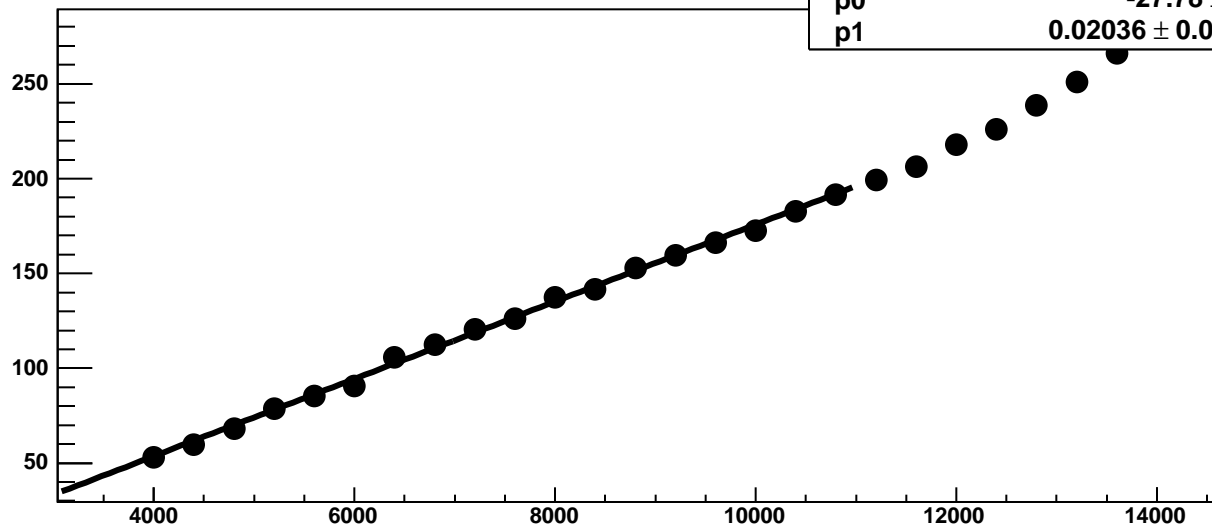
Chip 5, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC

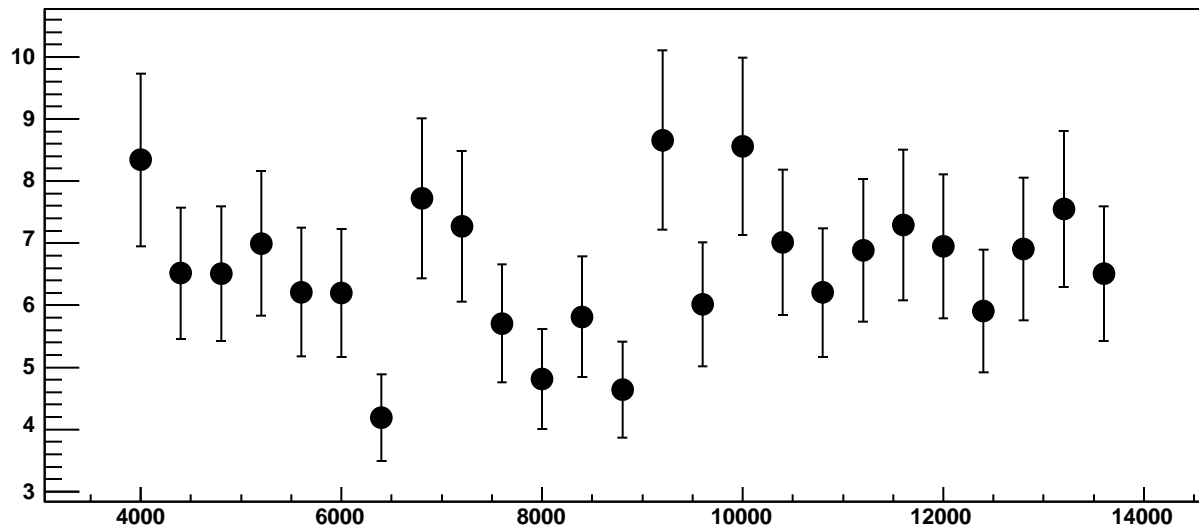


Chip 5, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

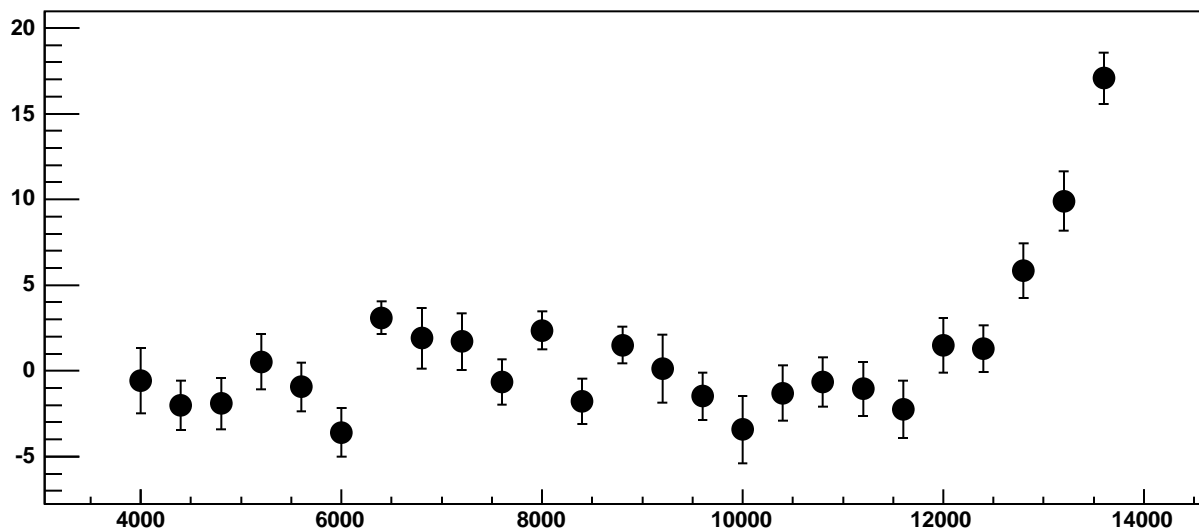


χ^2 / ndf 36.74 / 16
p0 -27.78 ± 1.335
p1 0.02036 ± 0.0001741

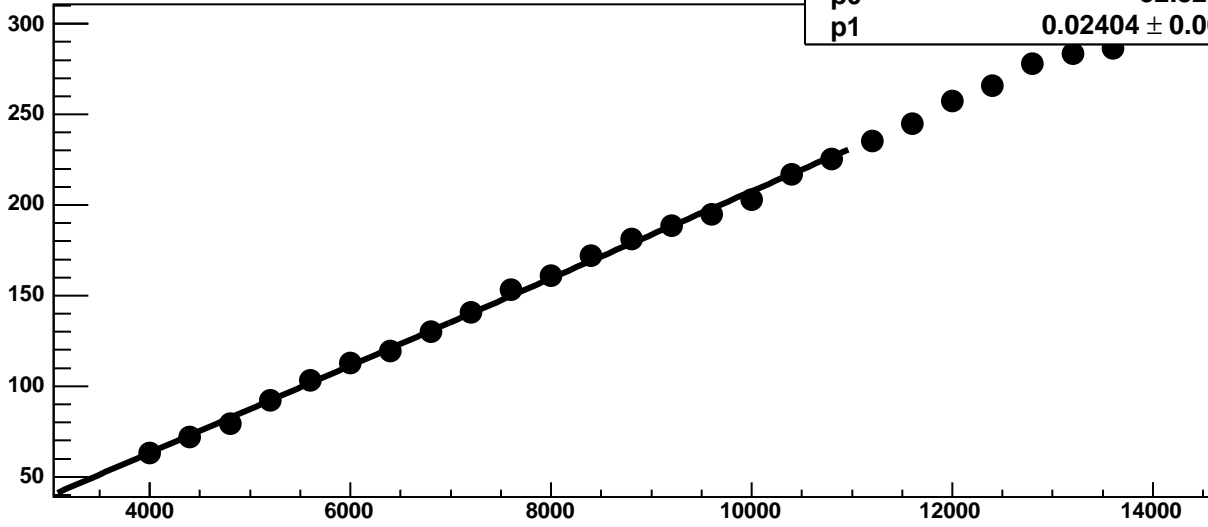
Chip 5, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

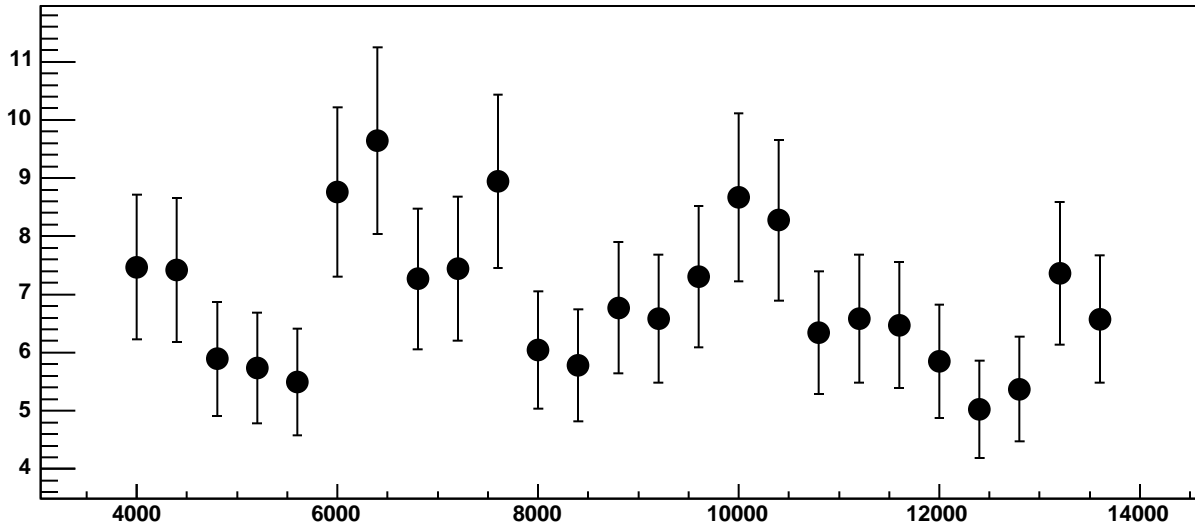


Chip 5, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

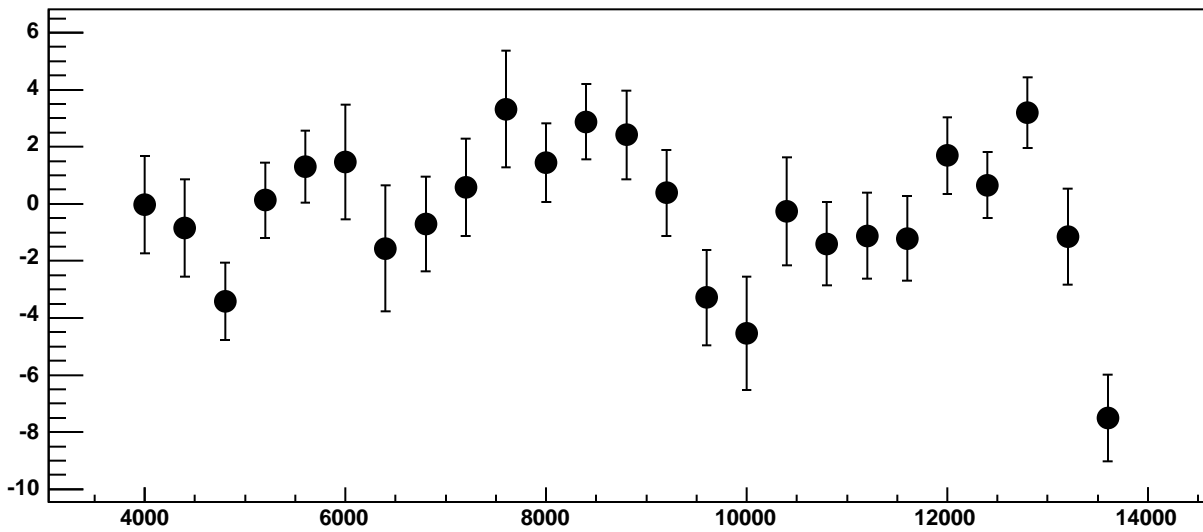


χ^2 / ndf 29.93 / 16
p0 -32.82 ± 1.37
p1 0.02404 ± 0.0001804

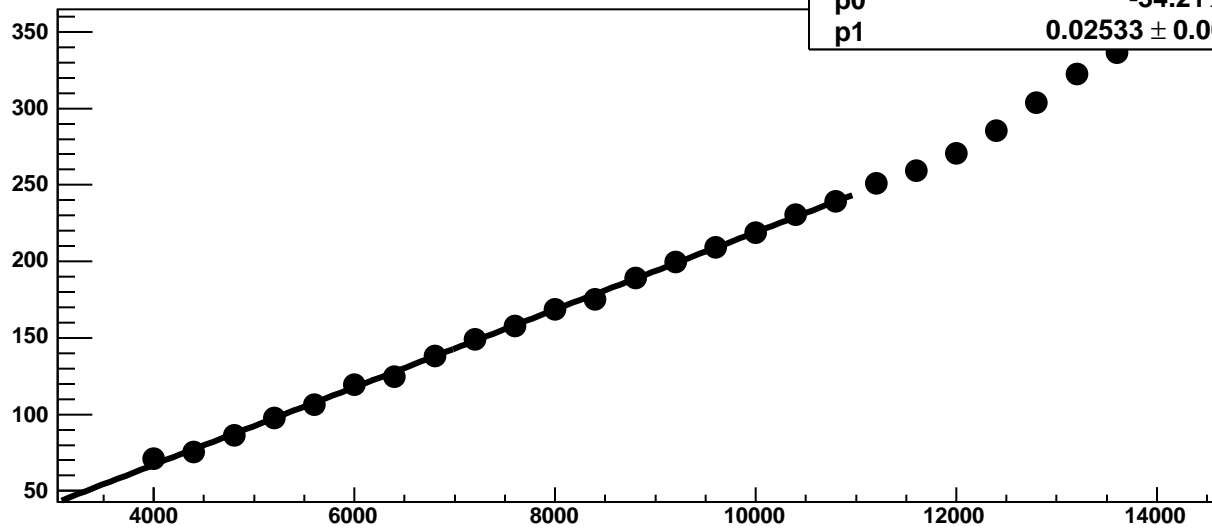
Chip 5, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

23.43 / 16

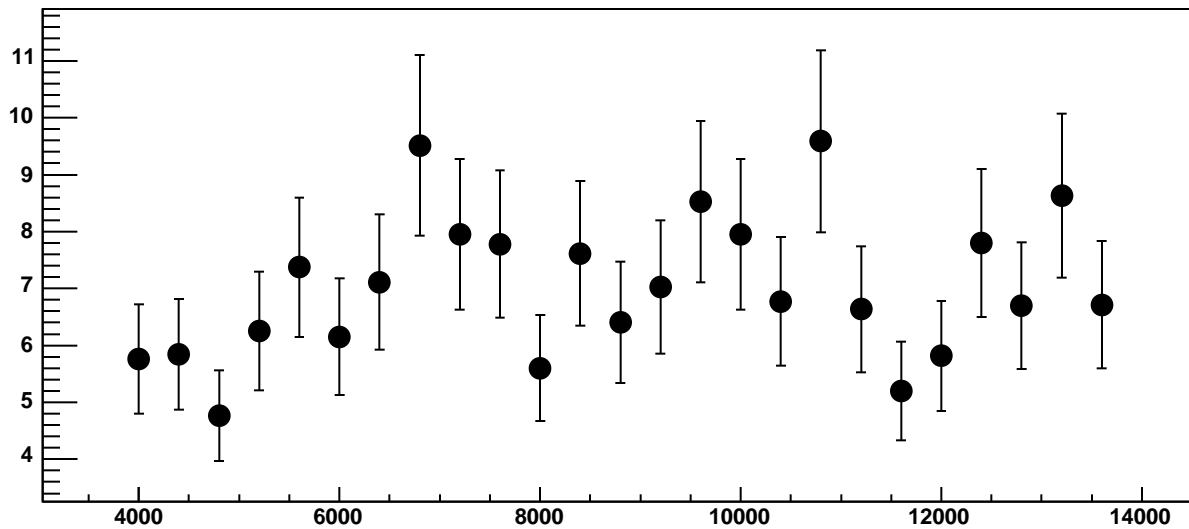
p0

-34.21 ± 1.267

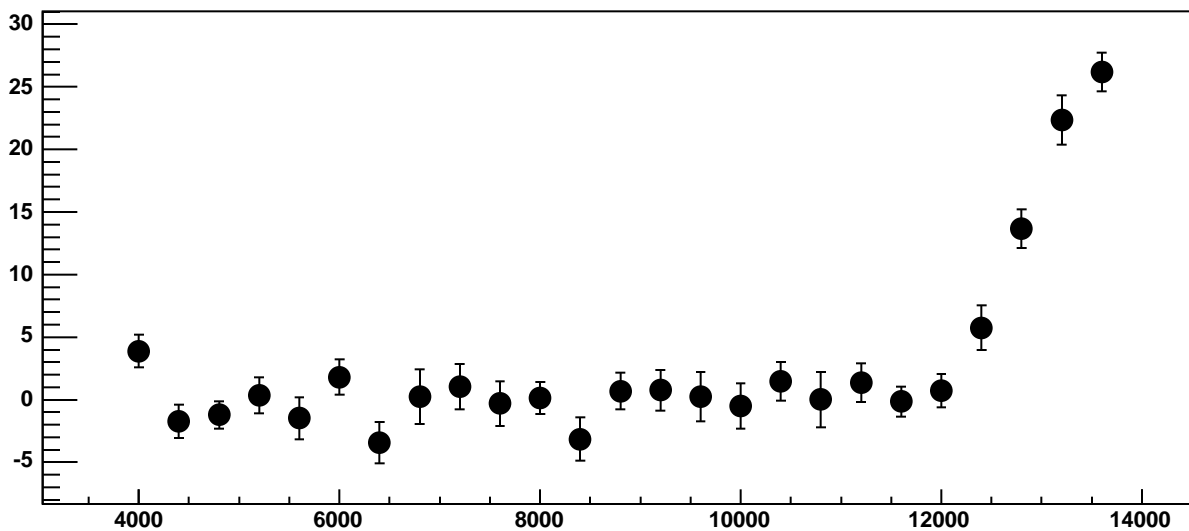
p1

0.02533 ± 0.0001746

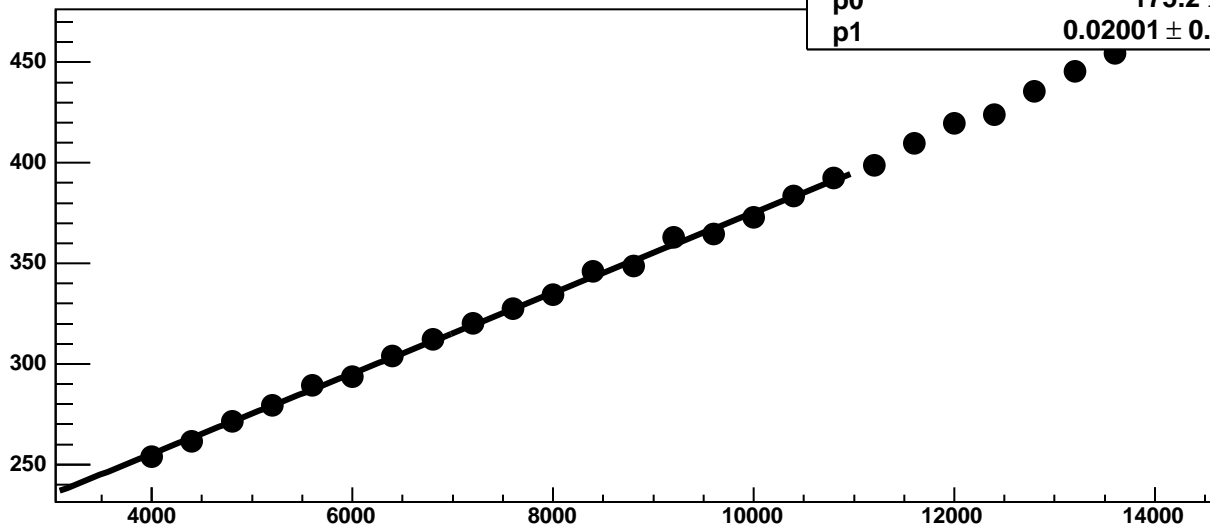
Chip 5, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



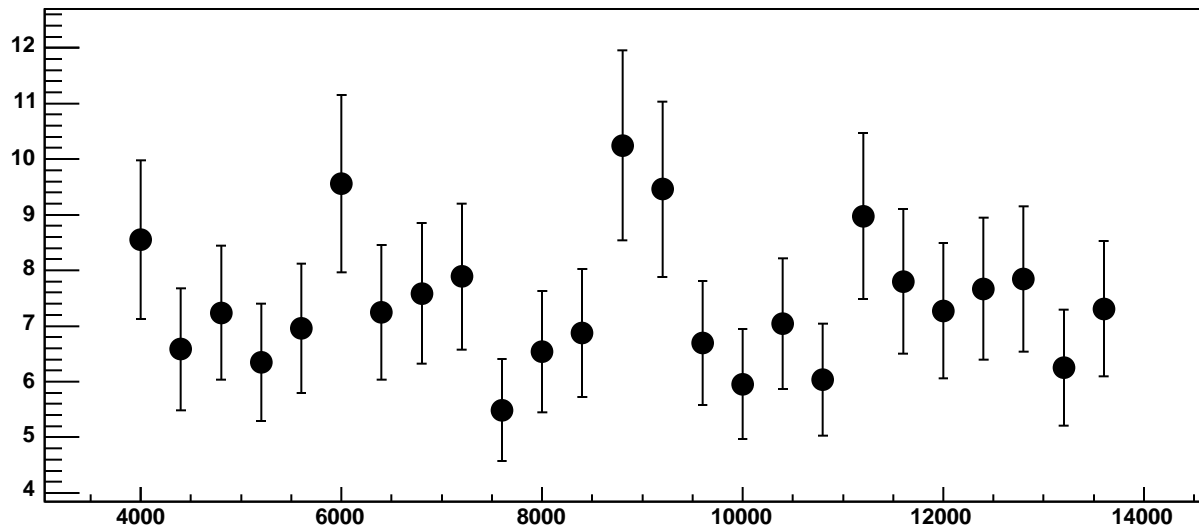
Chip 5, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC



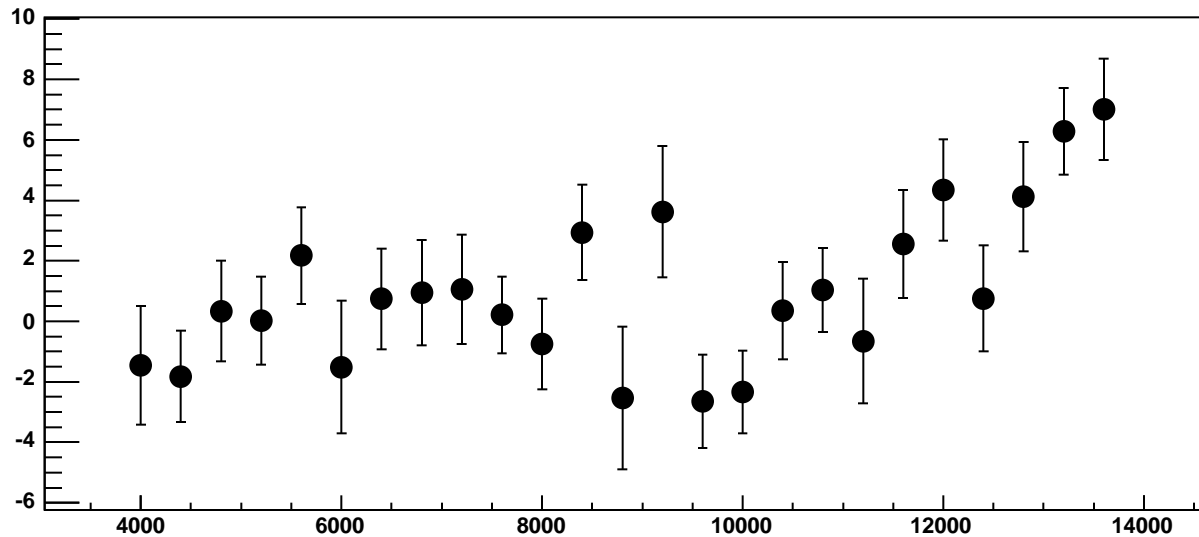
Chip 5, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



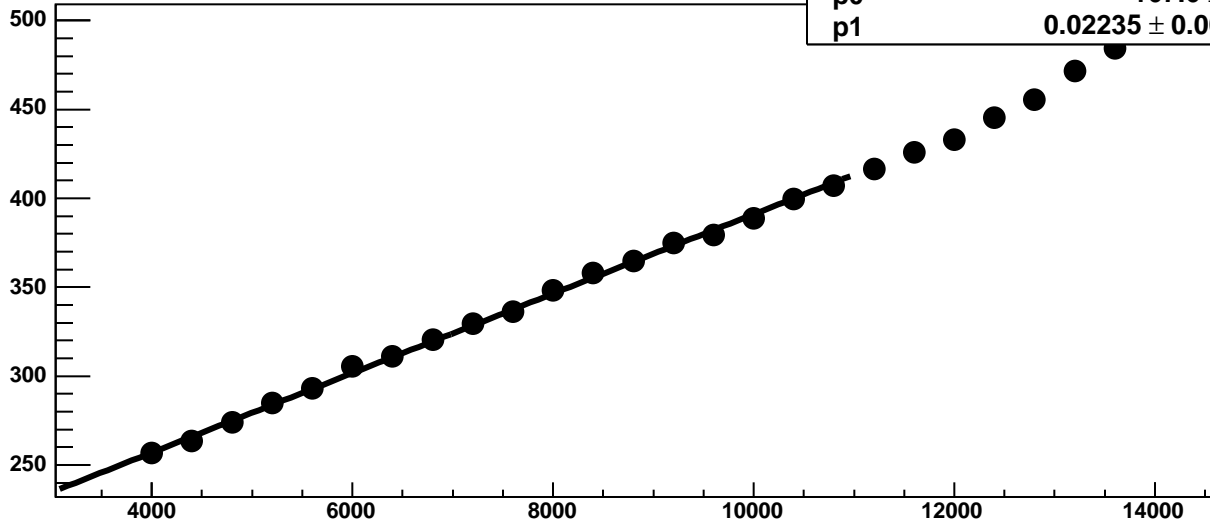
Chip 5, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

22.57 / 16

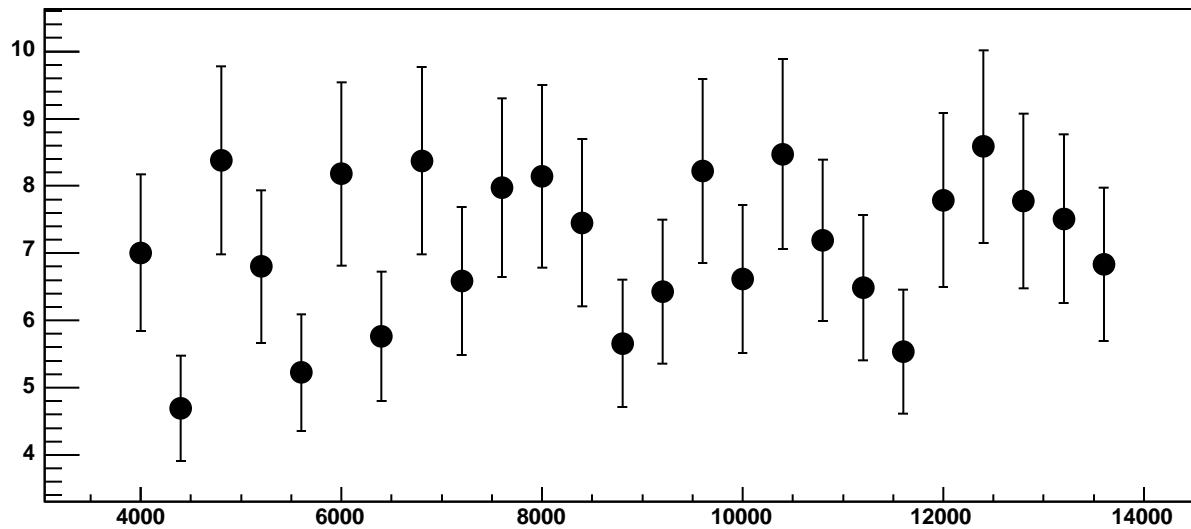
p0

167.6 ± 1.294

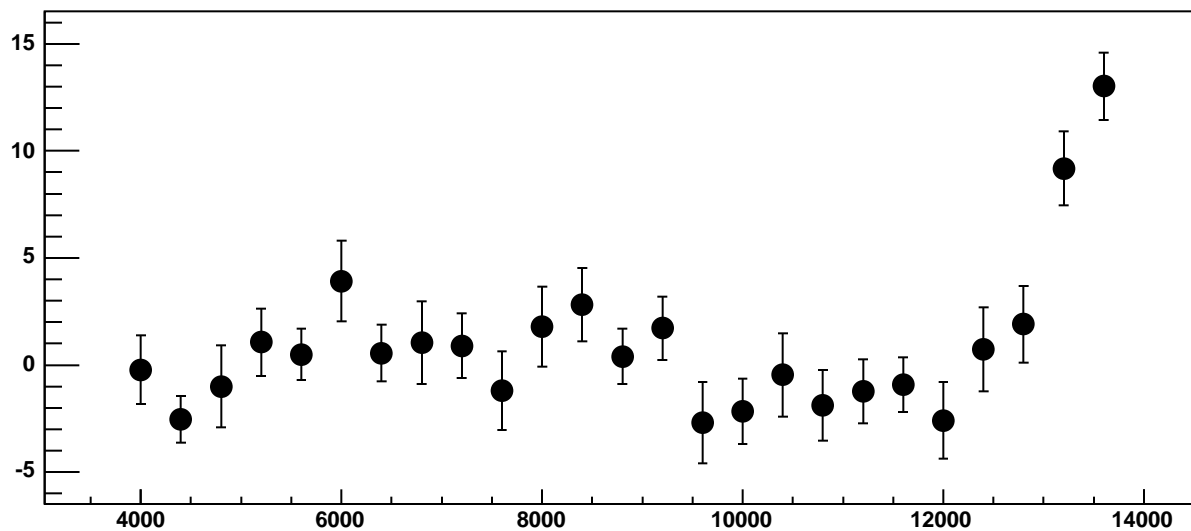
p1

0.02235 ± 0.0001739

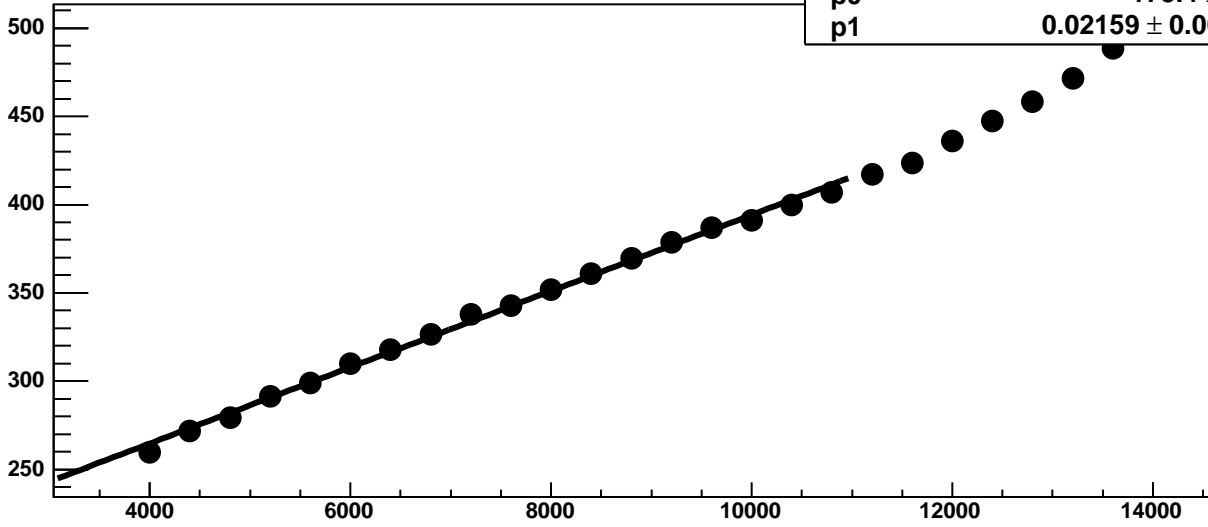
Chip 5, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

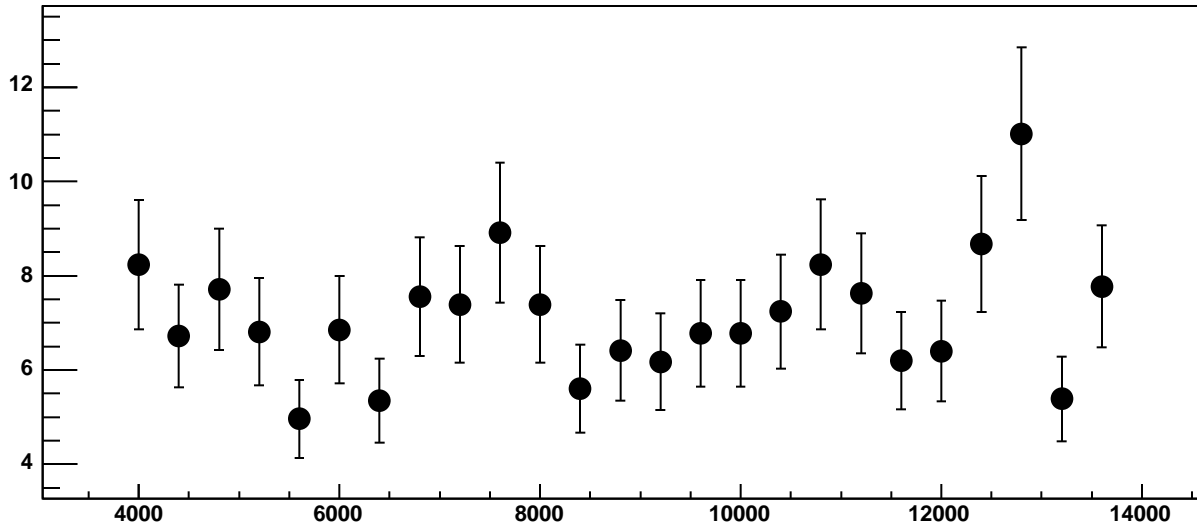


Chip 5, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

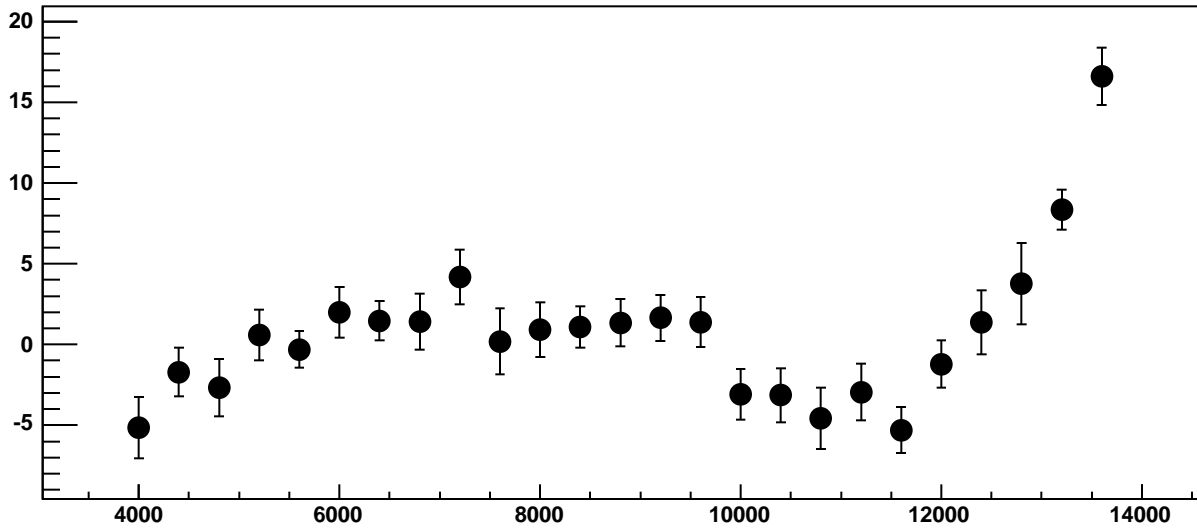


χ^2 / ndf 38.41 / 16
p0 178.4 ± 1.389
p1 0.02159 ± 0.0001827

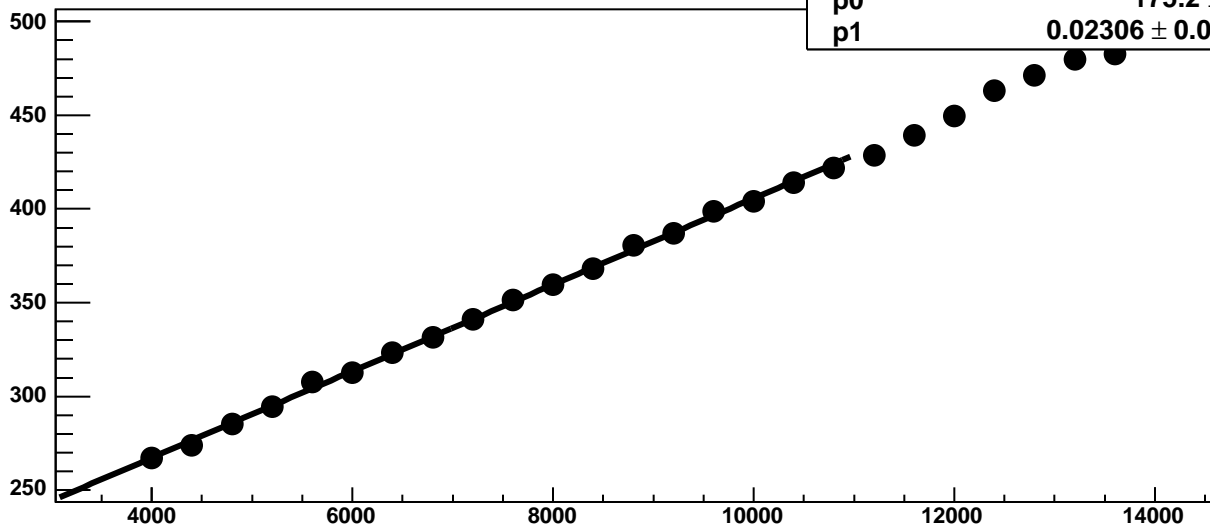
Chip 5, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

18.46 / 16

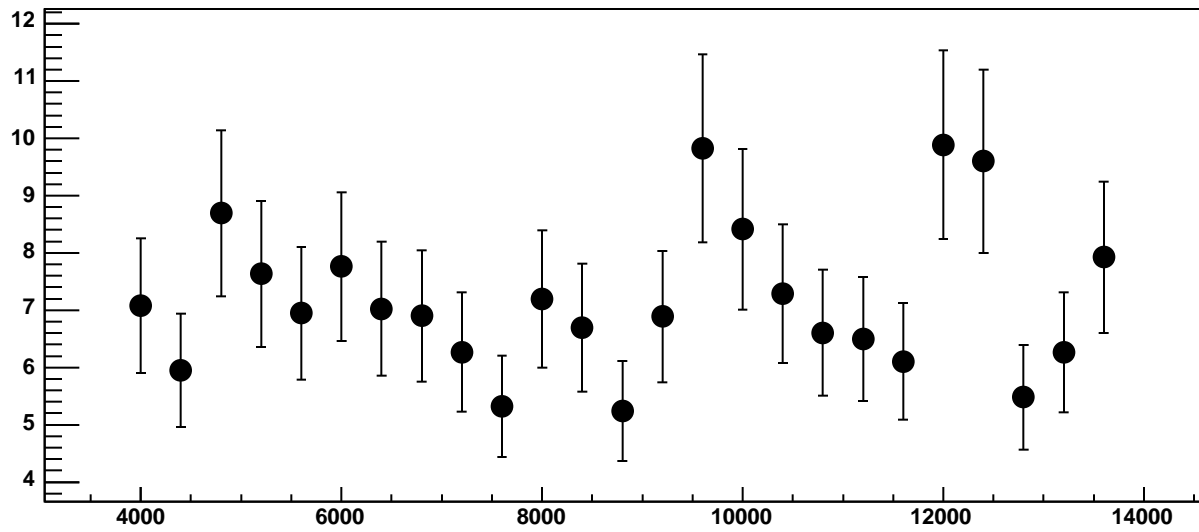
p0

175.2 ± 1.419

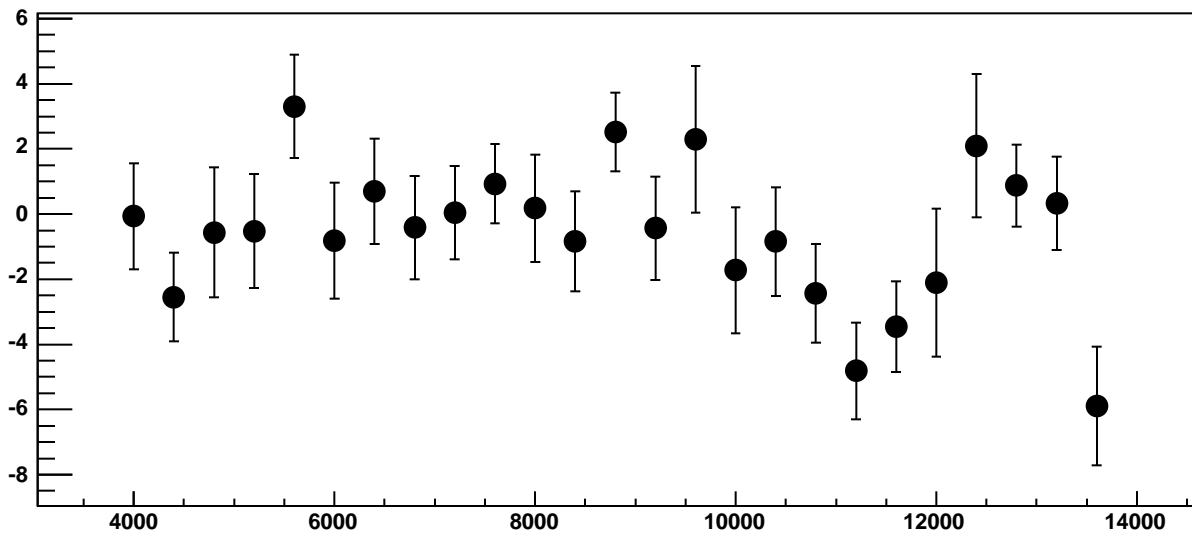
p1

0.02306 ± 0.0001851

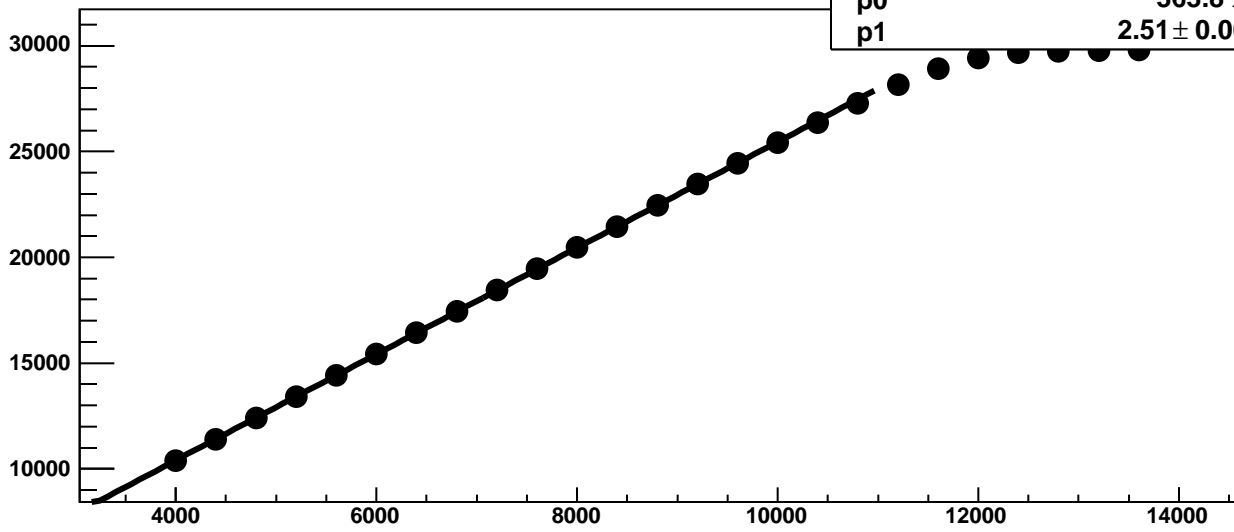
Chip 5, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

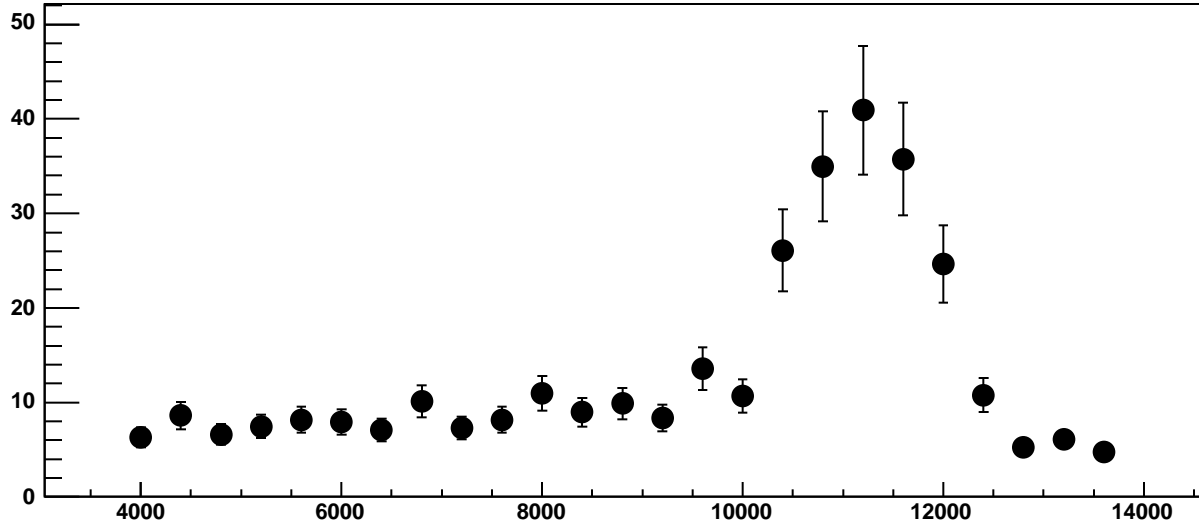


Chip 5, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC

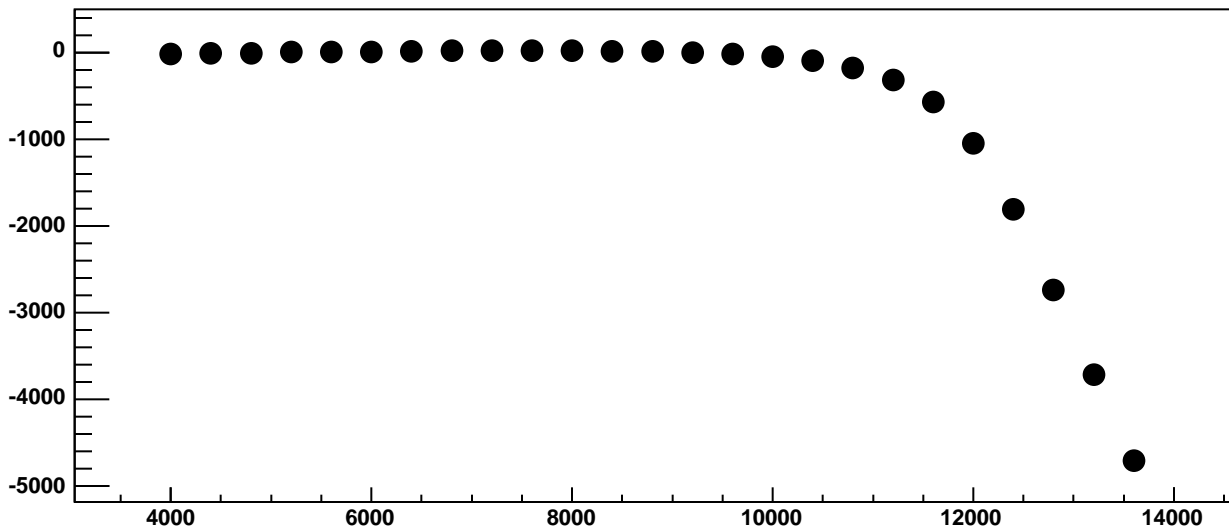


χ^2 / ndf 1930 / 16
p0 363.8 ± 1.769
p1 2.51 ± 0.0002605

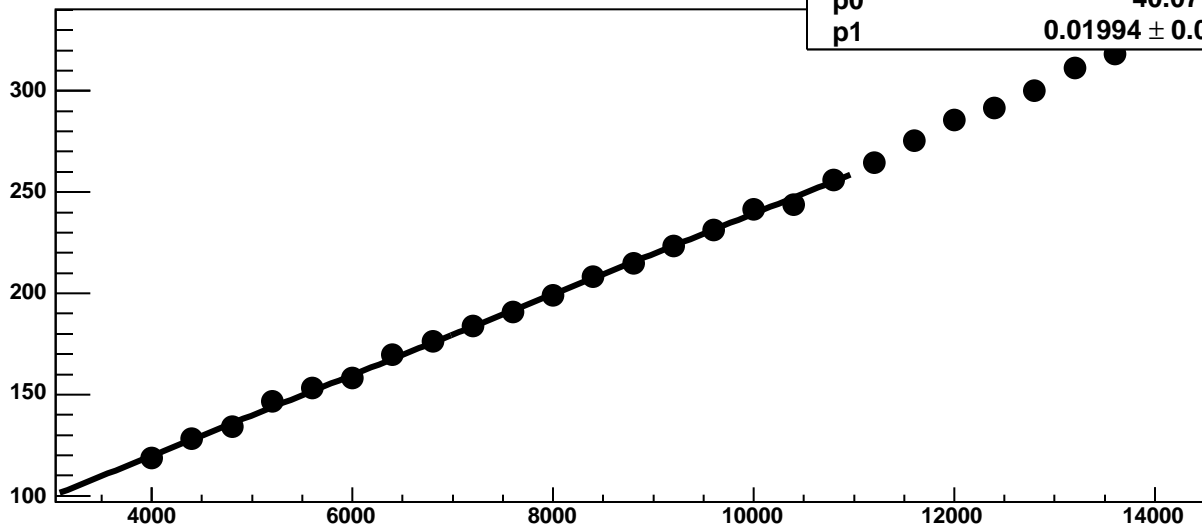
Chip 5, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC

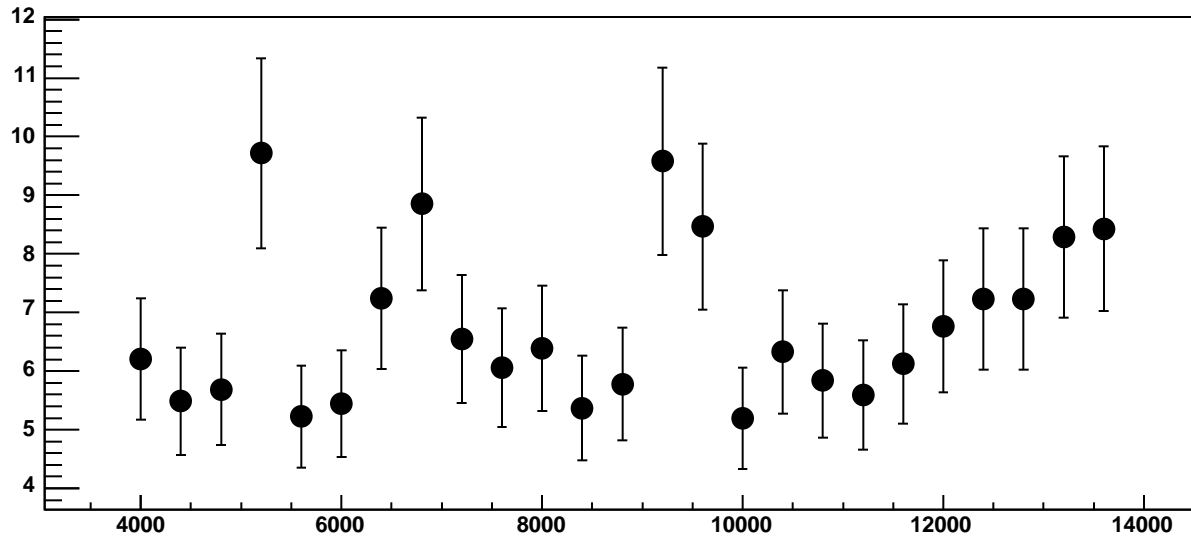


Chip 5, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

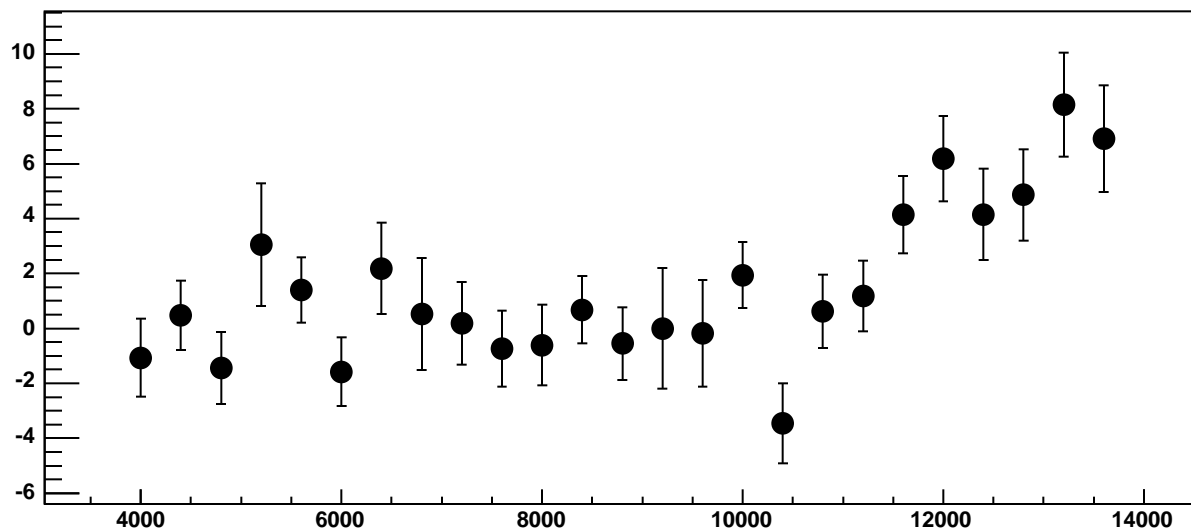


χ^2 / ndf 18.05 / 16
p0 40.07 ± 1.217
p1 0.01994 ± 0.0001588

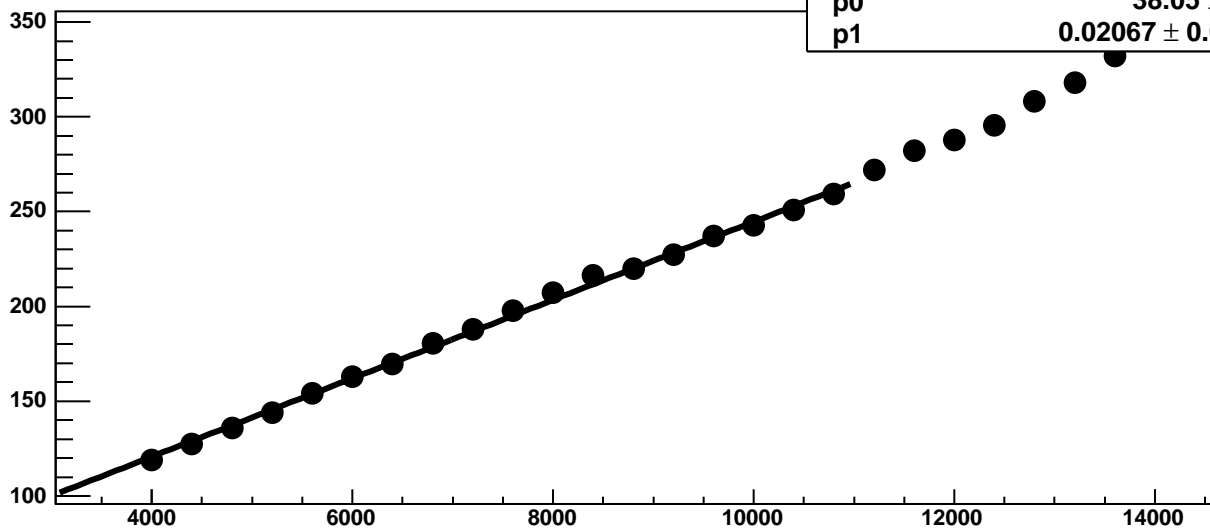
Chip 5, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

27.81 / 16

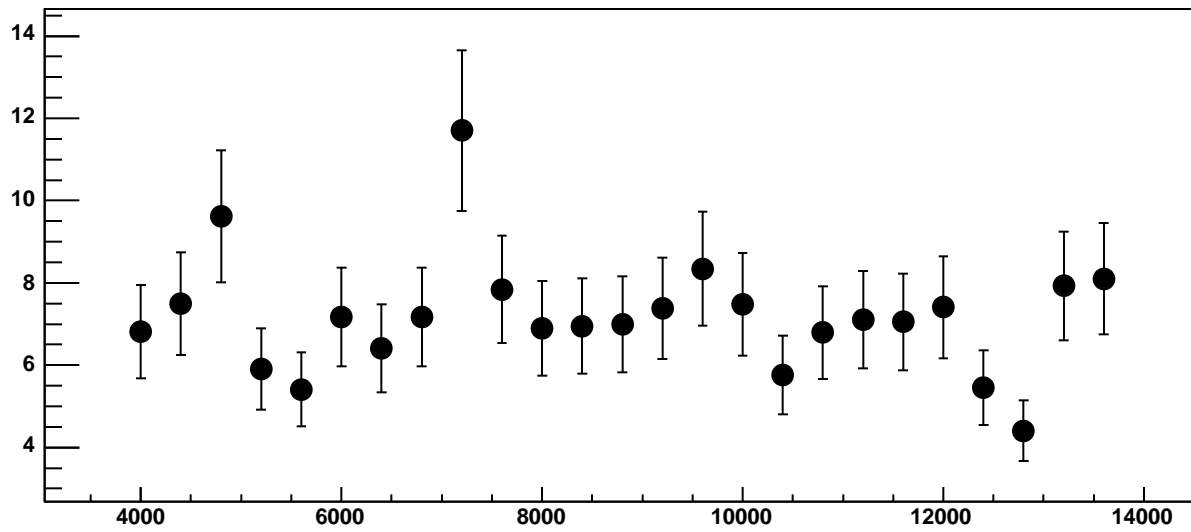
p0

38.05 ± 1.377

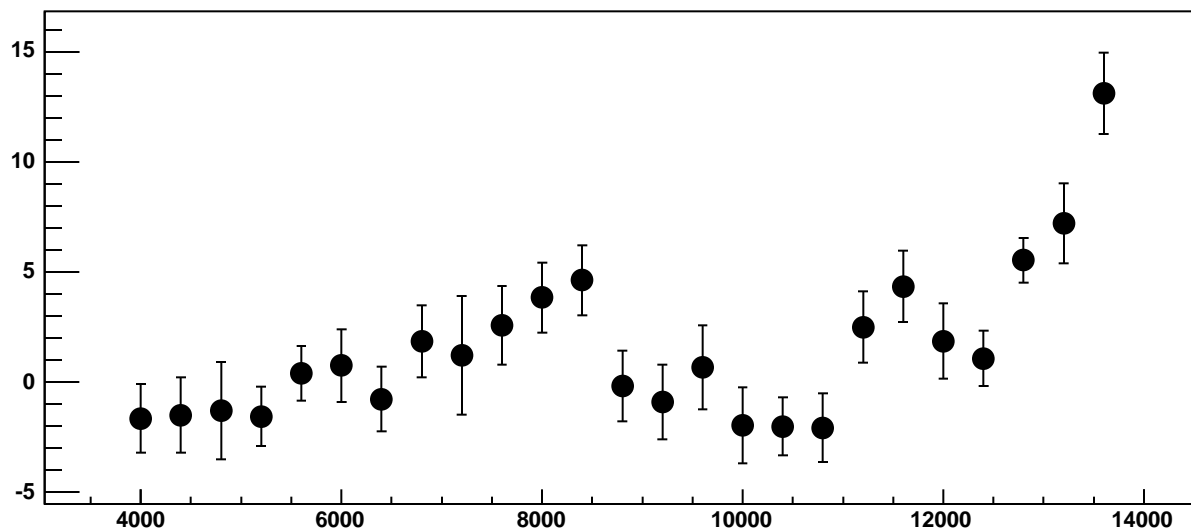
p1

0.02067 ± 0.000179

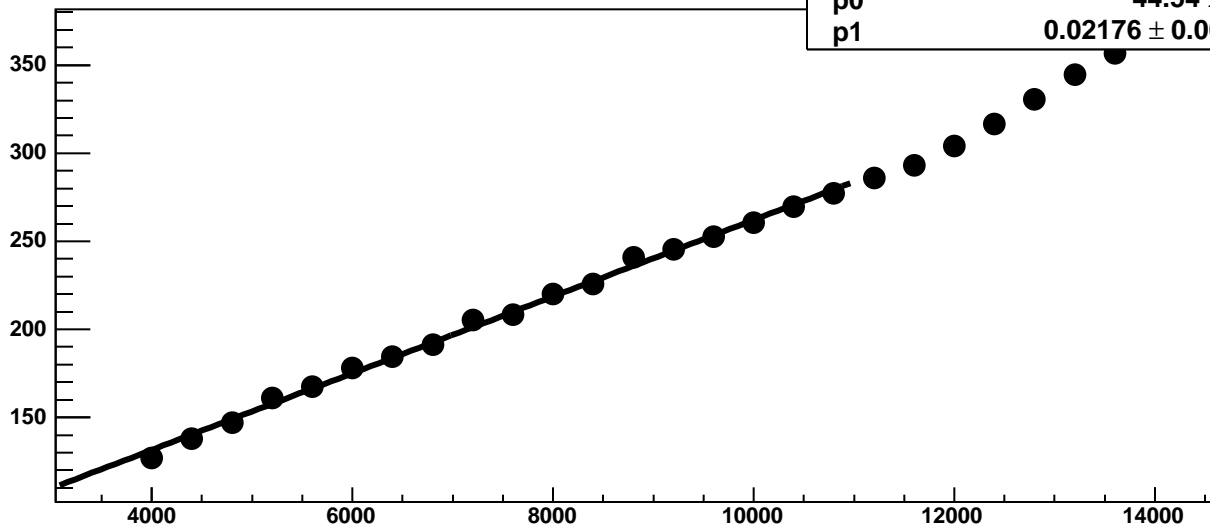
Chip 5, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

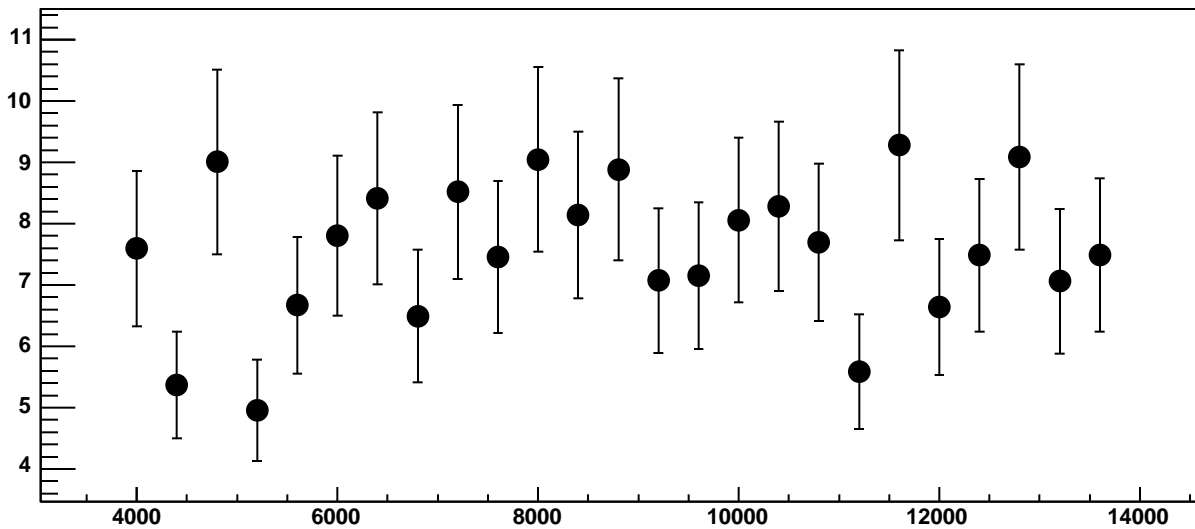


Chip 5, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC

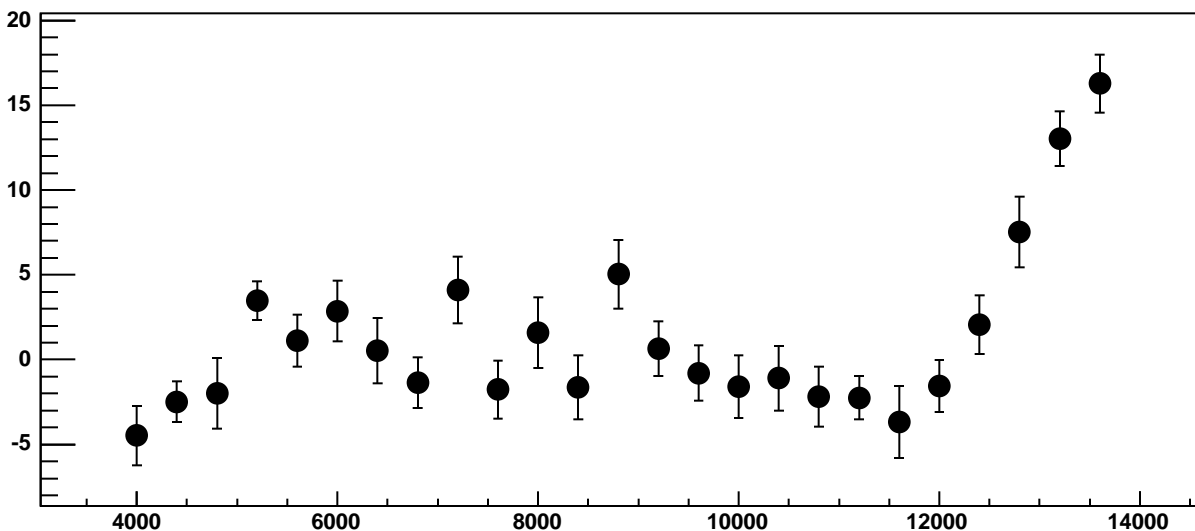


χ^2 / ndf 40.98 / 16
p0 44.54 ± 1.365
p1 0.02176 ± 0.0001855

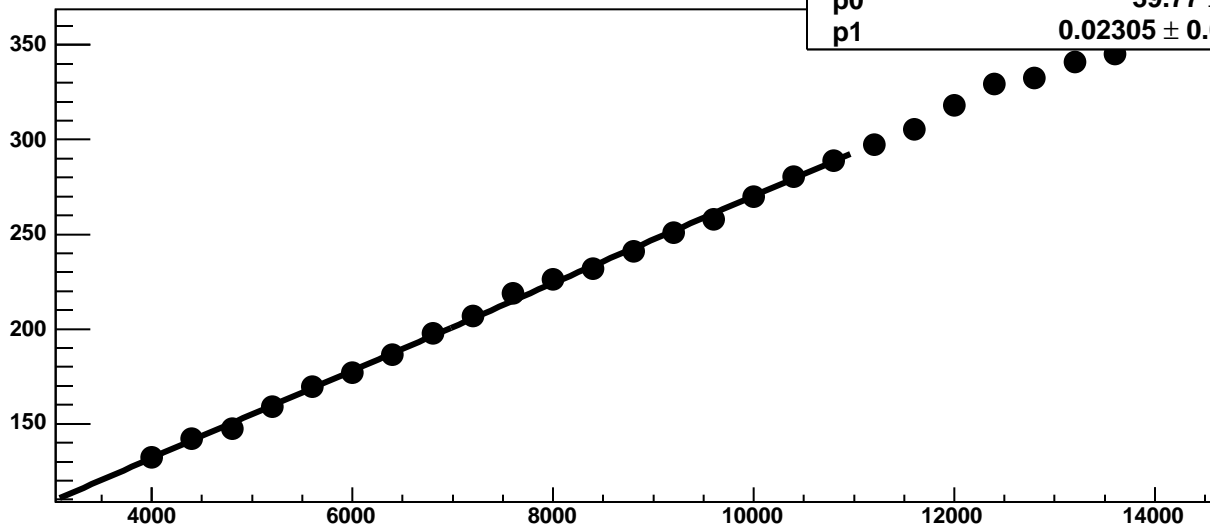
Chip 5, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

15.78 / 16

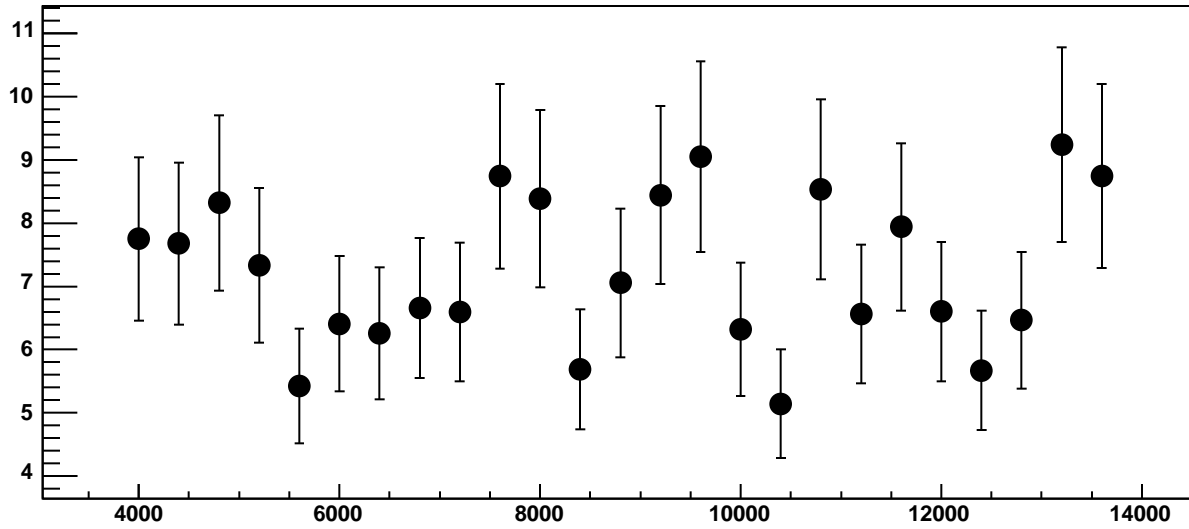
p0

39.77 ± 1.415

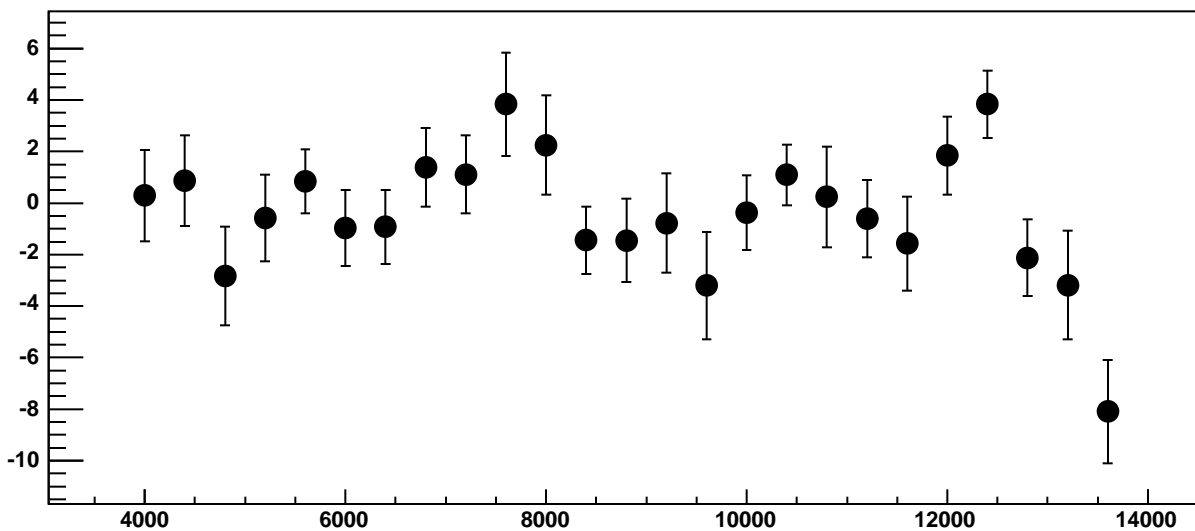
p1

0.02305 ± 0.000183

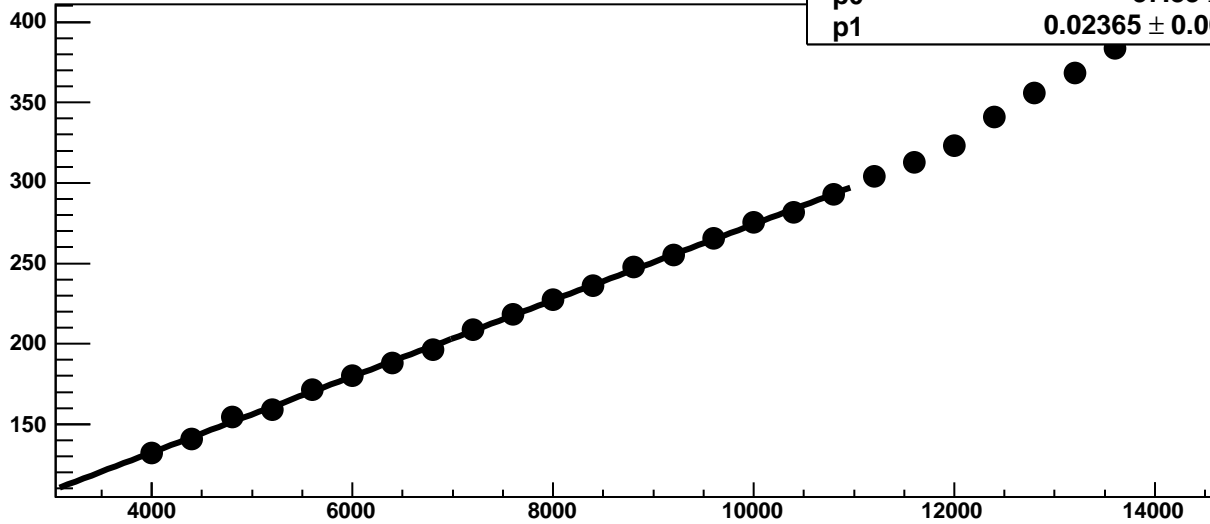
Chip 5, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

15.33 / 16

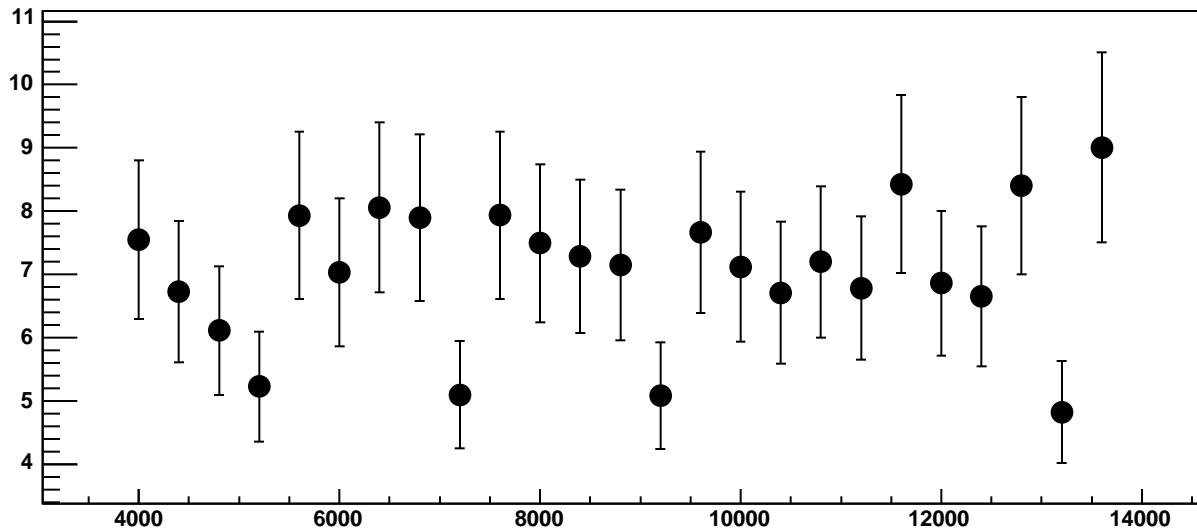
p0

37.83 ± 1.345

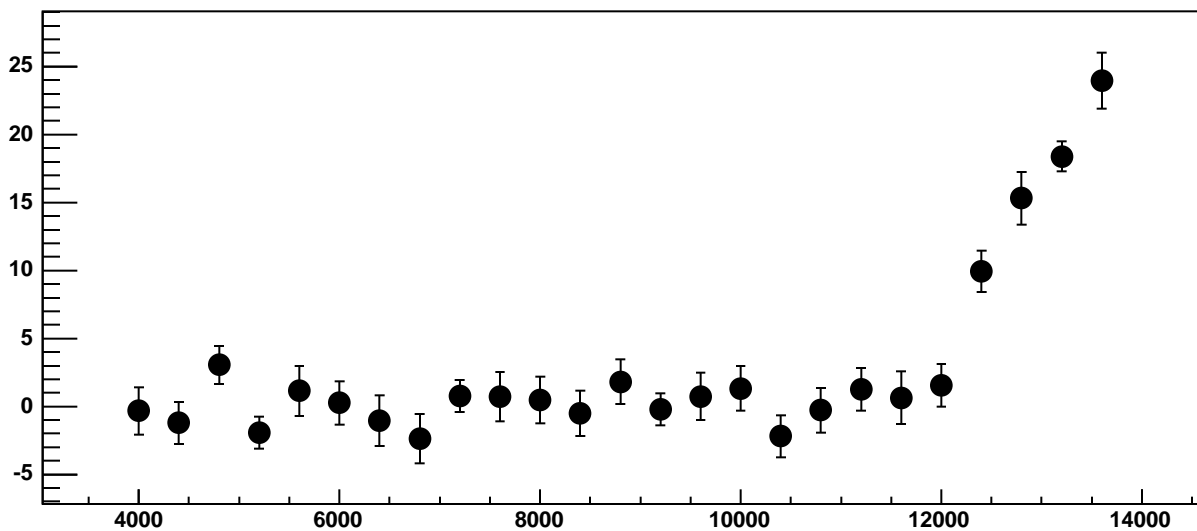
p1

0.02365 ± 0.0001759

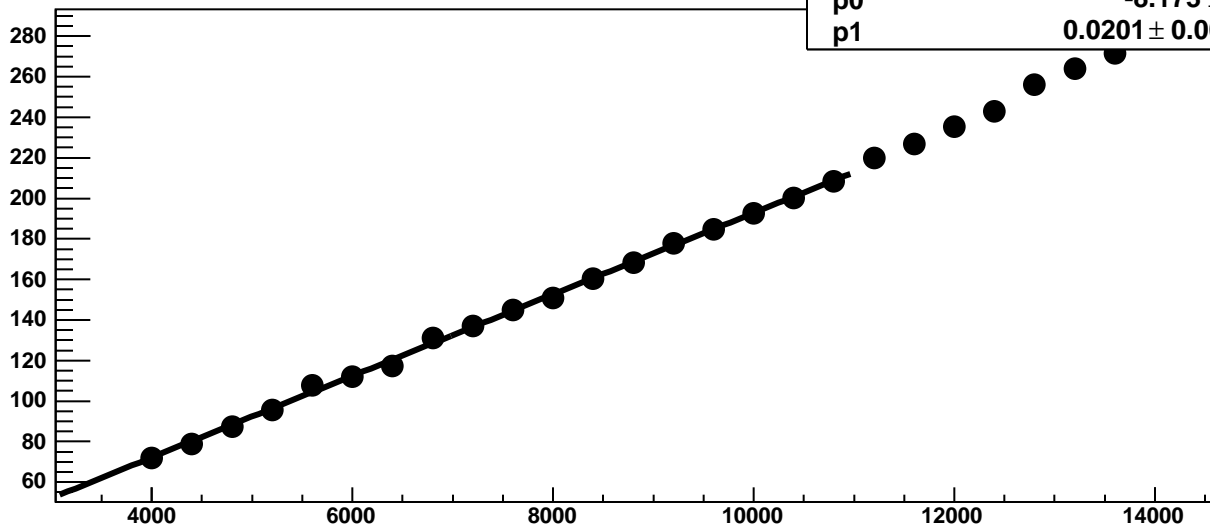
Chip 5, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

18.2 / 16

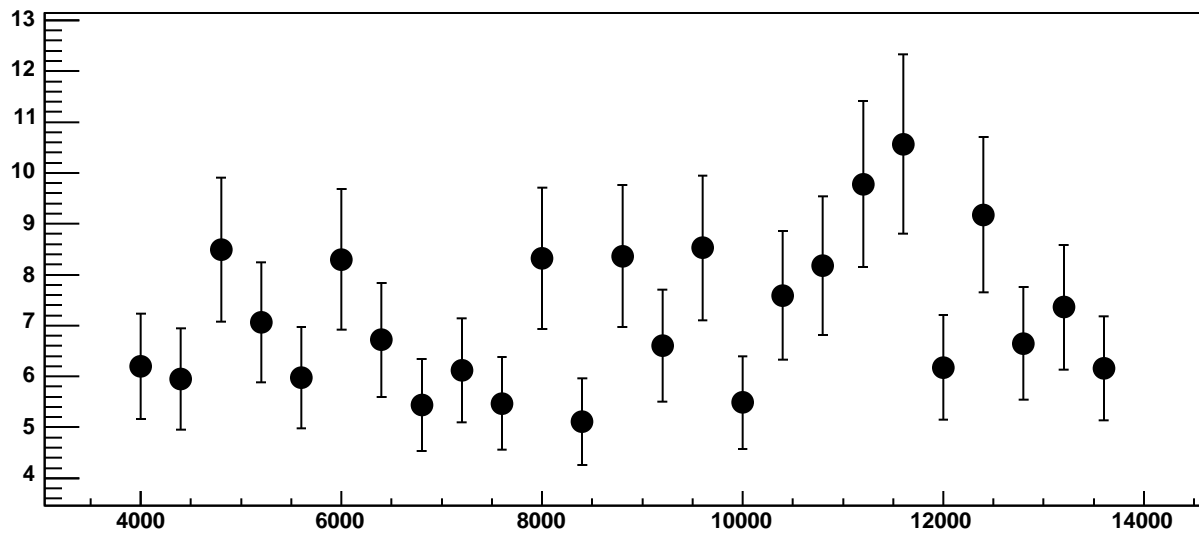
p0

-8.173 ± 1.347

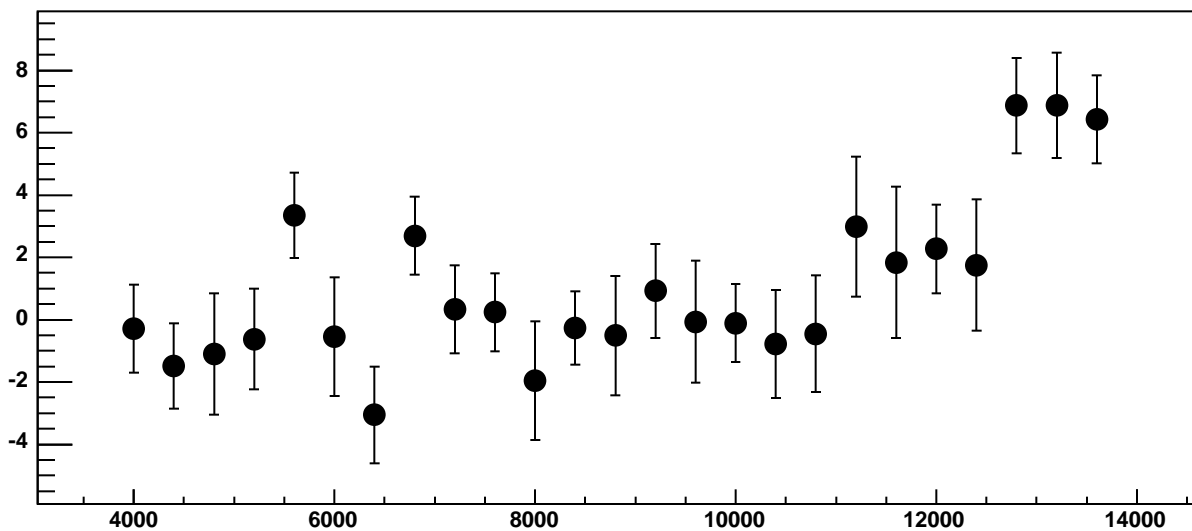
p1

0.0201 ± 0.0001778

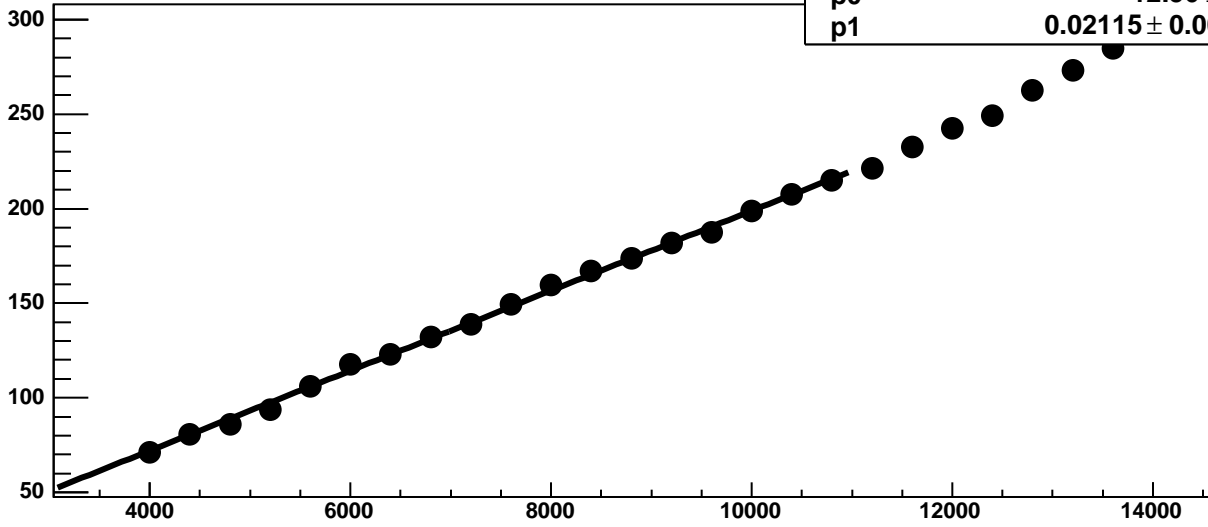
Chip 5, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

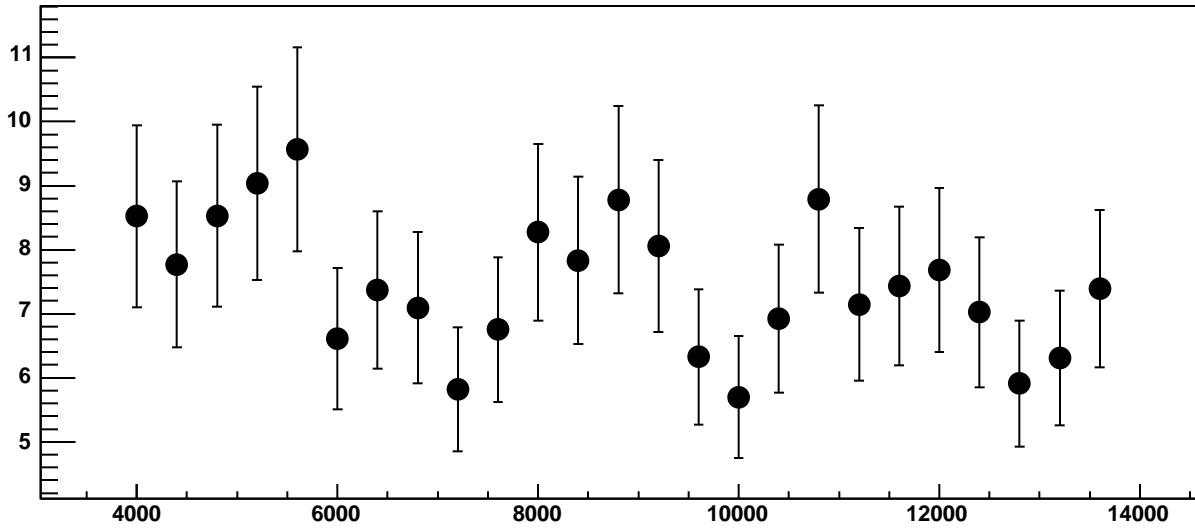


Chip 5, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

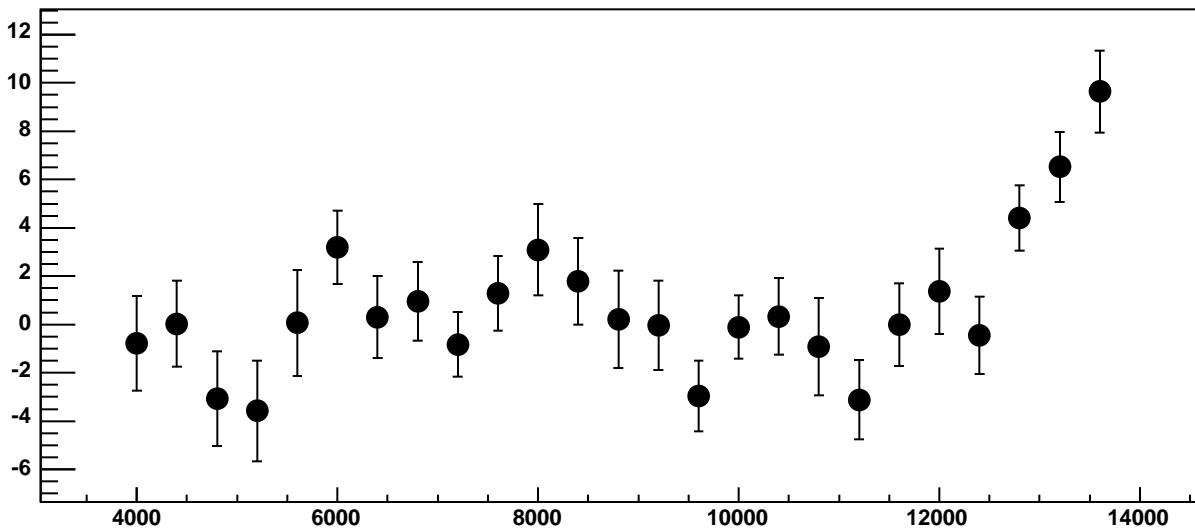


χ^2 / ndf 19.53 / 16
p0 -12.56 ± 1.574
p1 0.02115 ± 0.0001999

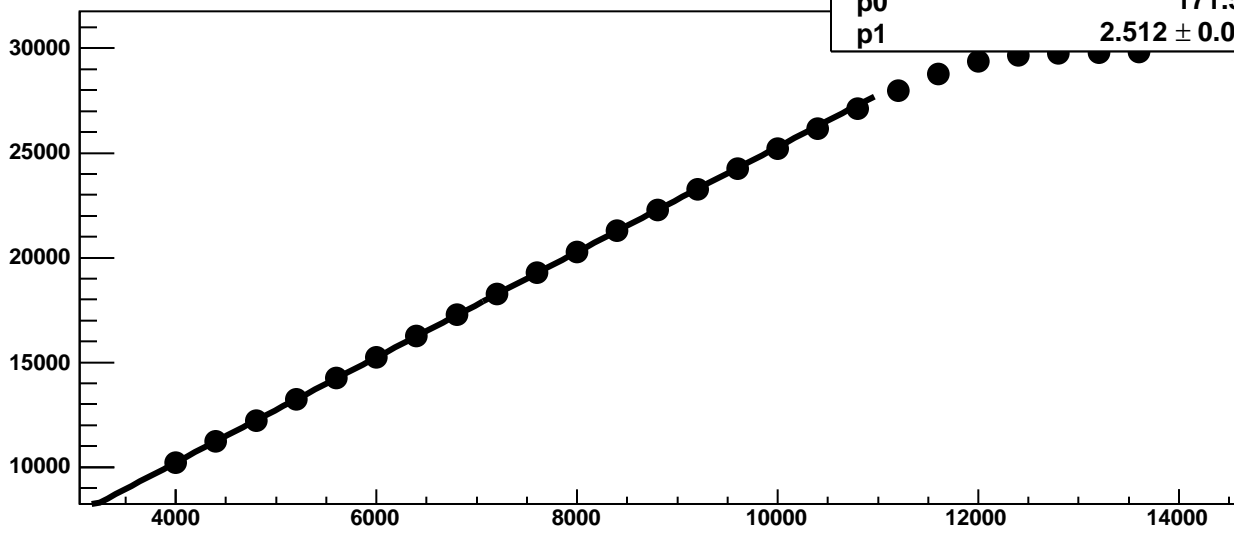
Chip 5, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

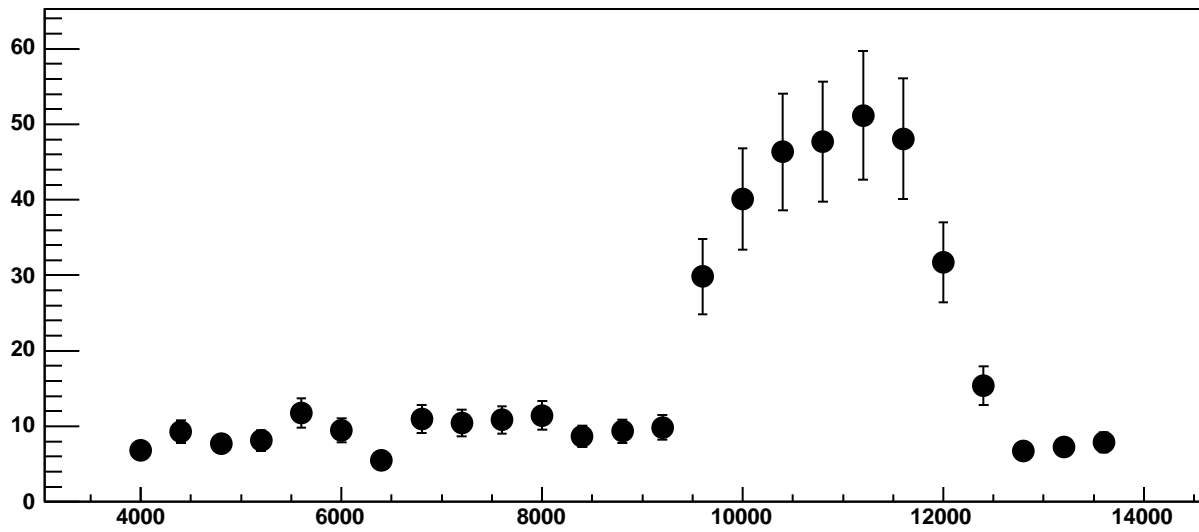


Chip 5, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC

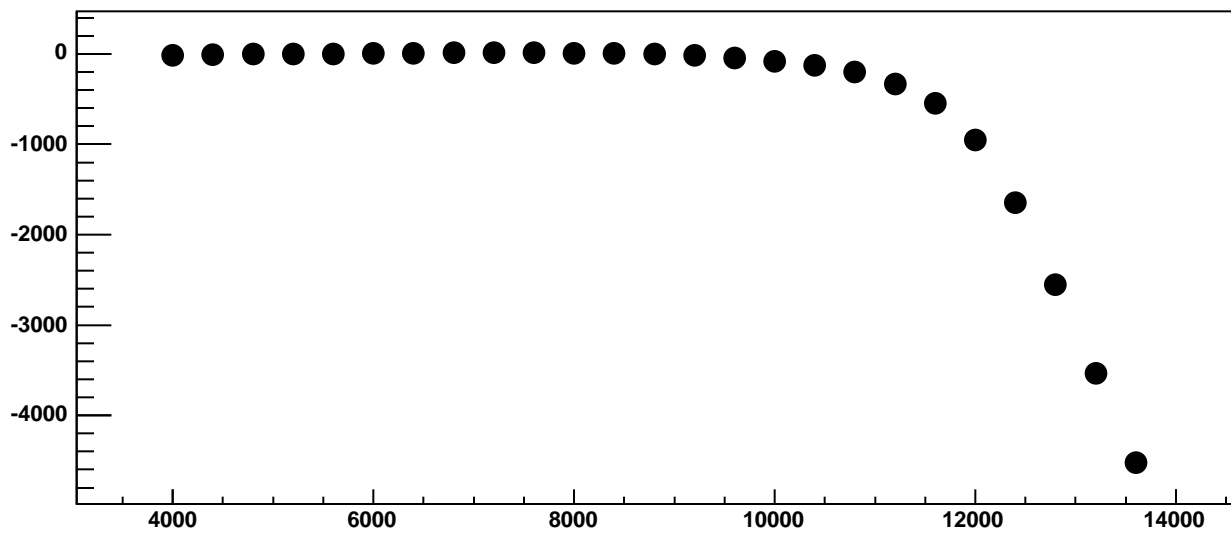


χ^2 / ndf 883.1 / 16
p0 171.5 ± 2.1
p1 2.512 ± 0.0003191

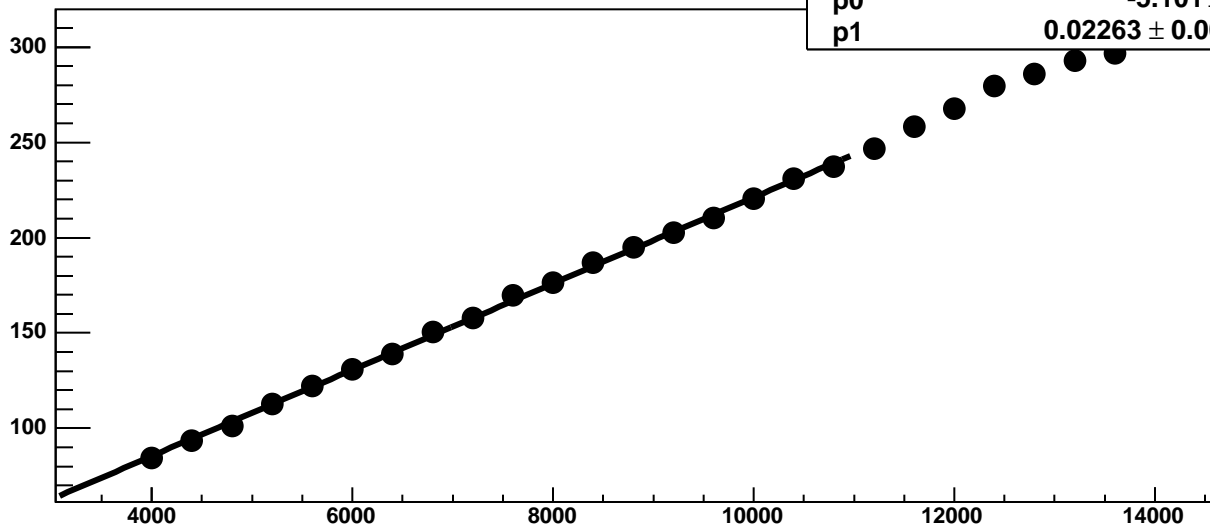
Chip 5, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC

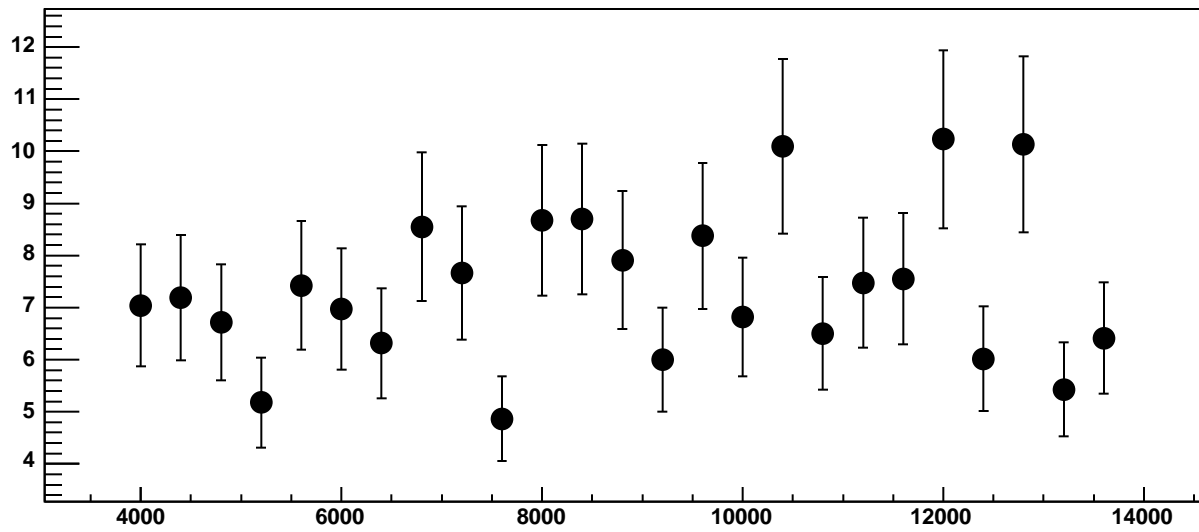


Chip 5, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

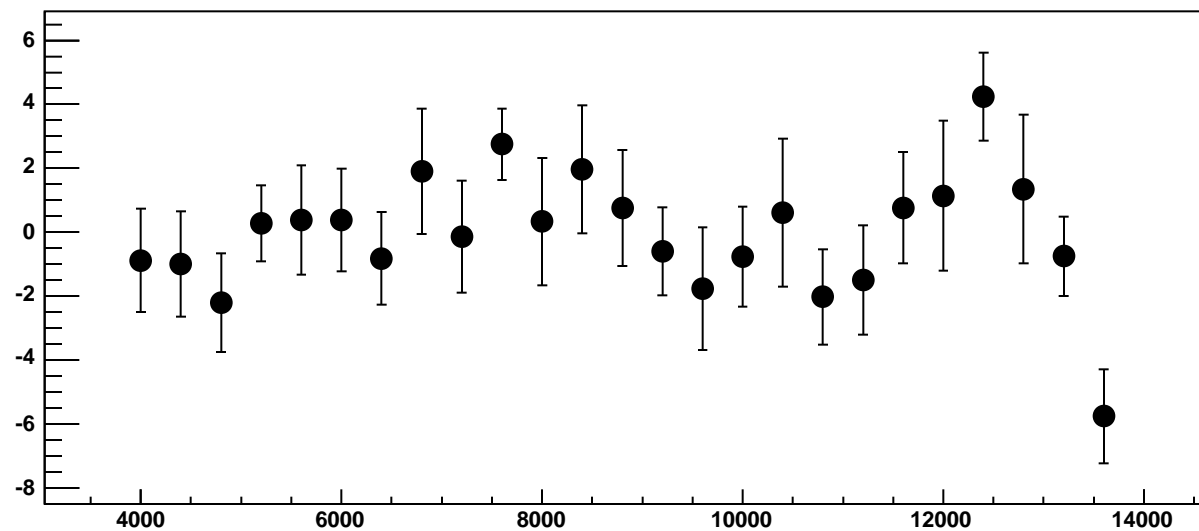


χ^2 / ndf 14.59 / 16
p0 -5.101 ± 1.374
p1 0.02263 ± 0.0001829

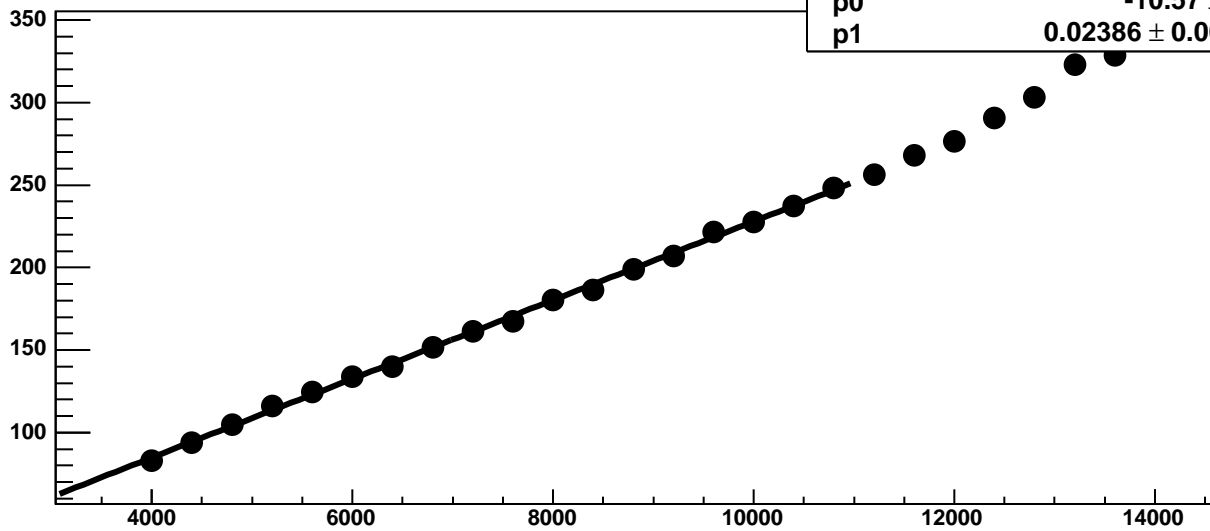
Chip 5, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

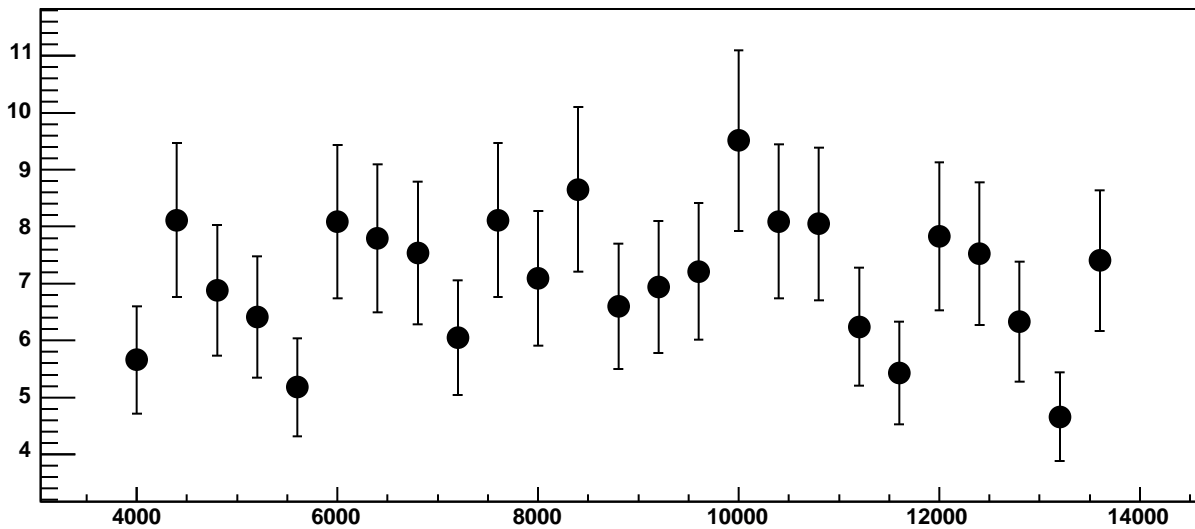


Chip 5, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

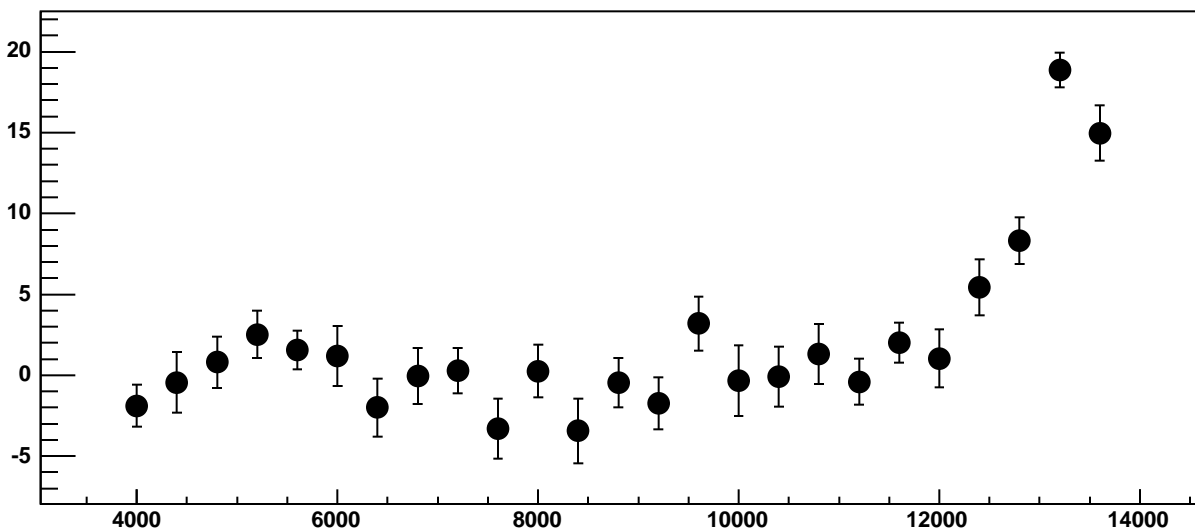


χ^2 / ndf 20.55 / 16
p0 -10.57 ± 1.375
p1 0.02386 ± 0.0001865

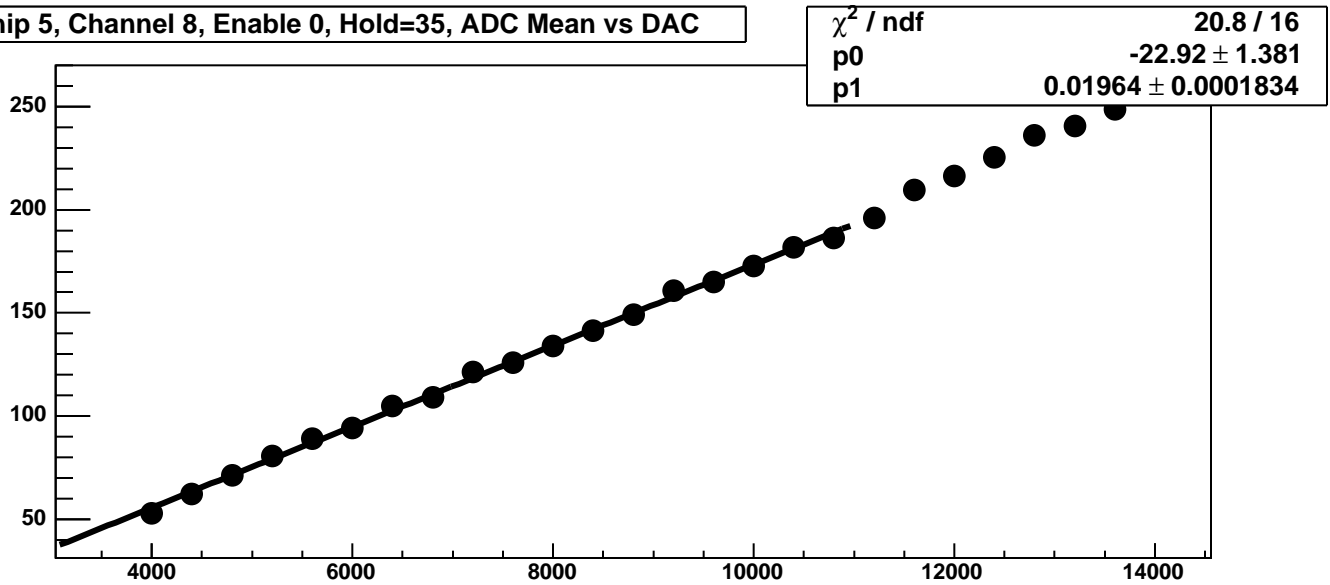
Chip 5, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



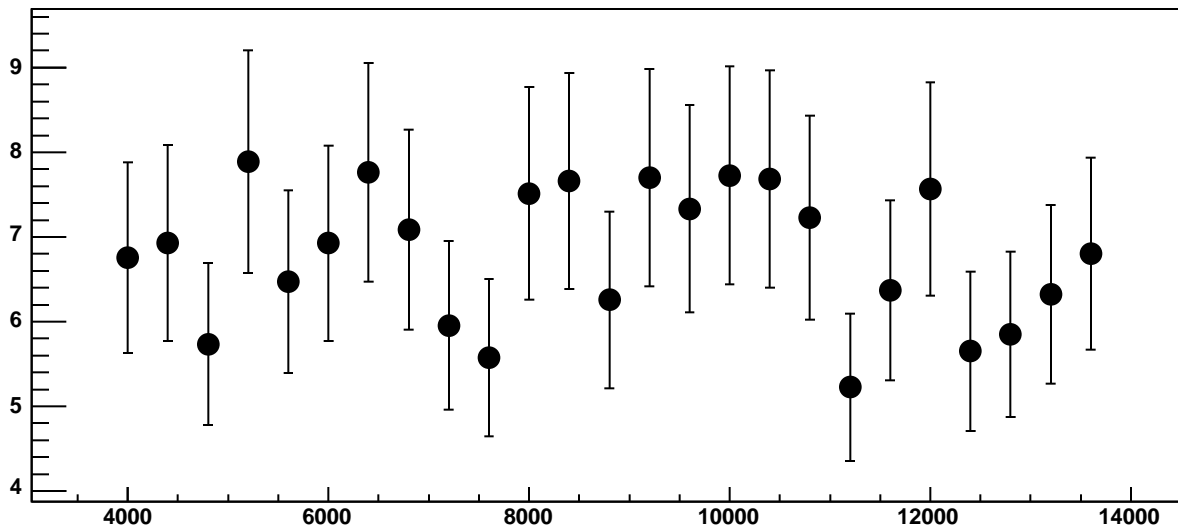
Chip 5, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



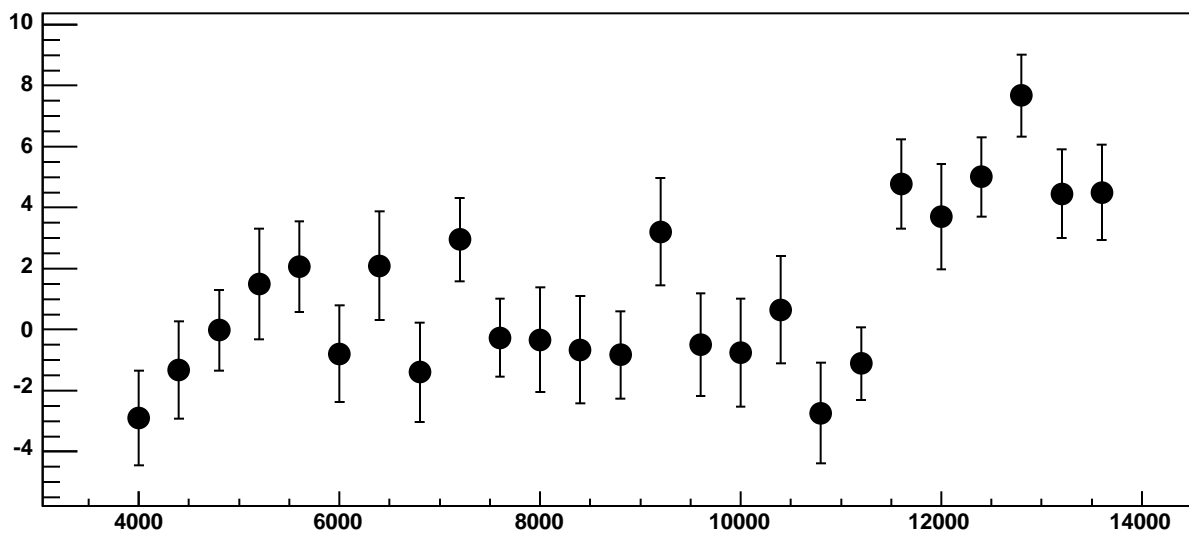
Chip 5, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



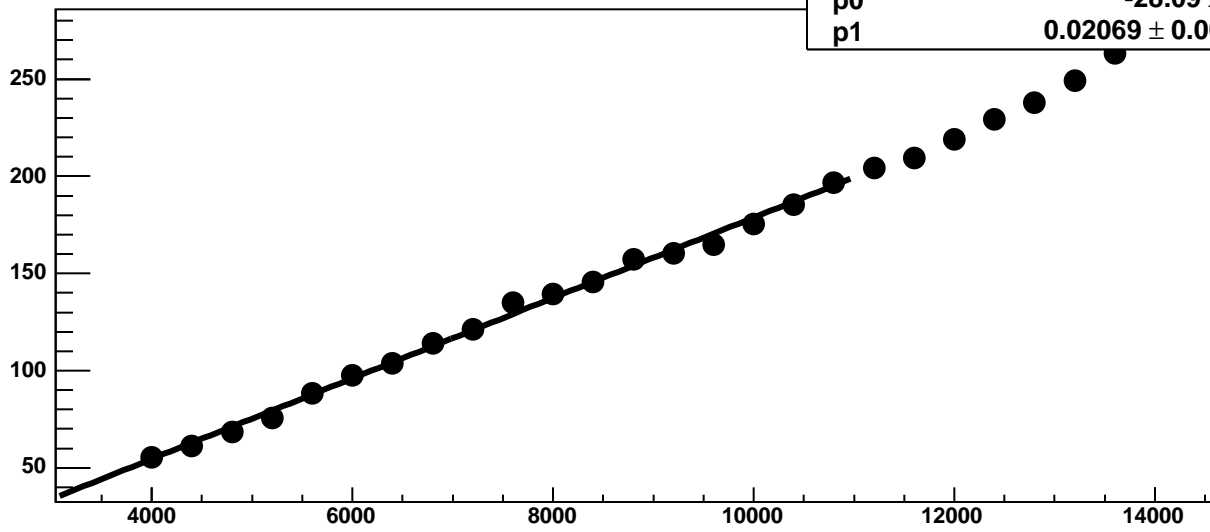
Chip 5, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

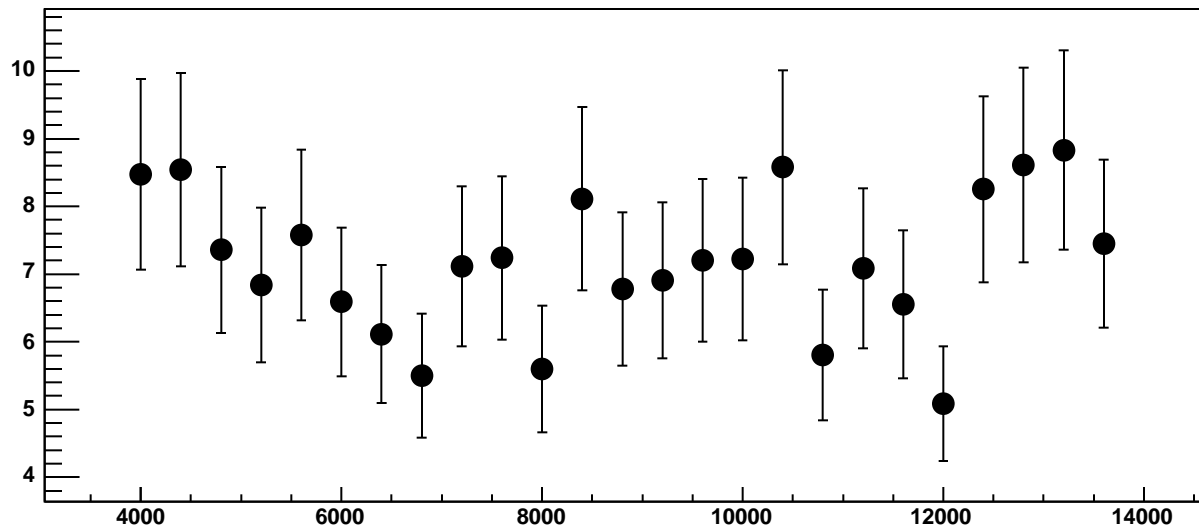


Chip 5, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

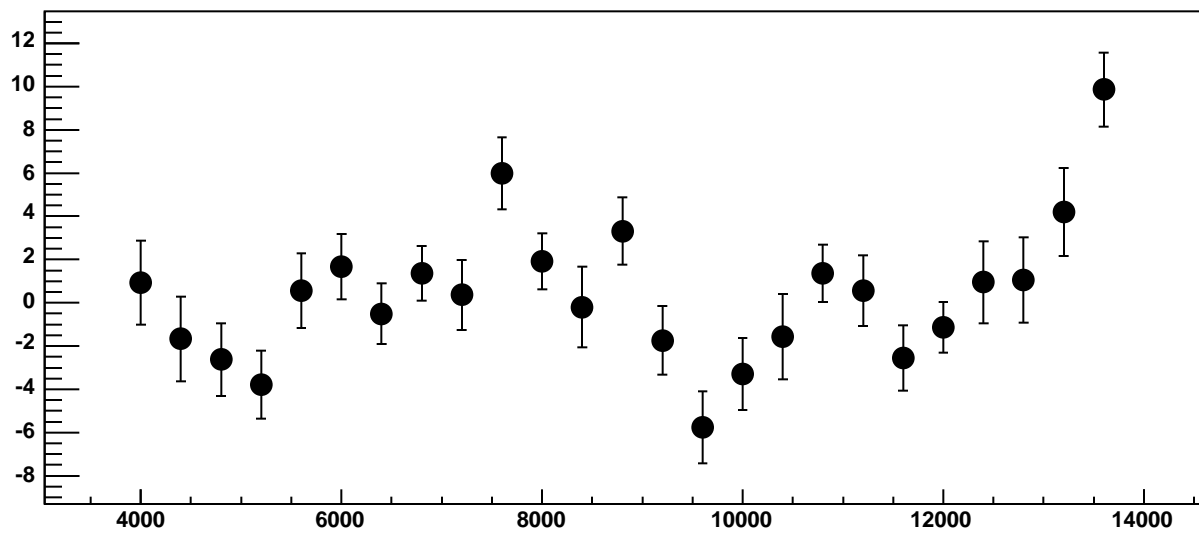


χ^2 / ndf 50.64 / 16
p0 -28.09 ± 1.467
p1 0.02069 ± 0.0001889

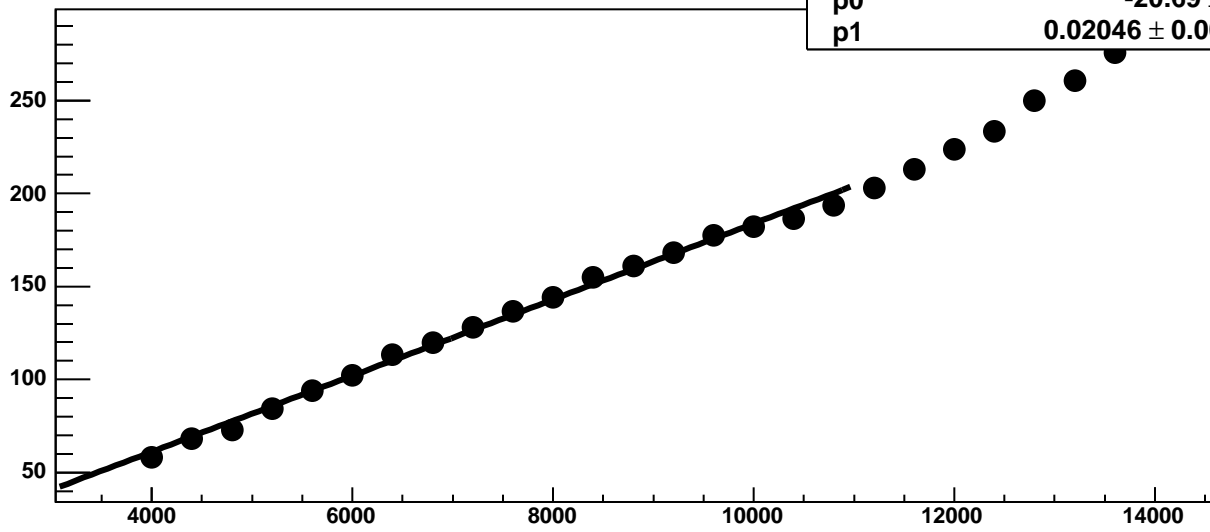
Chip 5, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

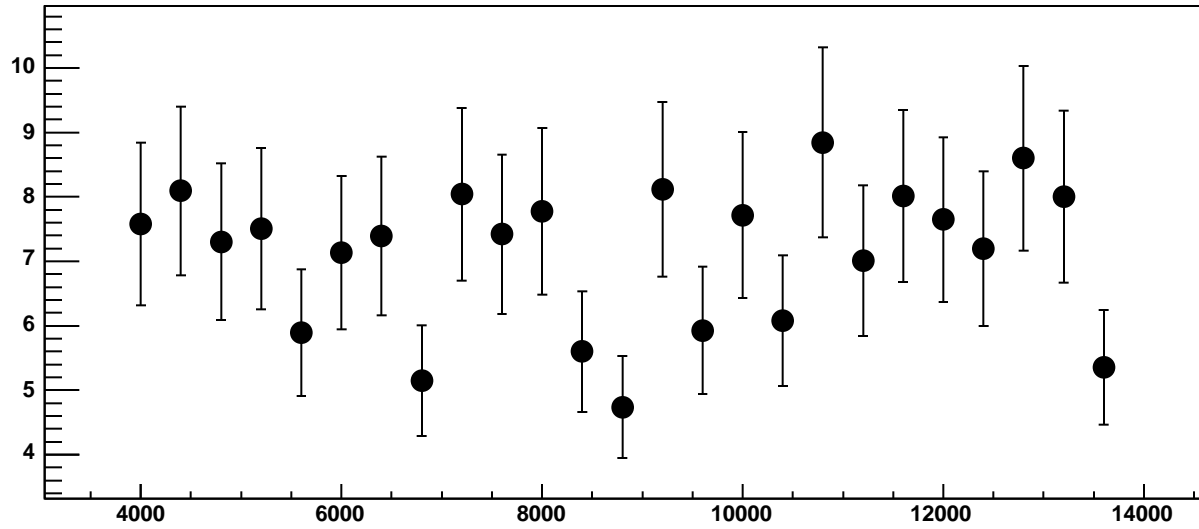


Chip 5, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC

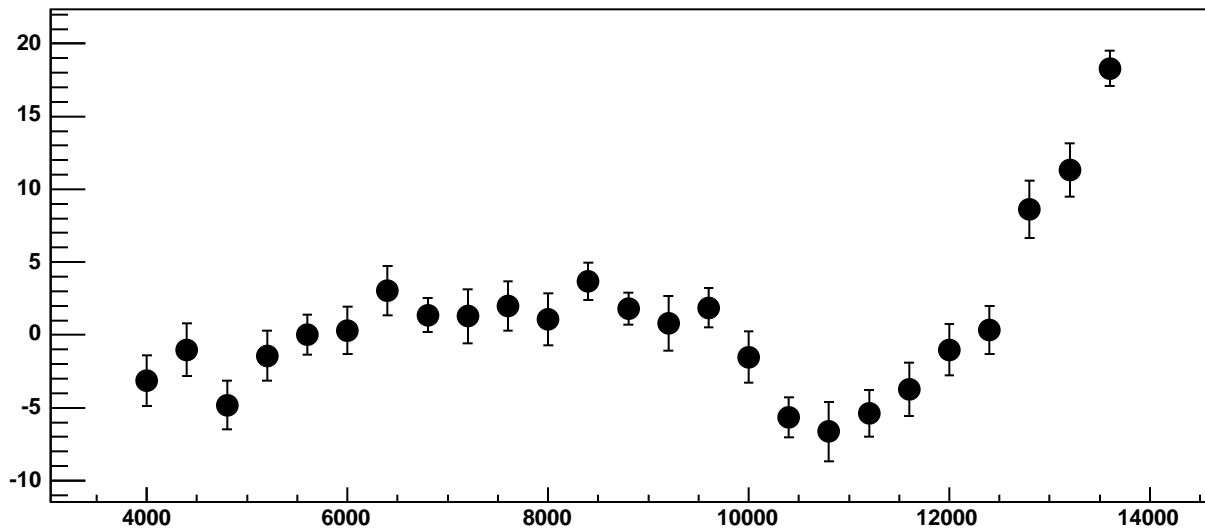


χ^2 / ndf 60.27 / 16
p0 -20.69 ± 1.439
p1 0.02046 ± 0.0001852

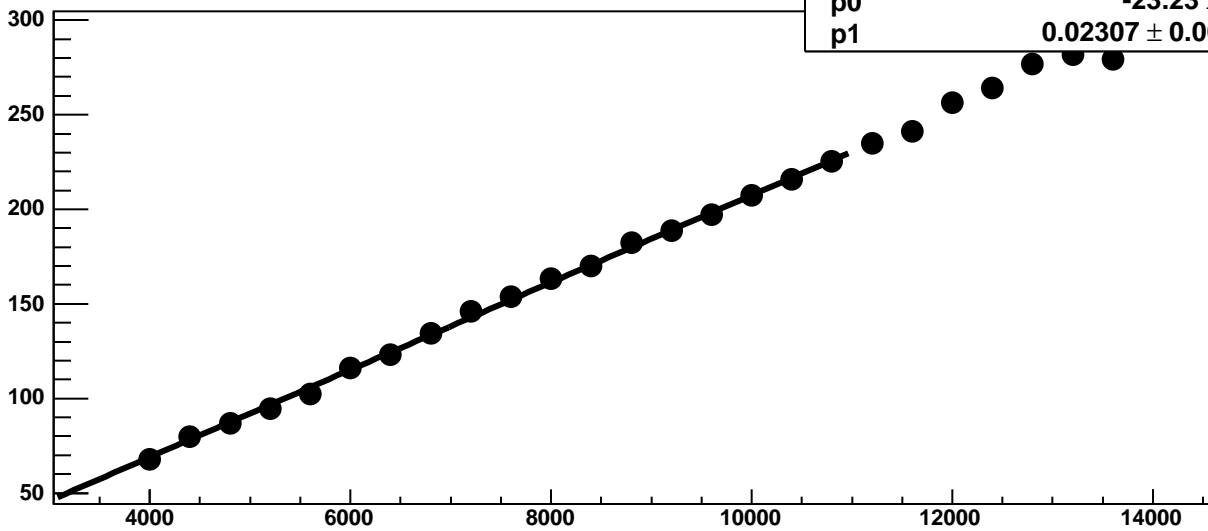
Chip 5, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC

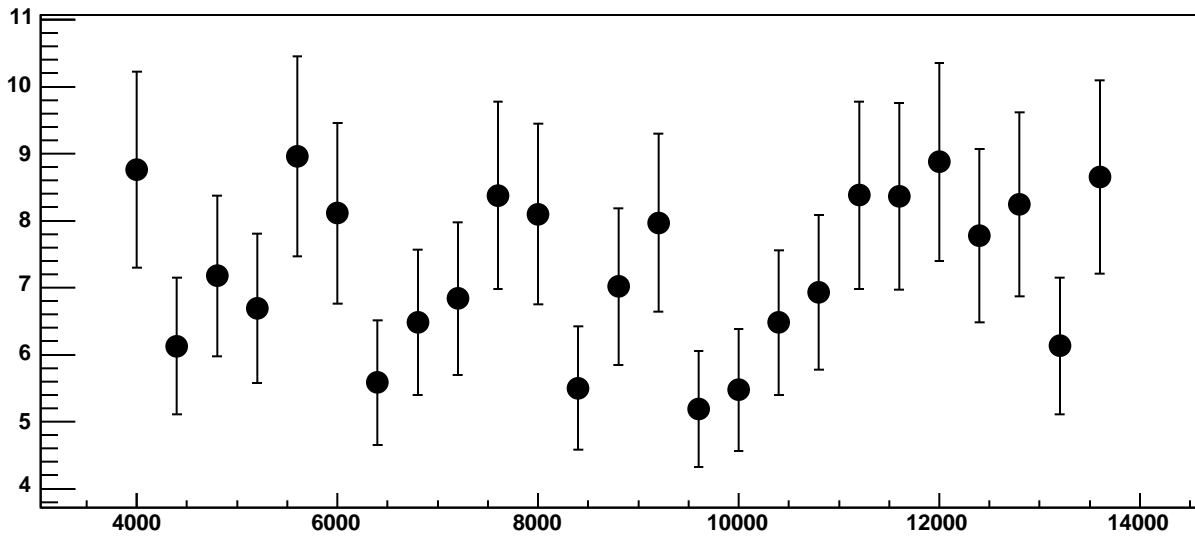


Chip 5, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

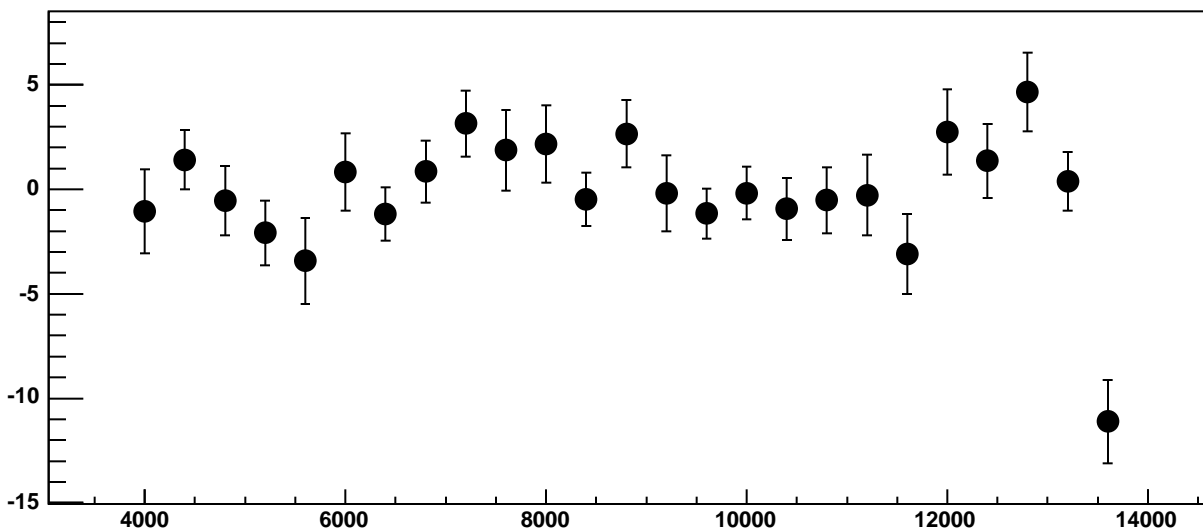


χ^2 / ndf 18.08 / 16
p0 -23.23 ± 1.388
p1 0.02307 ± 0.0001755

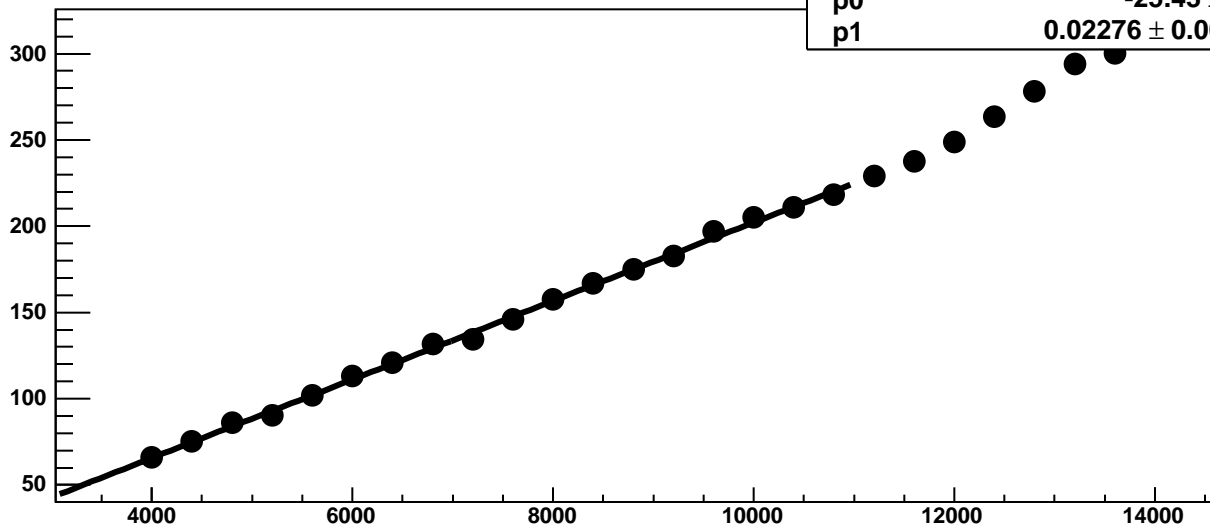
Chip 5, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

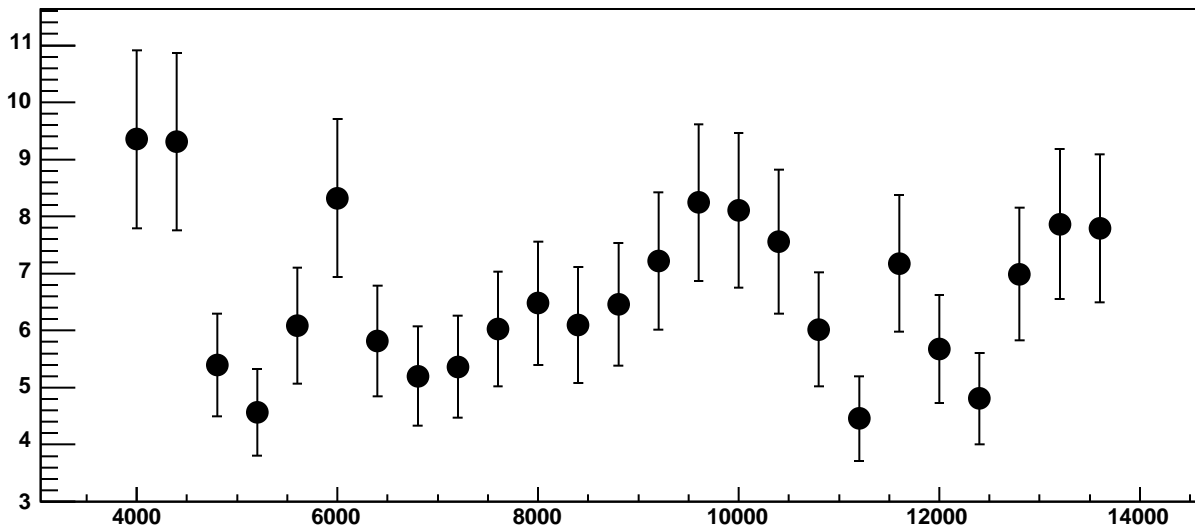


Chip 5, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

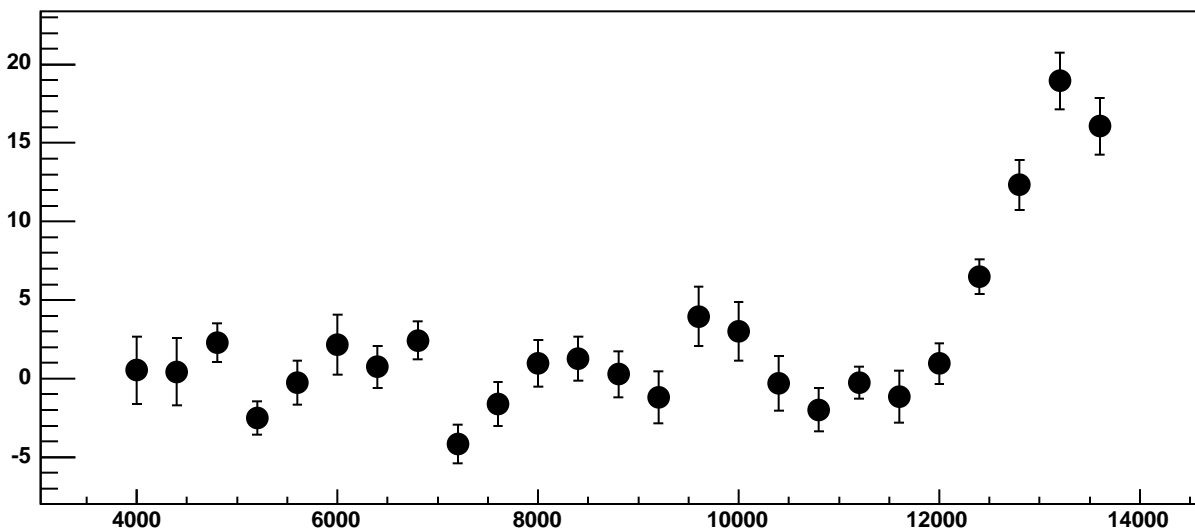


χ^2 / ndf 38.68 / 16
p0 -25.43 ± 1.342
p1 0.02276 ± 0.0001787

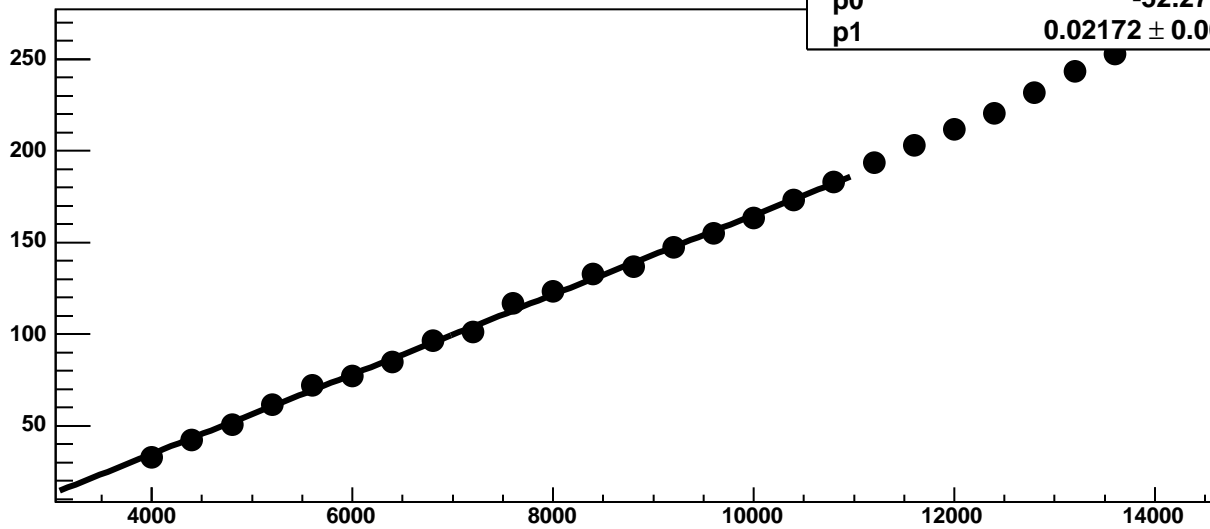
Chip 5, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

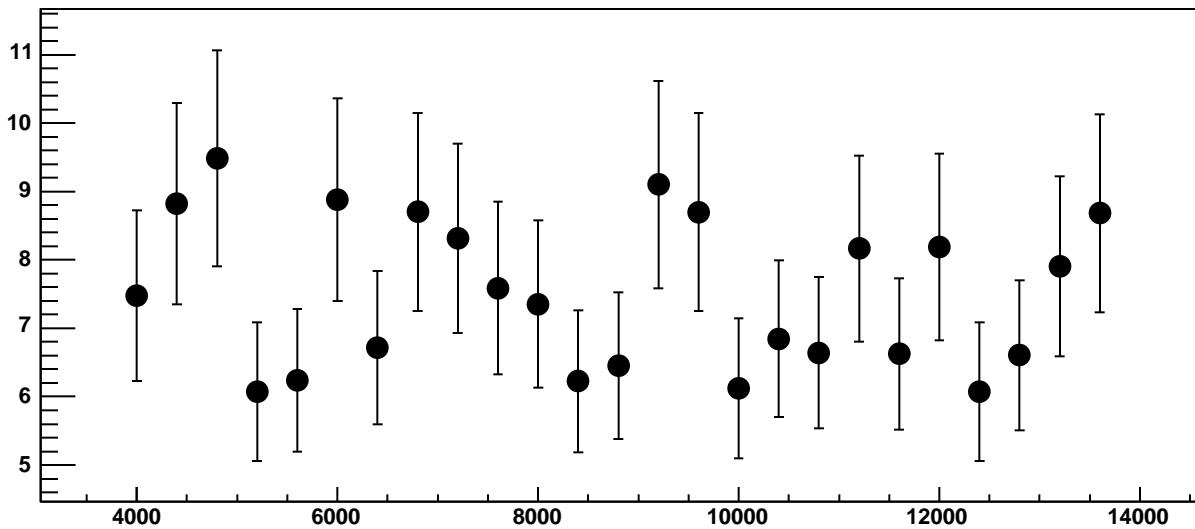


Chip 5, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

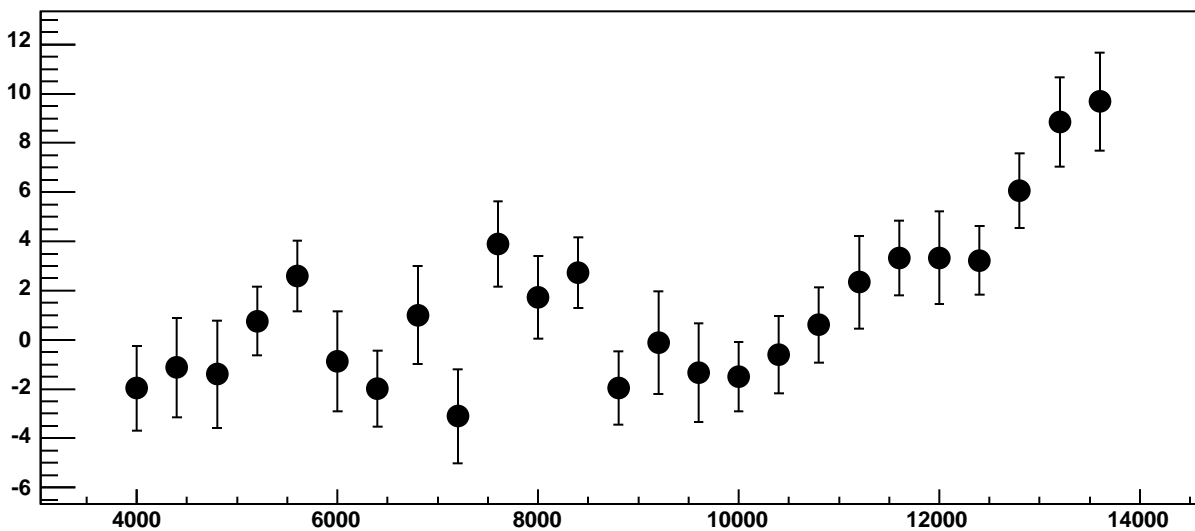


χ^2 / ndf 23.73 / 16
p0 -52.27 ± 1.48
p1 0.02172 ± 0.0001889

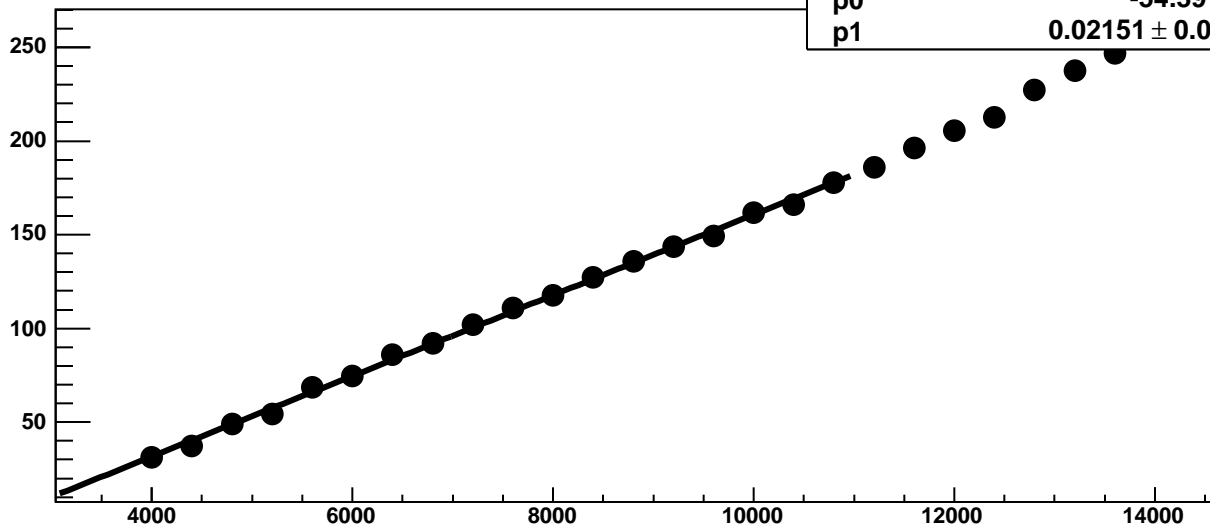
Chip 5, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

22.25 / 16

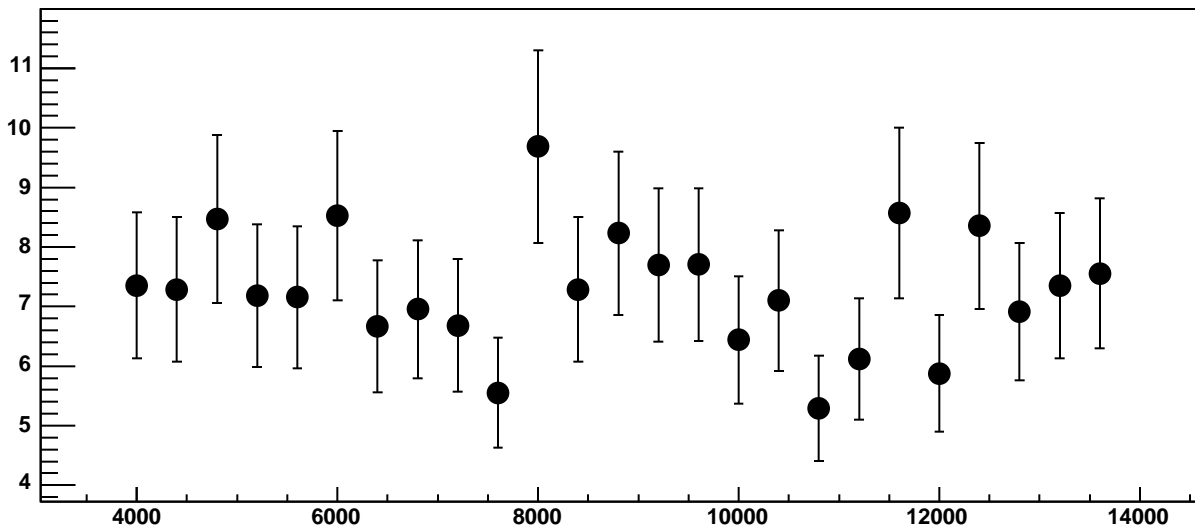
p0

-54.39 ± 1.421

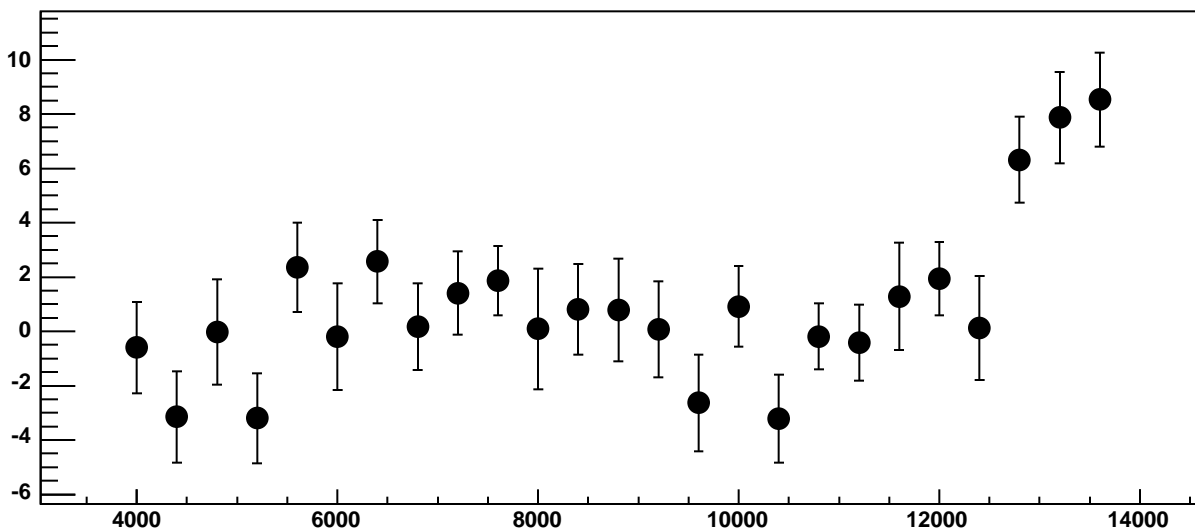
p1

0.02151 ± 0.0001801

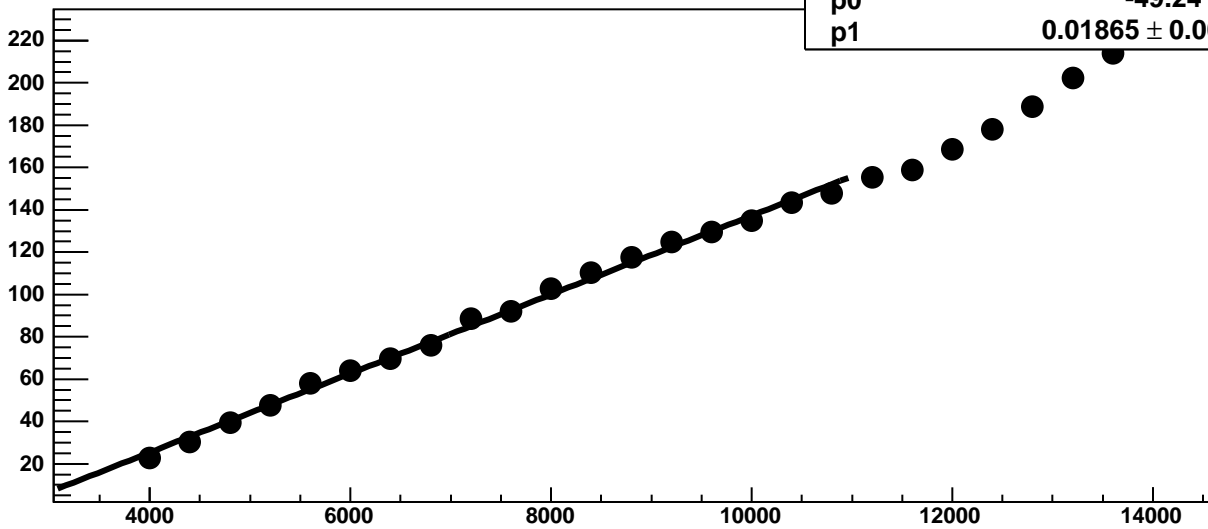
Chip 5, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

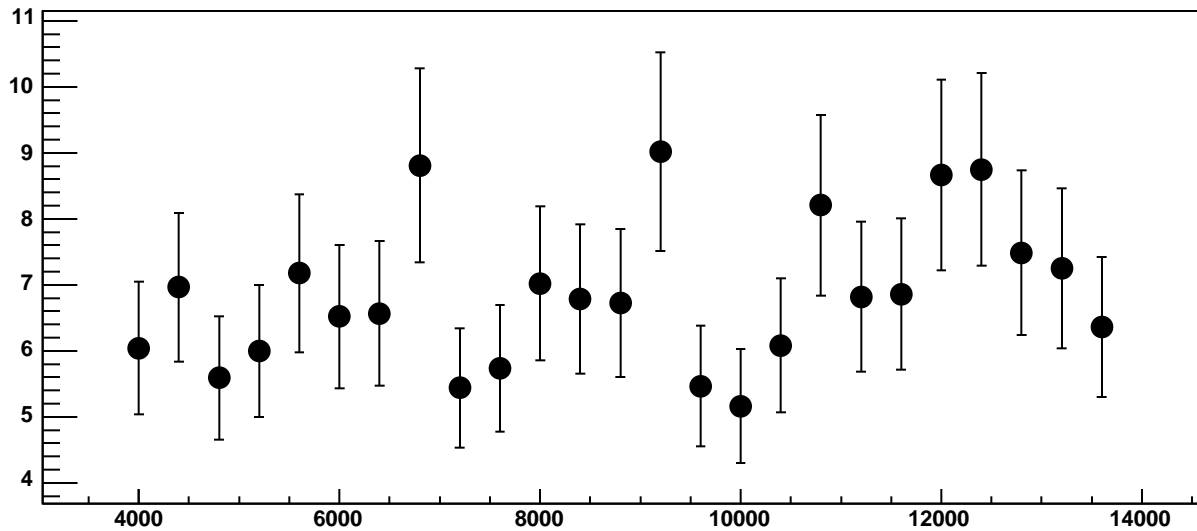


Chip 5, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

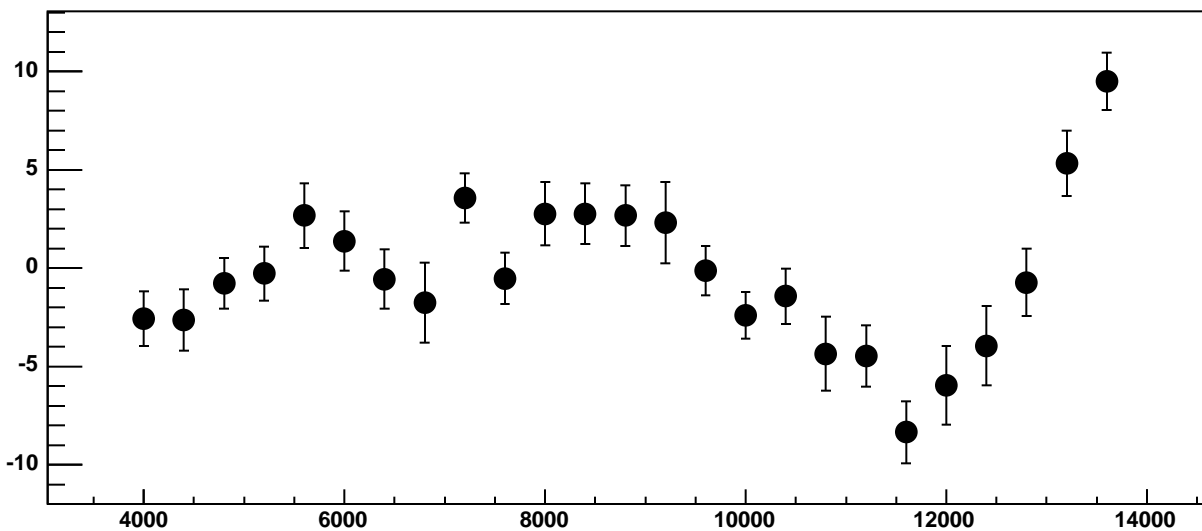


χ^2 / ndf 40.33 / 16
p0 -49.24 ± 1.261
p1 0.01865 ± 0.0001645

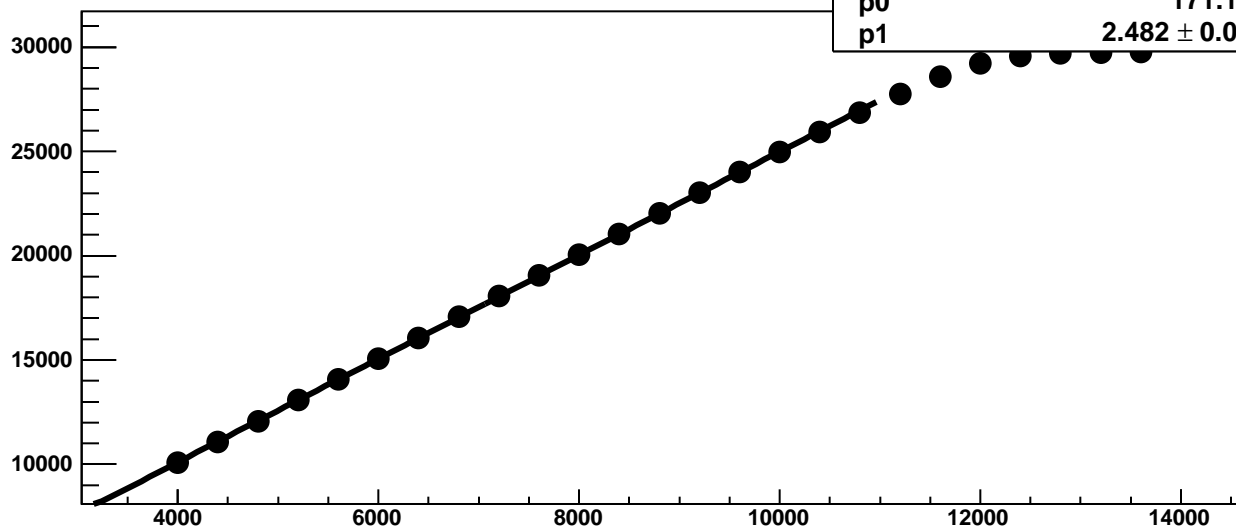
Chip 5, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



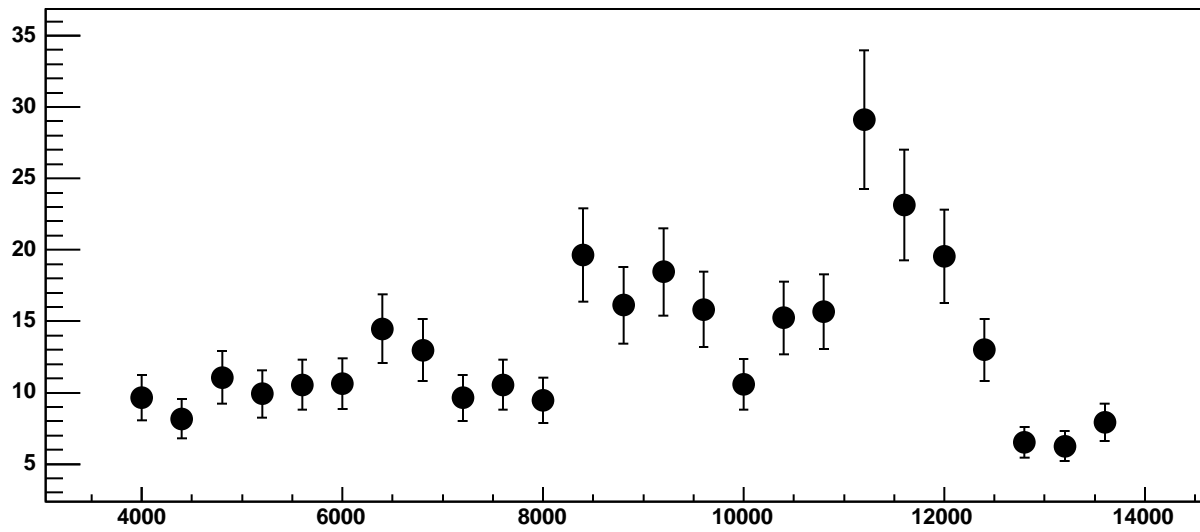
Chip 5, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



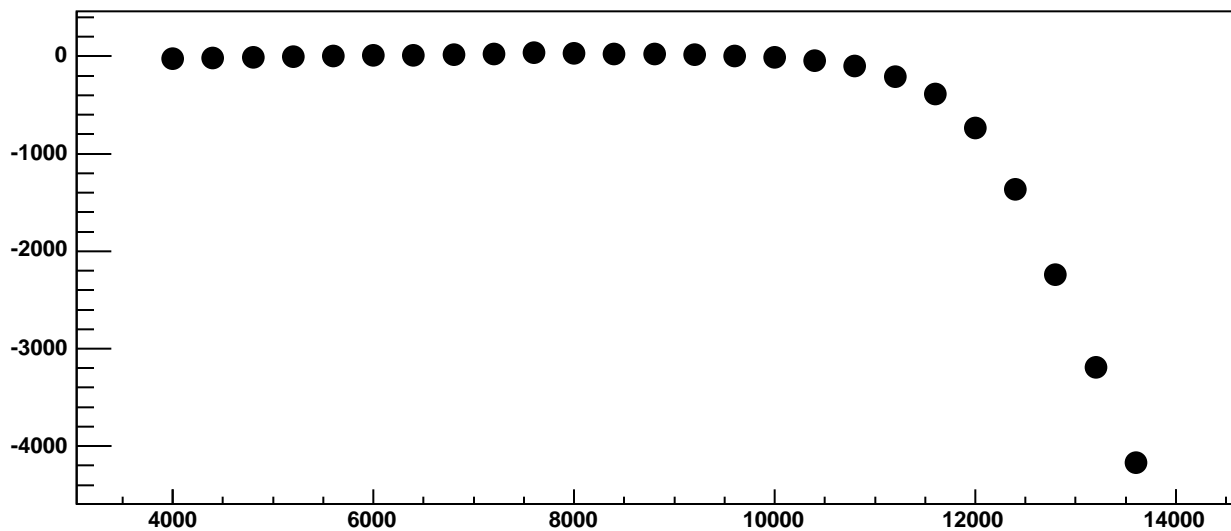
Chip 5, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC



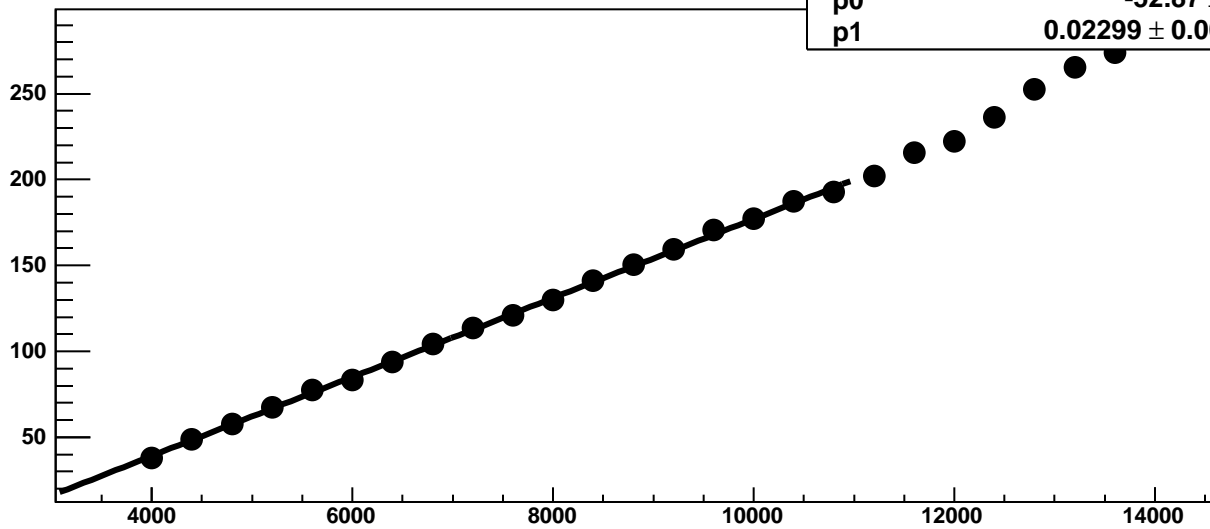
Chip 5, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



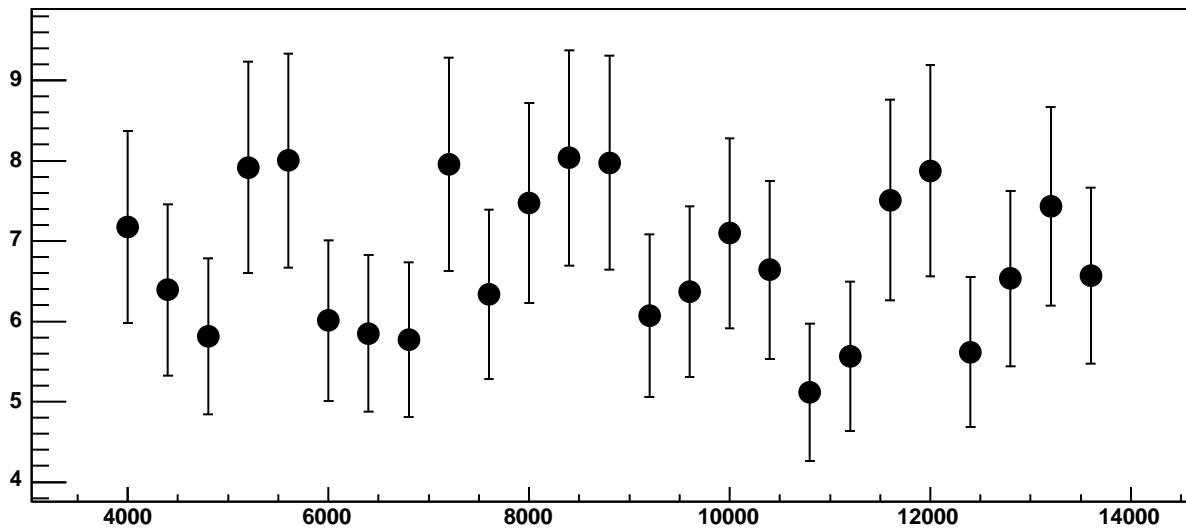
Chip 5, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC



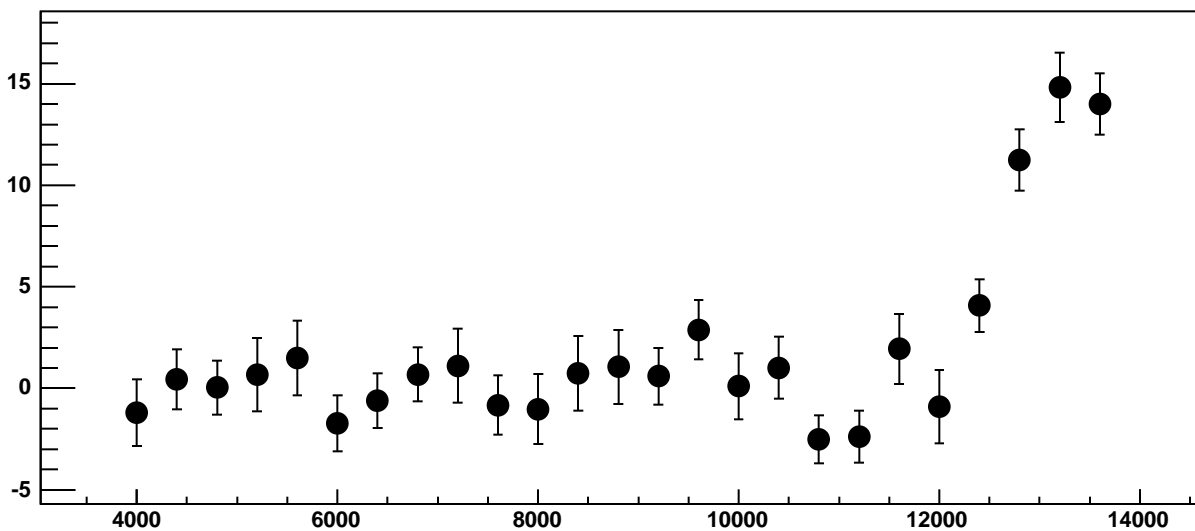
Chip 5, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



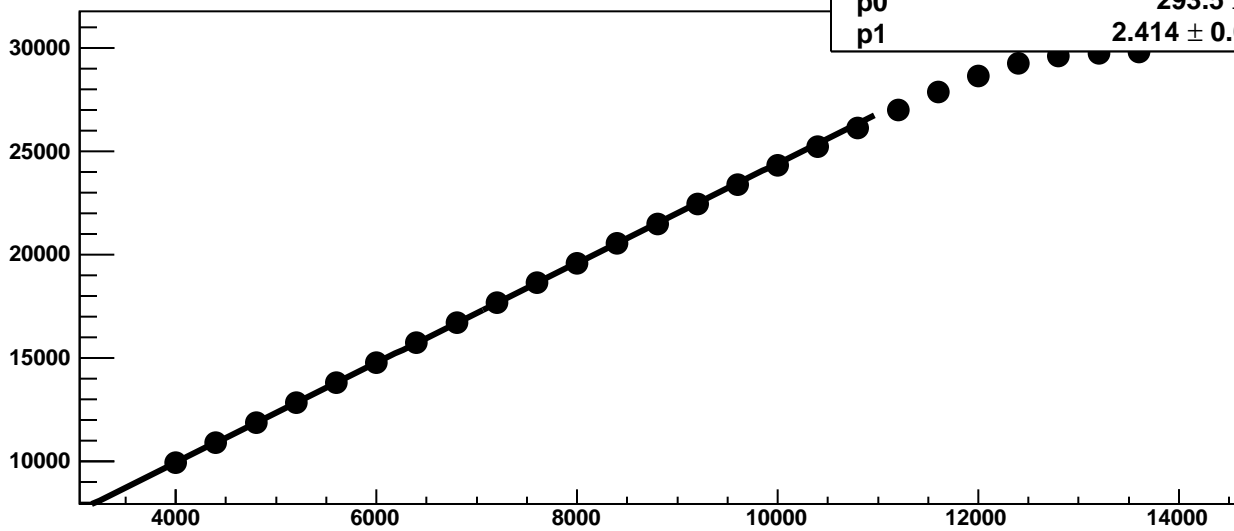
Chip 5, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



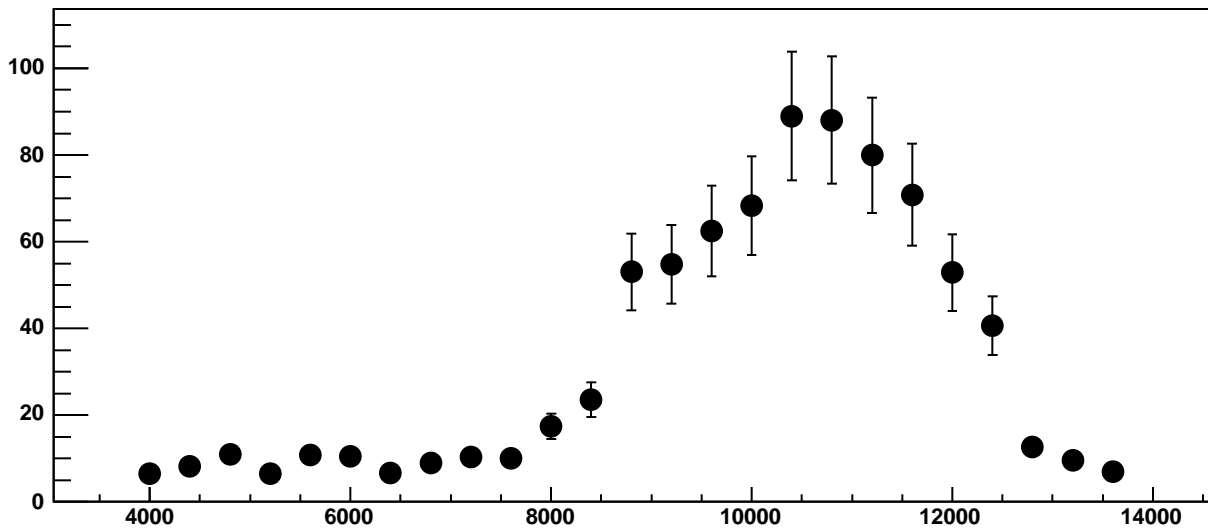
Chip 5, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



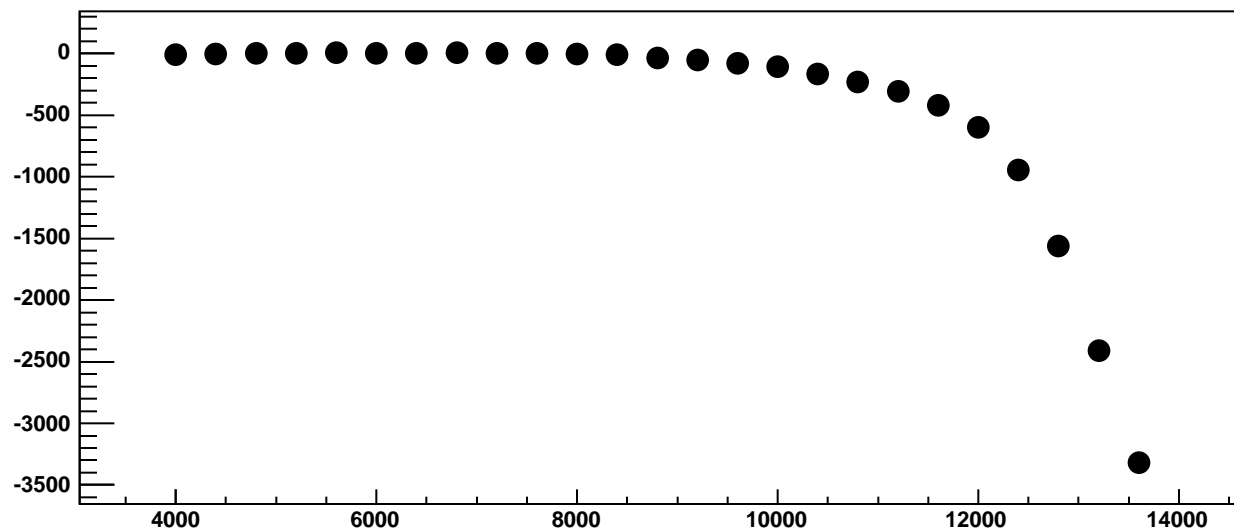
Chip 5, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC



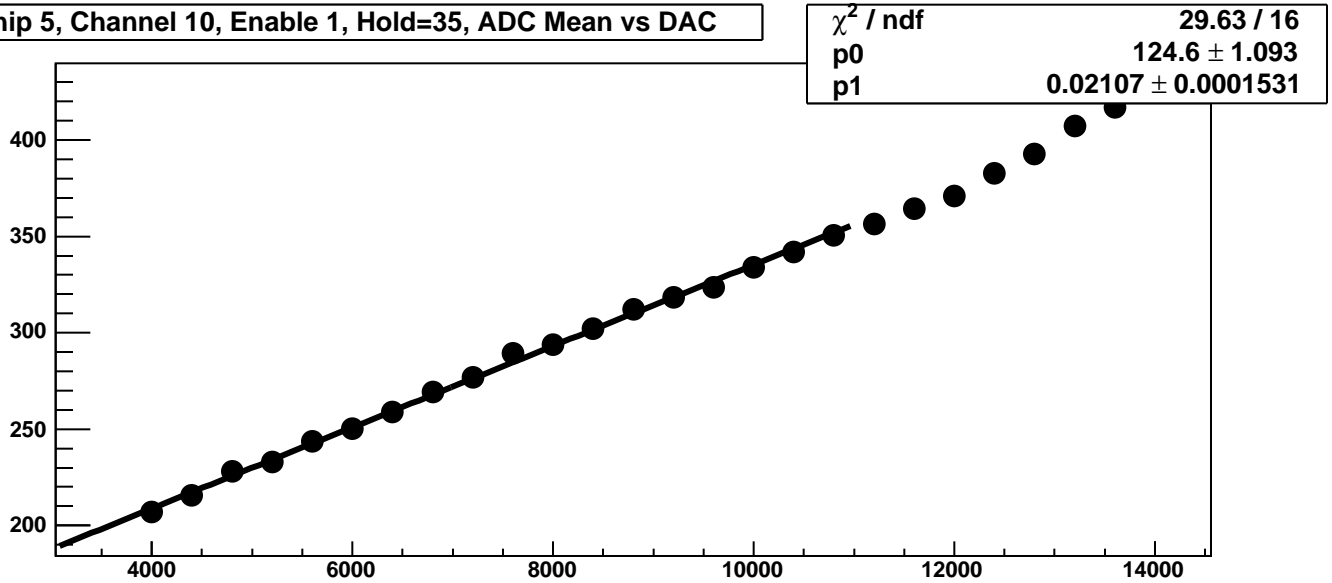
Chip 5, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



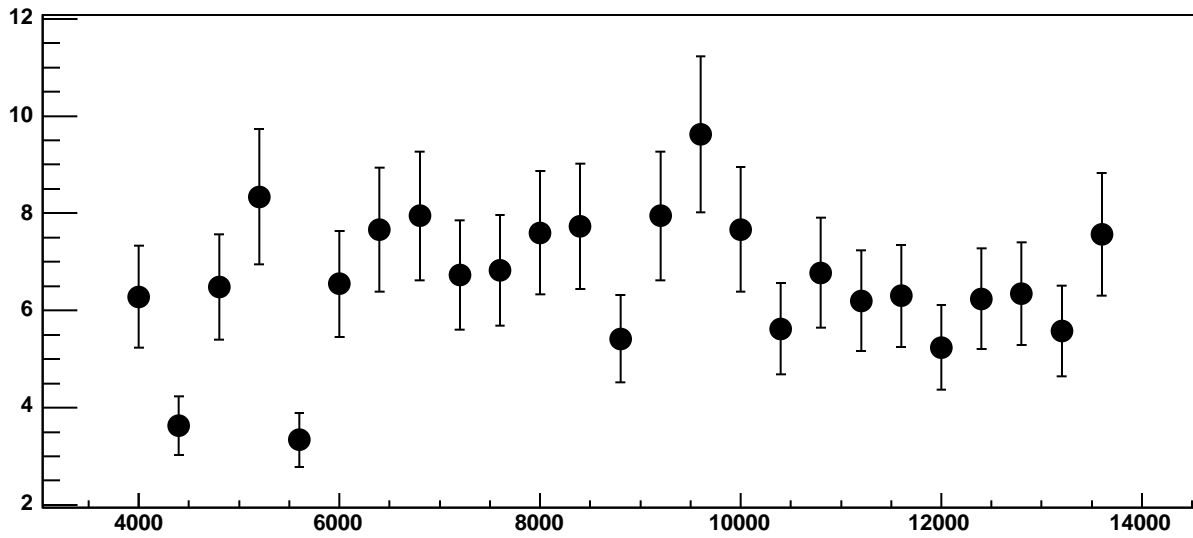
Chip 5, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC



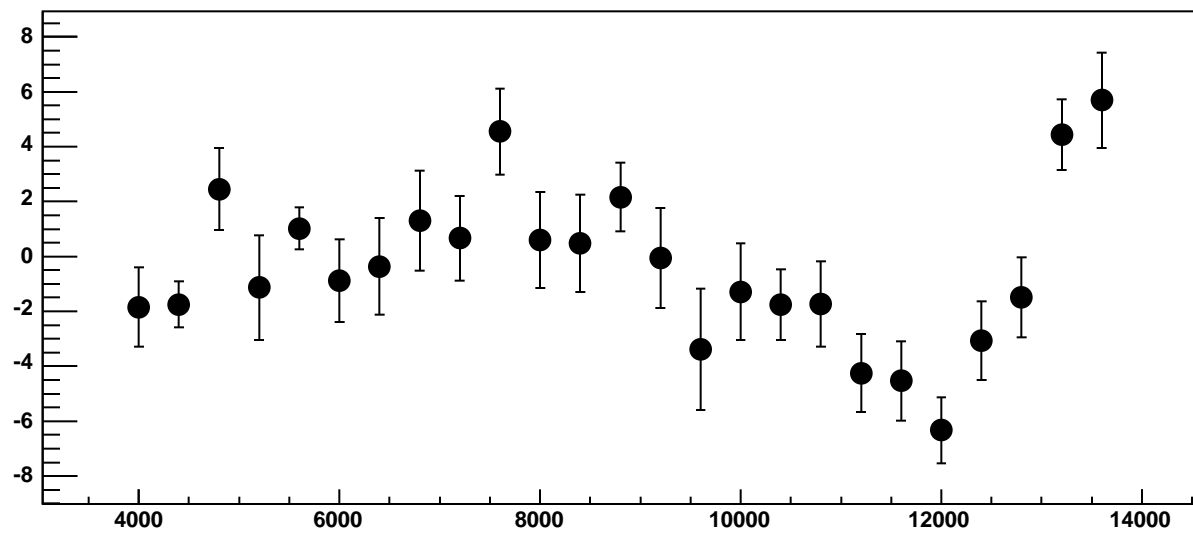
Chip 5, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



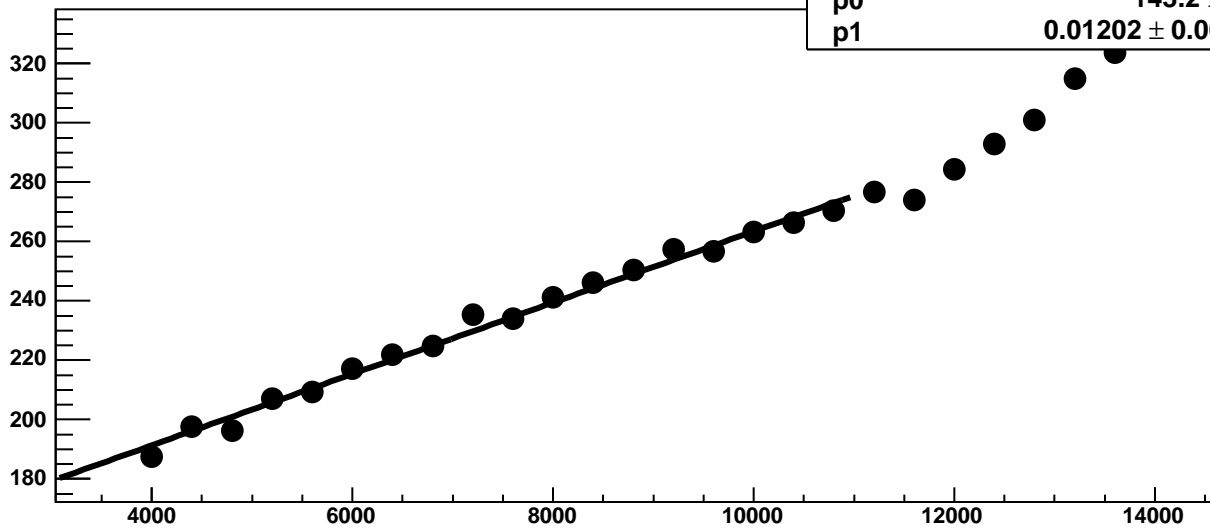
Chip 5, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



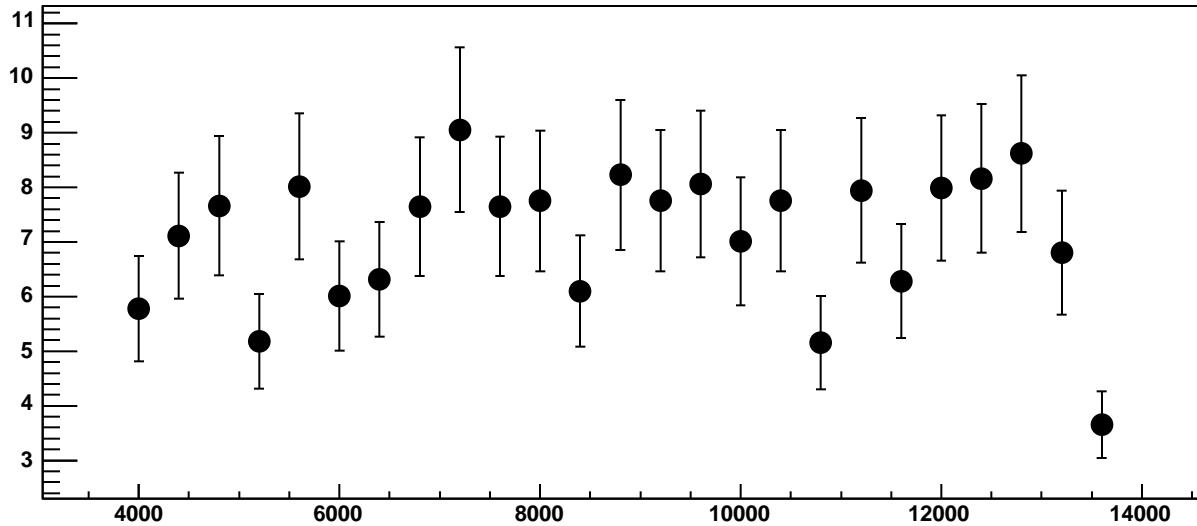
Chip 5, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



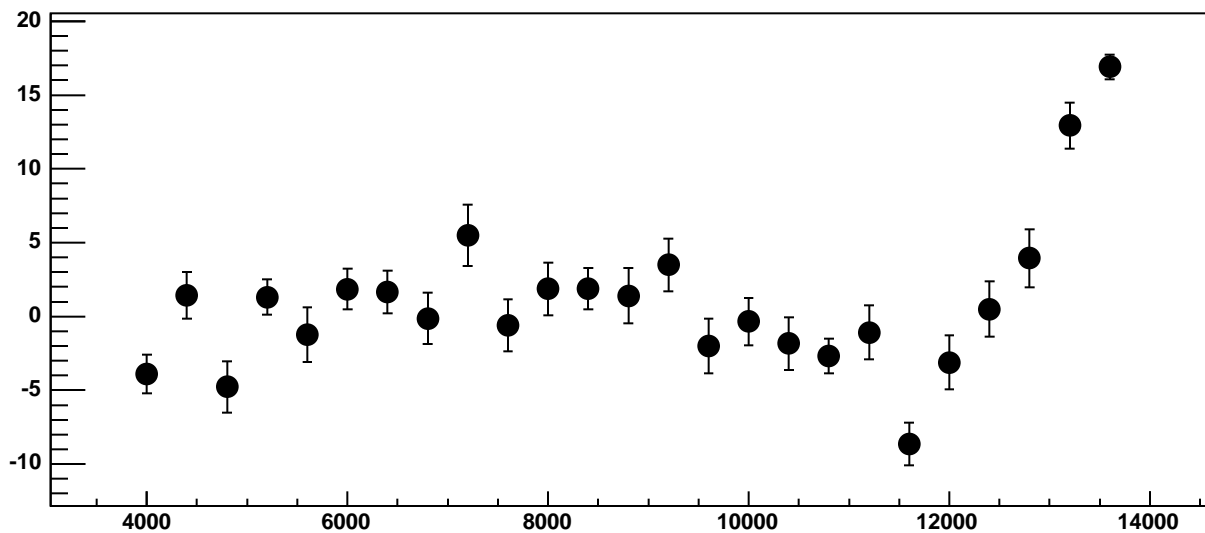
Chip 5, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



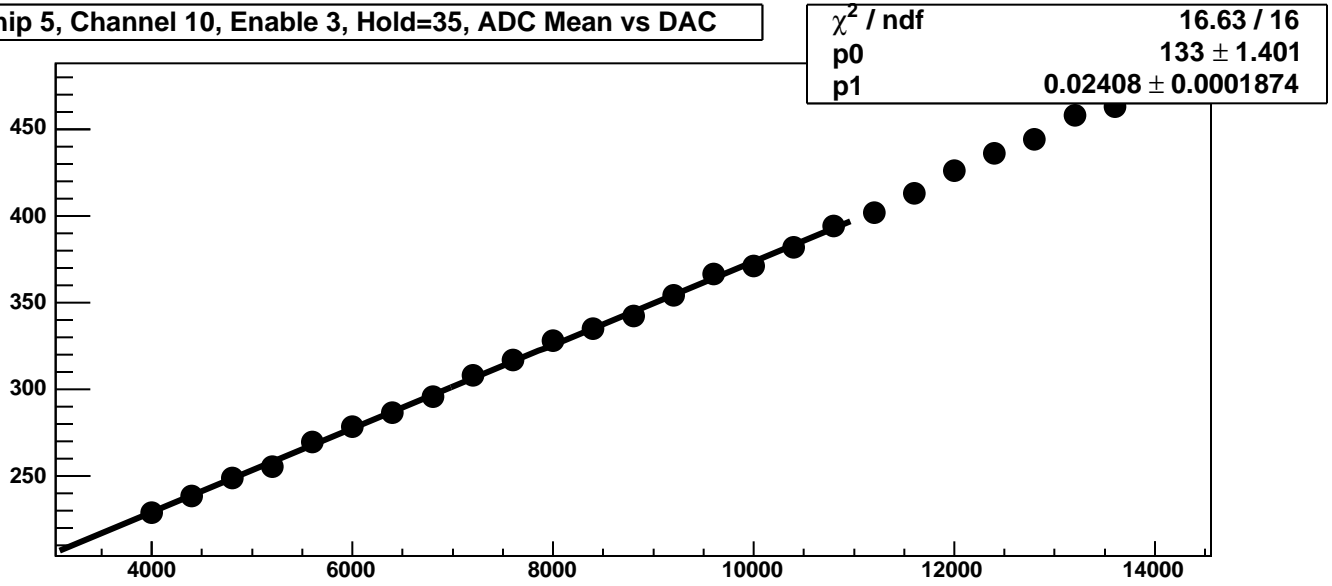
Chip 5, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



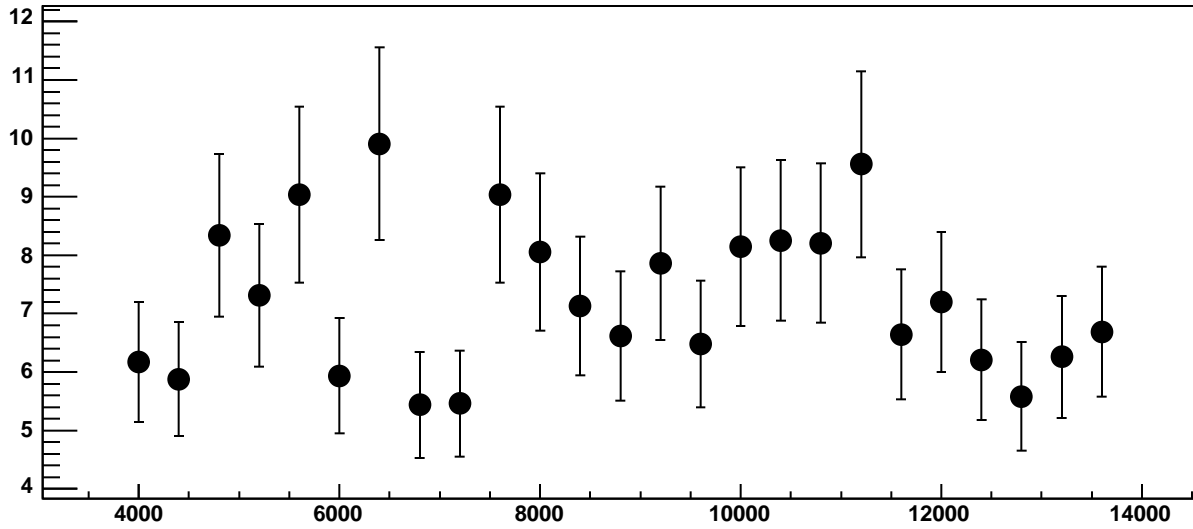
Chip 5, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



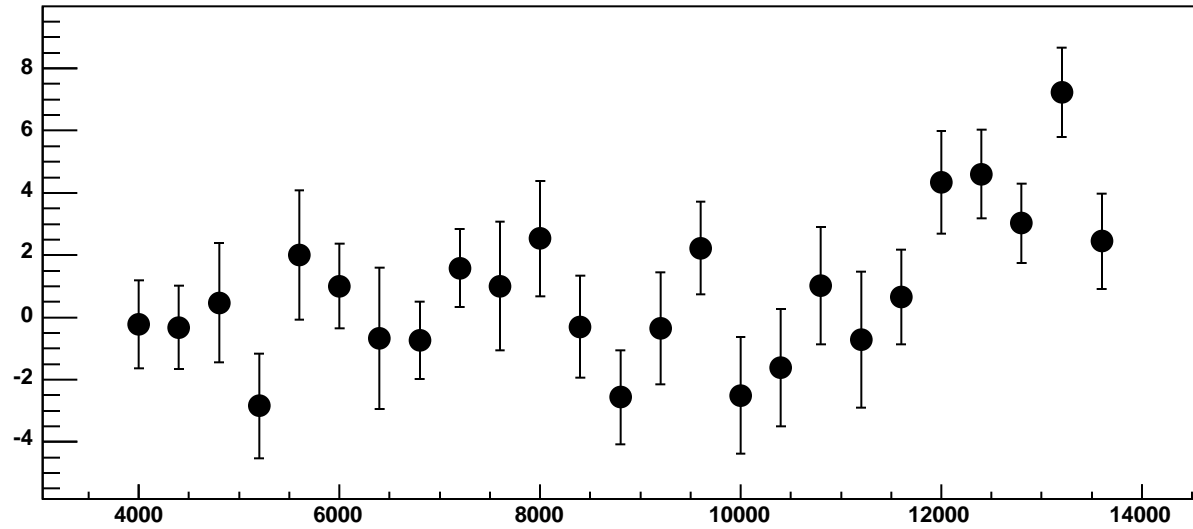
Chip 5, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC



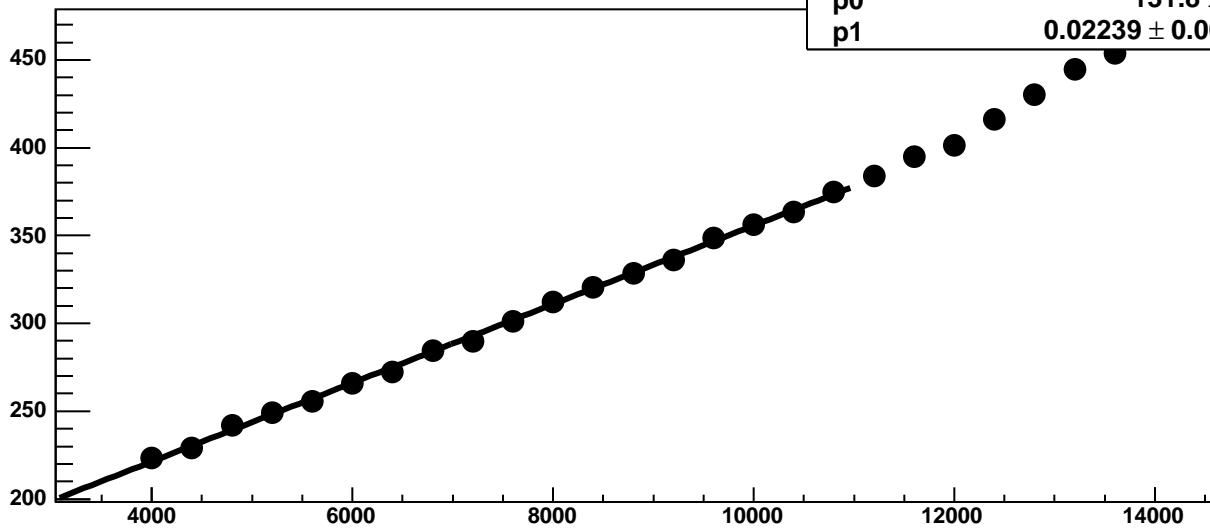
Chip 5, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

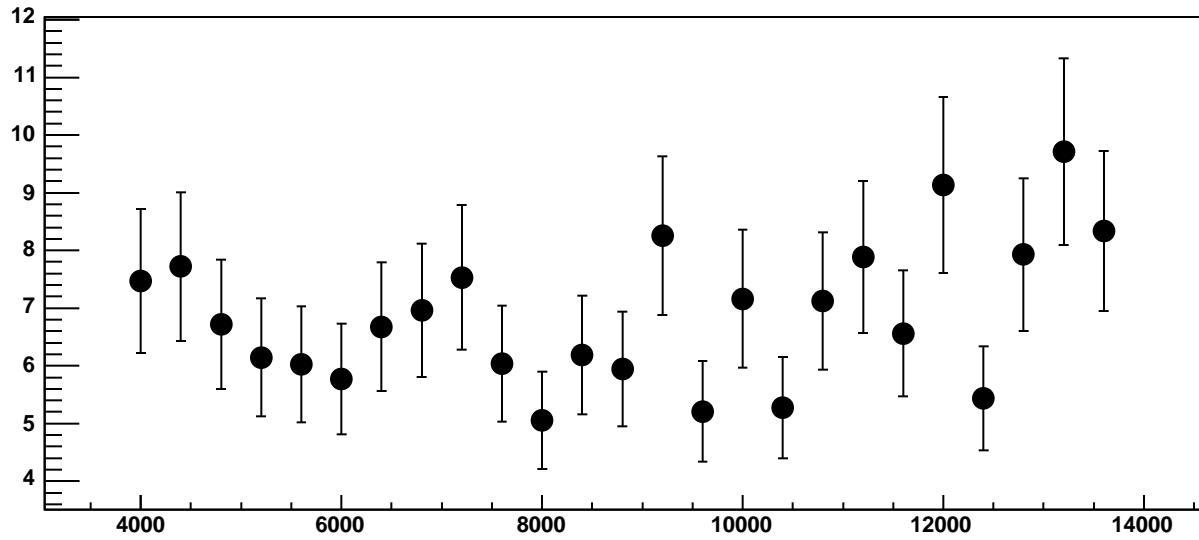


Chip 5, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

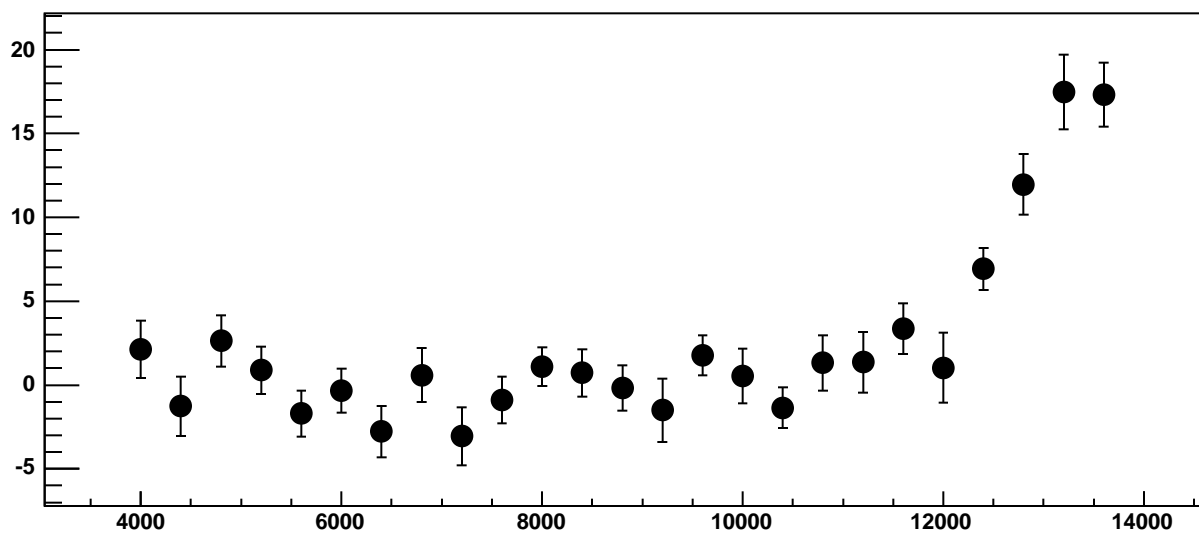


χ^2 / ndf 19.82 / 16
p0 131.8 ± 1.334
p1 0.02239 ± 0.0001705

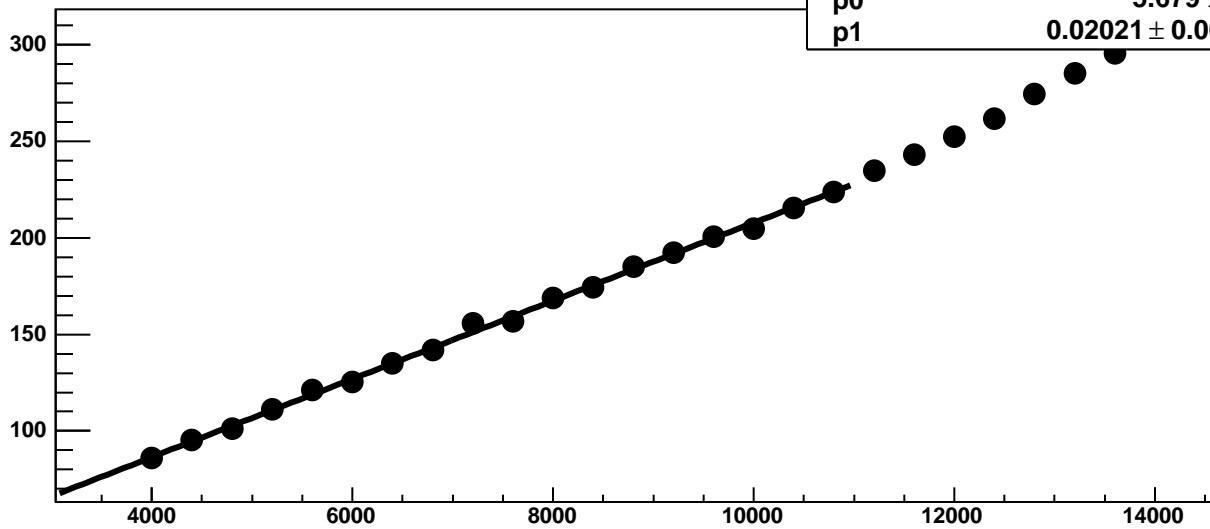
Chip 5, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



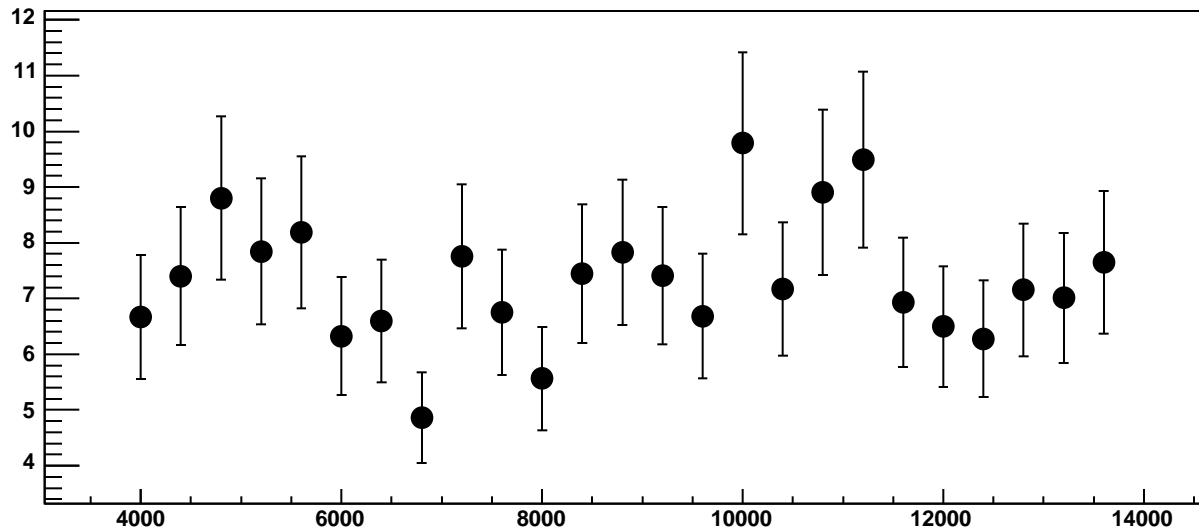
Chip 5, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



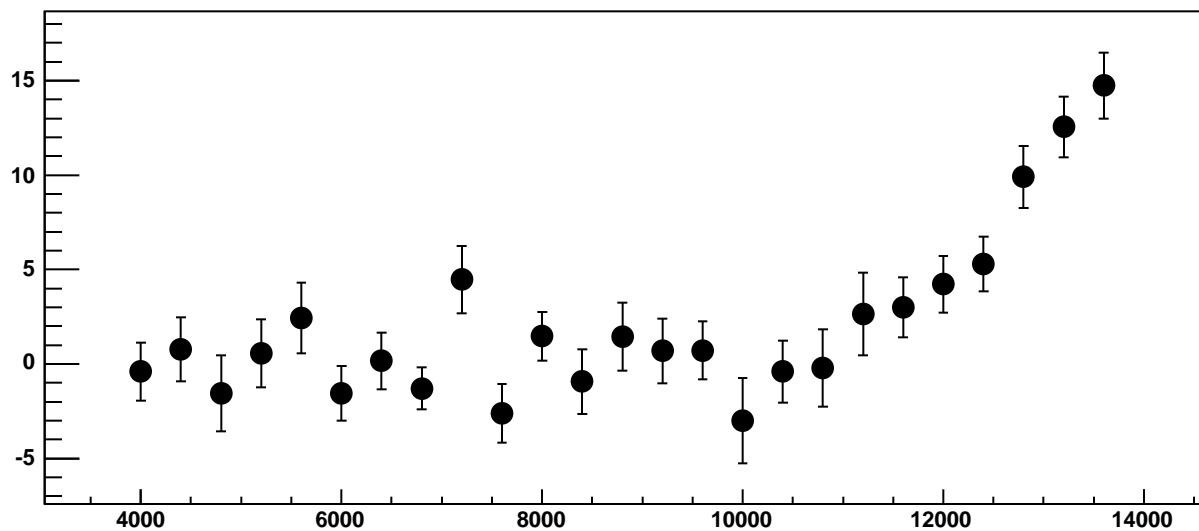
Chip 5, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC



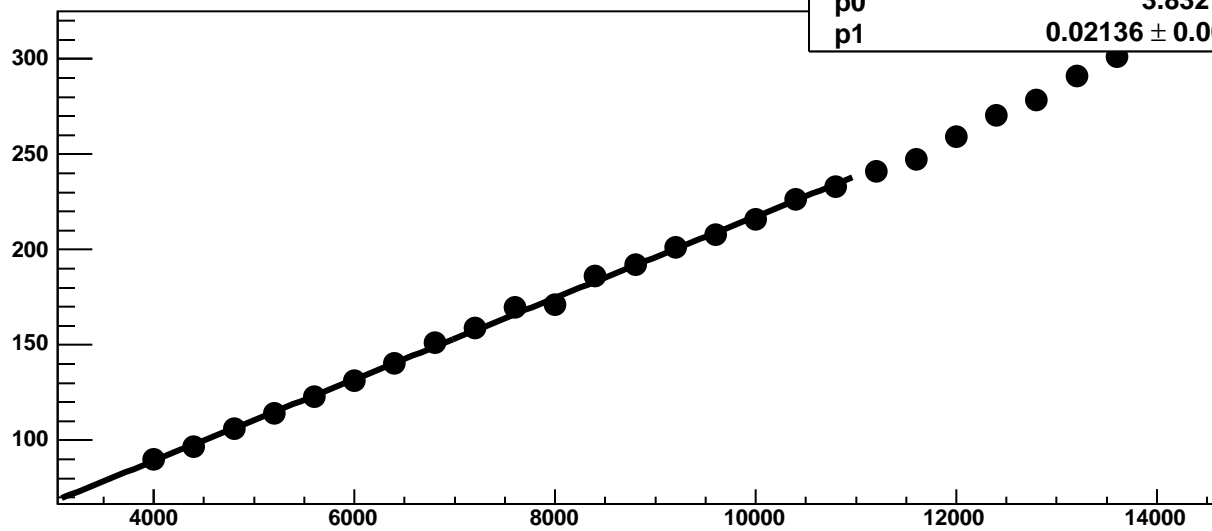
Chip 5, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

15.69 / 16

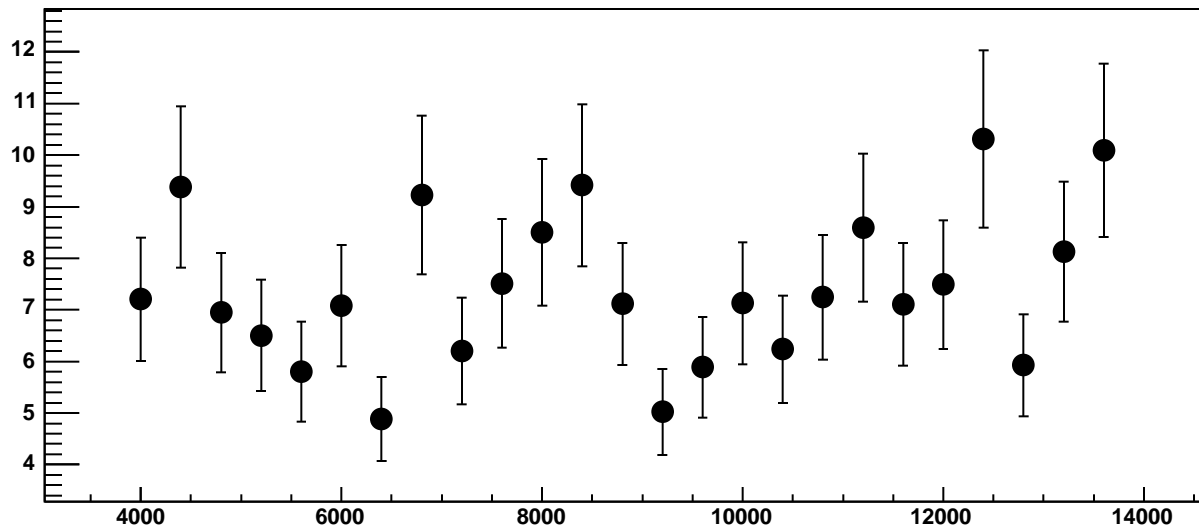
p0

3.832 ± 1.38

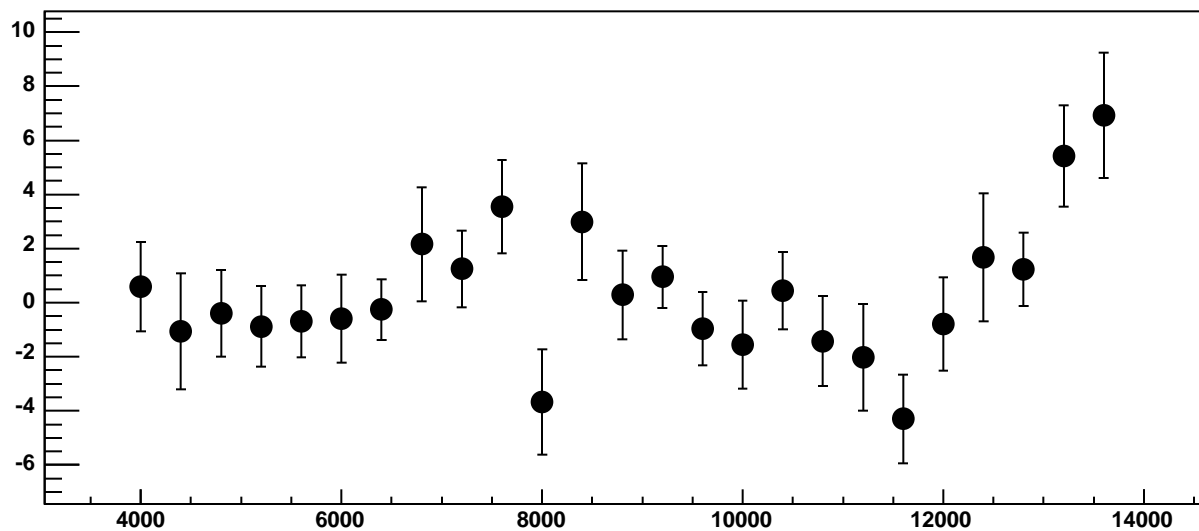
p1

0.02136 ± 0.0001777

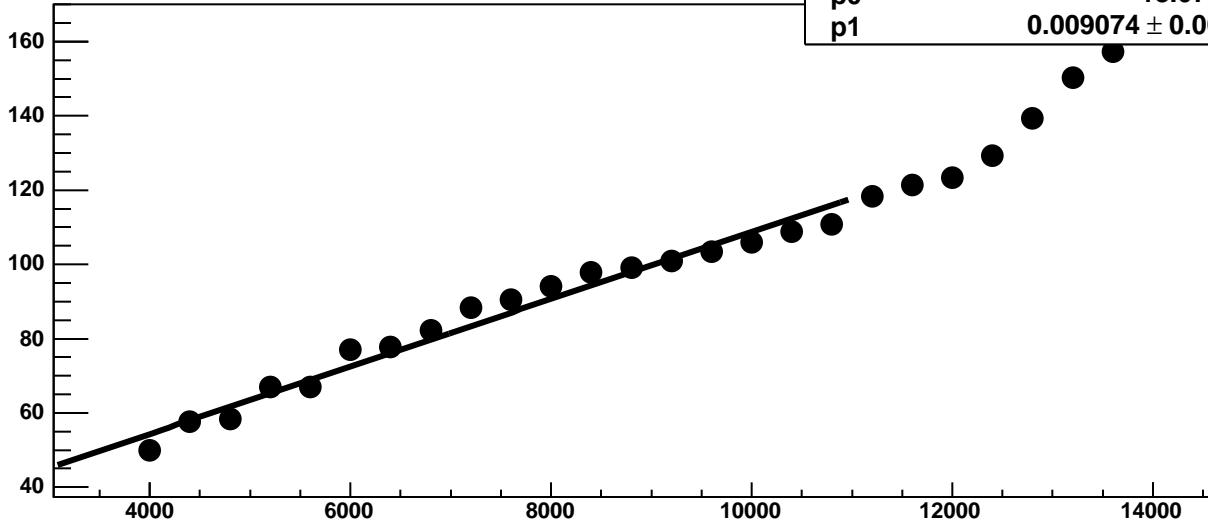
Chip 5, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



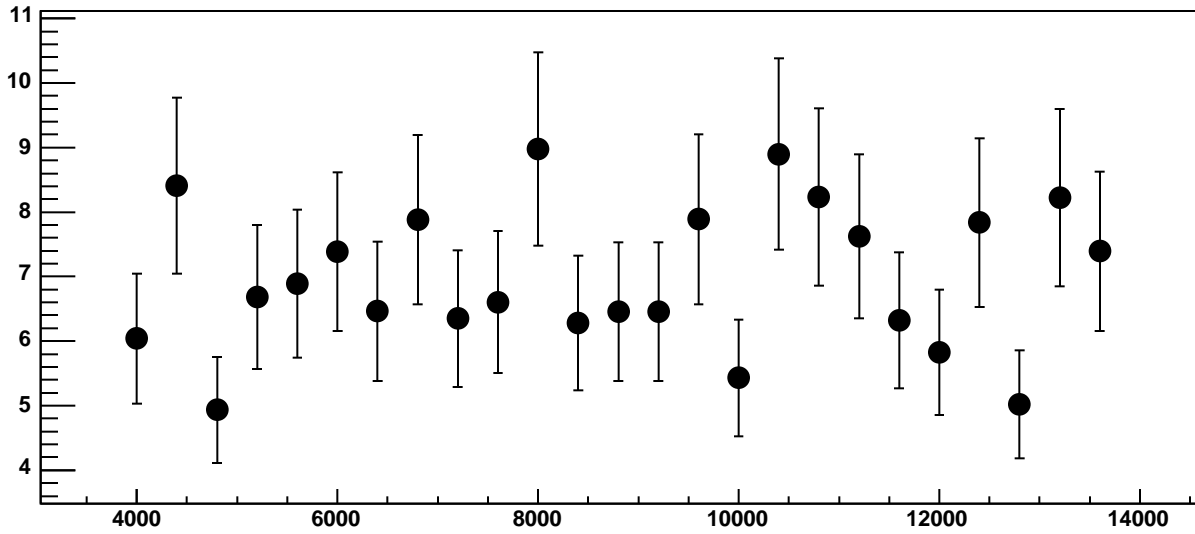
Chip 5, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



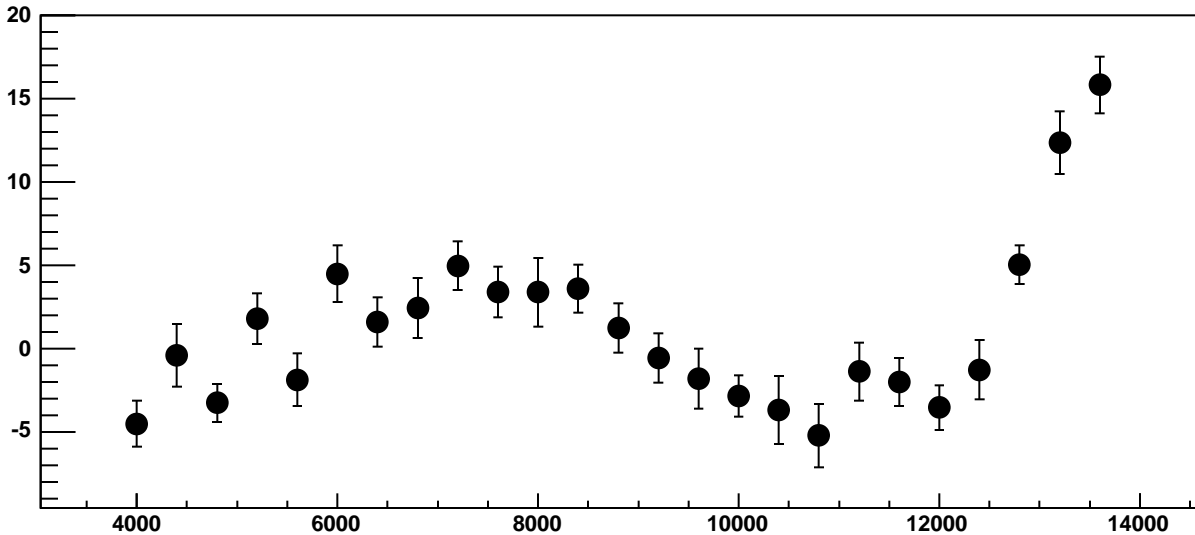
Chip 5, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



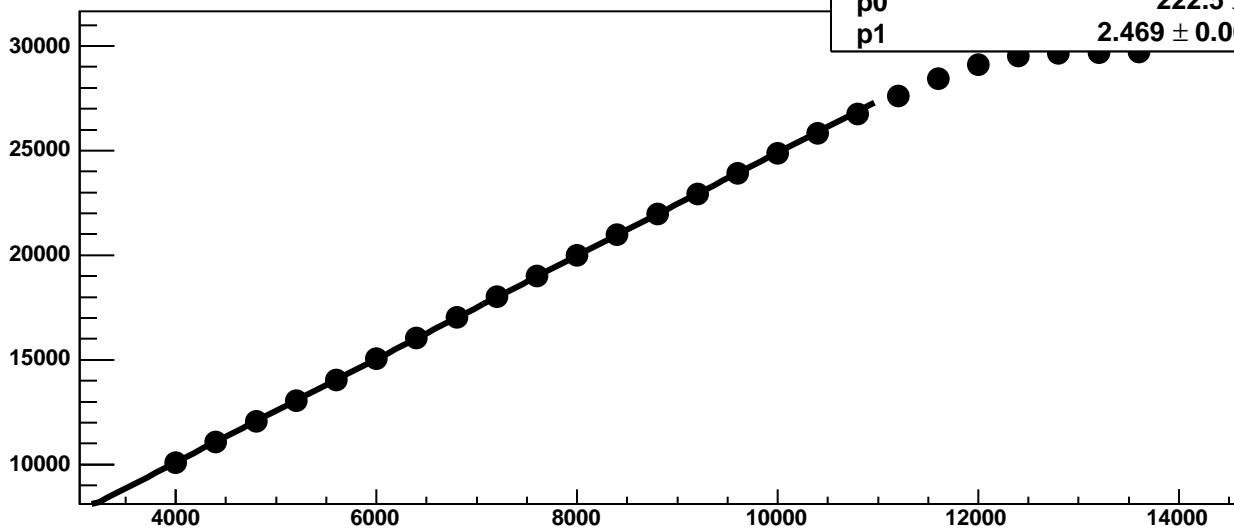
Chip 5, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



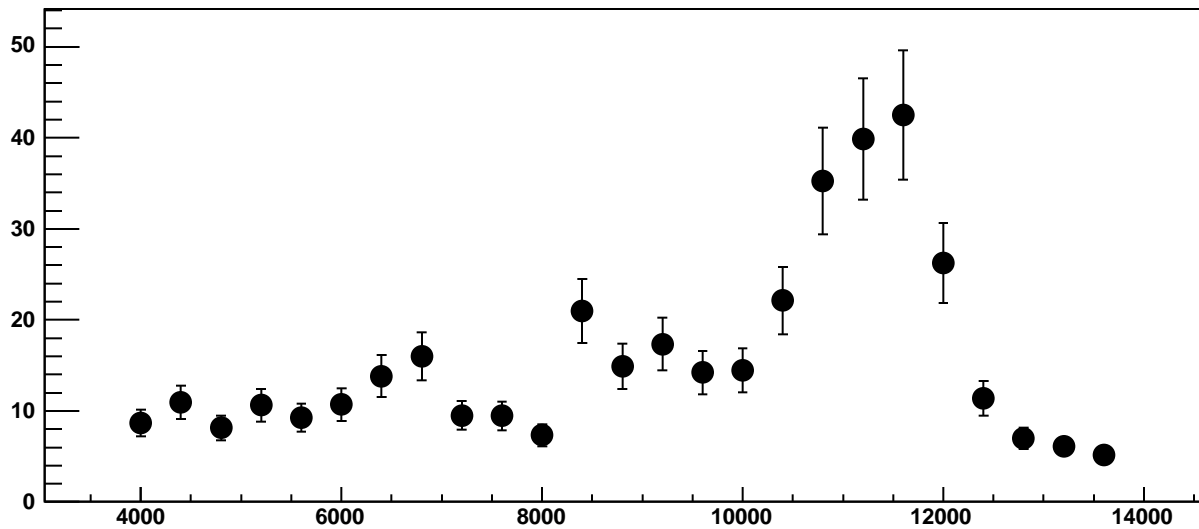
Chip 5, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



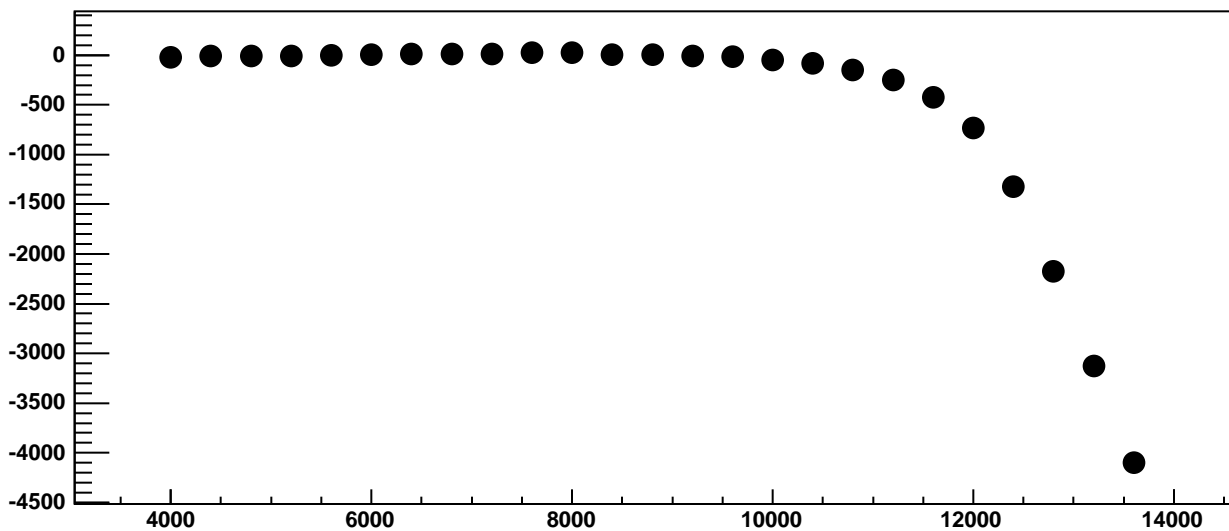
Chip 5, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC



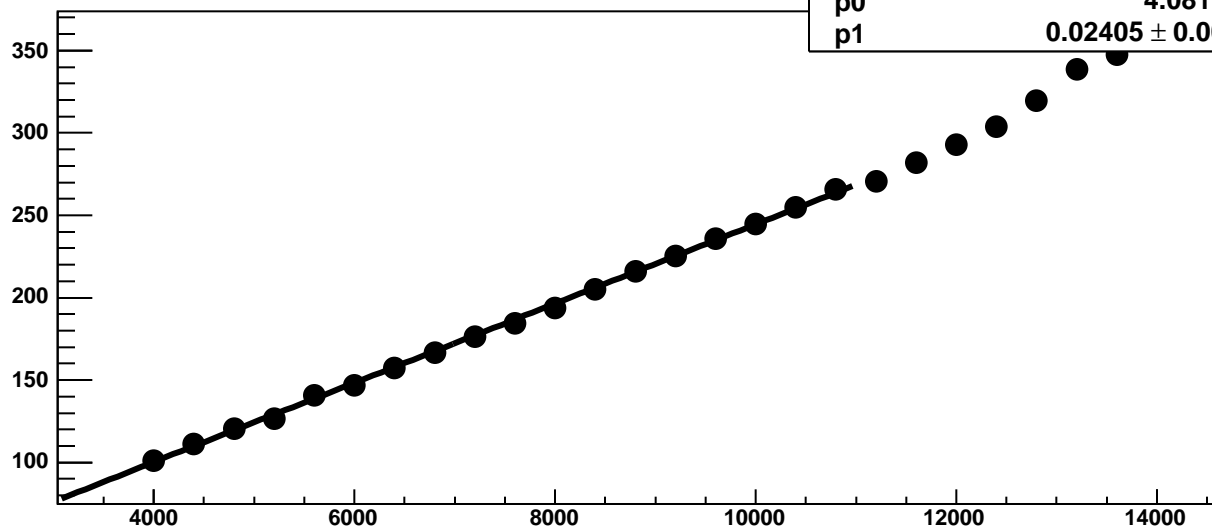
Chip 5, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

15.02 / 16

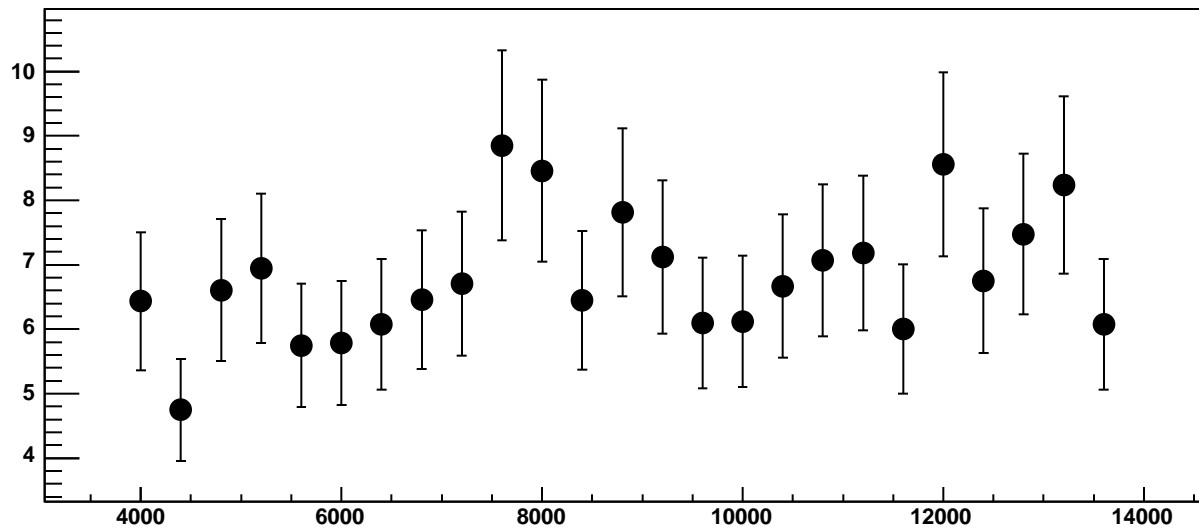
p0

4.081 ± 1.221

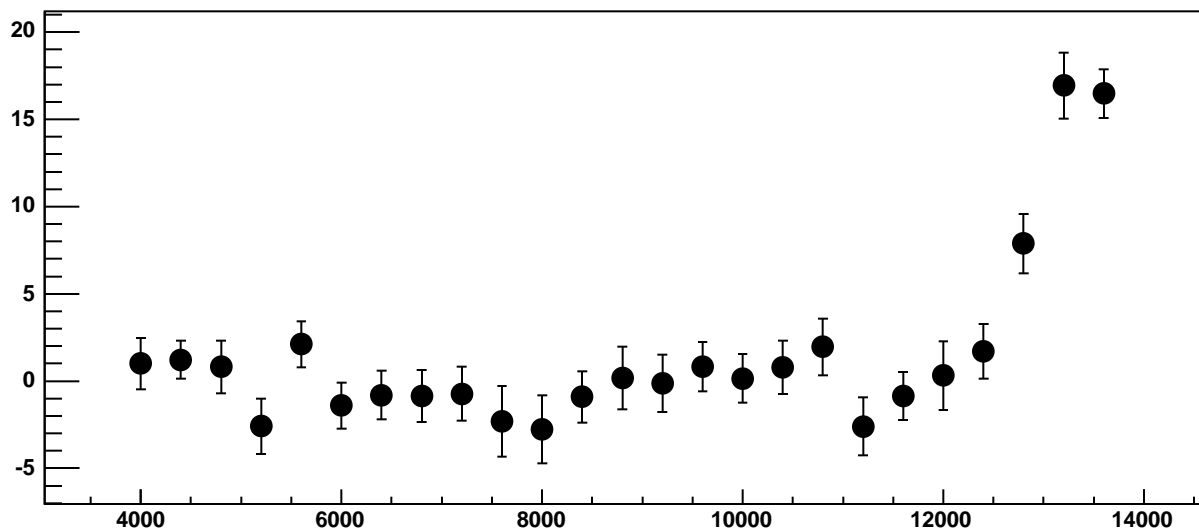
p1

0.02405 ± 0.0001634

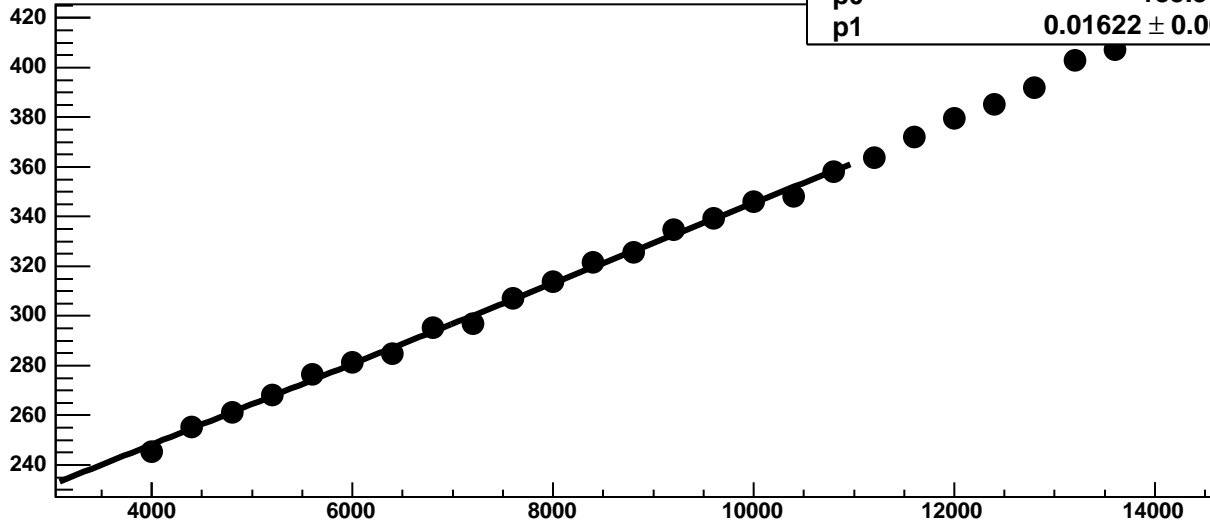
Chip 5, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

22.56 / 16

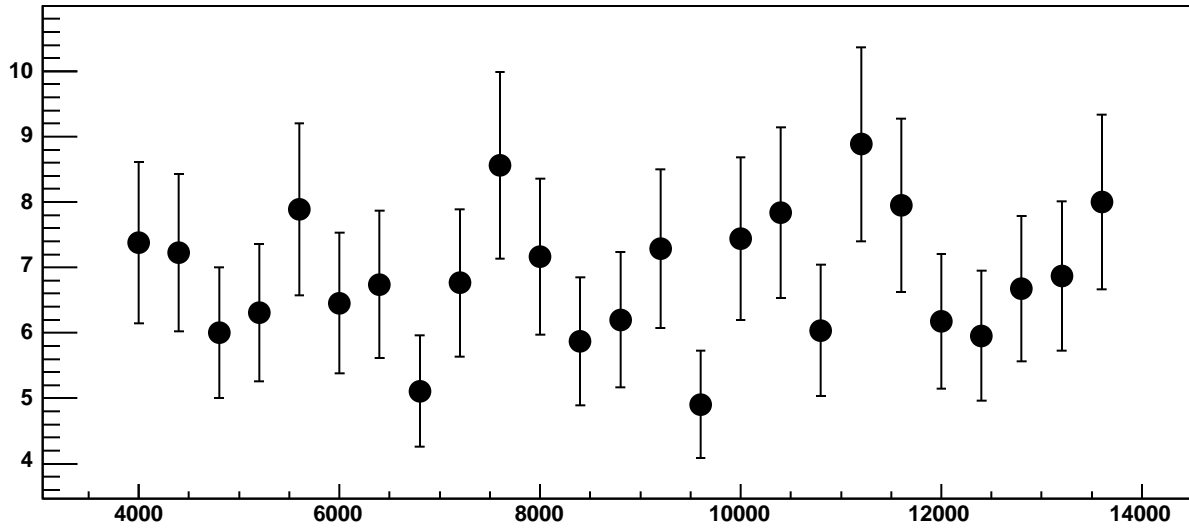
p0

183.3 ± 1.34

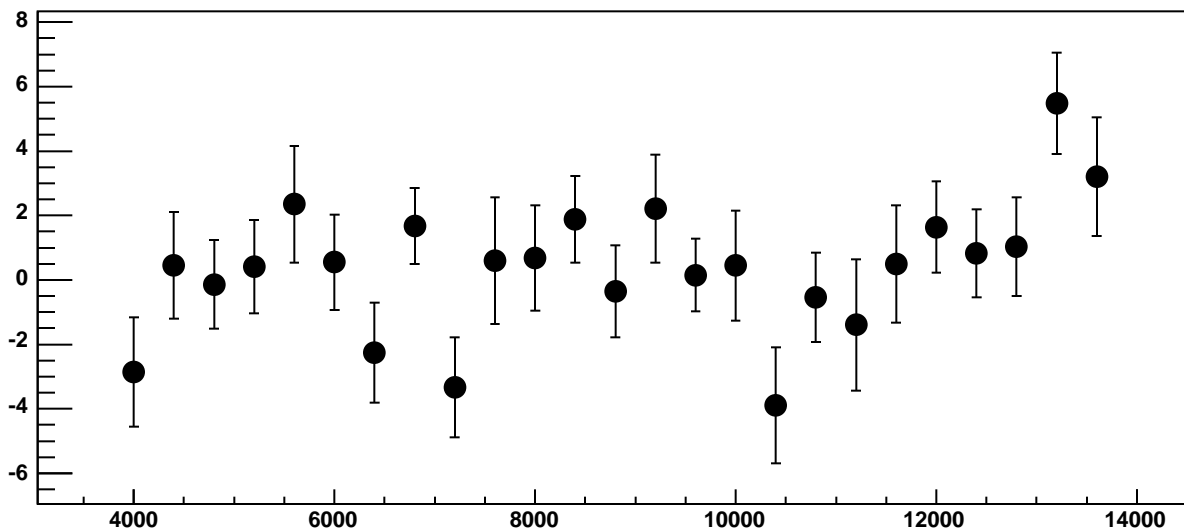
p1

0.01622 ± 0.0001728

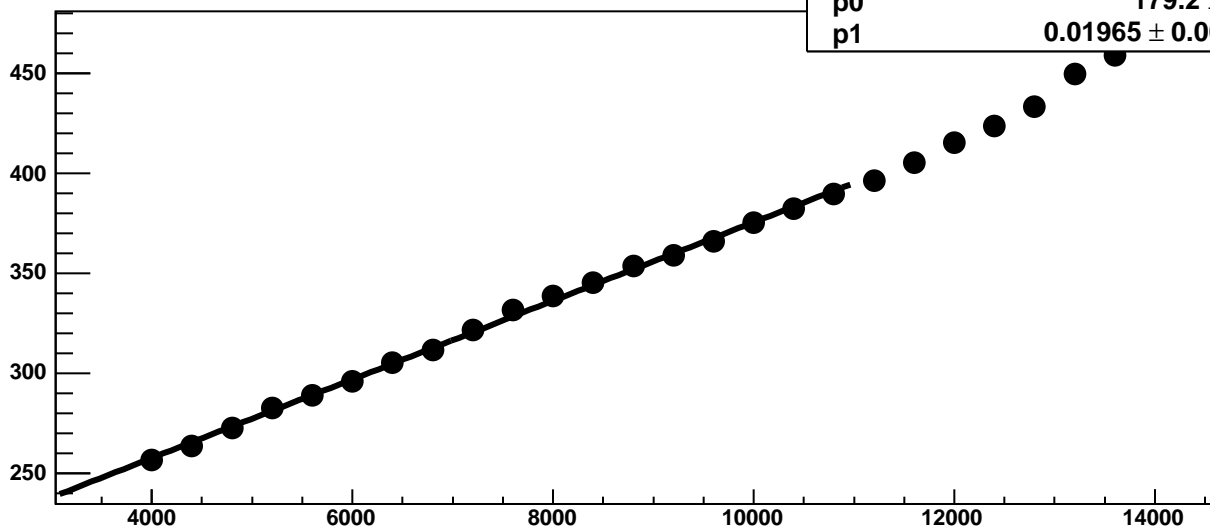
Chip 5, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

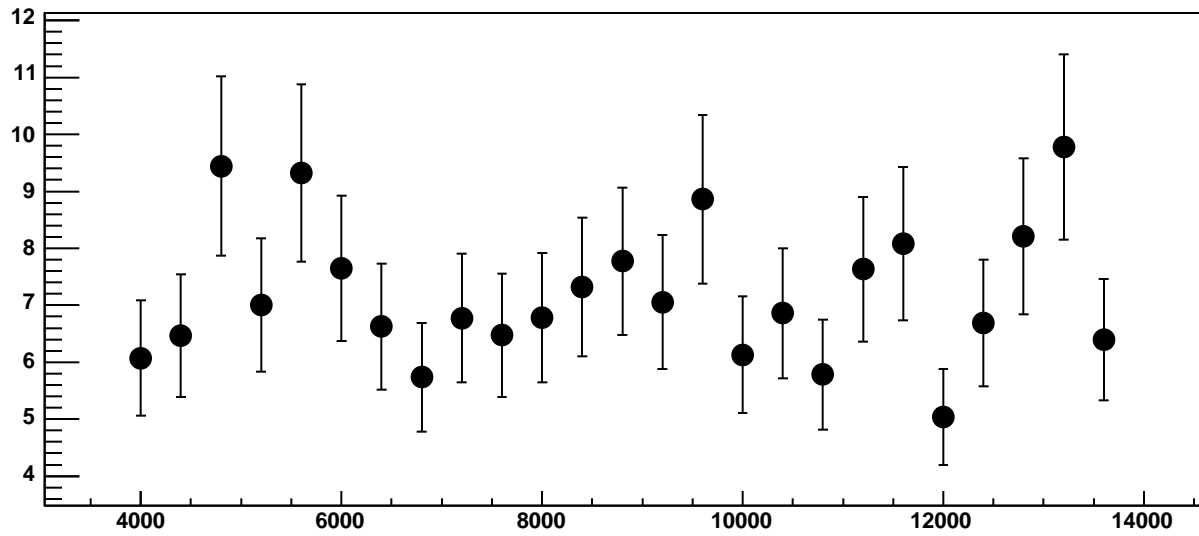


Chip 5, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

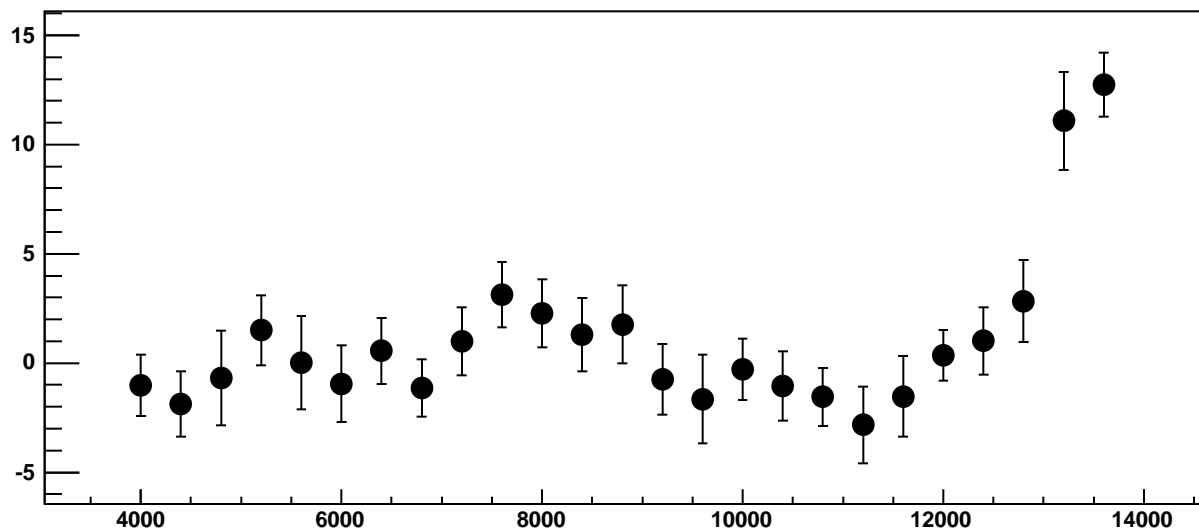


χ^2 / ndf 15.57 / 16
p0 179.2 ± 1.365
p1 0.01965 ± 0.0001755

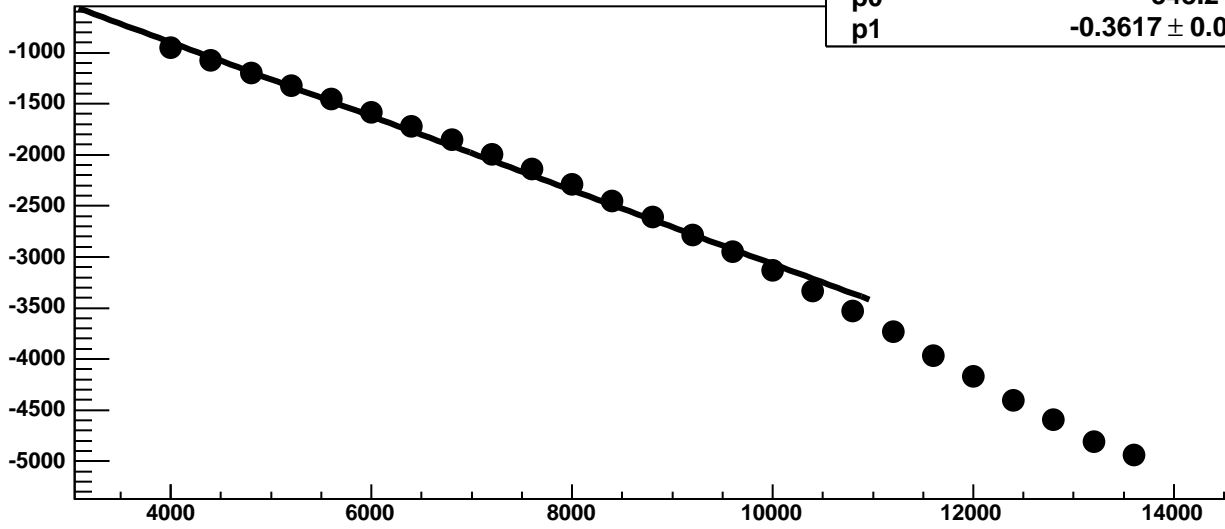
Chip 5, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

3274 / 16

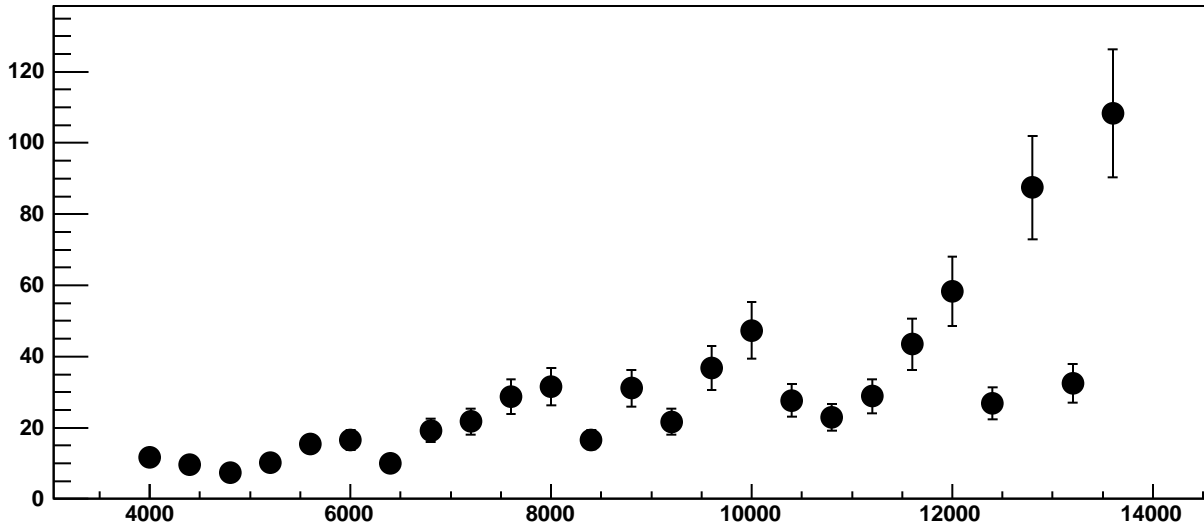
p0

548.2 ± 2.842

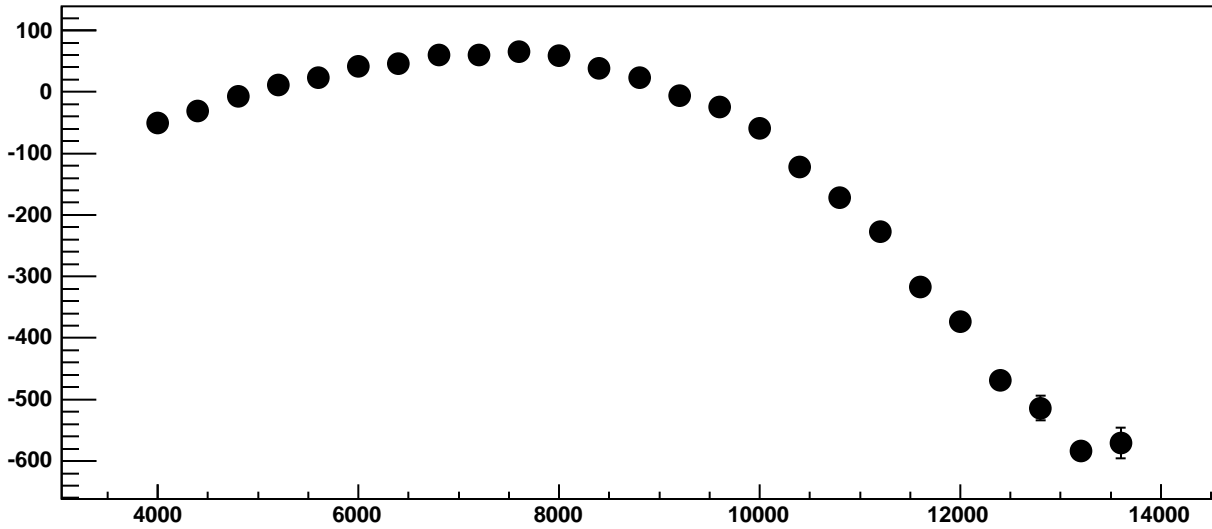
p1

-0.3617 ± 0.0004683

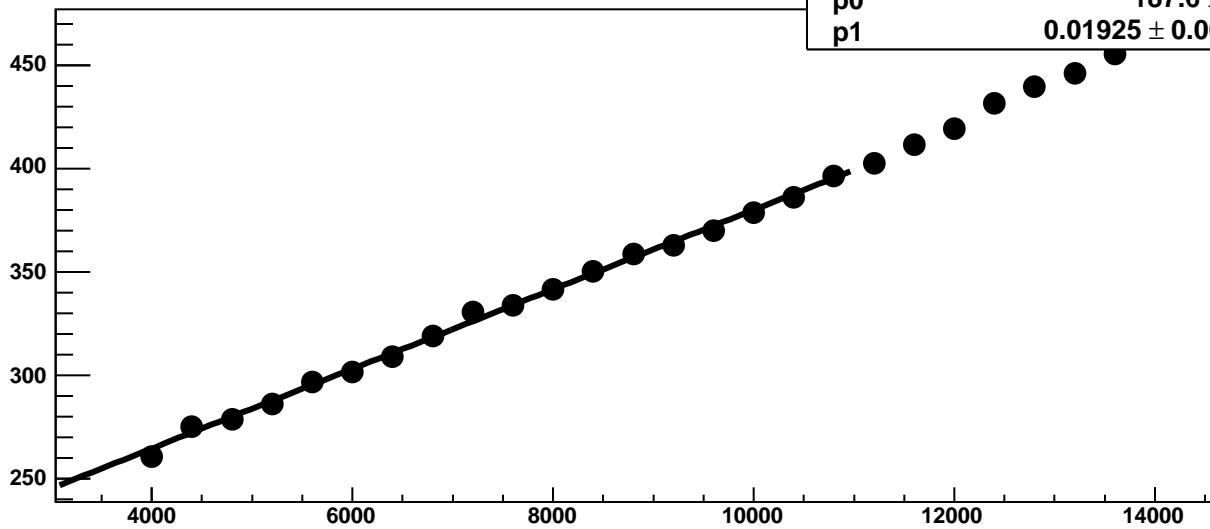
Chip 5, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

26.33 / 16

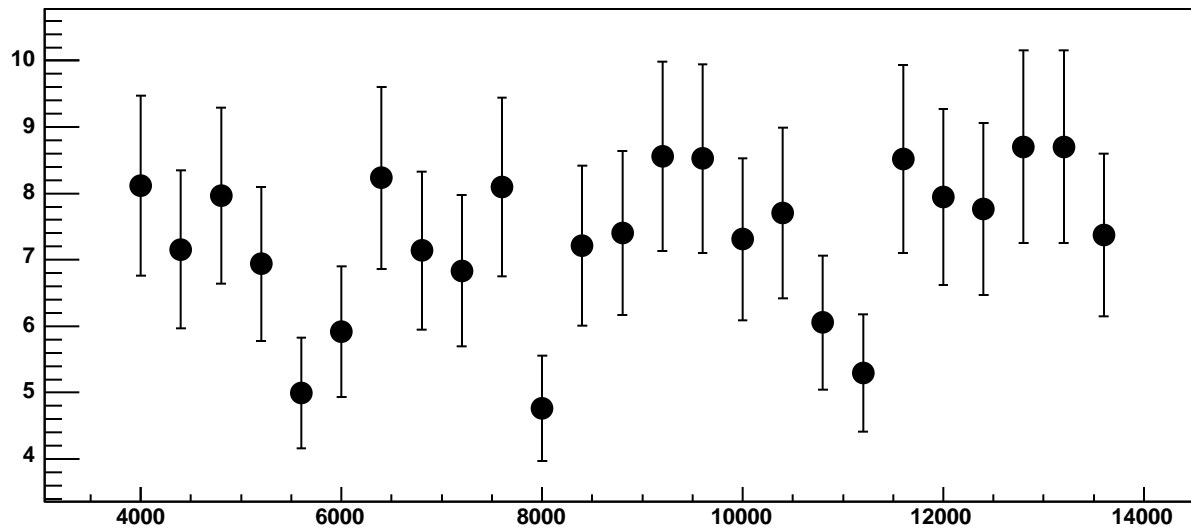
p0

187.6 ± 1.408

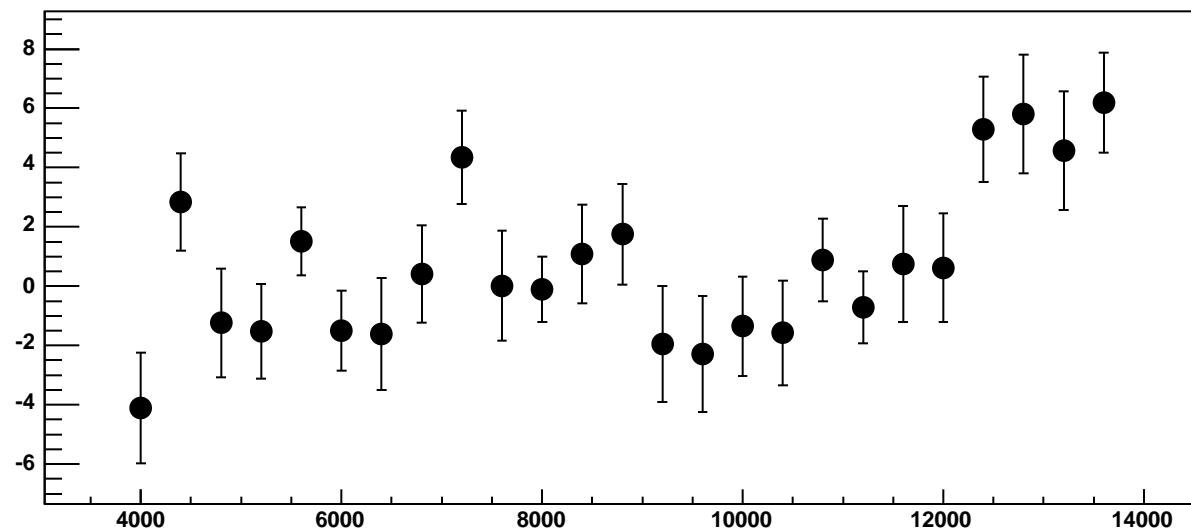
p1

0.01925 ± 0.0001848

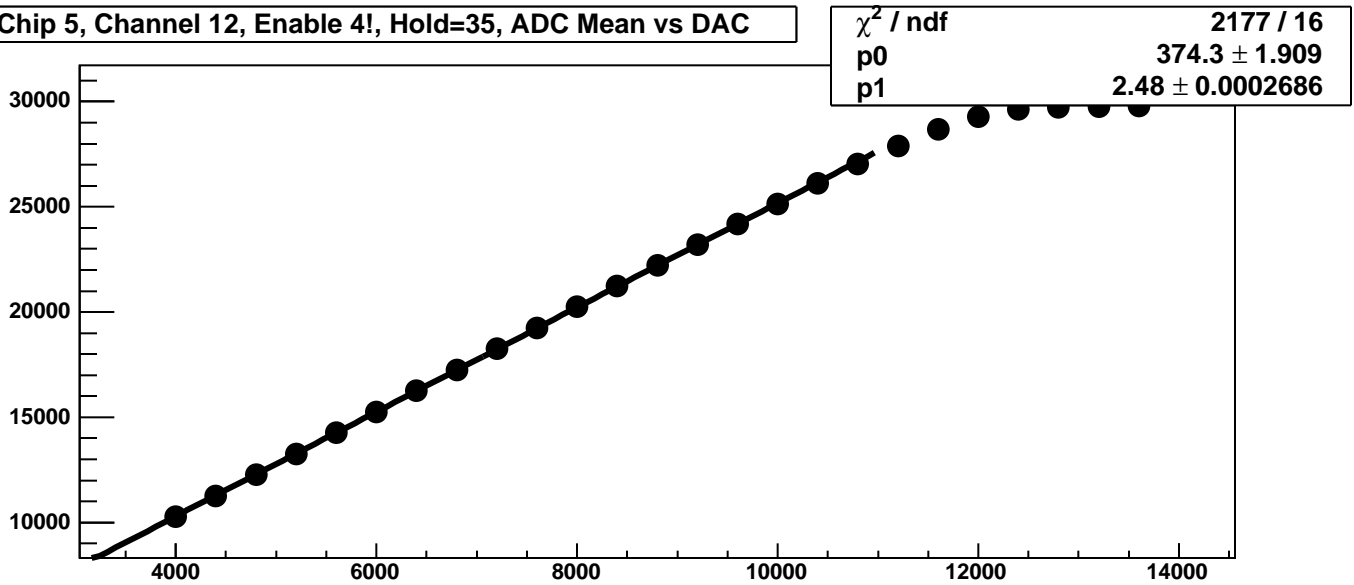
Chip 5, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



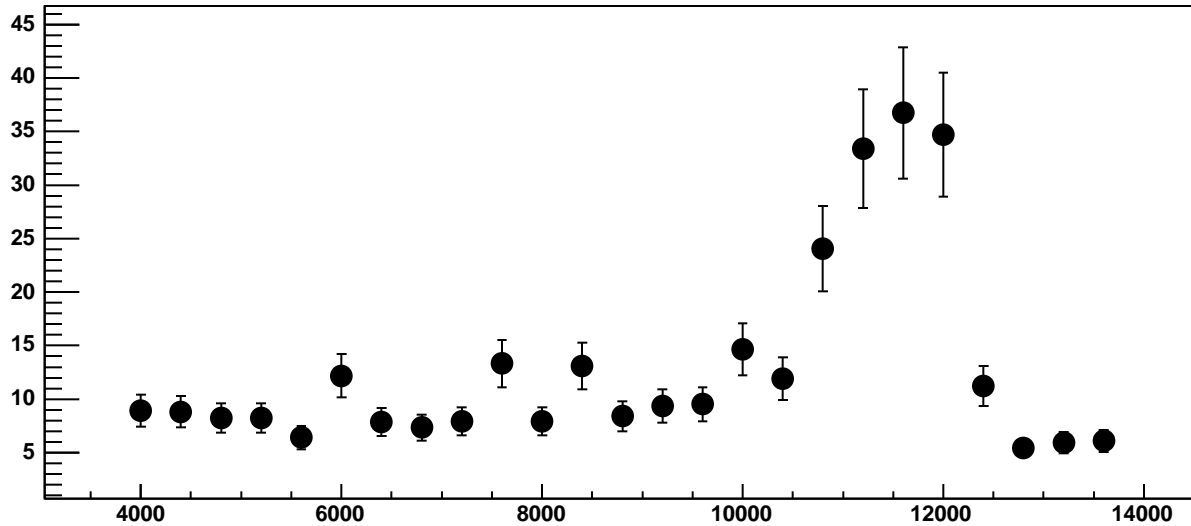
Chip 5, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



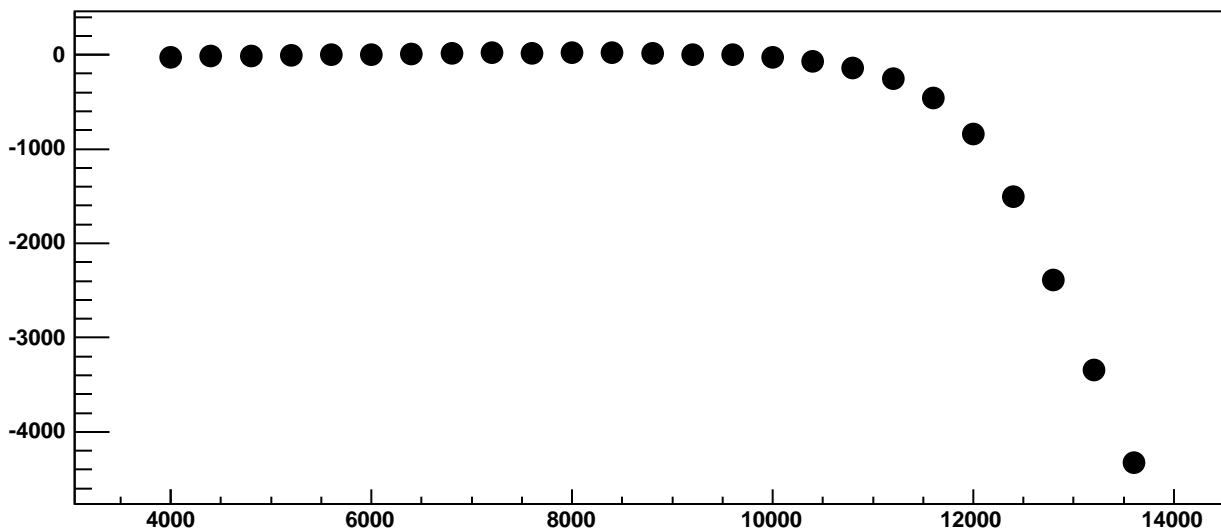
Chip 5, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC



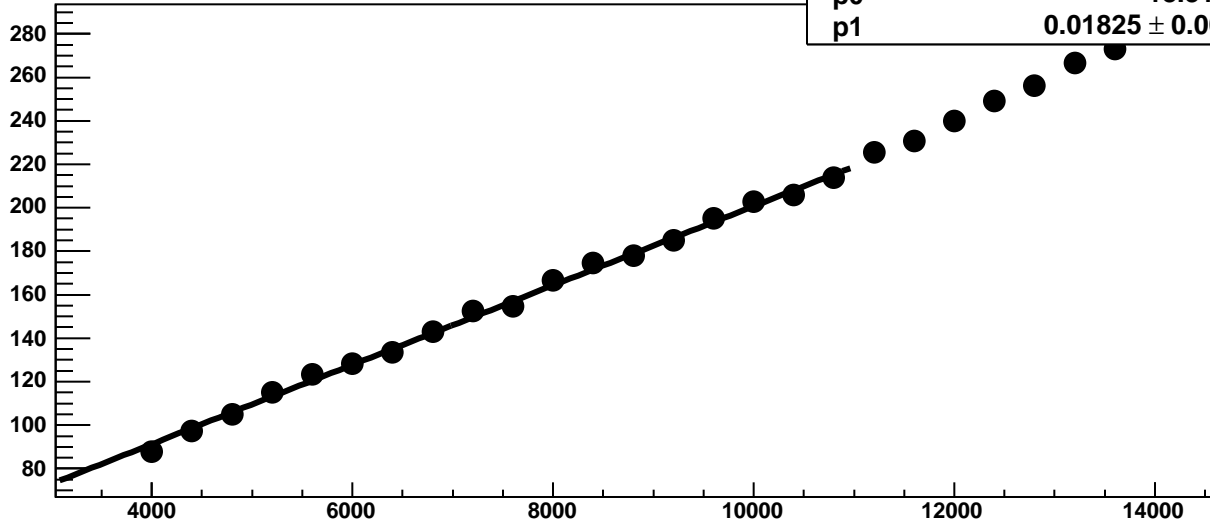
Chip 5, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC



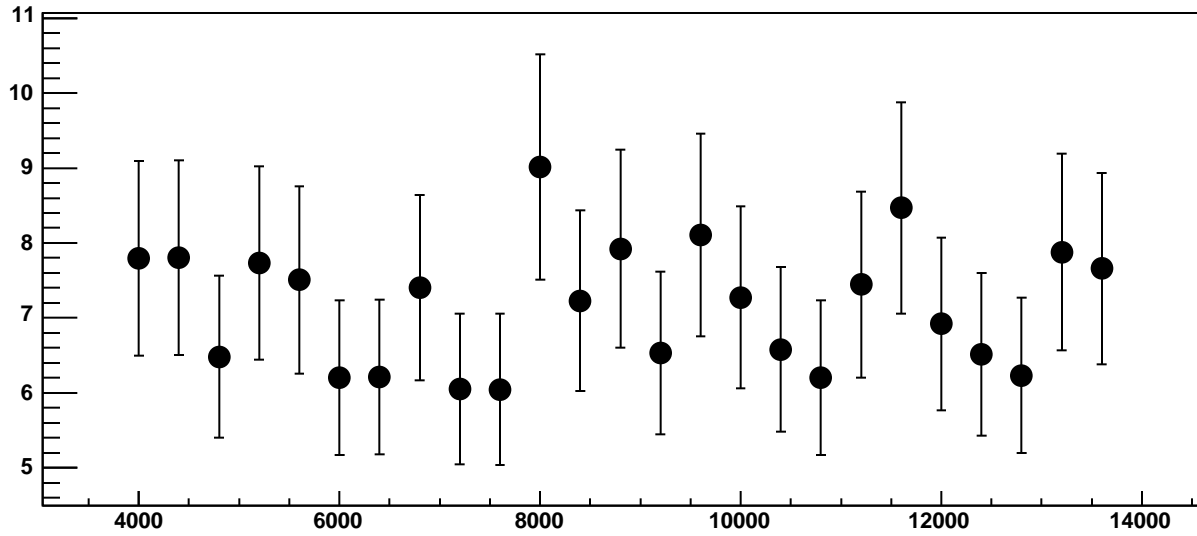
Chip 5, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC



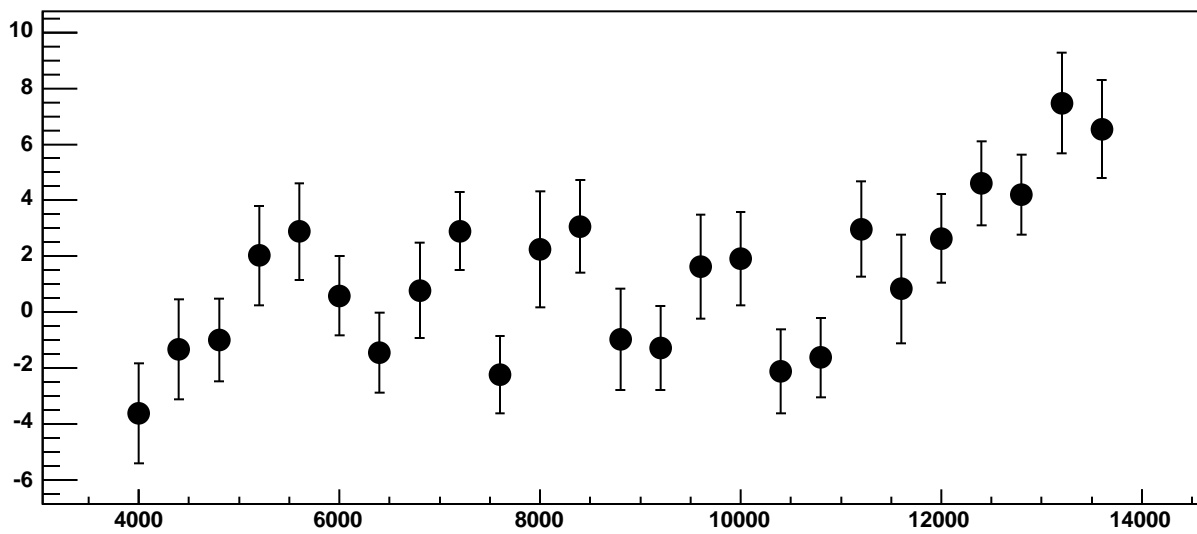
Chip 5, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



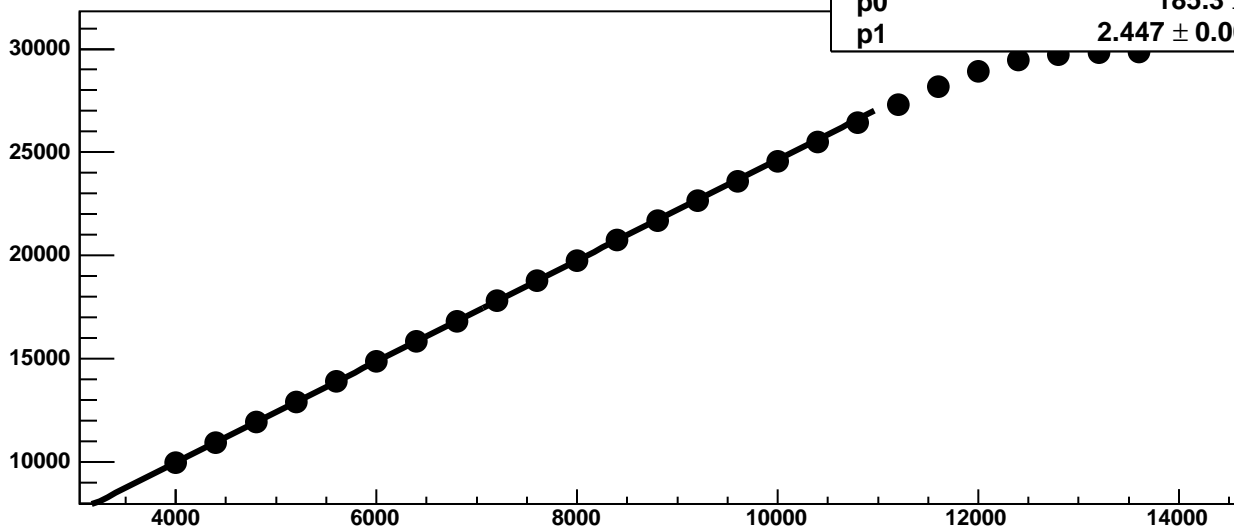
Chip 5, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



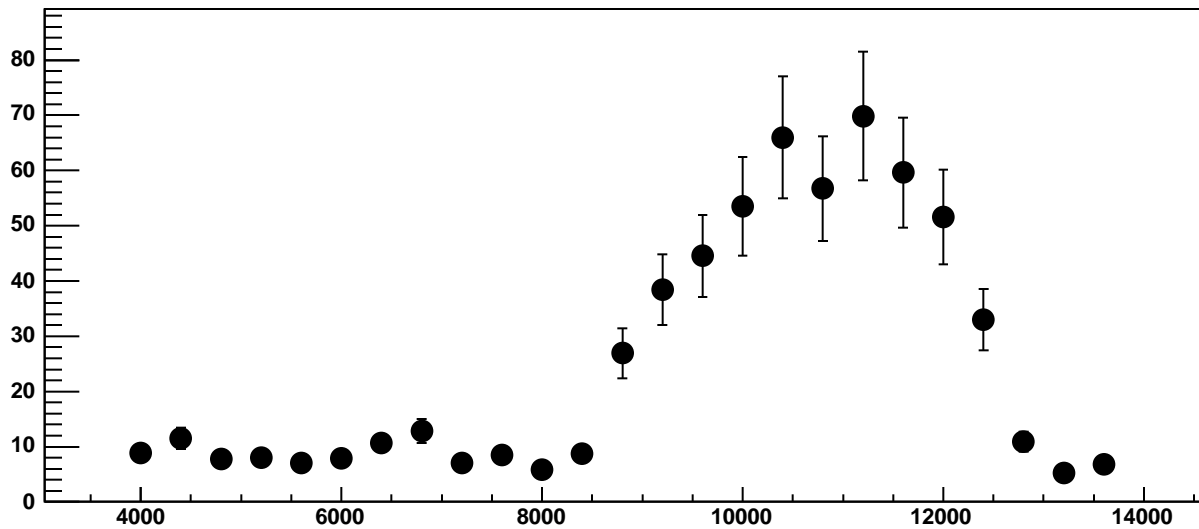
Chip 5, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



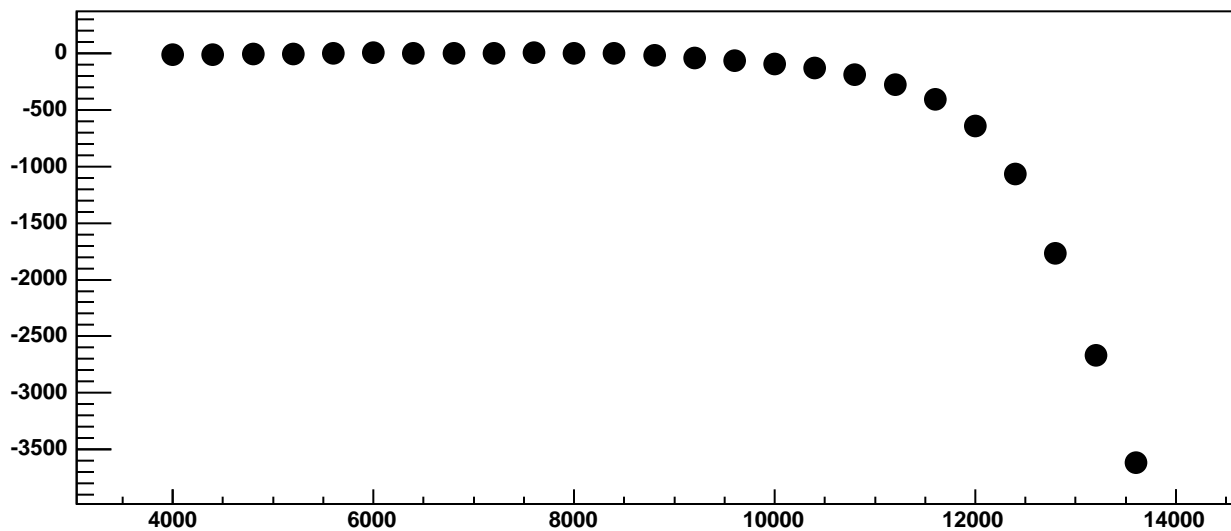
Chip 5, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC



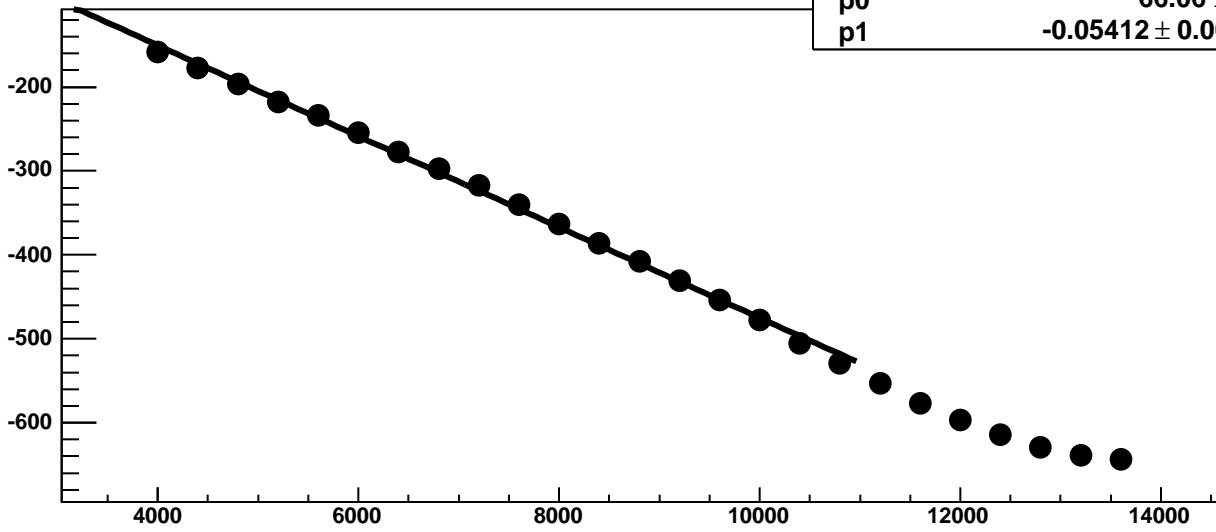
Chip 5, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

131.1 / 16

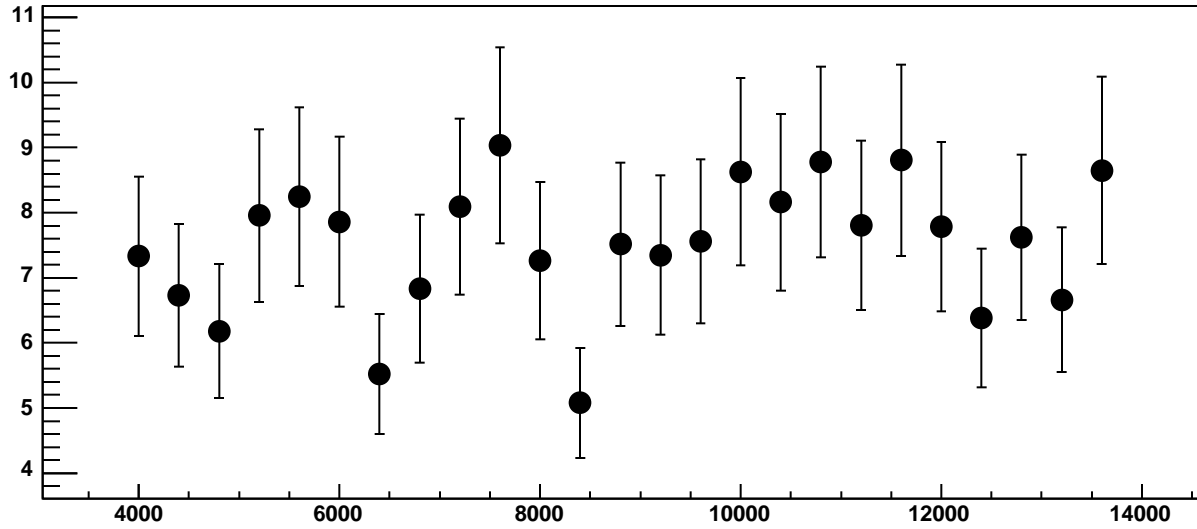
p0

66.06 ± 1.459

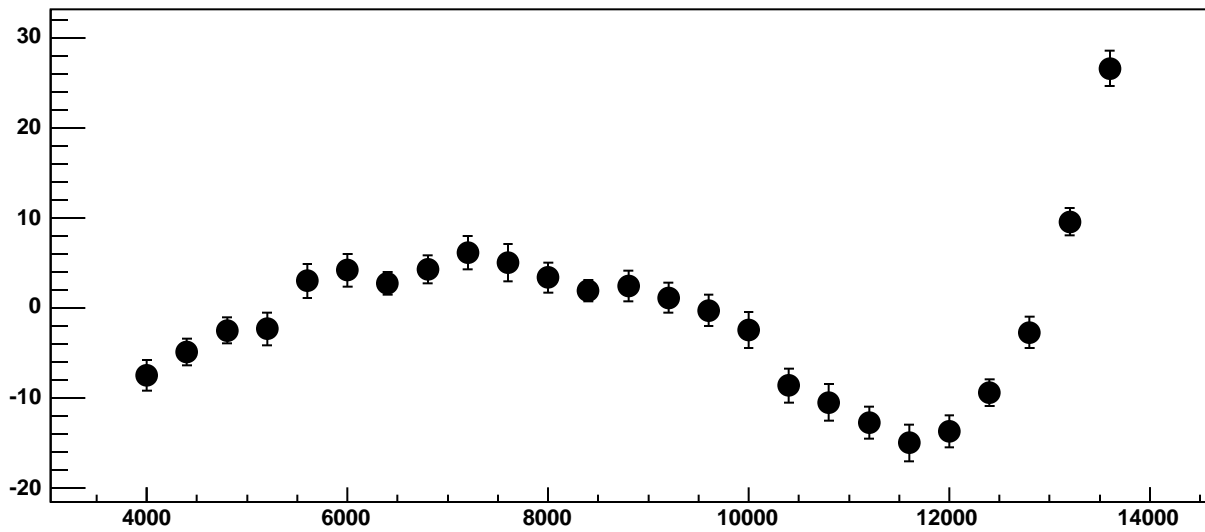
p1

-0.05412 ± 0.0001947

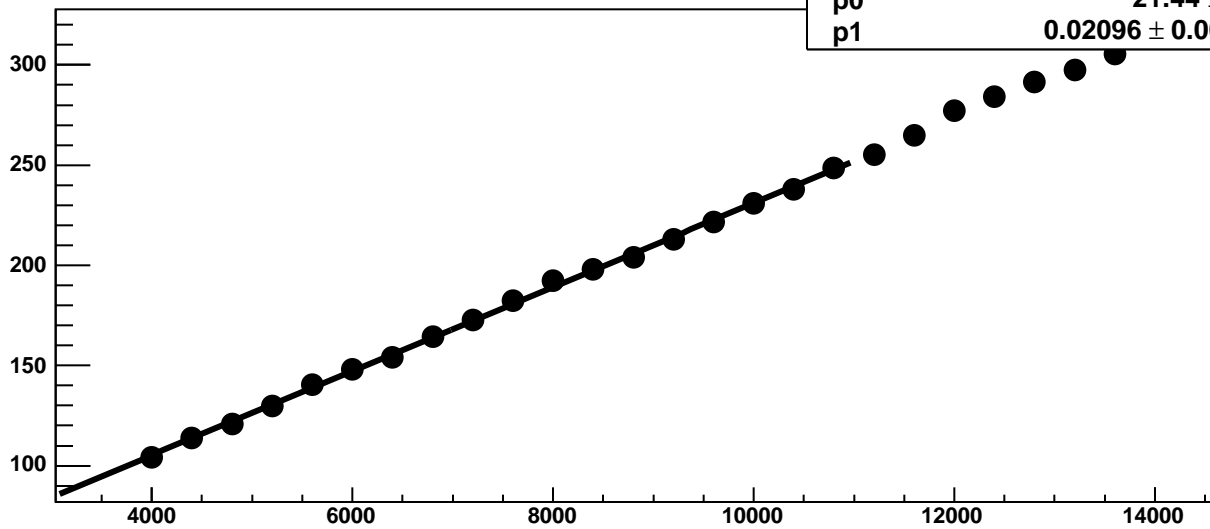
Chip 5, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

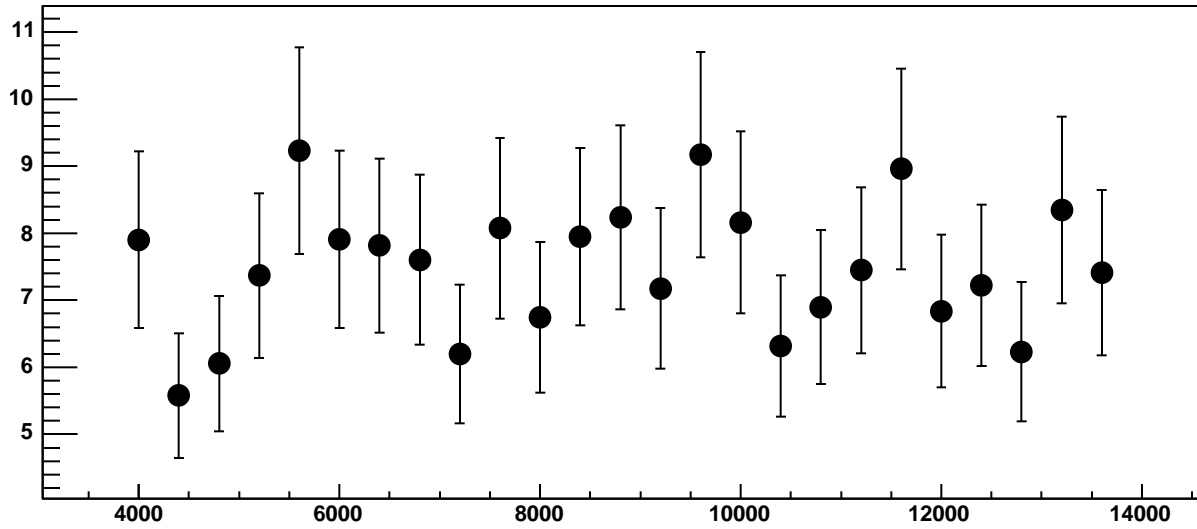


Chip 5, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

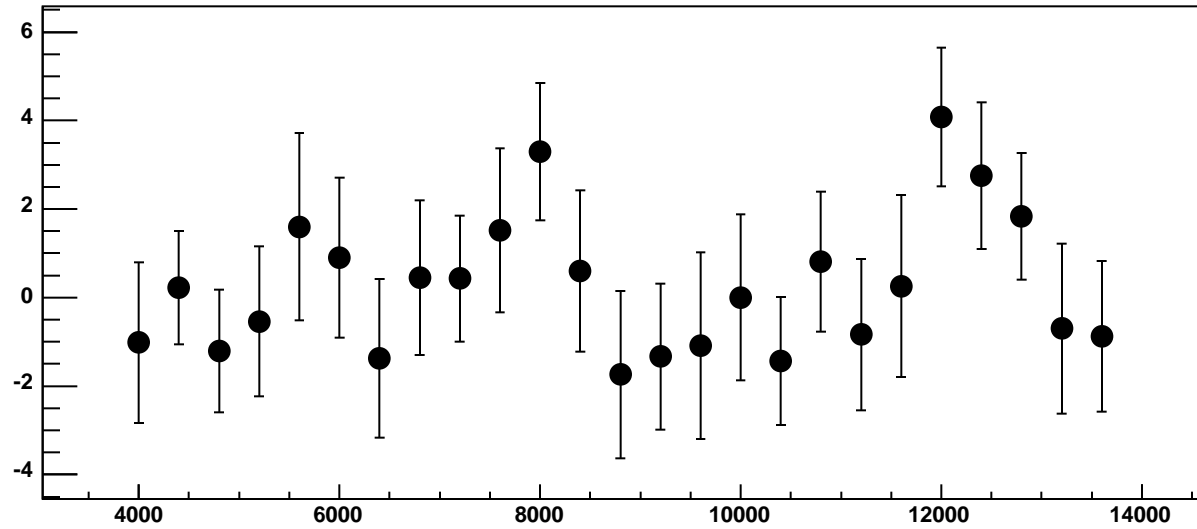


χ^2 / ndf 11.1 / 16
p0 21.44 ± 1.387
p1 0.02096 ± 0.0001825

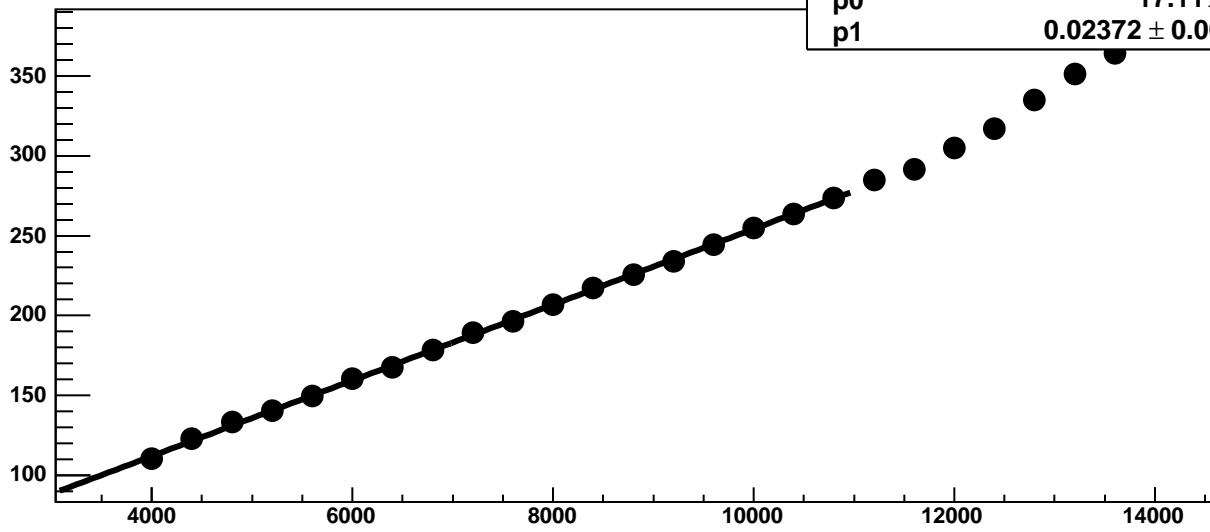
Chip 5, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

6.986 / 16

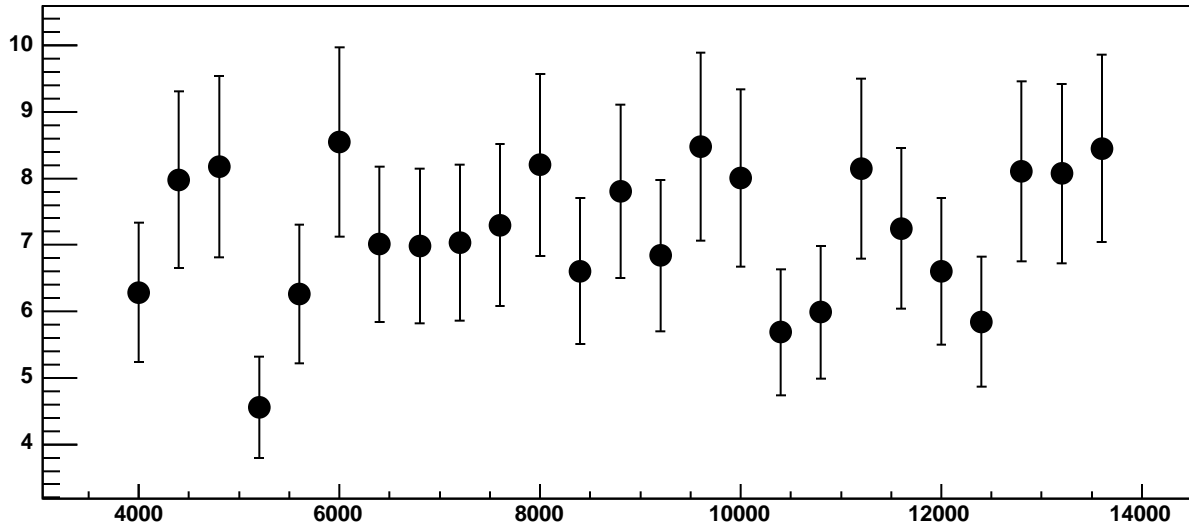
p0

17.11 ± 1.305

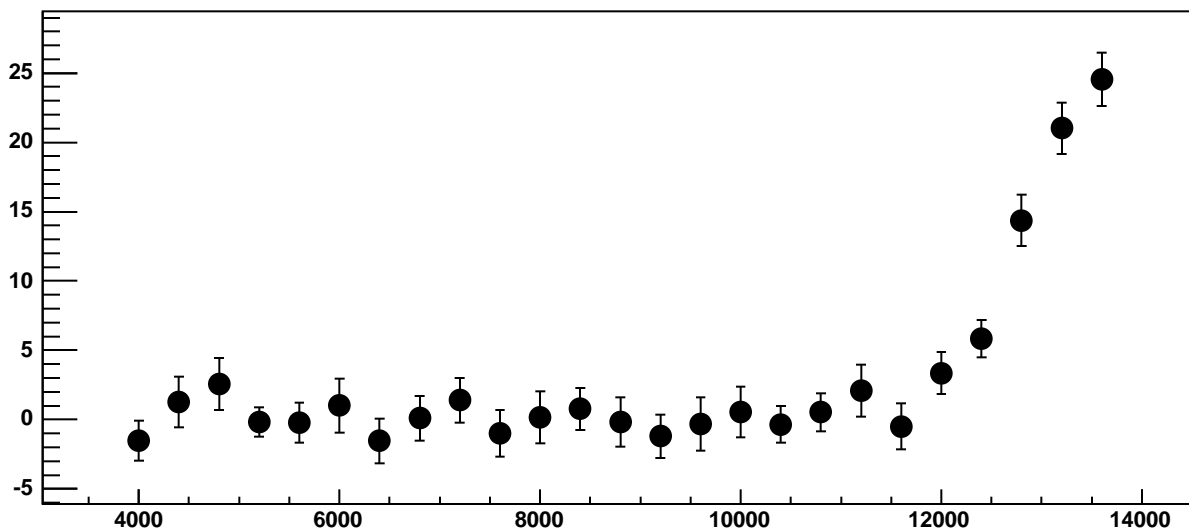
p1

0.02372 ± 0.0001706

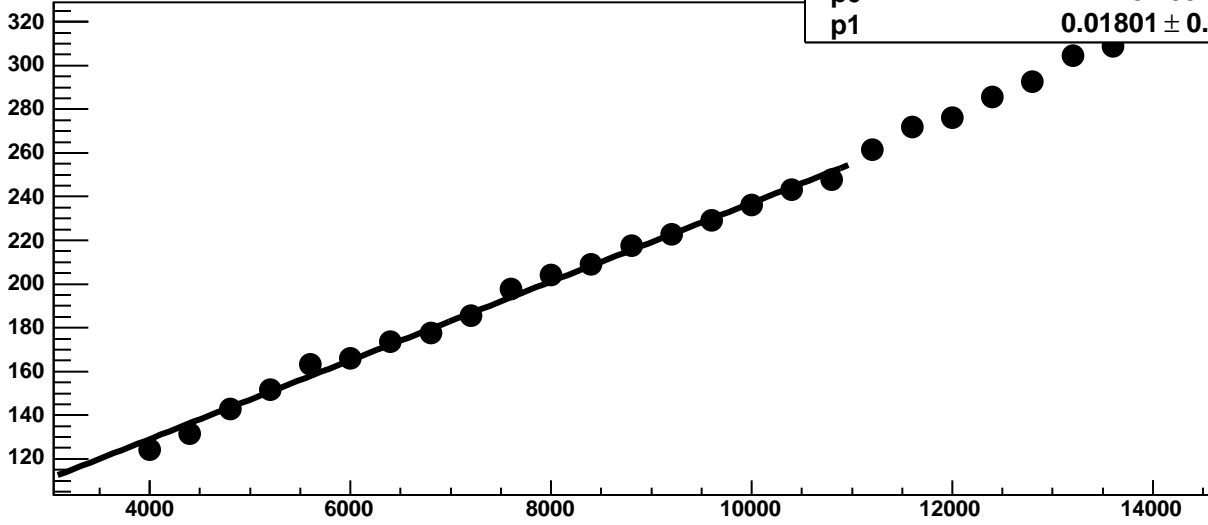
Chip 5, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

56.62 / 16

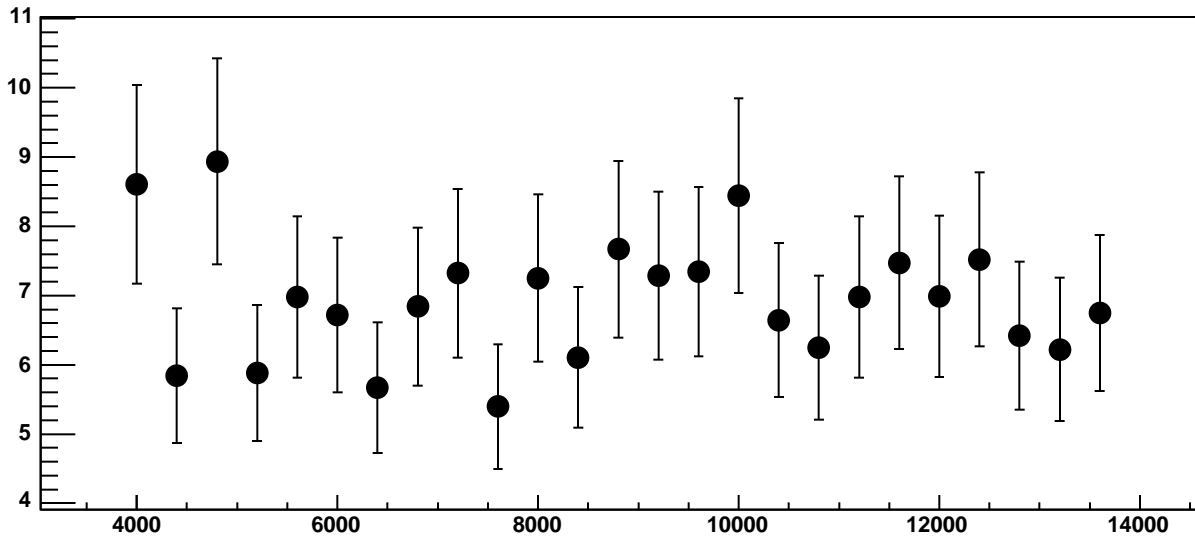
p0

57.05 ± 1.383

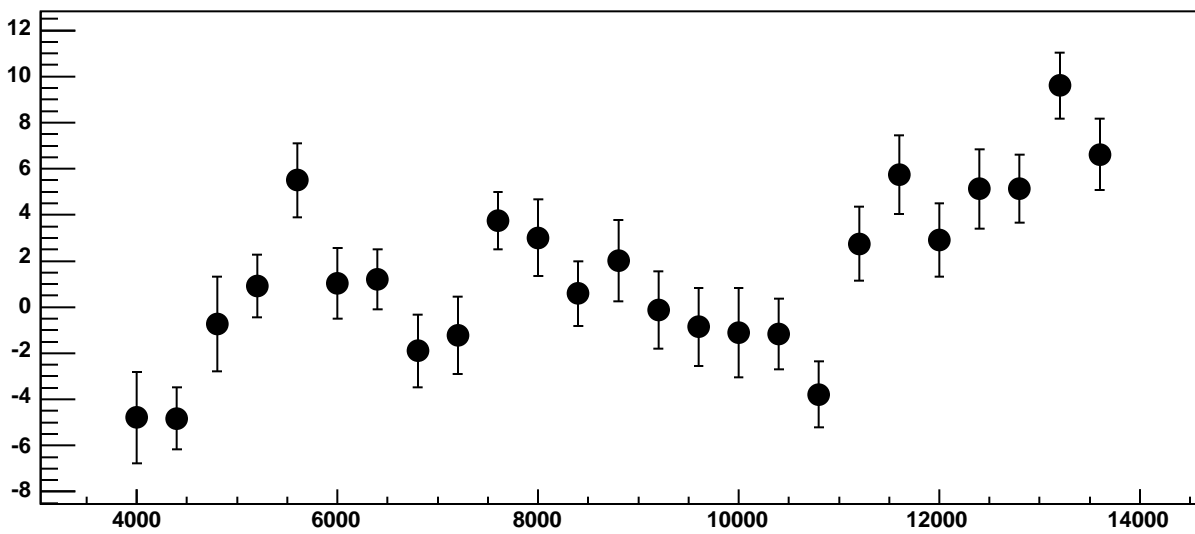
p1

0.01801 ± 0.000181

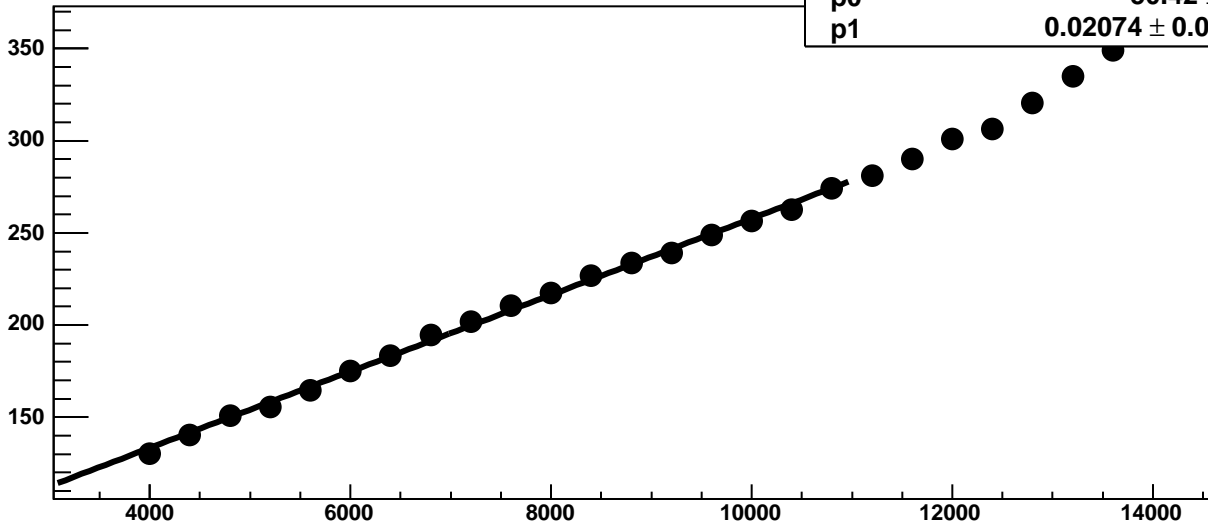
Chip 5, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

22.42 / 16

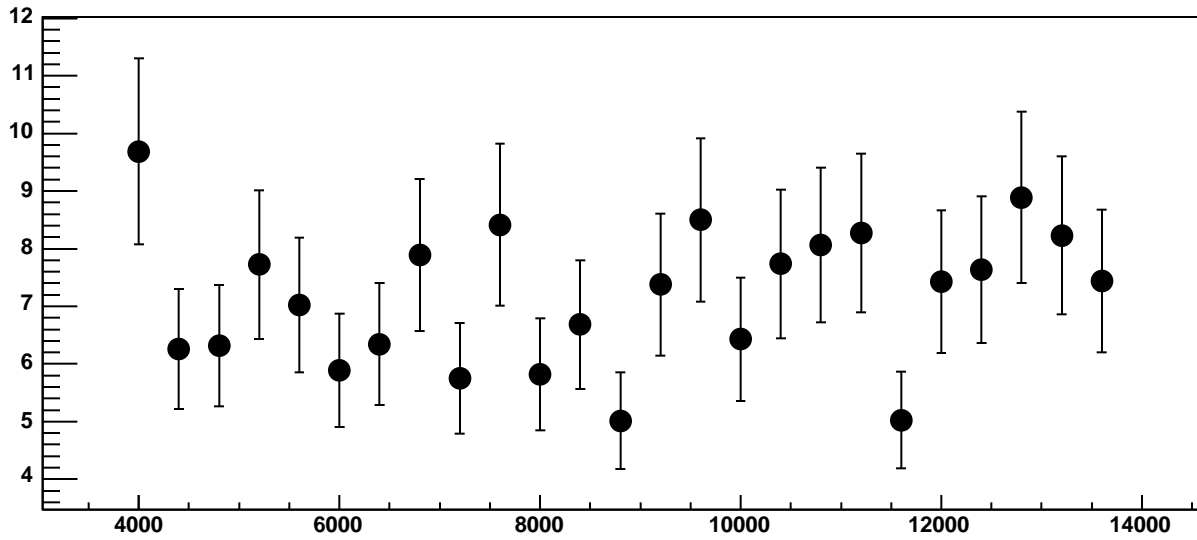
p0

50.42 ± 1.437

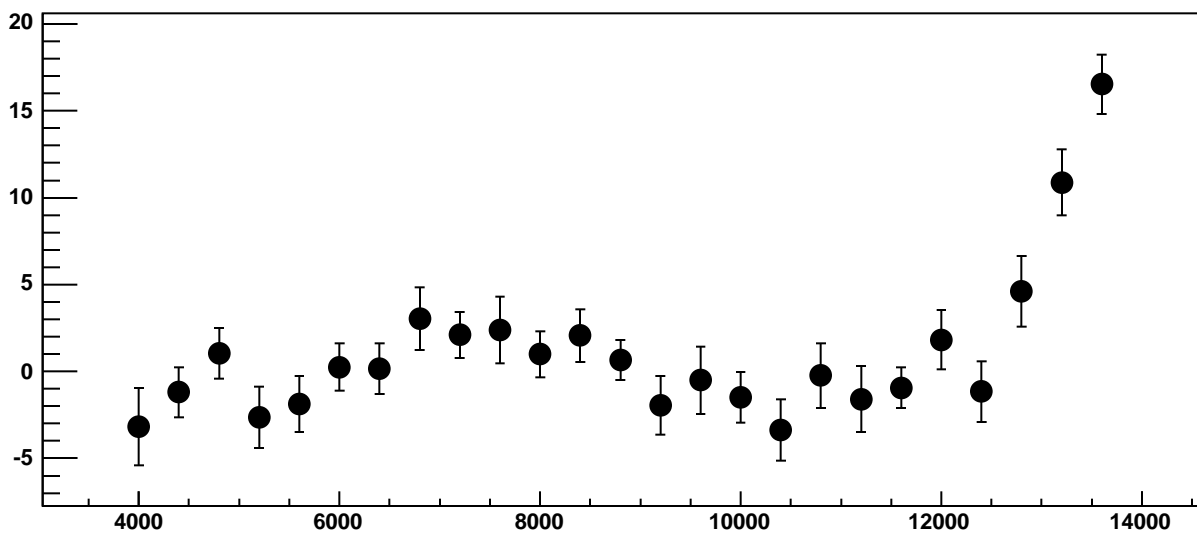
p1

0.02074 ± 0.0001881

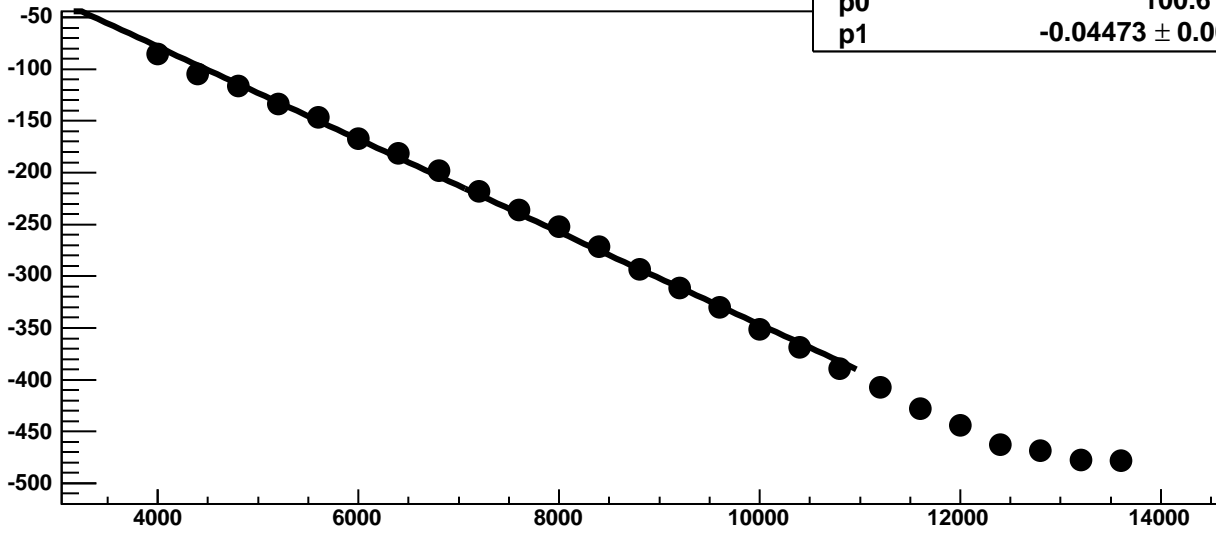
Chip 5, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



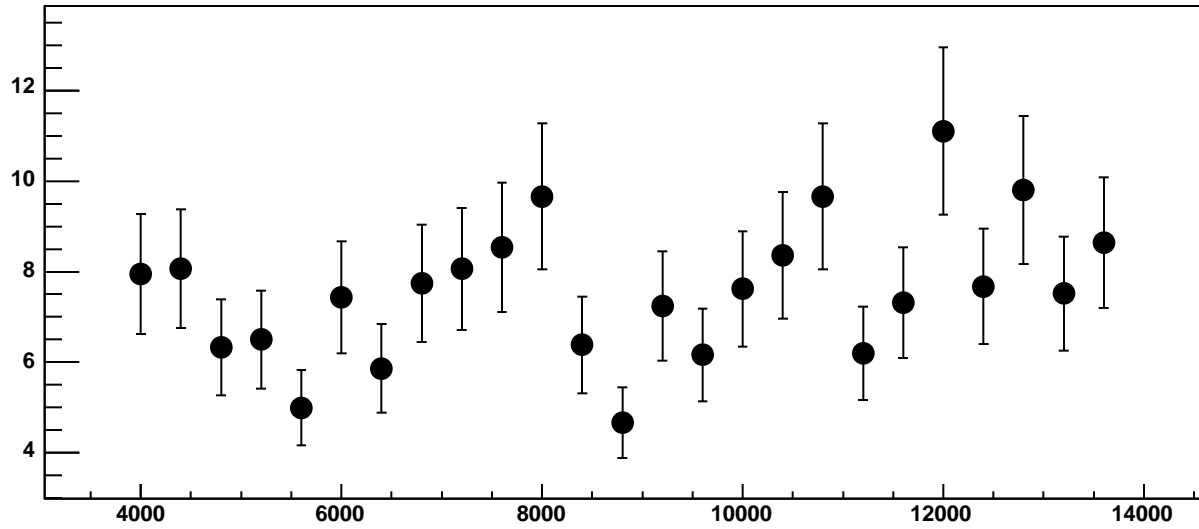
Chip 5, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC



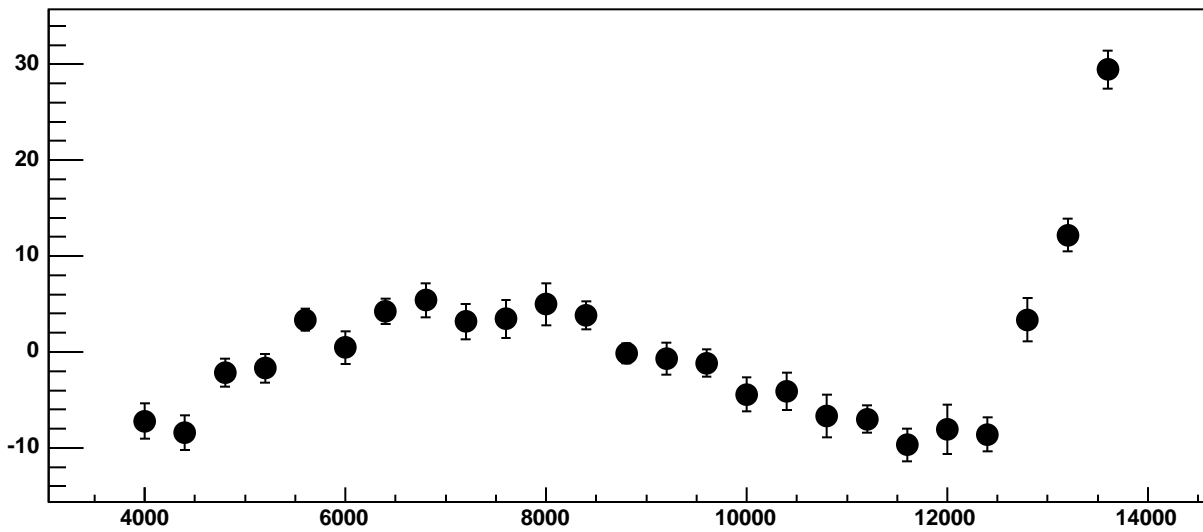
Chip 5, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



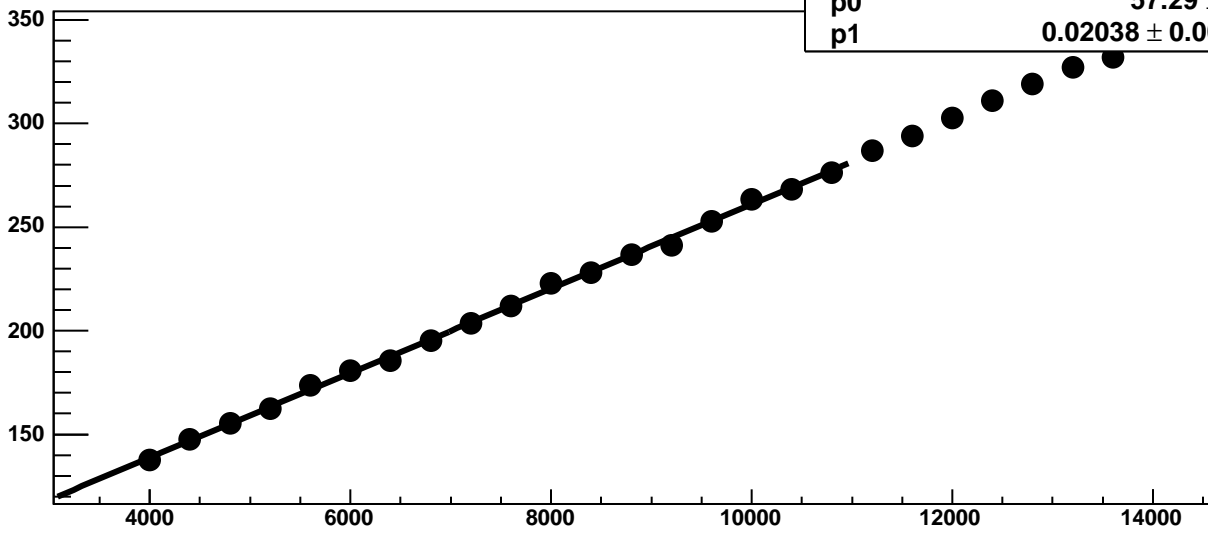
Chip 5, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

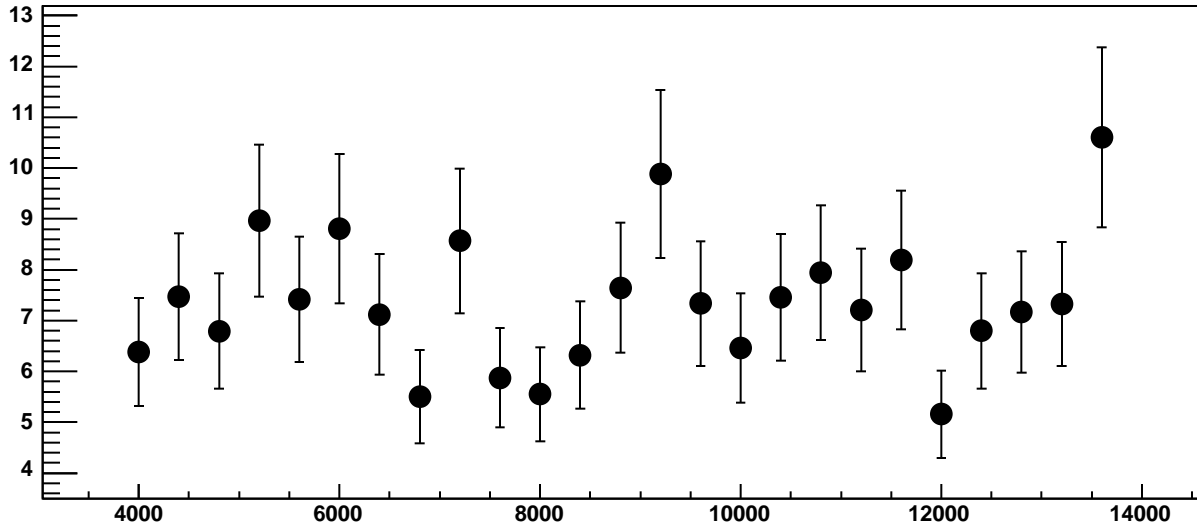


Chip 5, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

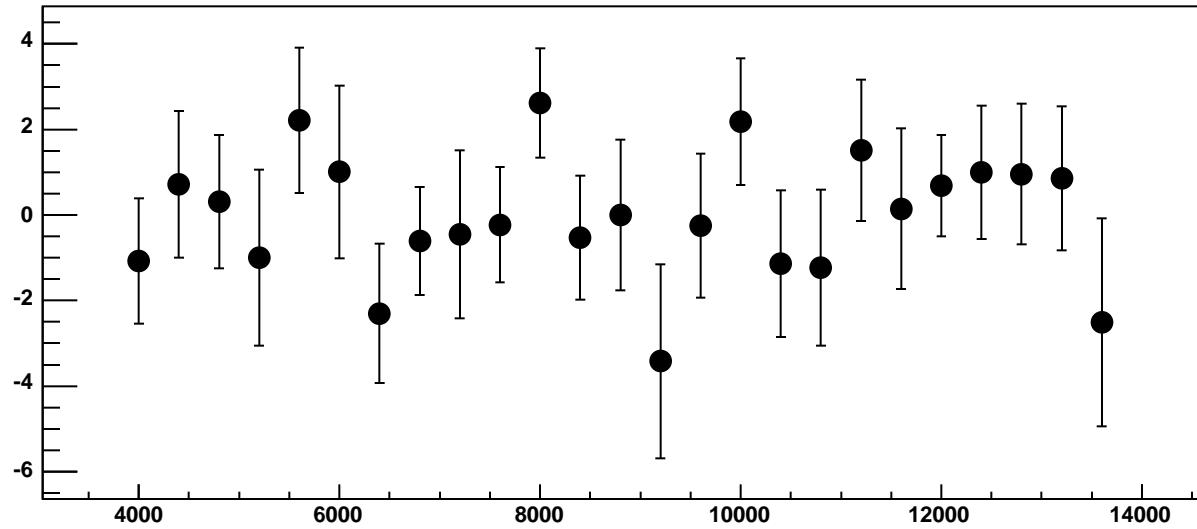


χ^2 / ndf 14.94 / 16
p0 57.29 ± 1.446
p1 0.02038 ± 0.0001892

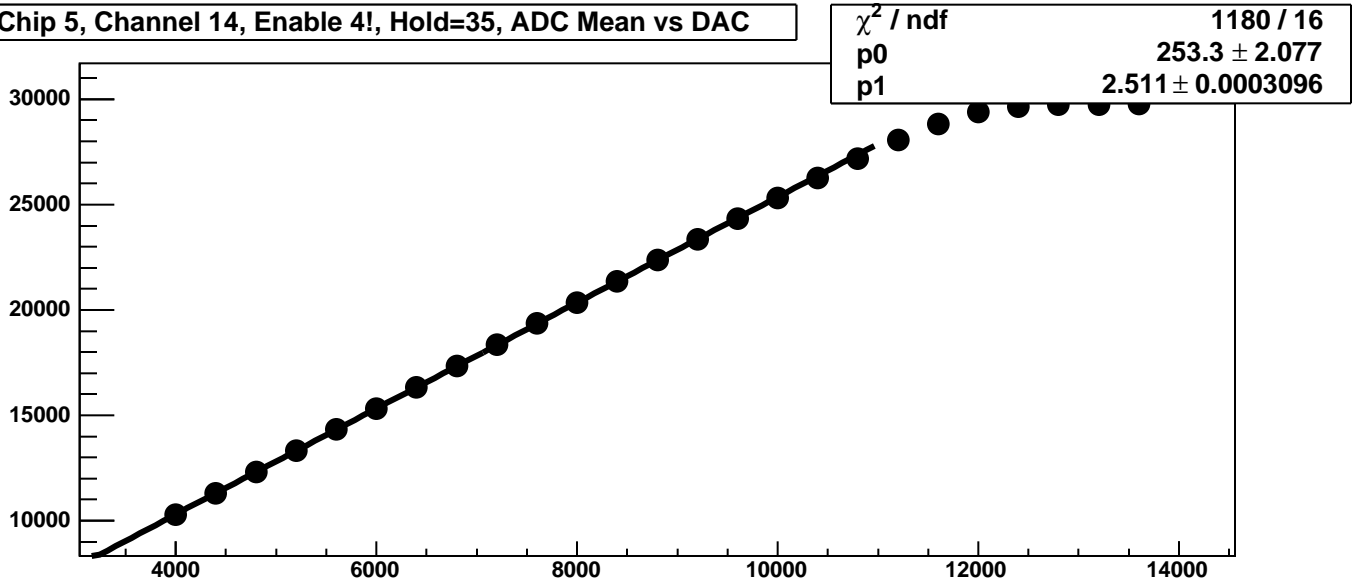
Chip 5, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



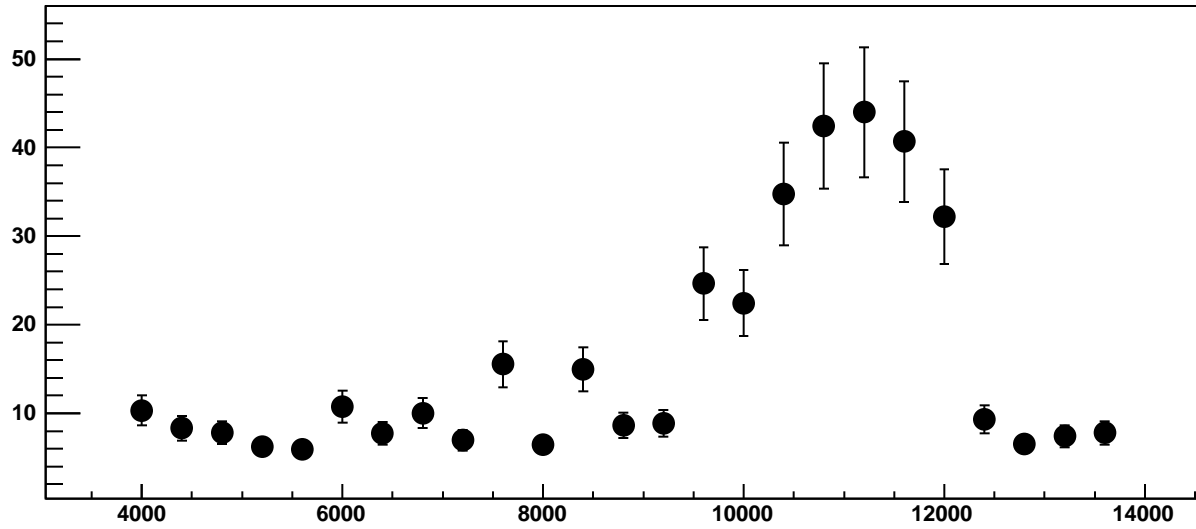
Chip 5, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



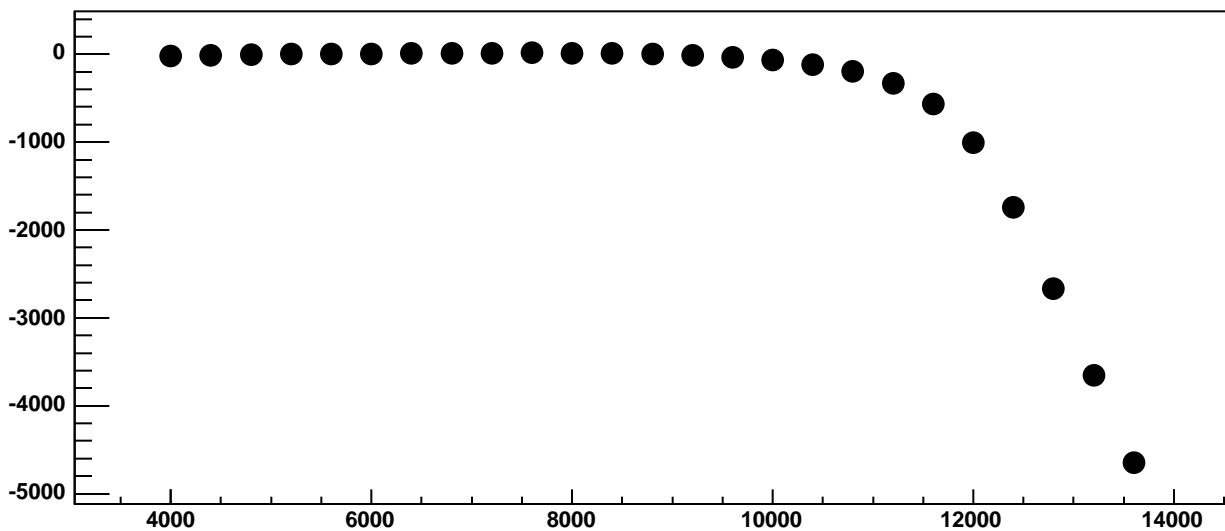
Chip 5, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC



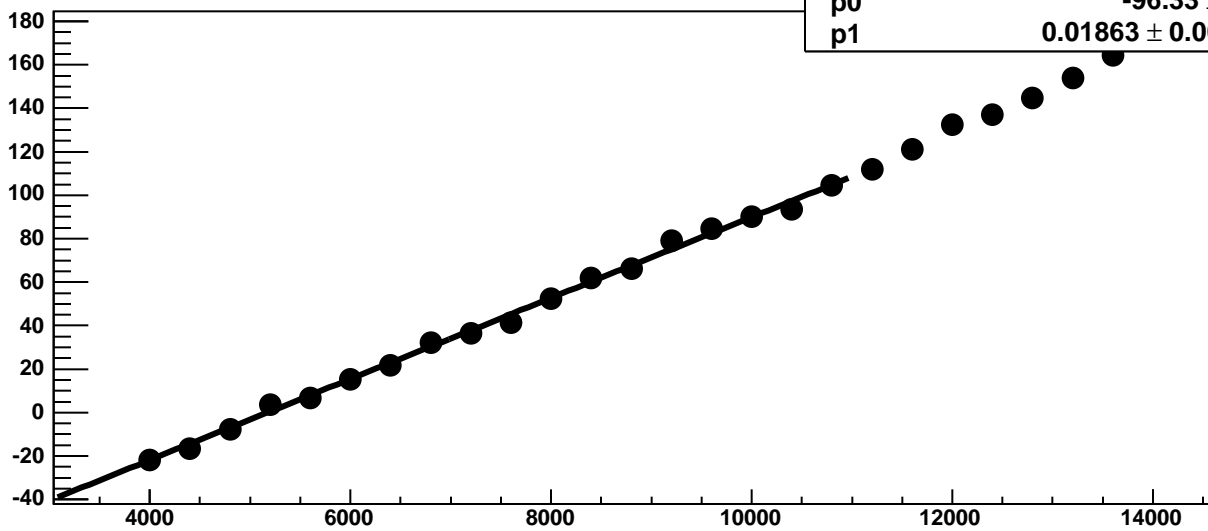
Chip 5, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



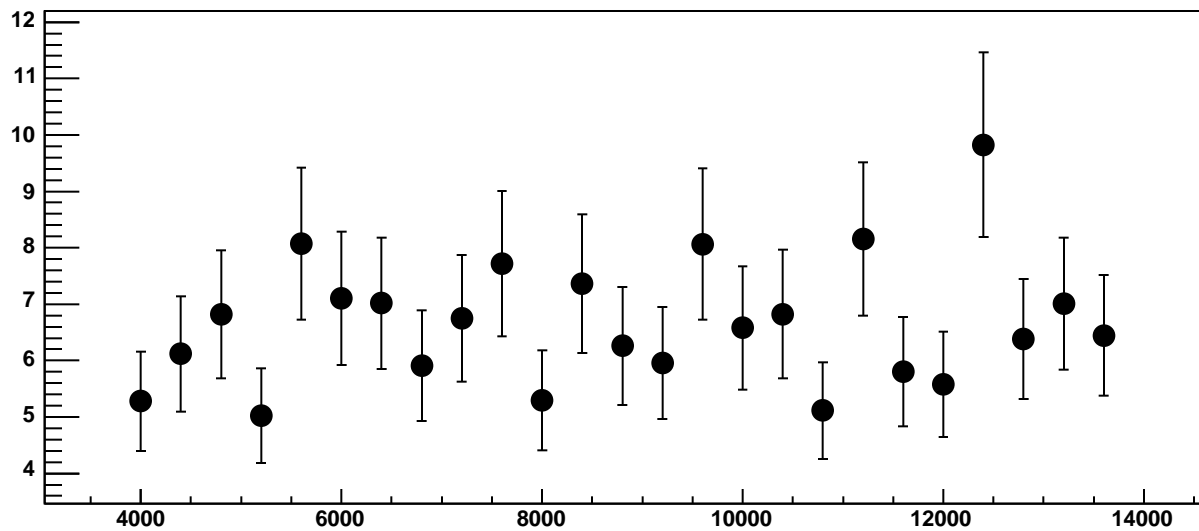
Chip 5, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC



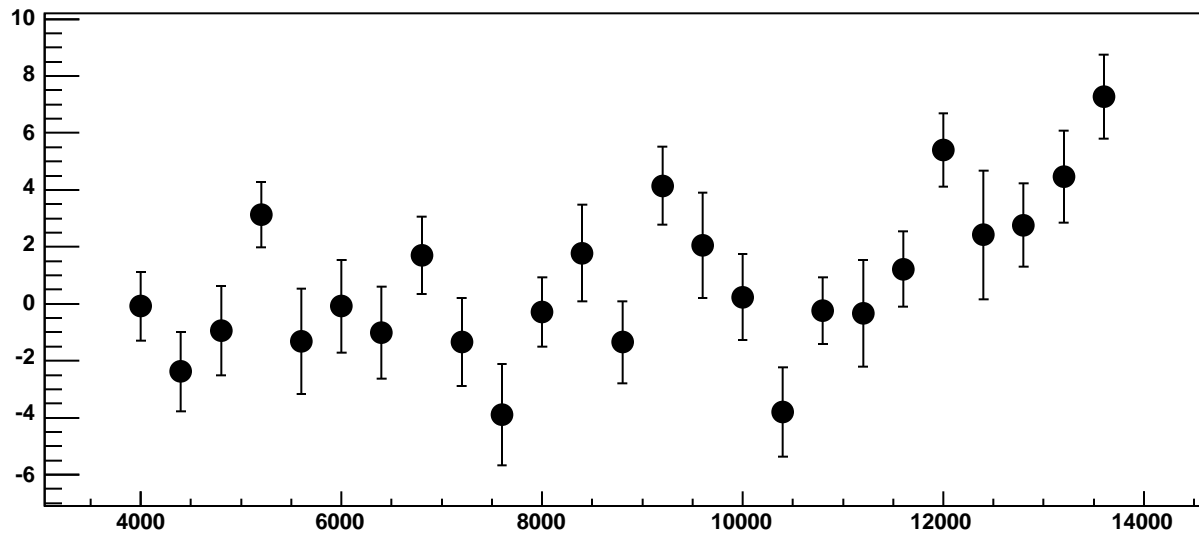
Chip 5, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



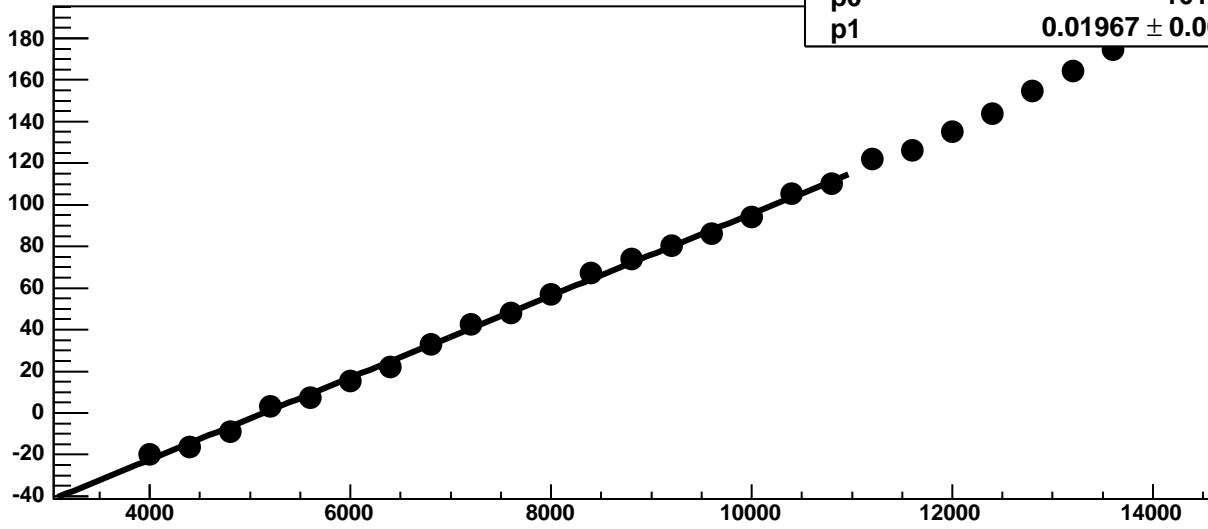
Chip 5, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

24.41 / 16

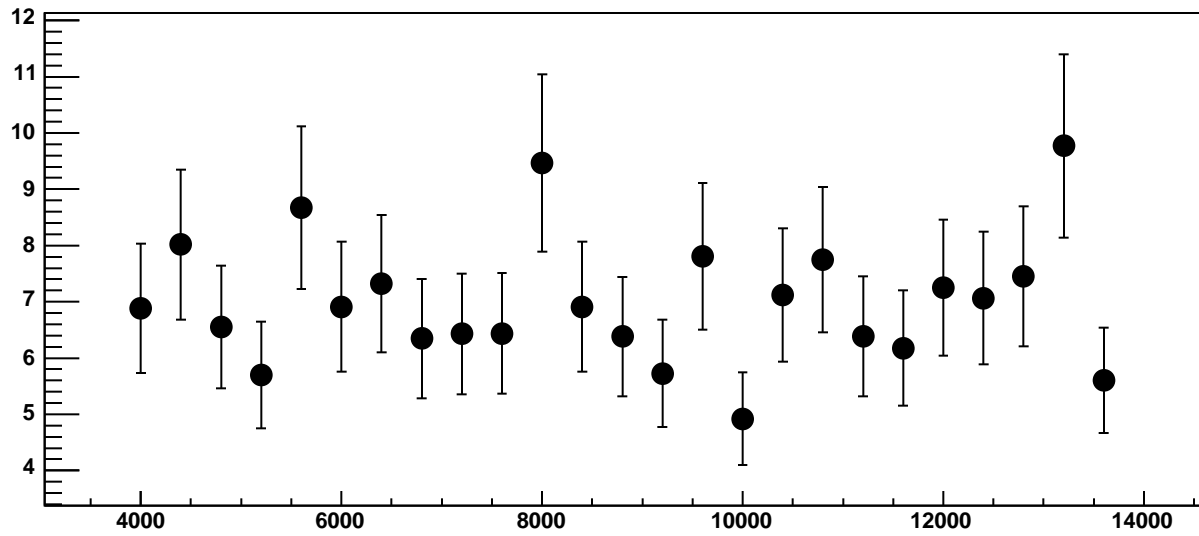
p0

-101 ± 1.37

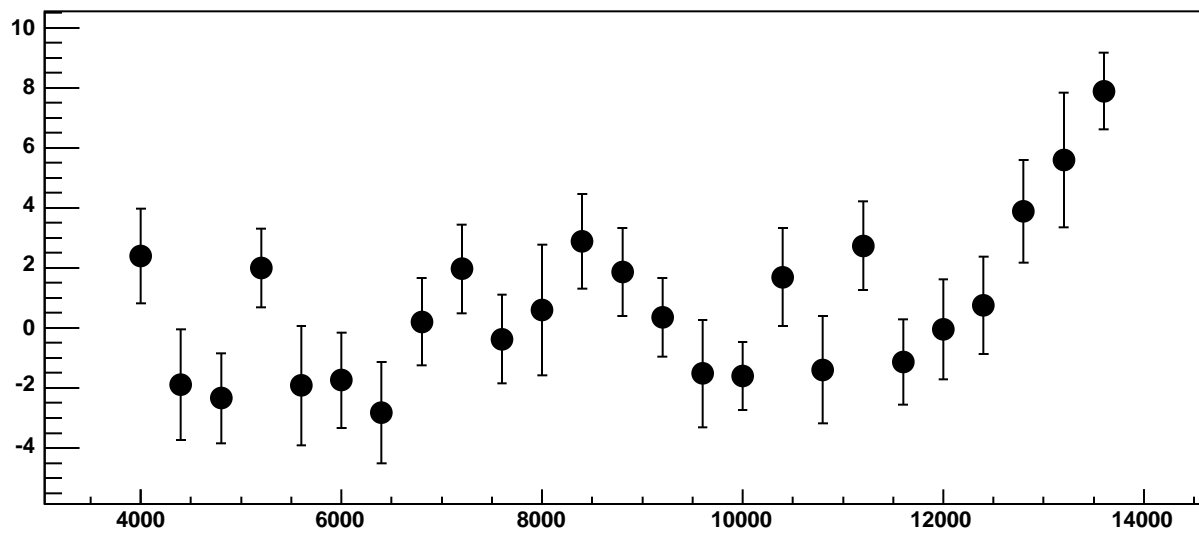
p1

0.01967 ± 0.0001759

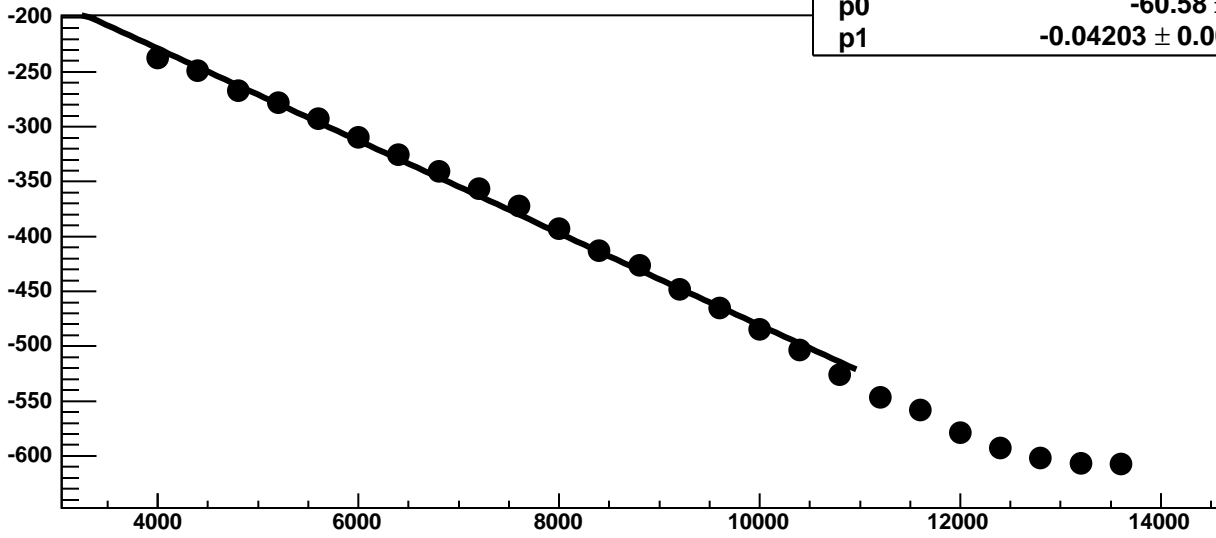
Chip 5, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



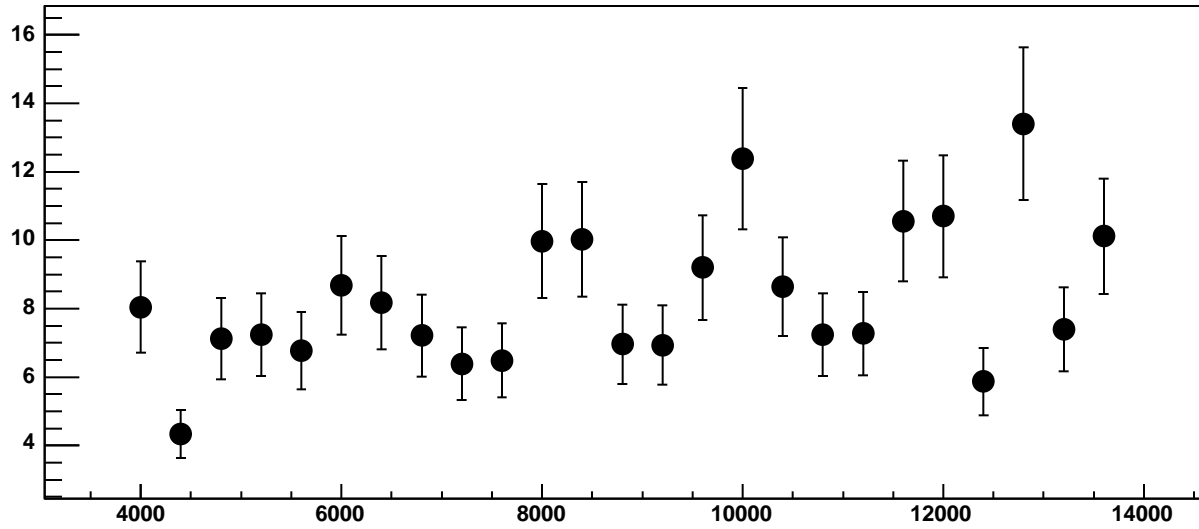
Chip 5, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



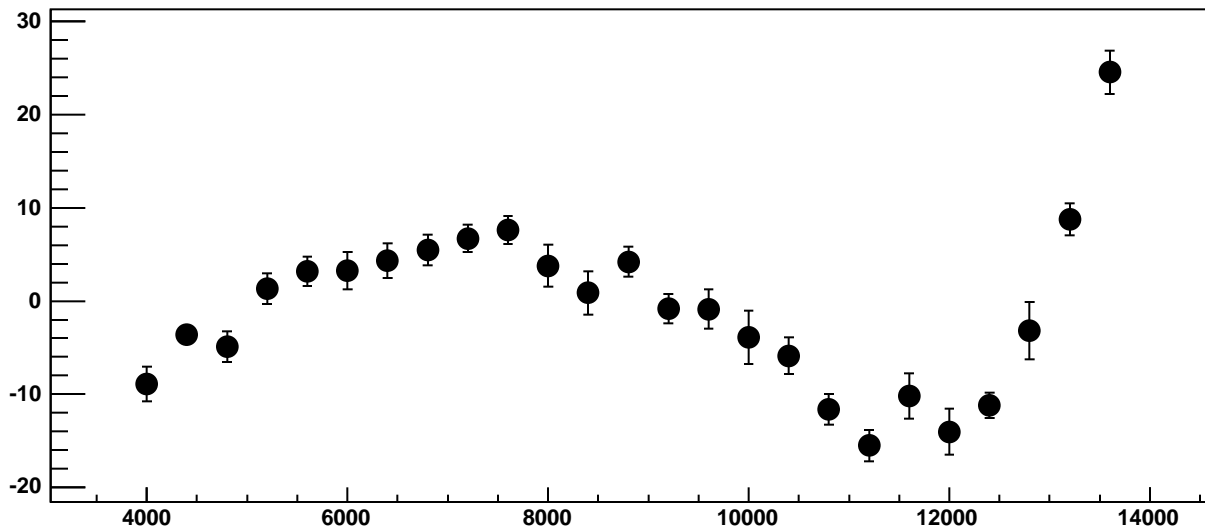
Chip 5, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC



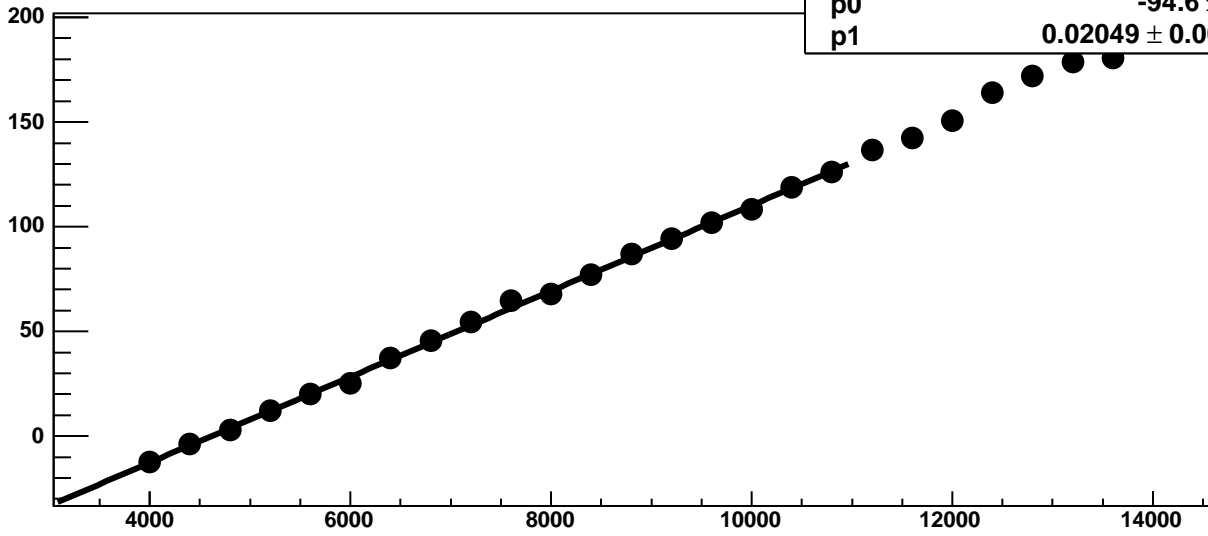
Chip 5, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC

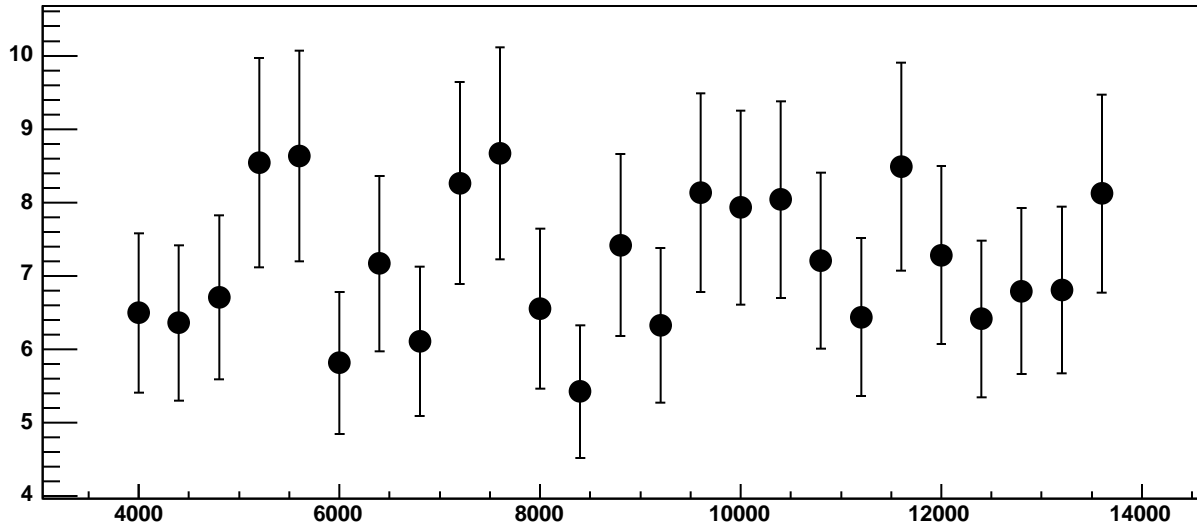


Chip 5, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

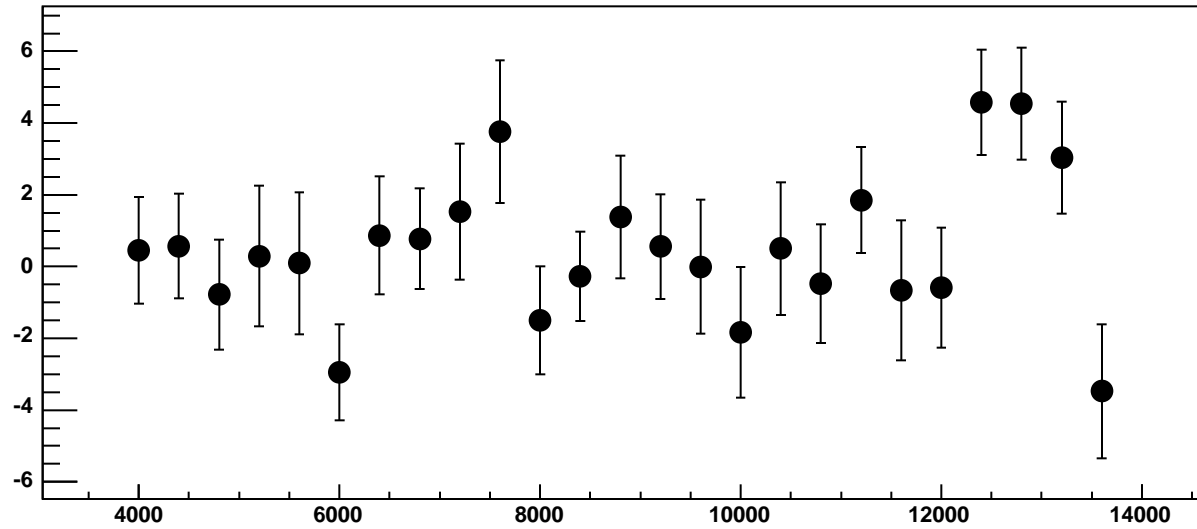


χ^2 / ndf 13.2 / 16
p0 -94.6 ± 1.399
p1 0.02049 ± 0.0001847

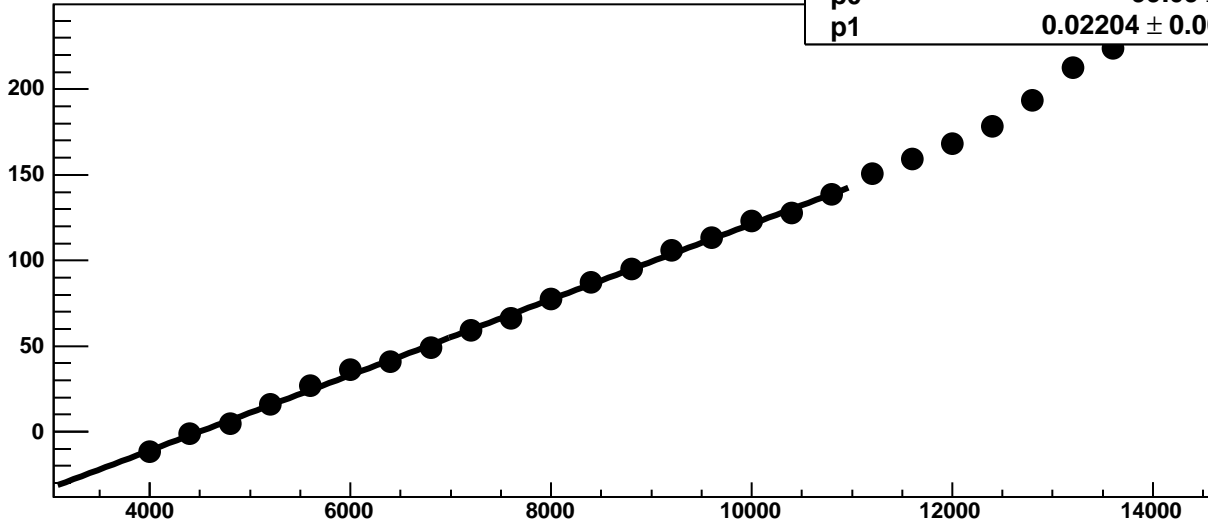
Chip 5, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

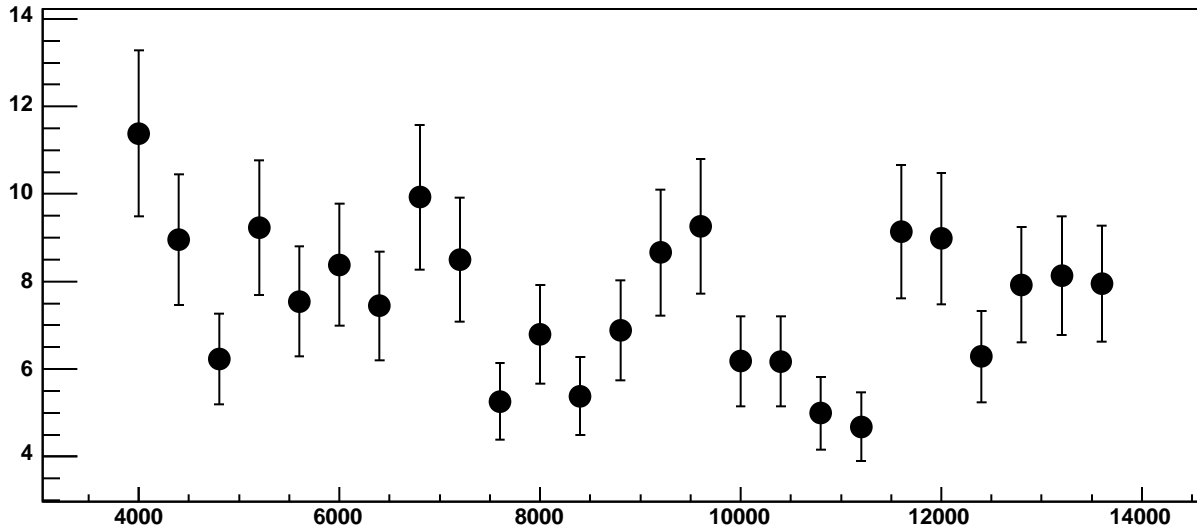


Chip 5, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

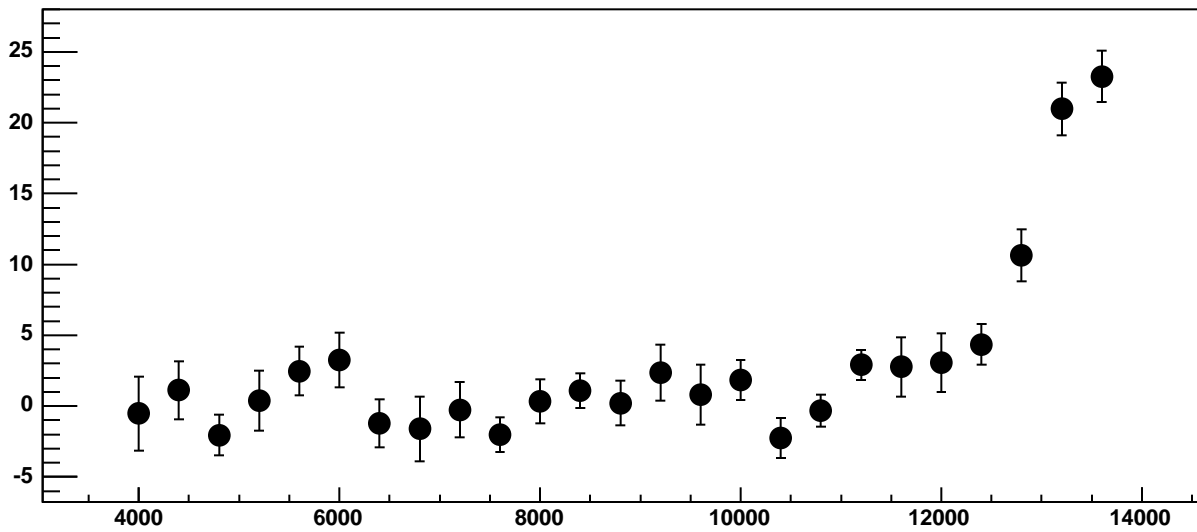


χ^2 / ndf 17.78 / 16
p0 -99.09 ± 1.524
p1 0.02204 ± 0.0001867

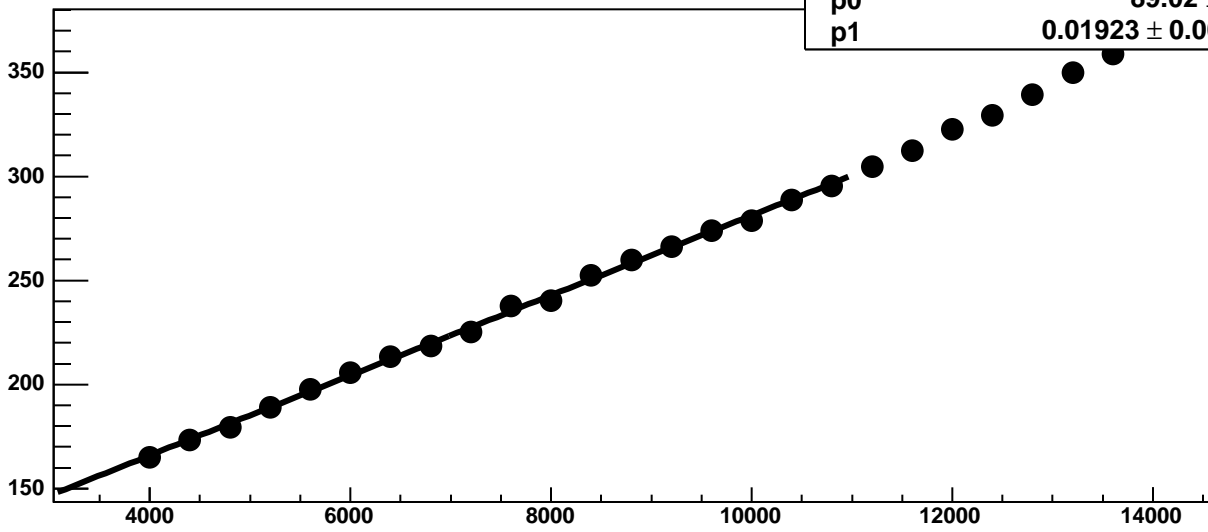
Chip 5, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

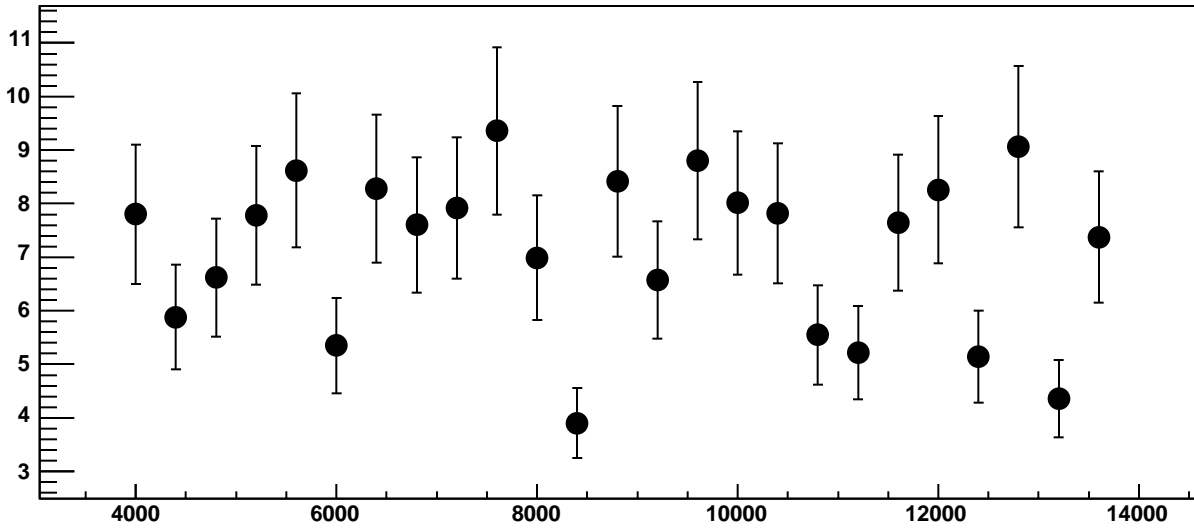


Chip 5, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

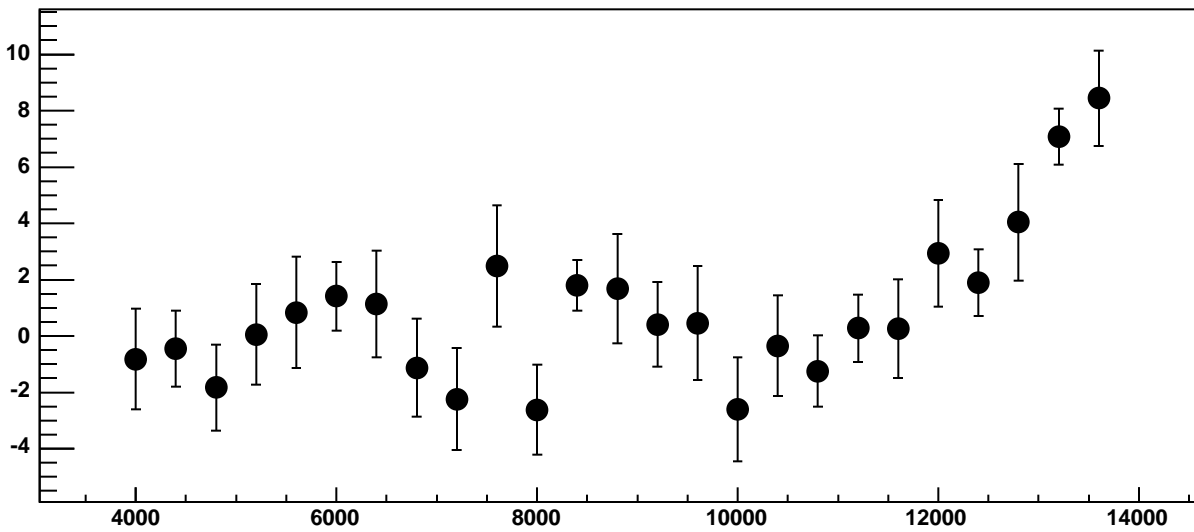


χ^2 / ndf 17.49 / 16
p0 89.02 ± 1.372
p1 0.01923 ± 0.0001769

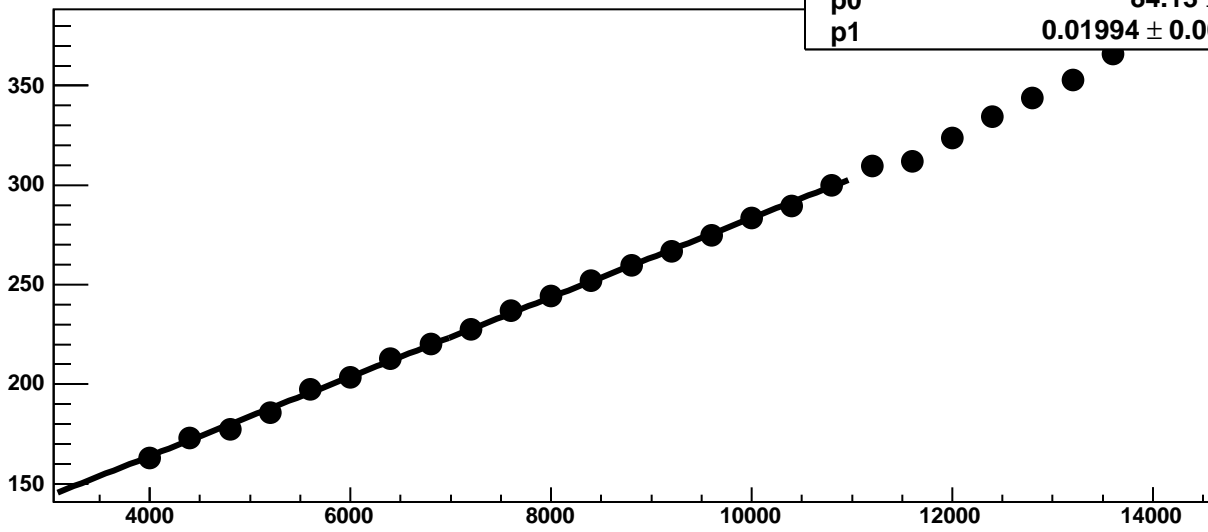
Chip 5, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

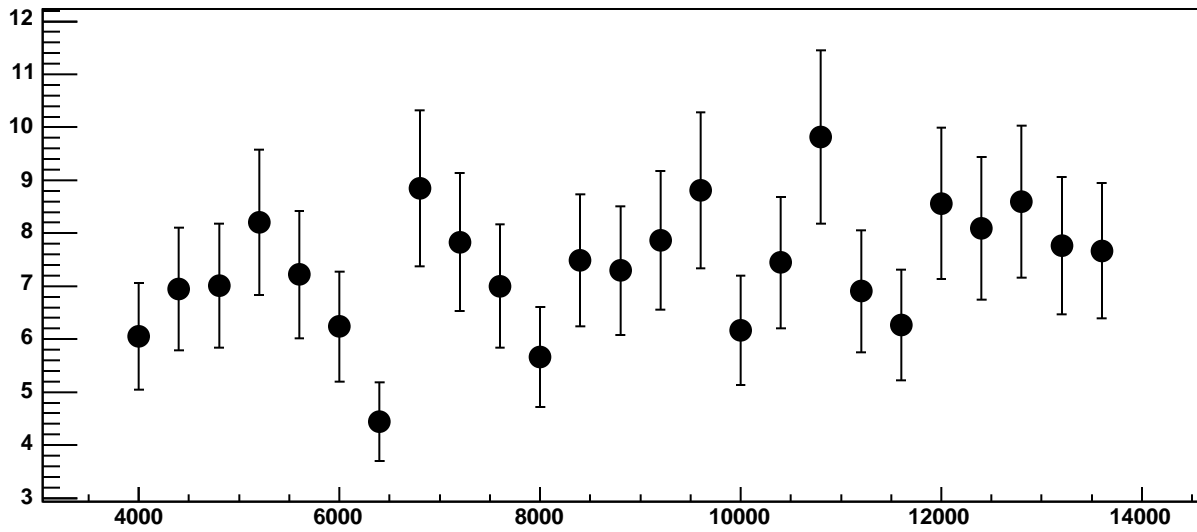


Chip 5, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

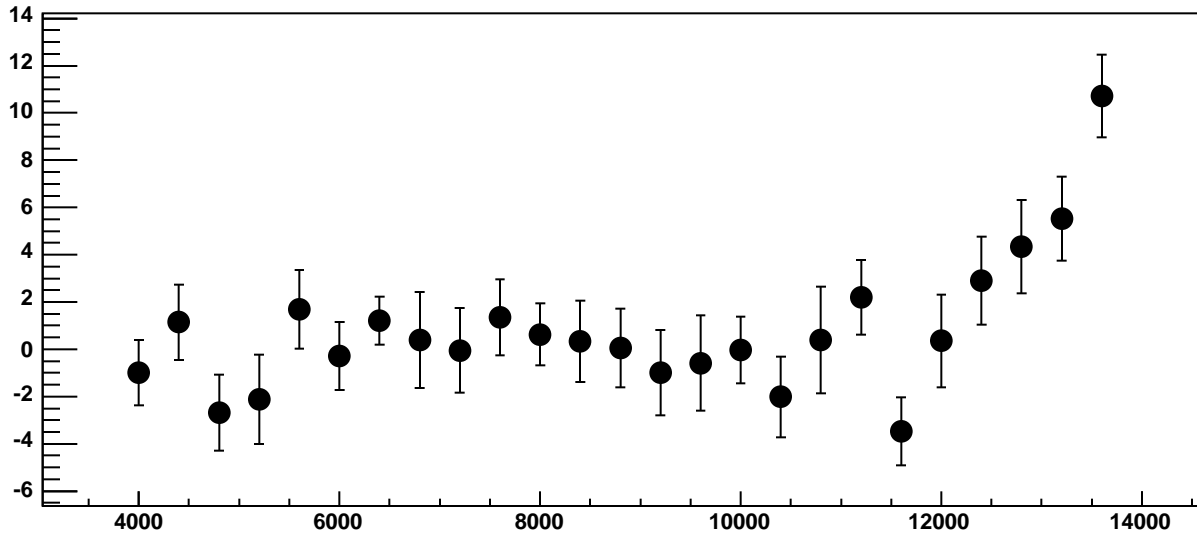


χ^2 / ndf 10.36 / 16
p0 84.13 ± 1.392
p1 0.01994 ± 0.0001873

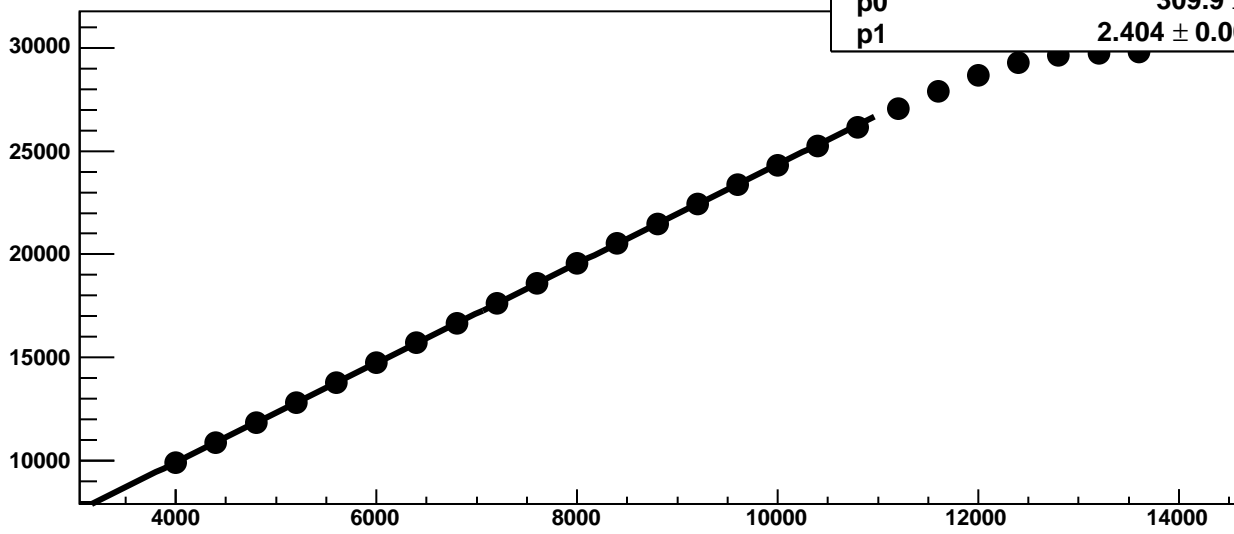
Chip 5, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



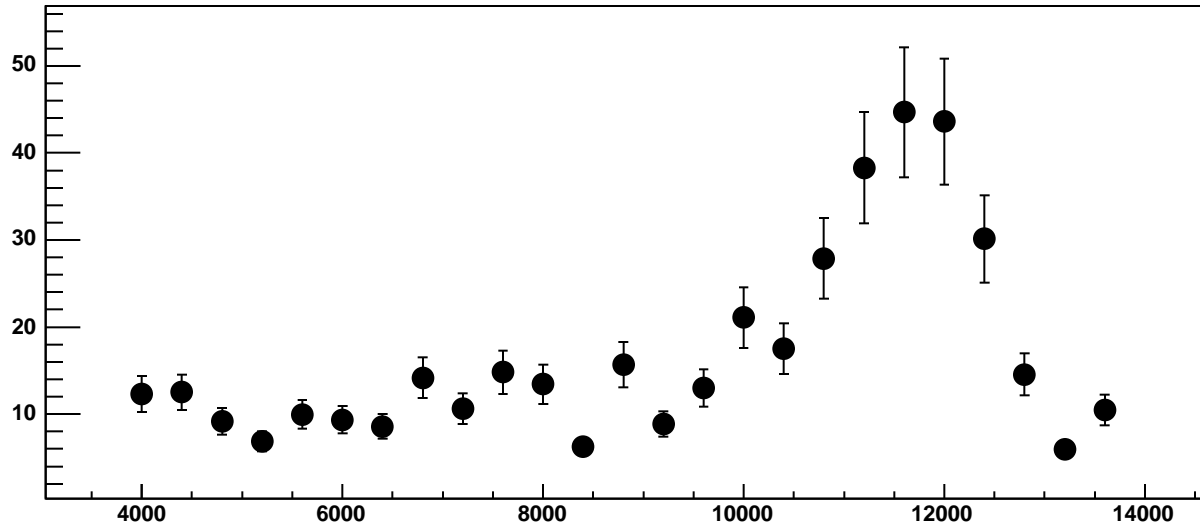
Chip 5, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



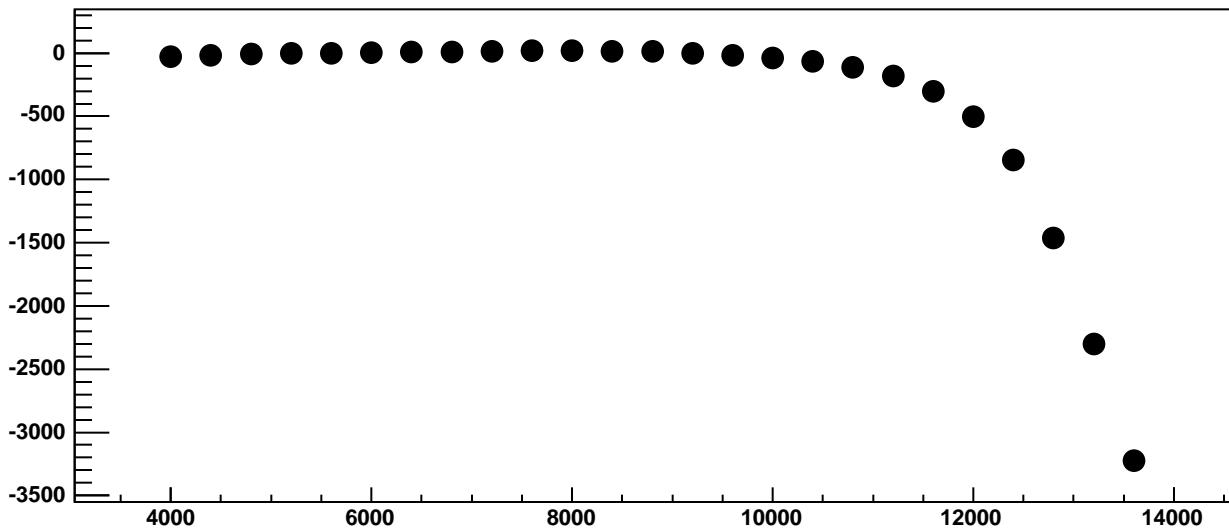
Chip 5, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC



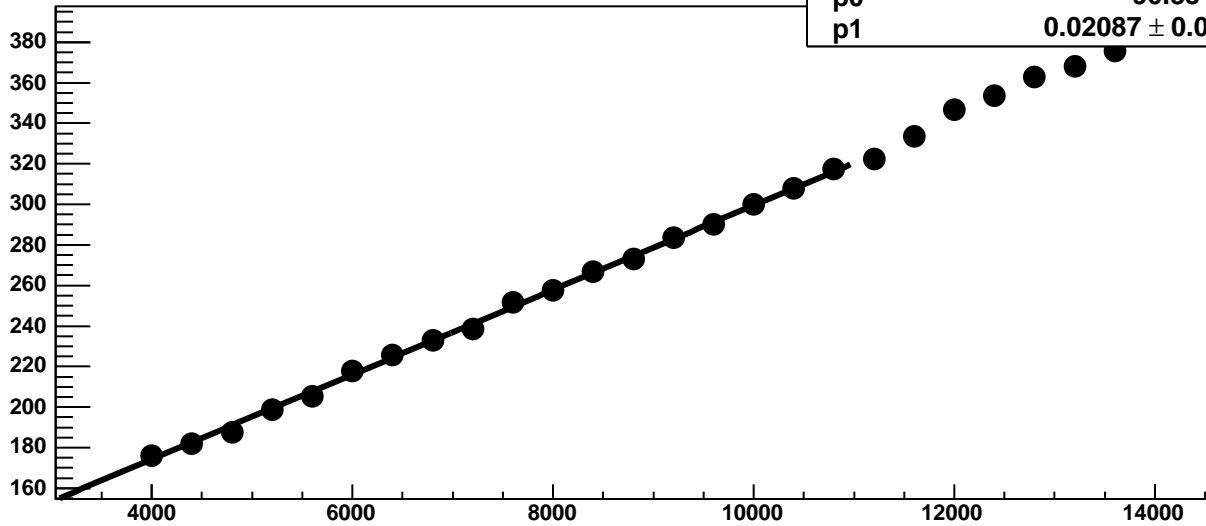
Chip 5, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

19.12 / 16

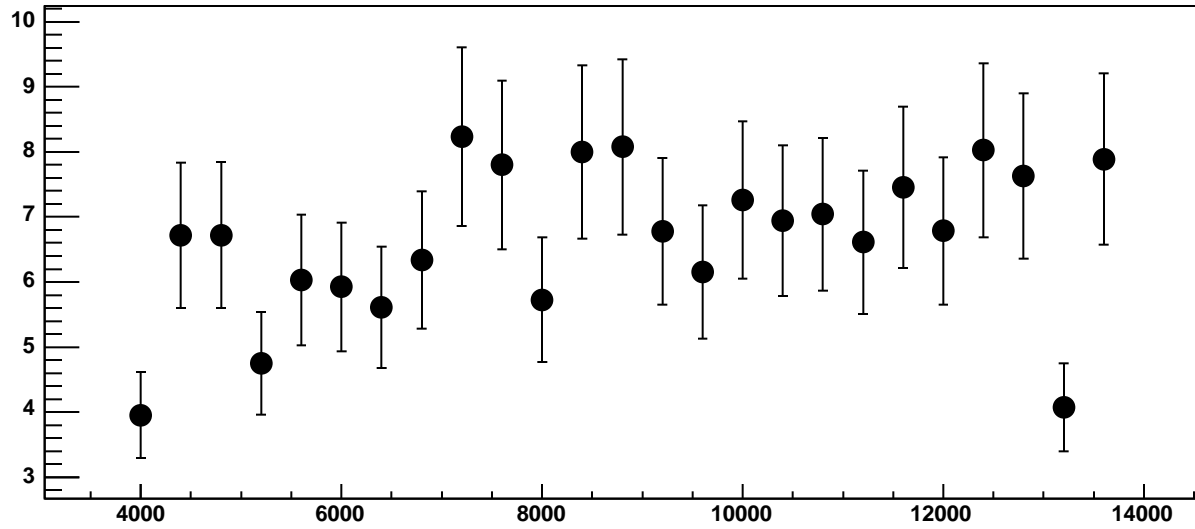
p0

90.83 ± 1.124

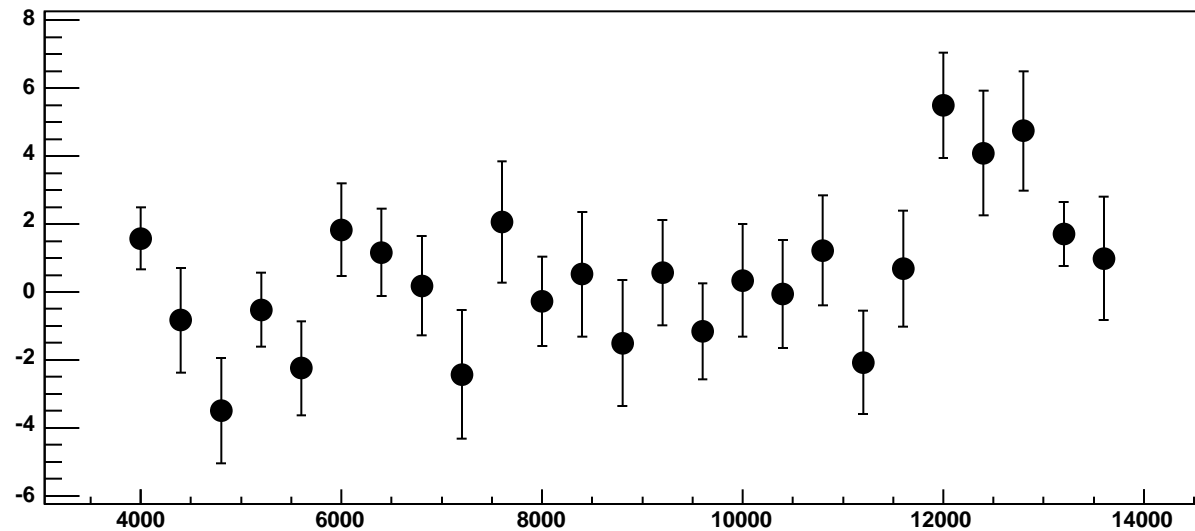
p1

0.02087 ± 0.0001559

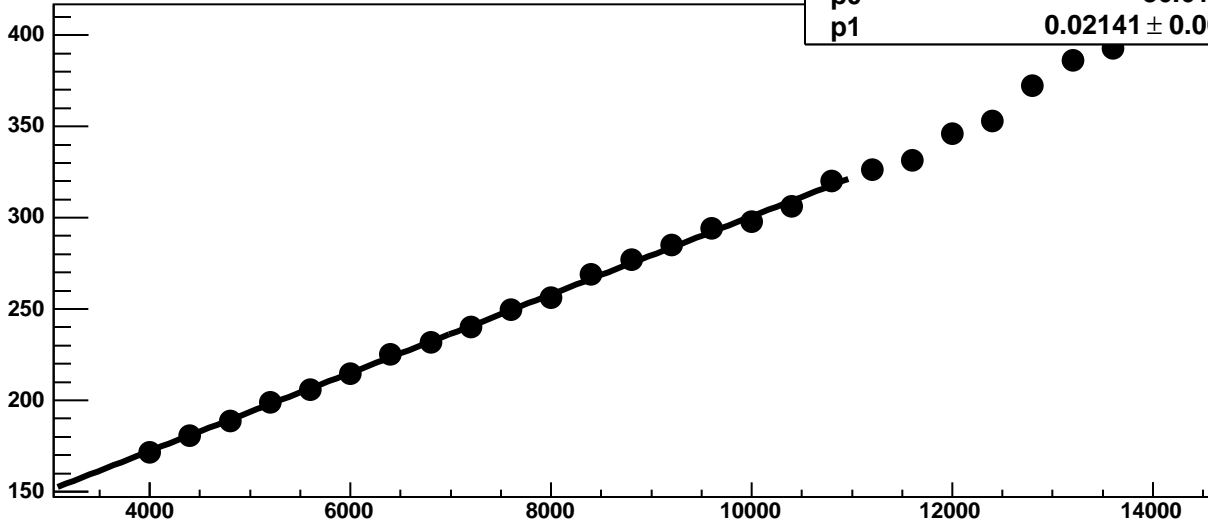
Chip 5, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



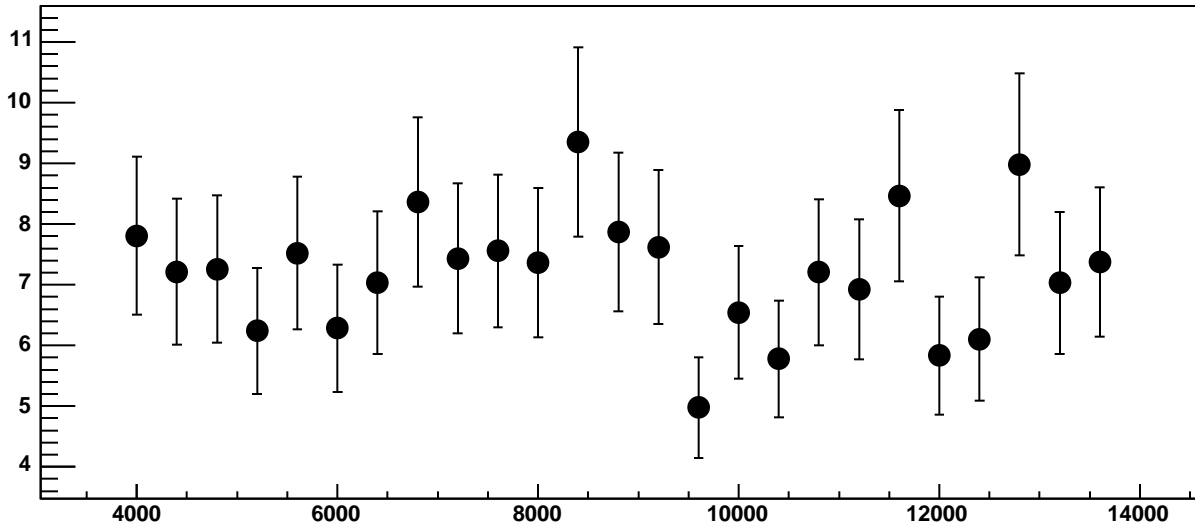
Chip 5, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



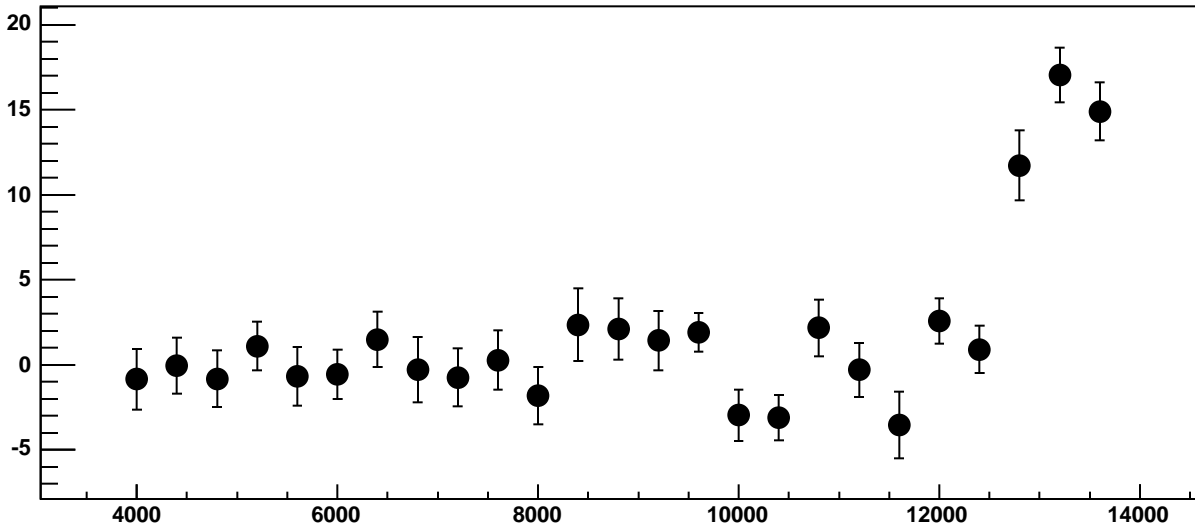
Chip 5, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



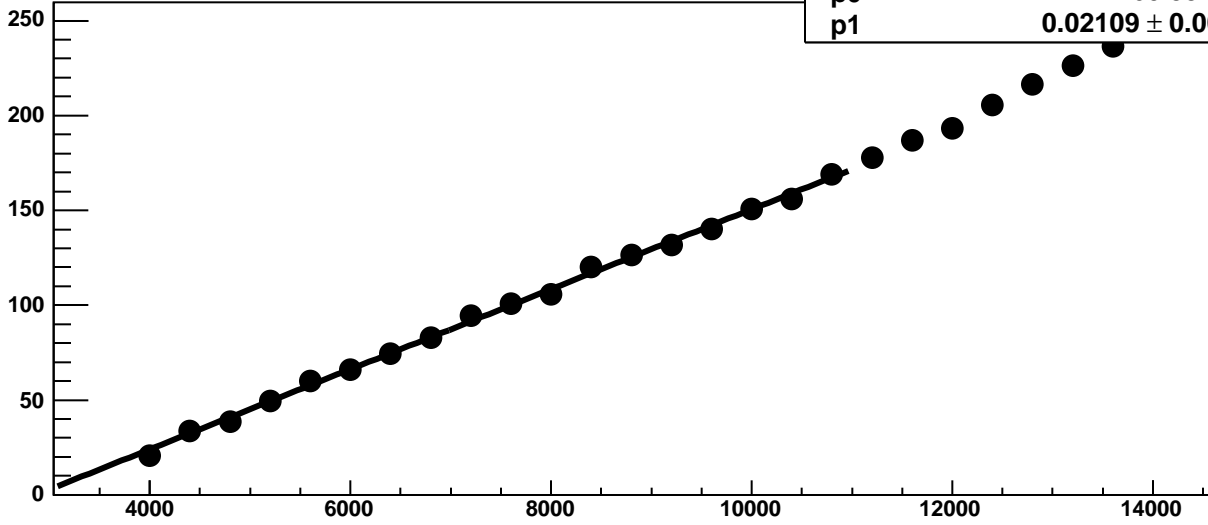
Chip 5, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



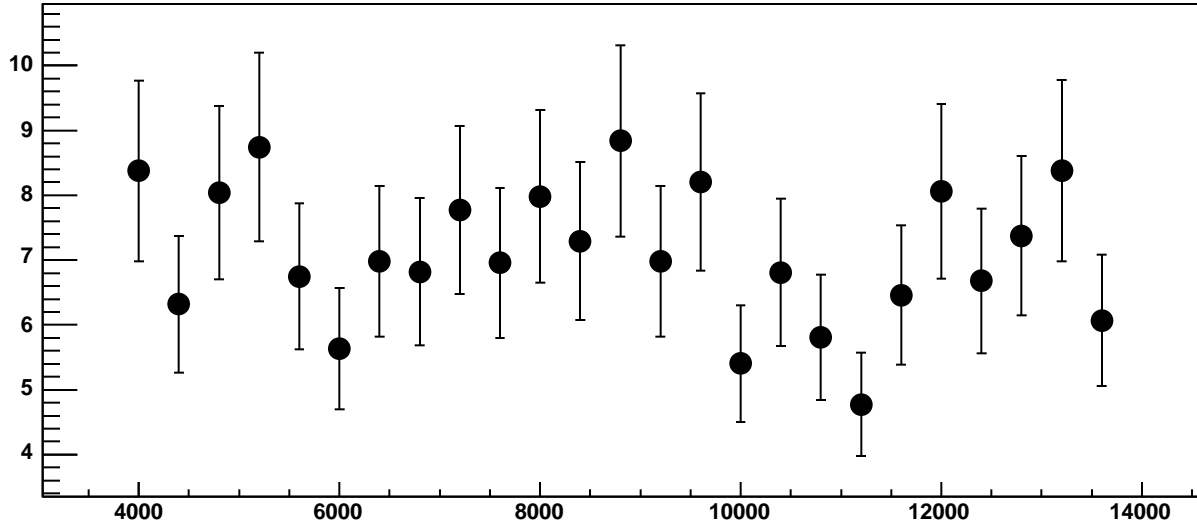
Chip 5, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



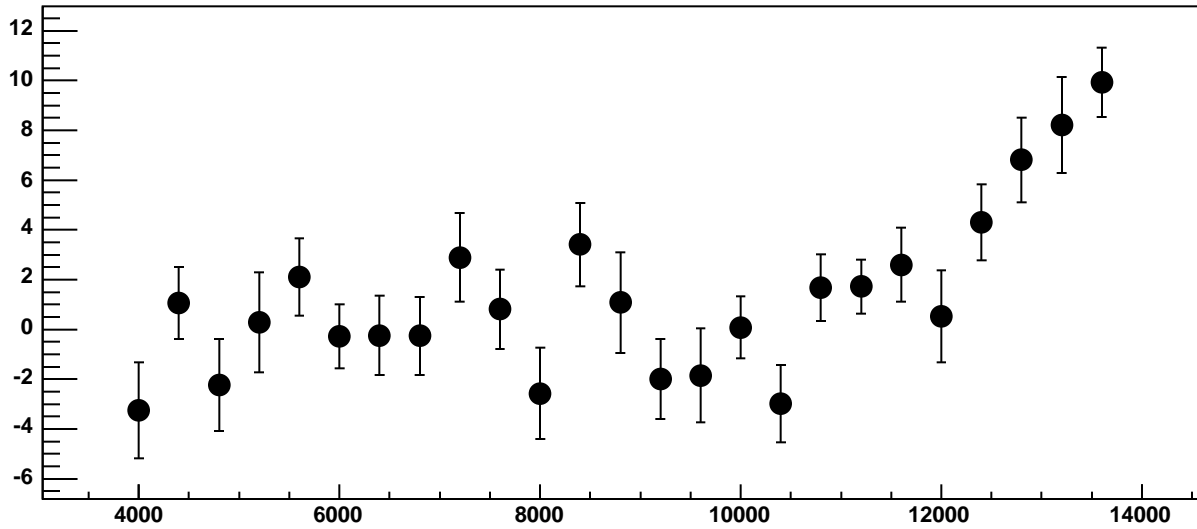
Chip 5, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



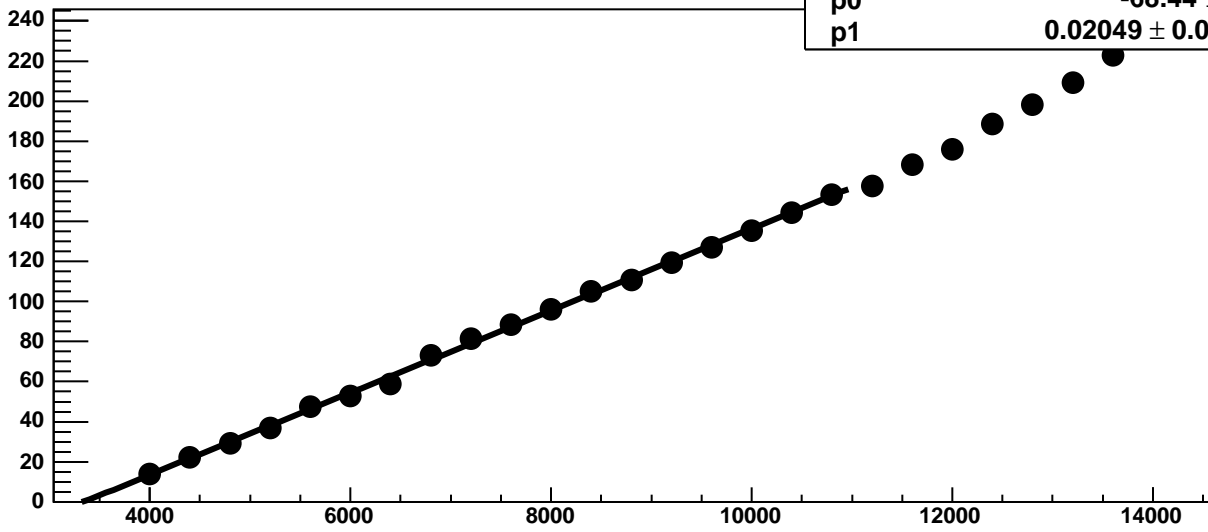
Chip 5, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

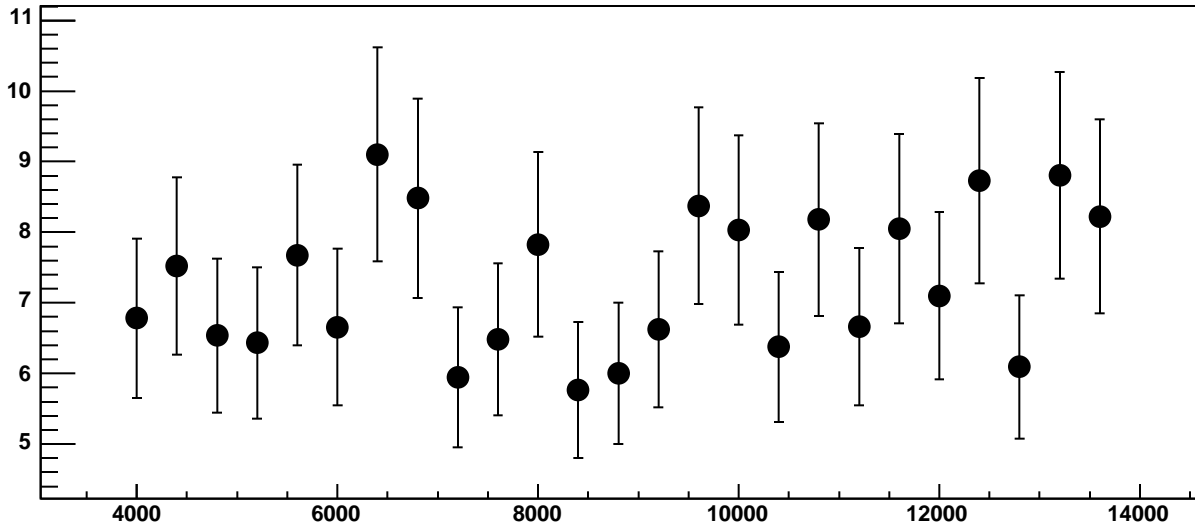


Chip 5, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

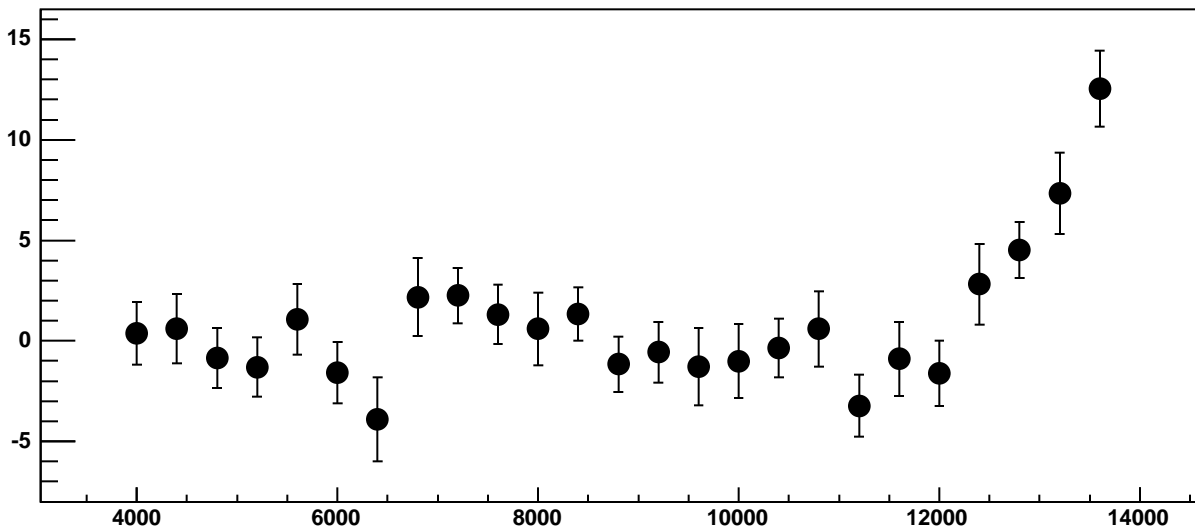


χ^2 / ndf 13.88 / 16
p0 -68.44 ± 1.417
p1 0.02049 ± 0.0001851

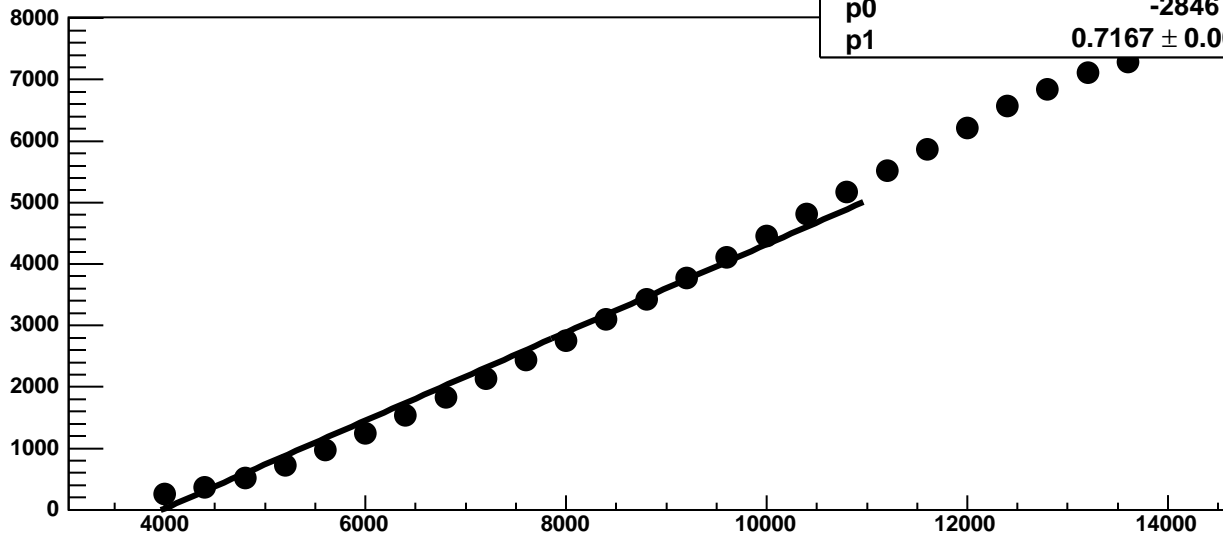
Chip 5, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



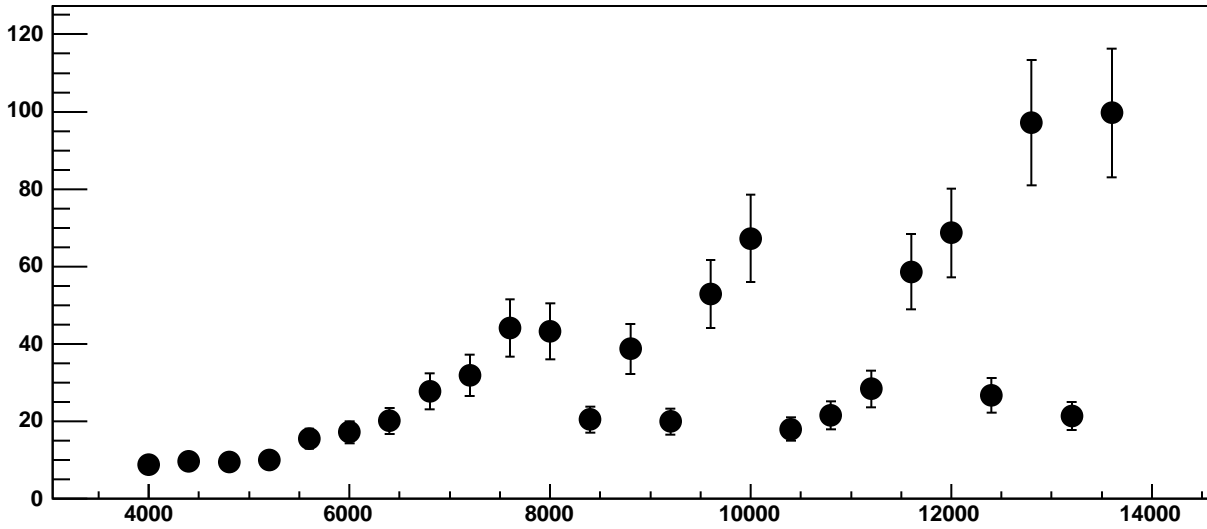
Chip 5, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



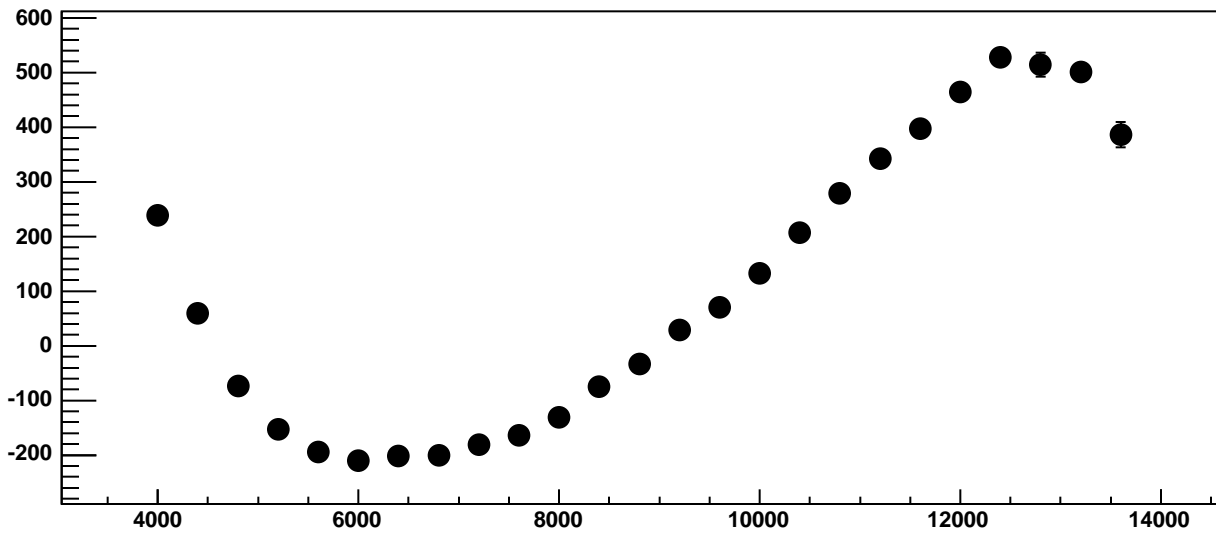
Chip 5, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC



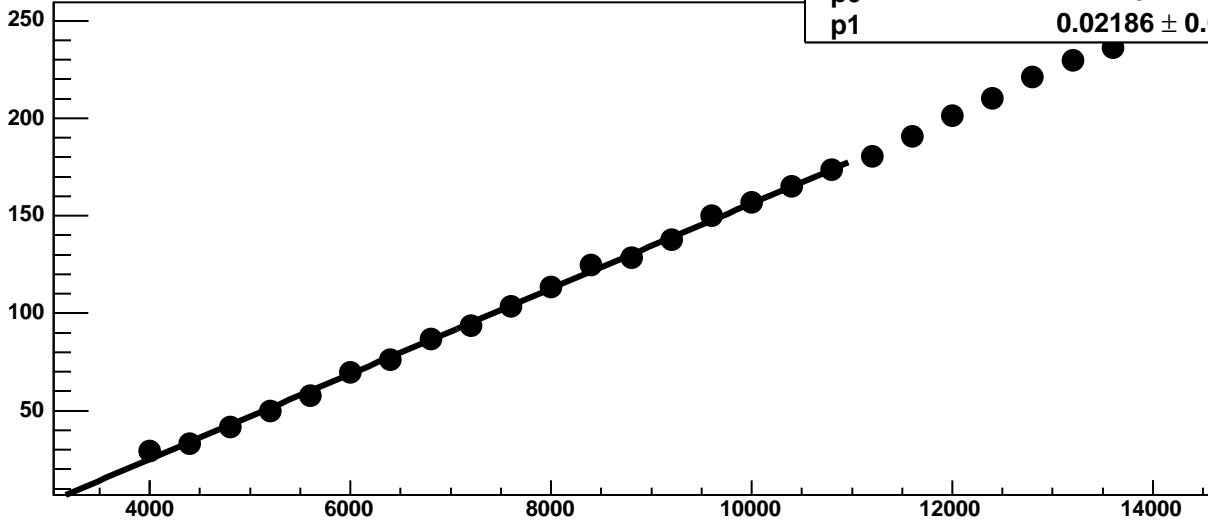
Chip 5, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC

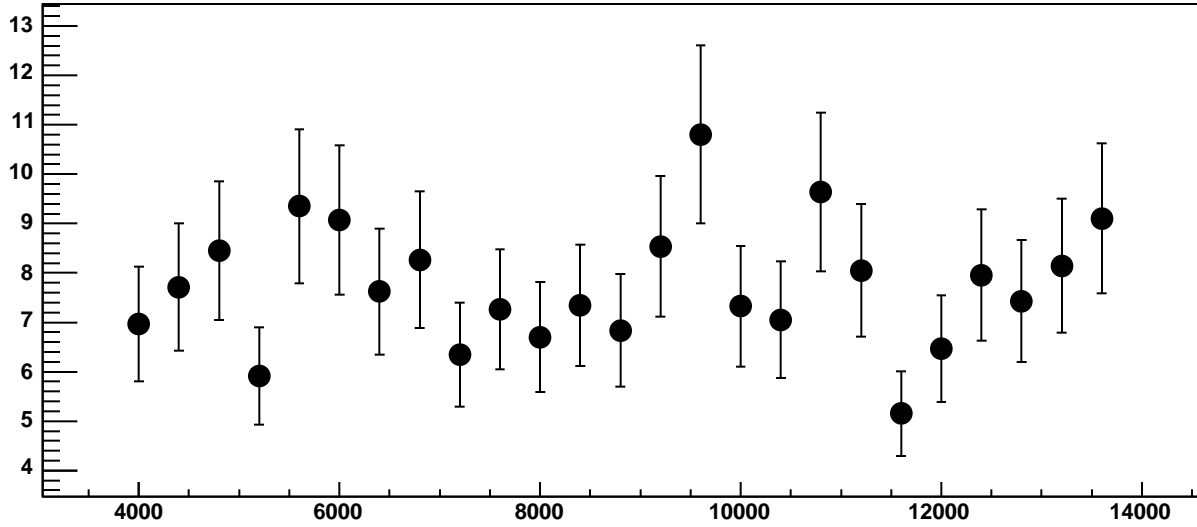


Chip 5, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf 19.51 / 16
p0 -62.22 ± 1.529
p1 0.02186 ± 0.000202

Chip 5, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

