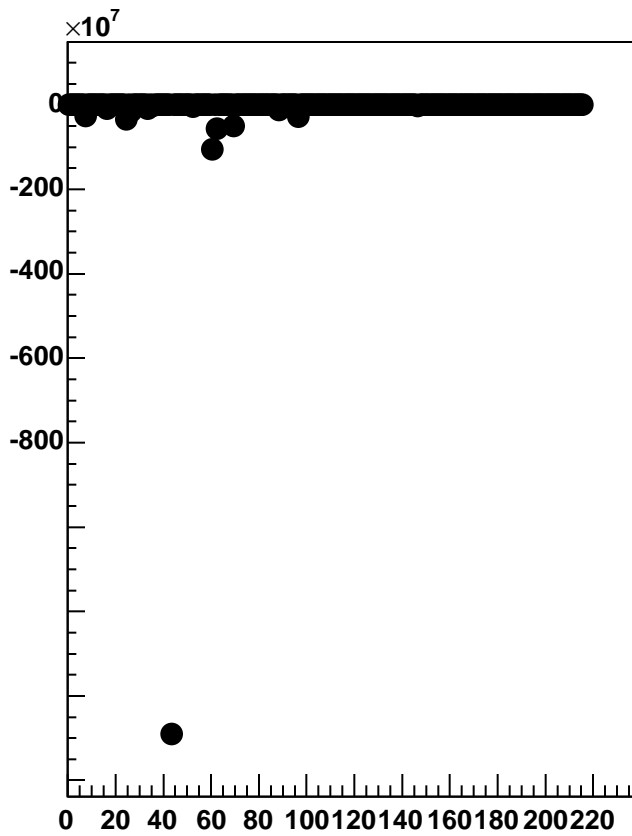
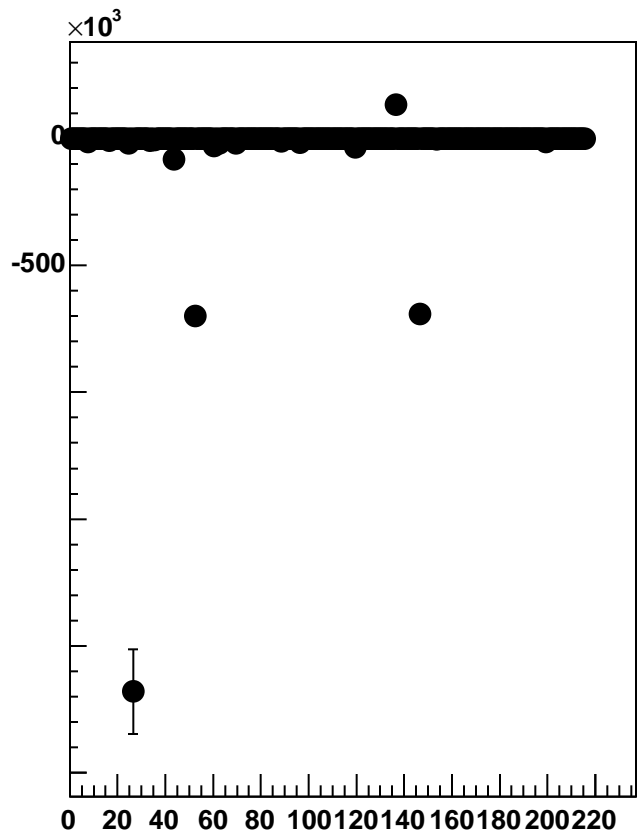


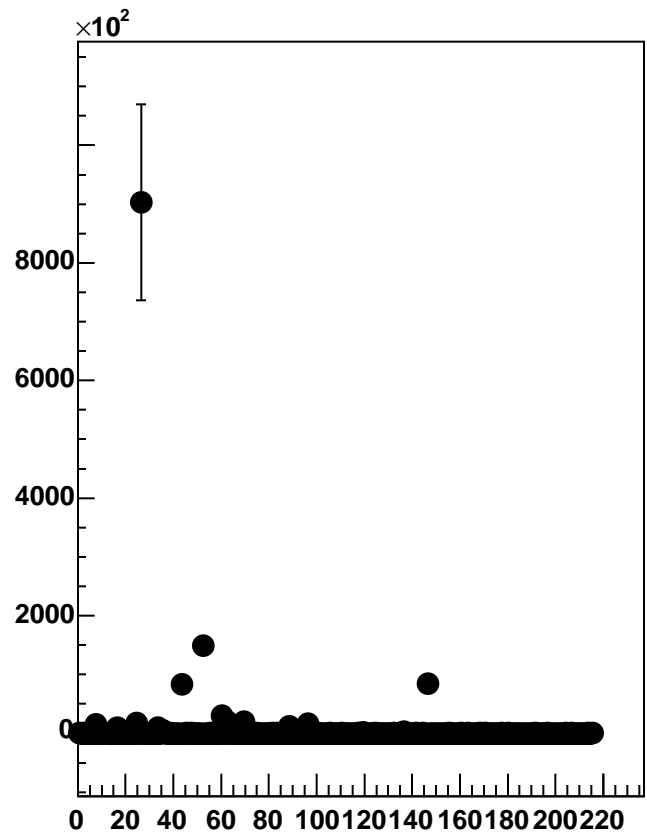
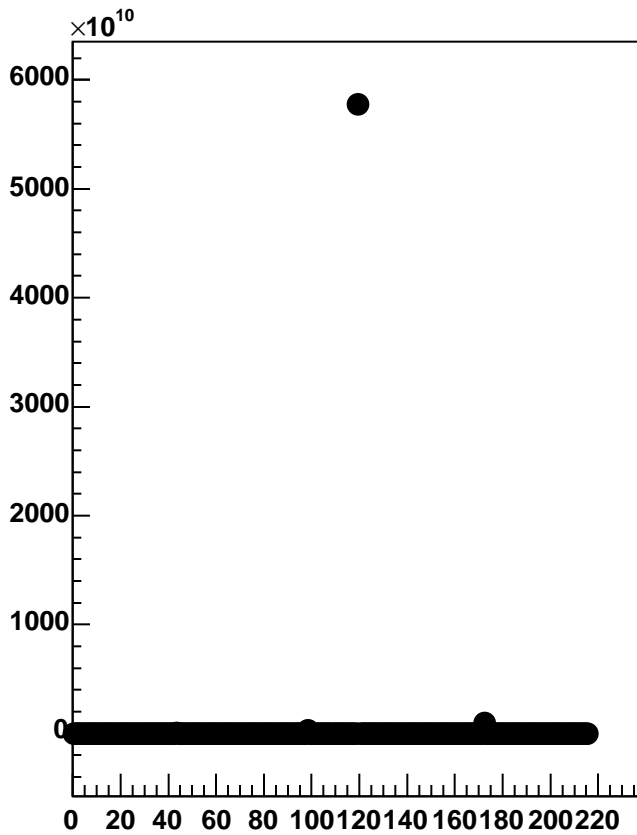
Channel Enabled, DAC=4000, Fit Baseline vs 18°Chip+Chan



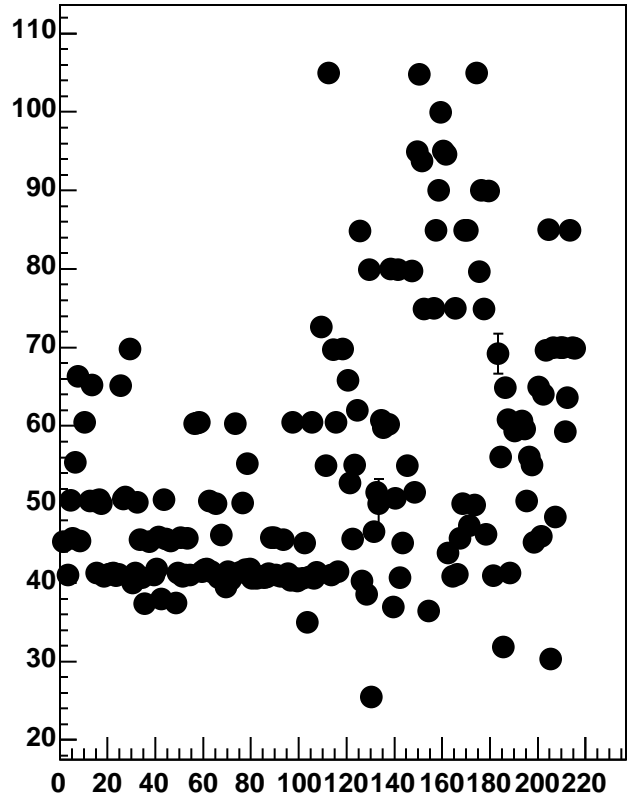
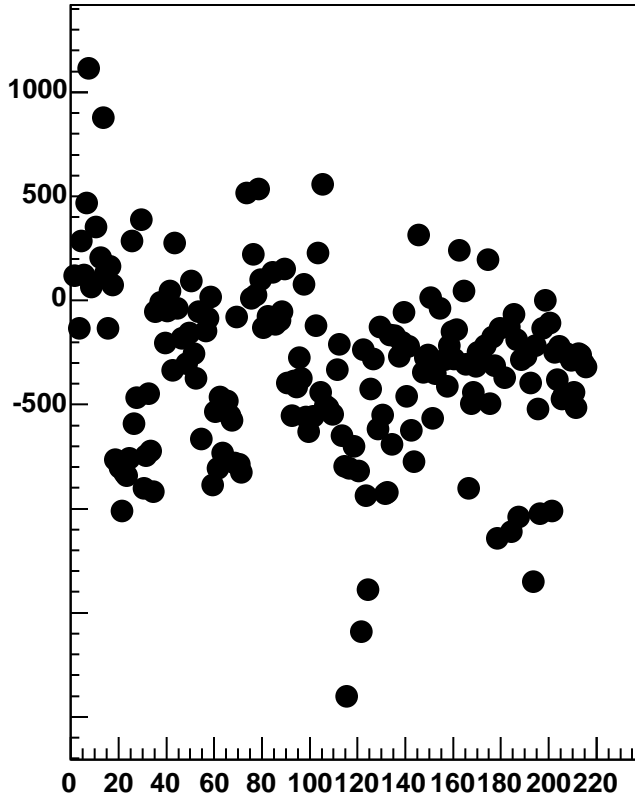
Channel Enabled, DAC=4000, Fit Start Time vs 18°Chip+Chan



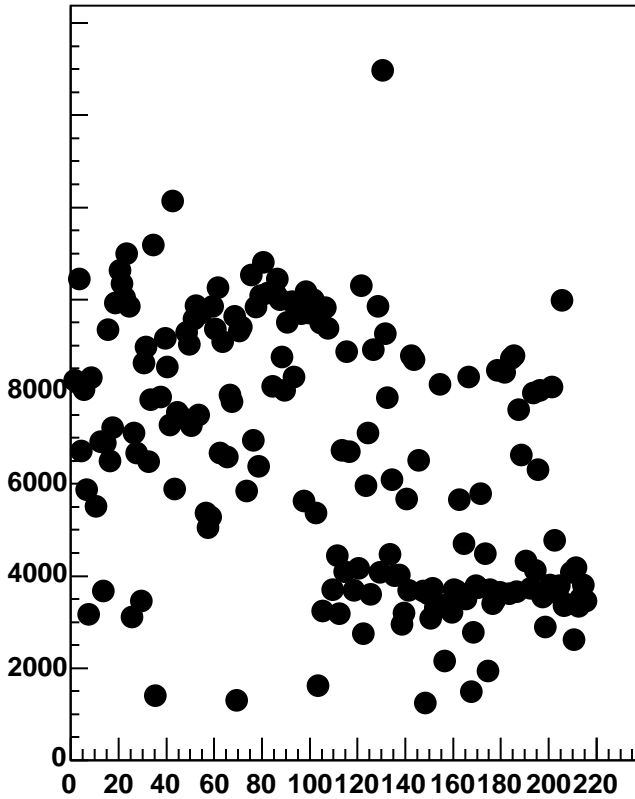
Channel Enabled, DAC=4000, Fit Normalisation vs 18°Chip+Chan



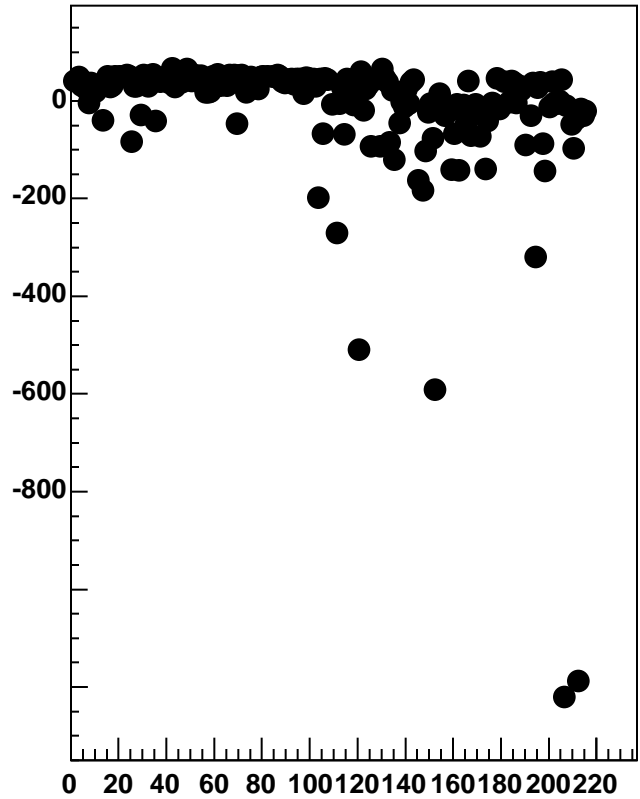
Disabled 0, DAC=4000, Fit Baseline vs 18°Chip+Chan



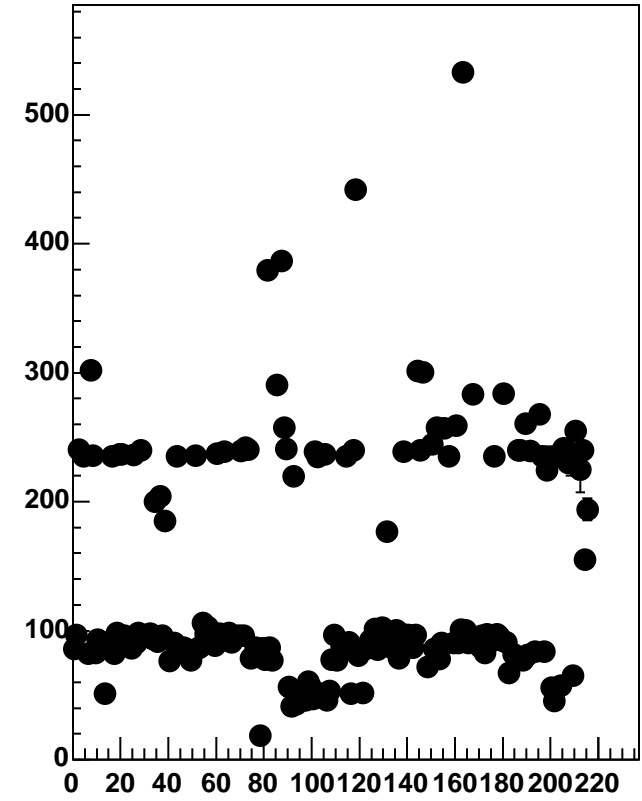
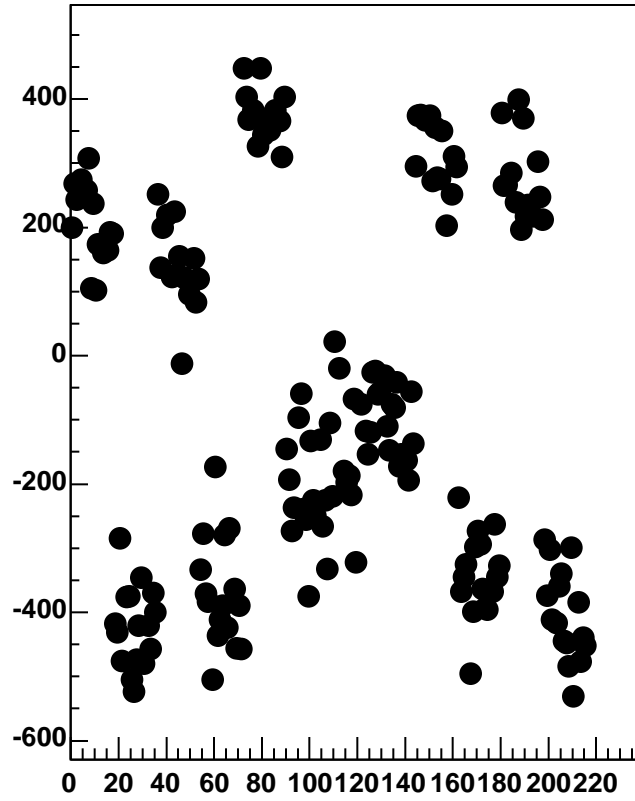
Disabled 0, DAC=4000, Fit Normalisation vs 18°Chip+Chan



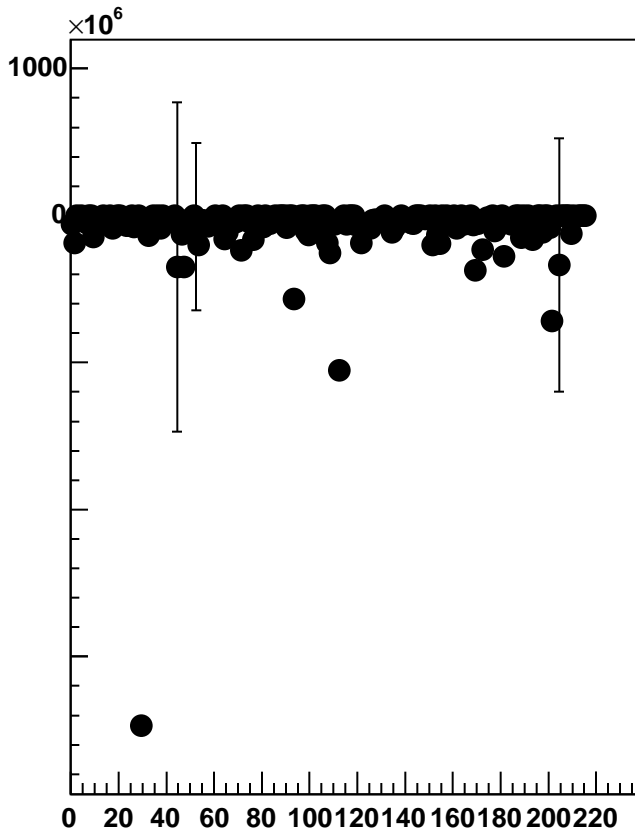
Disabled 0, DAC=4000, Fit Shaping Time vs 18°Chip+Chan



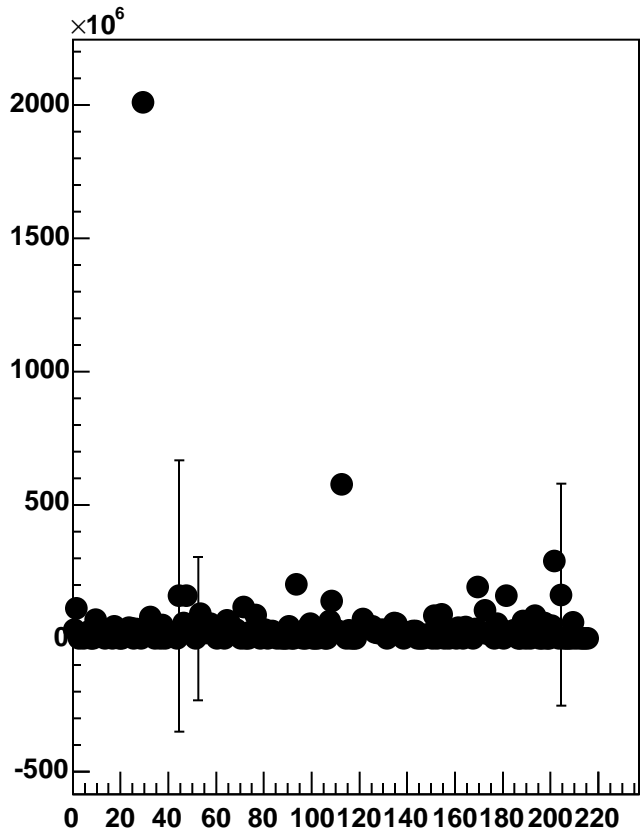
Disabled 1, DAC=4000, Fit Baseline vs 18°Chip+Chan



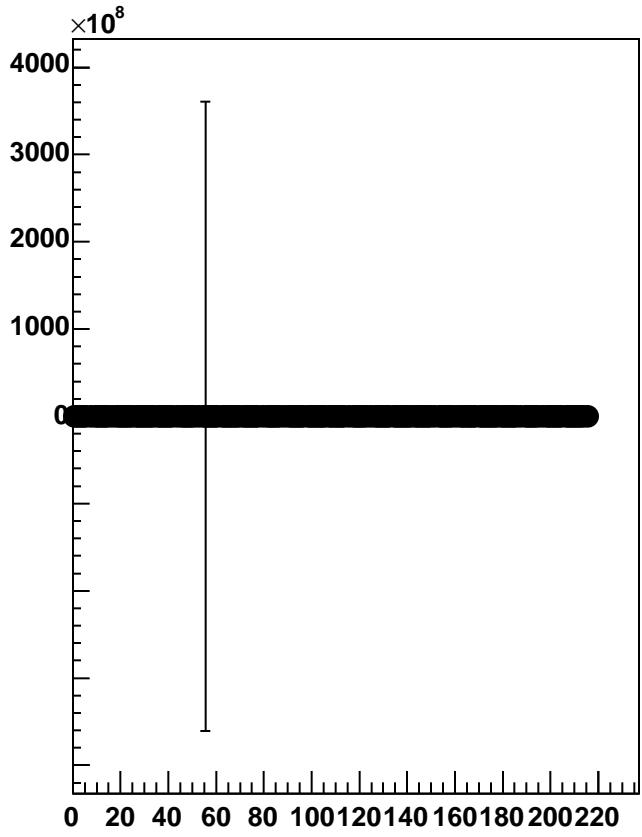
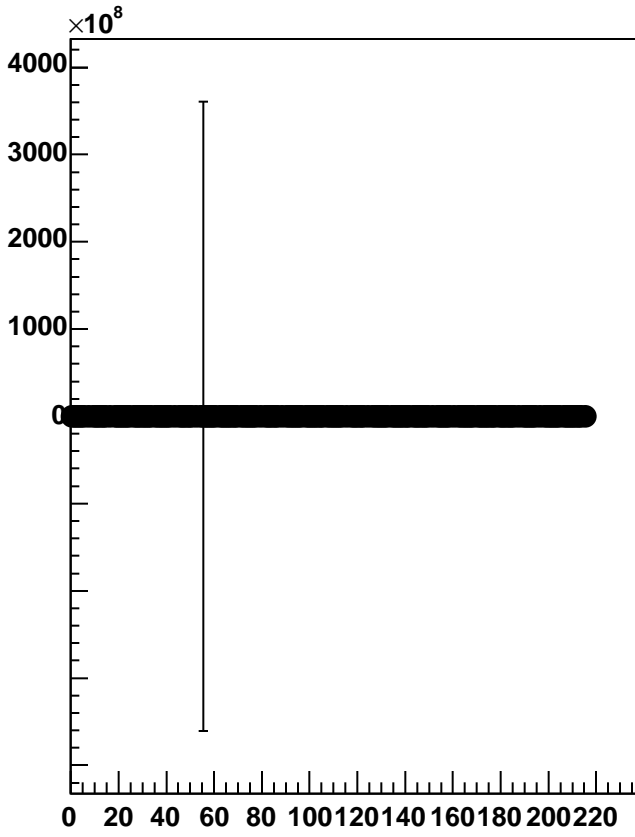
Disabled 1, DAC=4000, Fit Normalisation vs 18°Chip+Chan



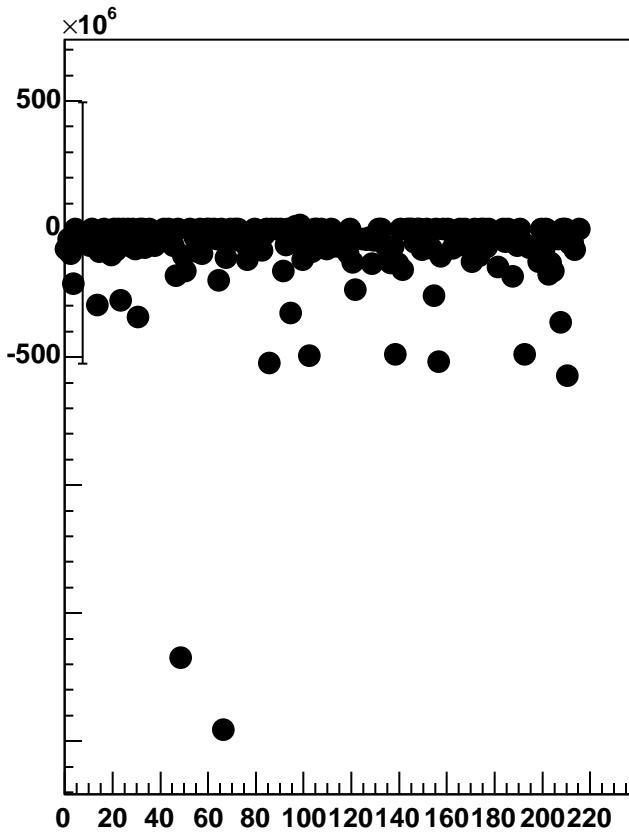
Disabled 1, DAC=4000, Fit Shaping Time vs 18°Chip+Chan



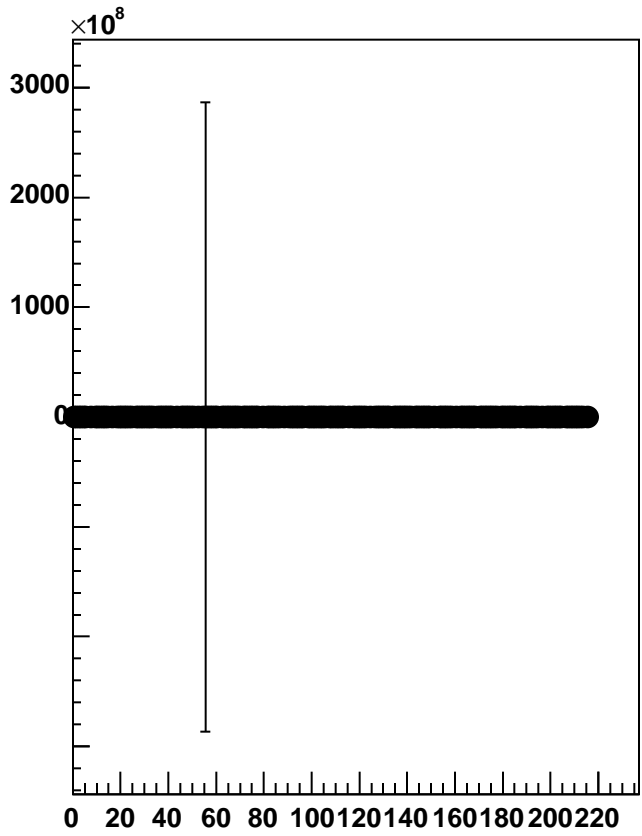
Disabled 2, DAC=4000, Fit Baseline vs 18°Chip+Chan



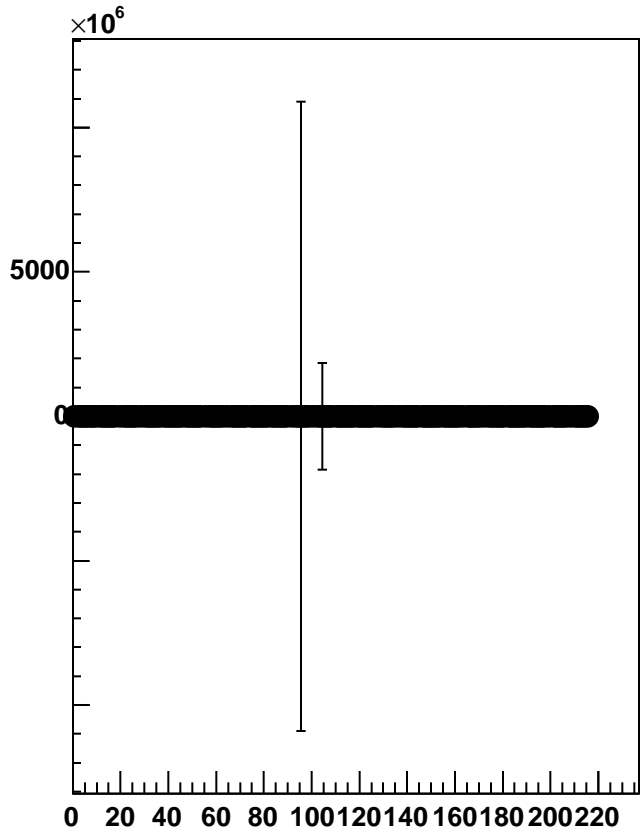
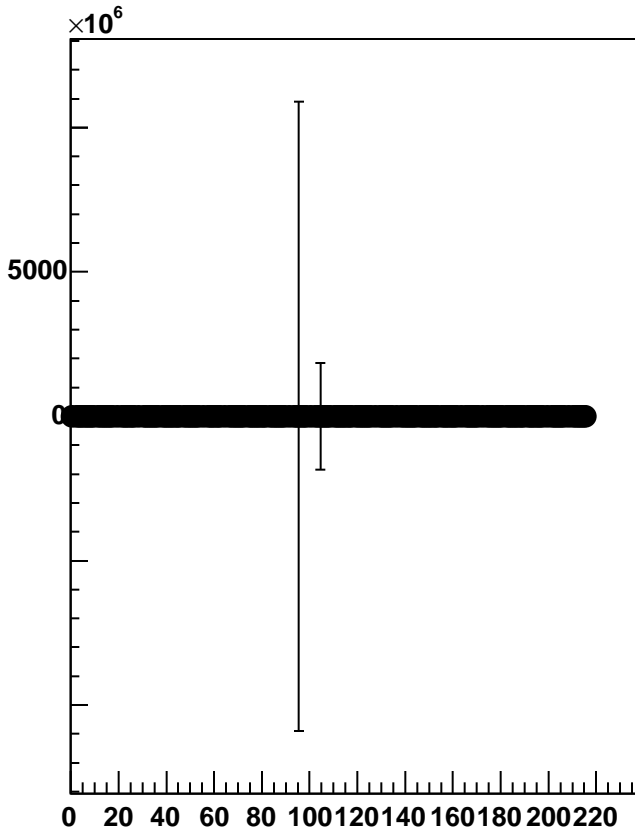
Disabled 2, DAC=4000, Fit Normalisation vs 18°Chip+Chan



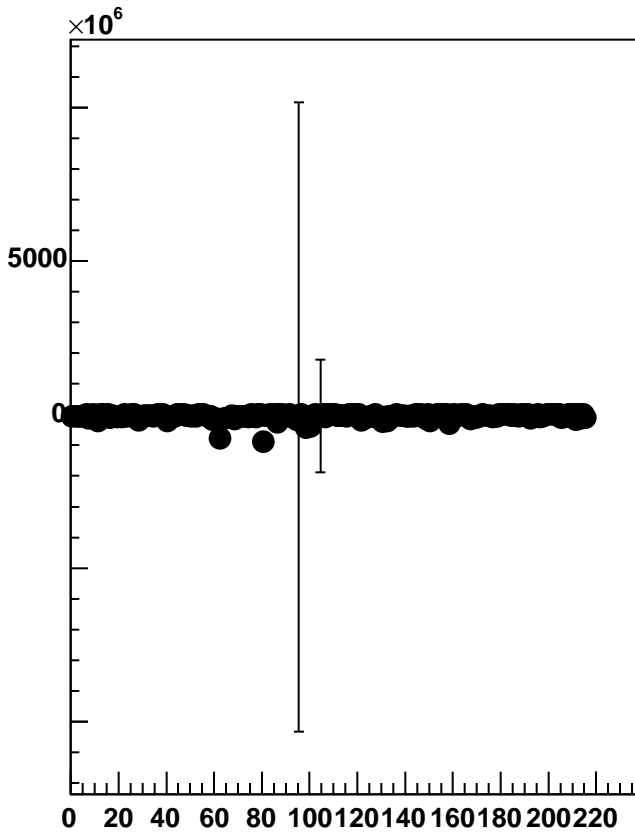
Disabled 2, DAC=4000, Fit Shaping Time vs 18°Chip+Chan



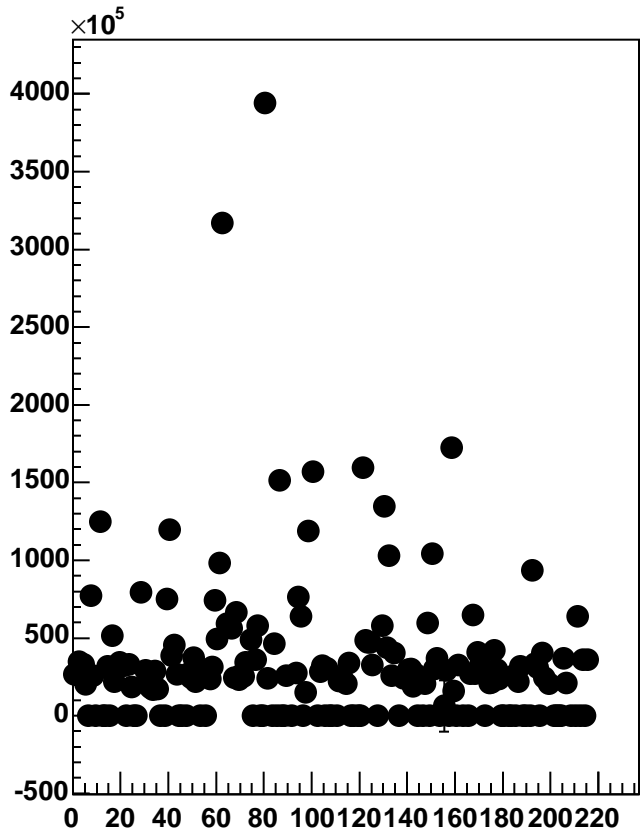
Disabled 3, DAC=4000, Fit Baseline vs 18°Chip+Chan



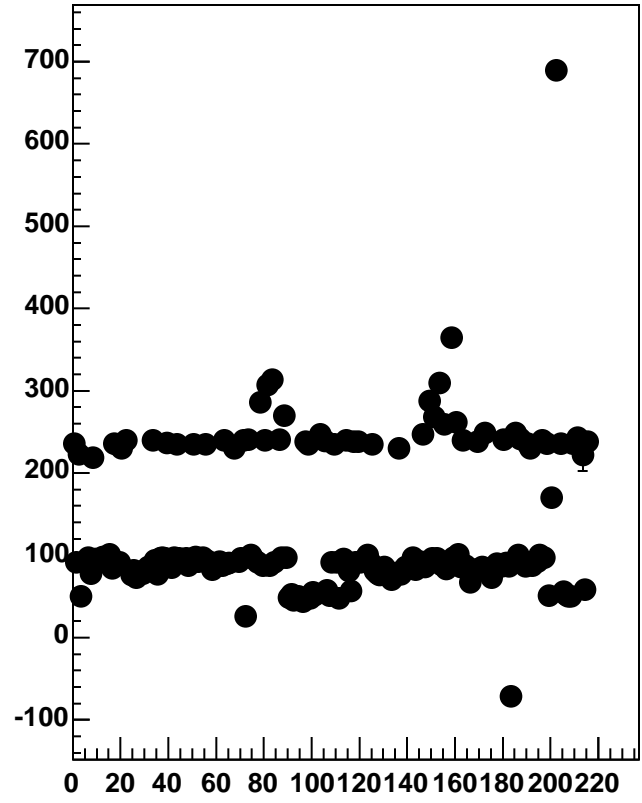
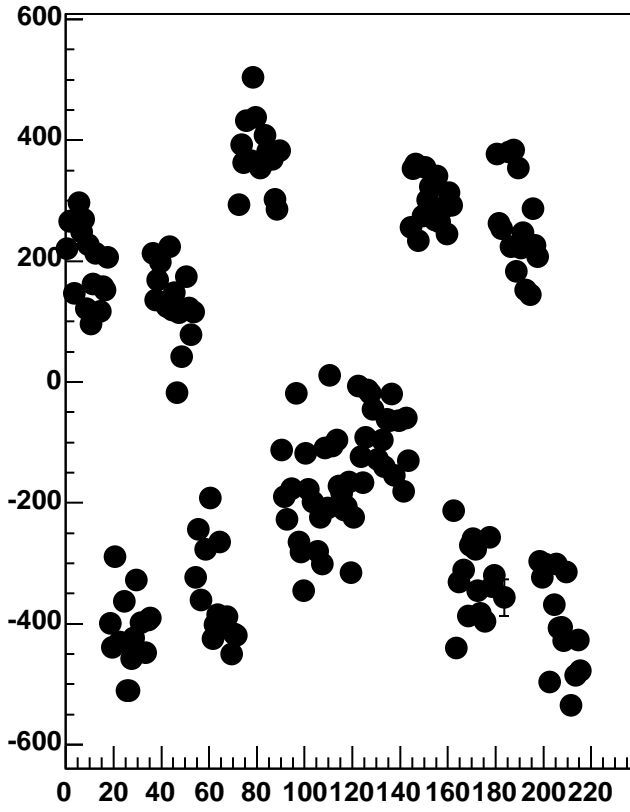
Disabled 3, DAC=4000, Fit Normalisation vs 18°Chip+Chan



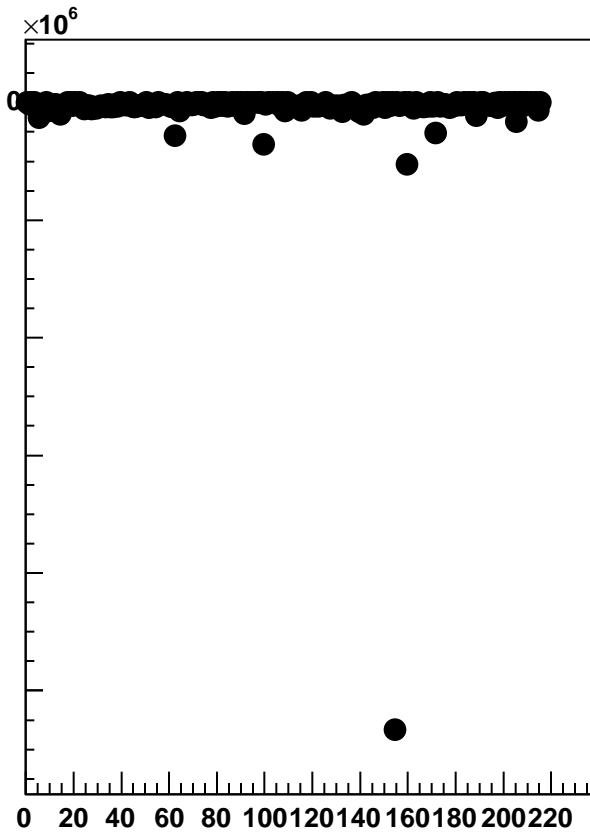
Disabled 3, DAC=4000, Fit Shaping Time vs 18°Chip+Chan



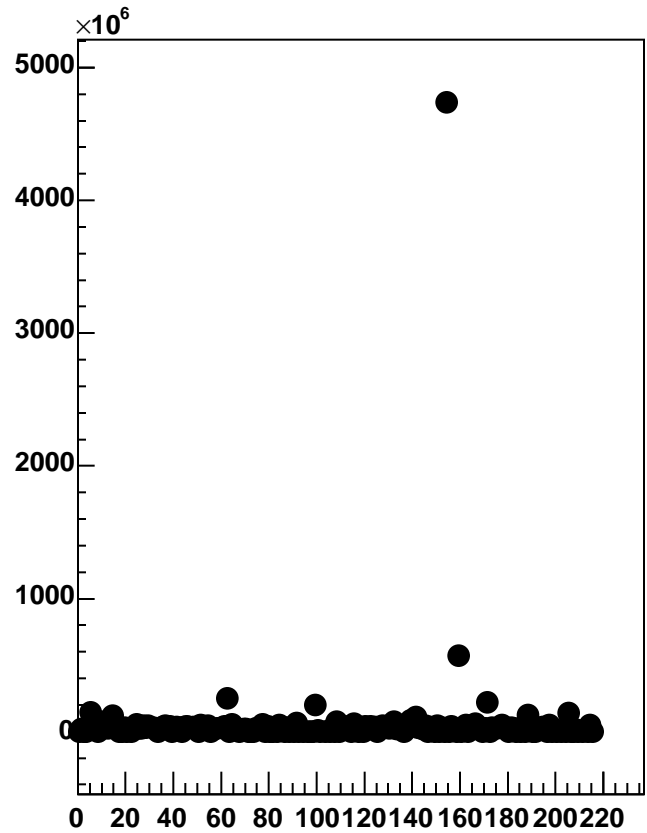
Disabled 4, DAC=4000, Fit Baseline vs 18*Chip+Chan



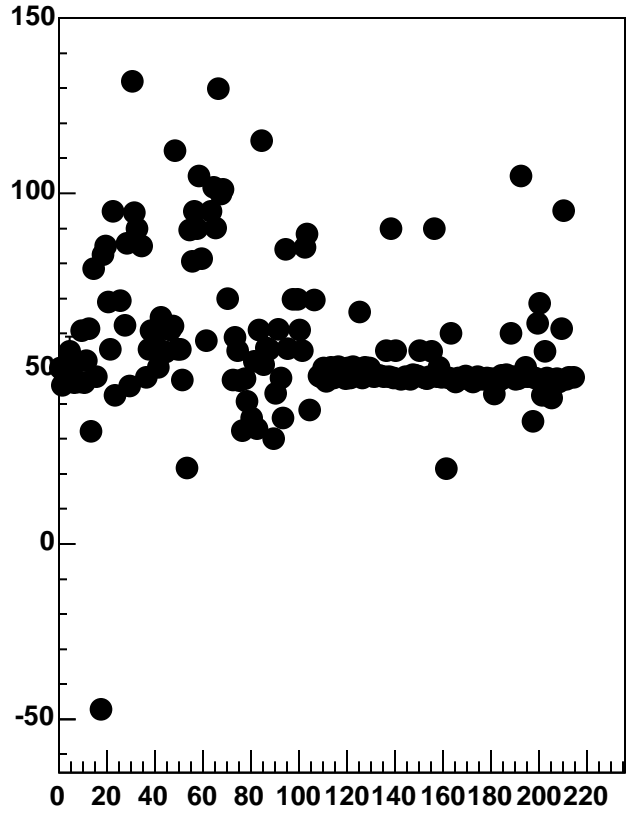
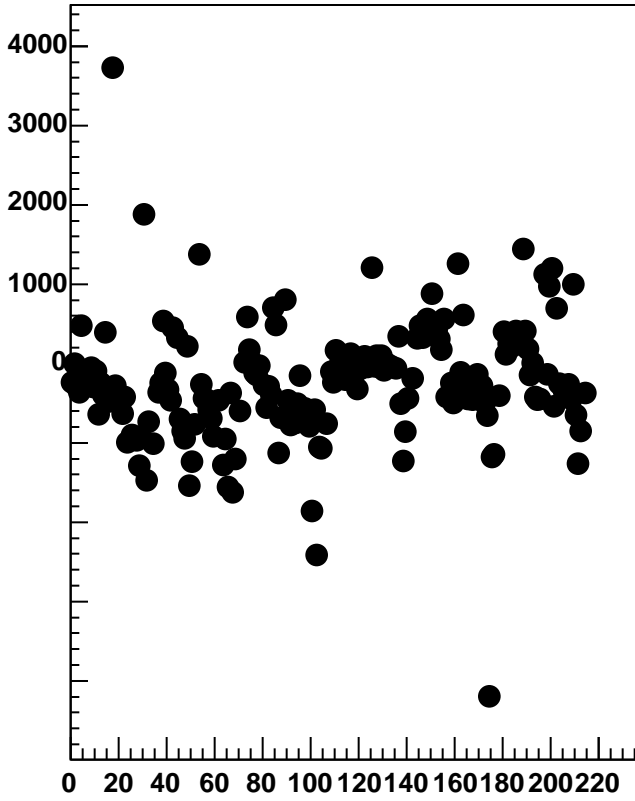
Disabled 4, DAC=4000, Fit Normalisation vs 18*Chip+Chan



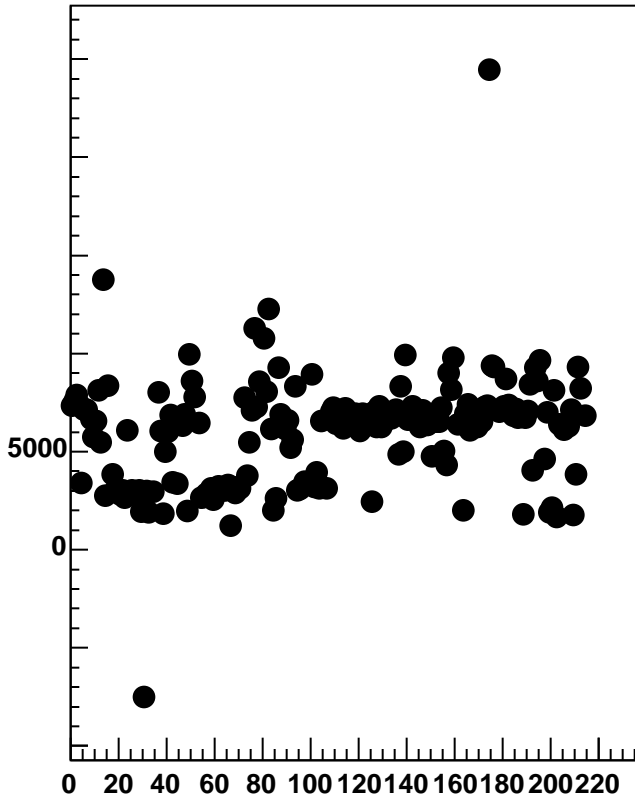
Disabled 4, DAC=4000, Fit Shaping Time vs 18*Chip+Chan



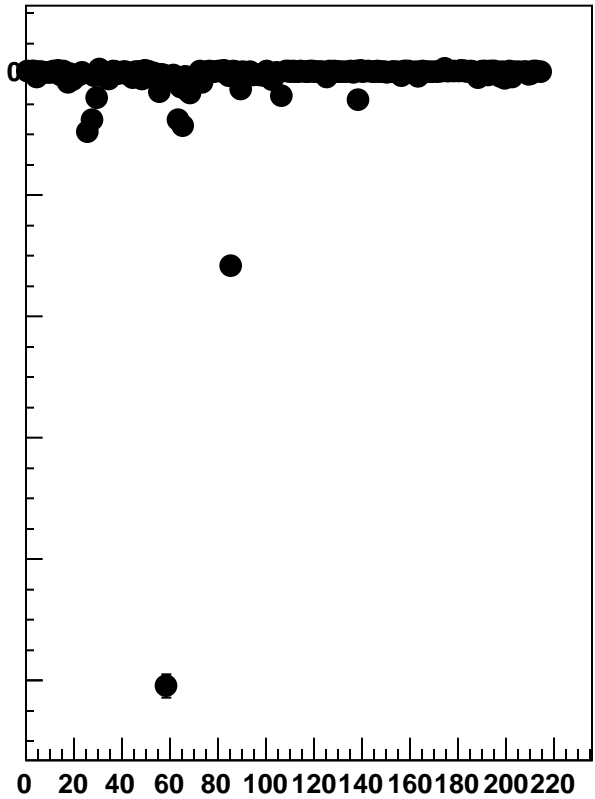
Disabled 5, DAC=4000, Fit Baseline vs 18°Chip+Chan



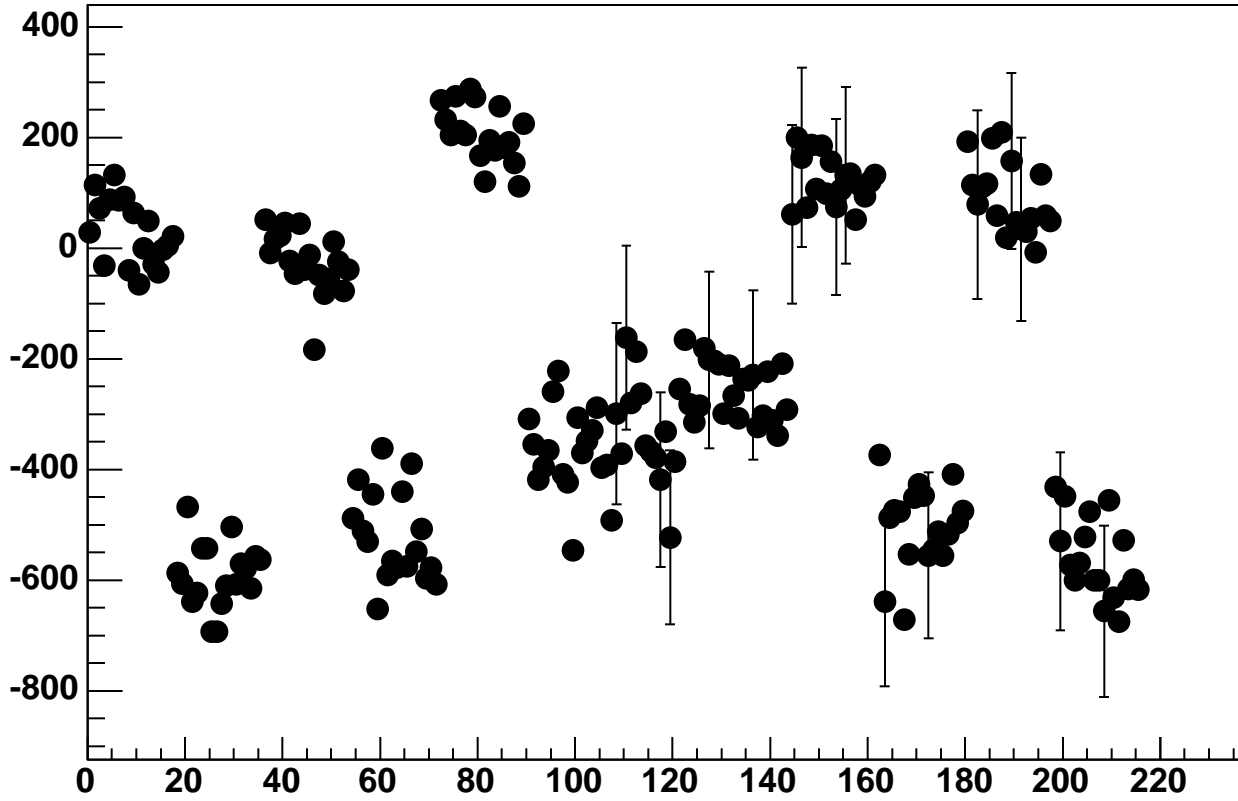
Disabled 5, DAC=4000, Fit Normalisation vs 18°Chip+Chan



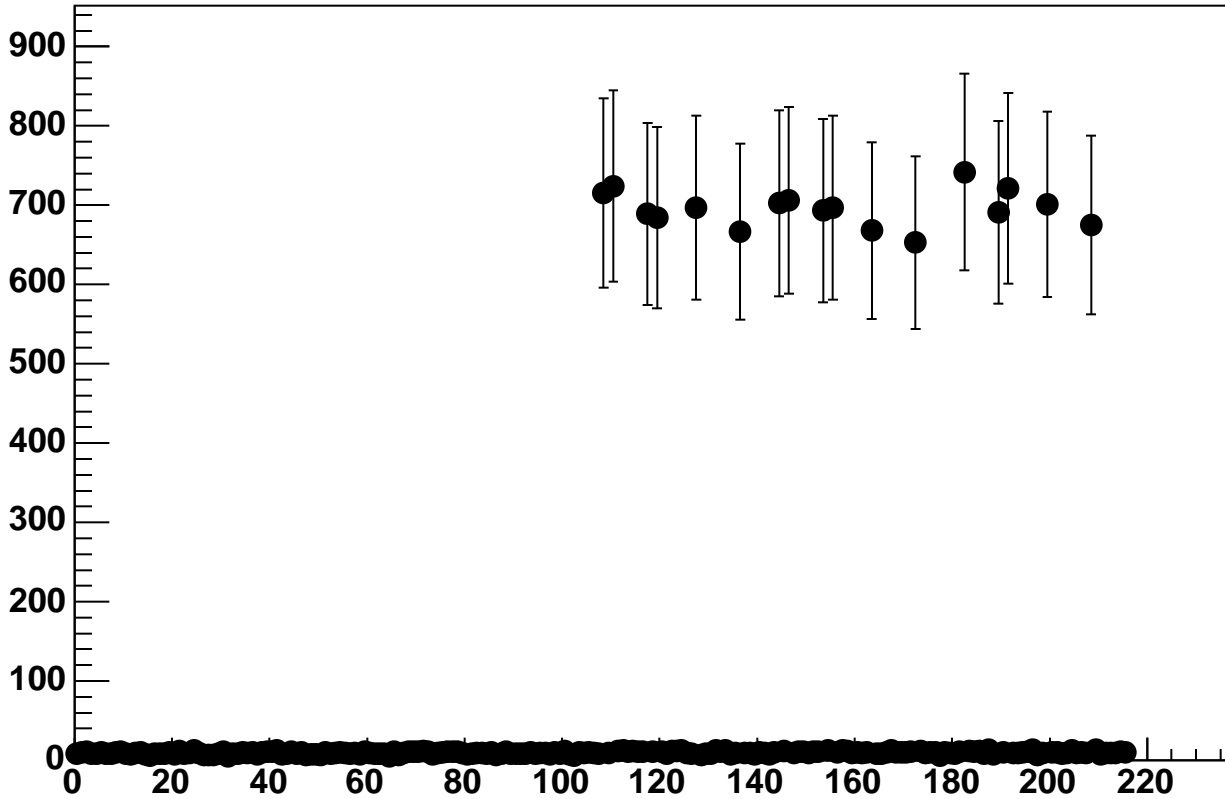
Disabled 5, DAC=4000, Fit Shaping Time vs 18°Chip+Chan



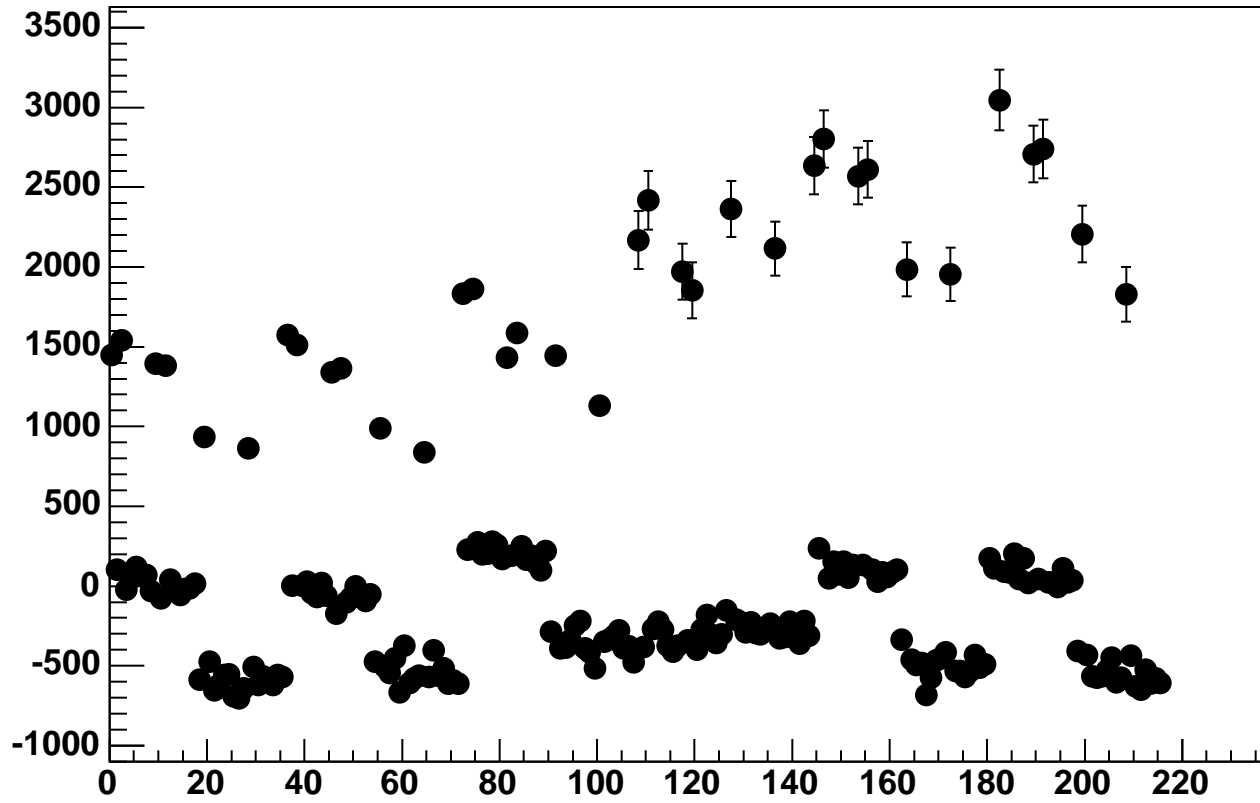
Enable 0, DAC=4000, Hold=0, ADC Mean vs 18*Chip+Chan



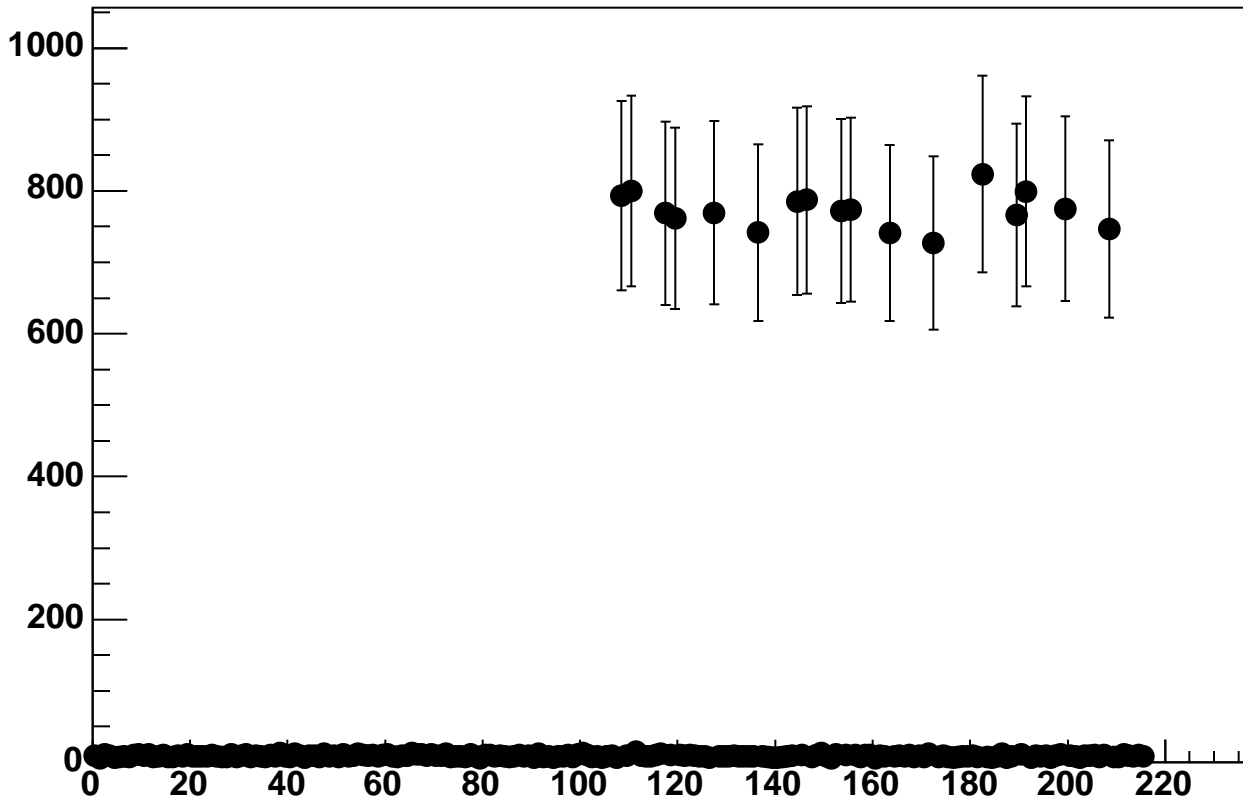
Enable 0, DAC=4000, Hold=0, ADC Noise vs 18*Chip+Chan



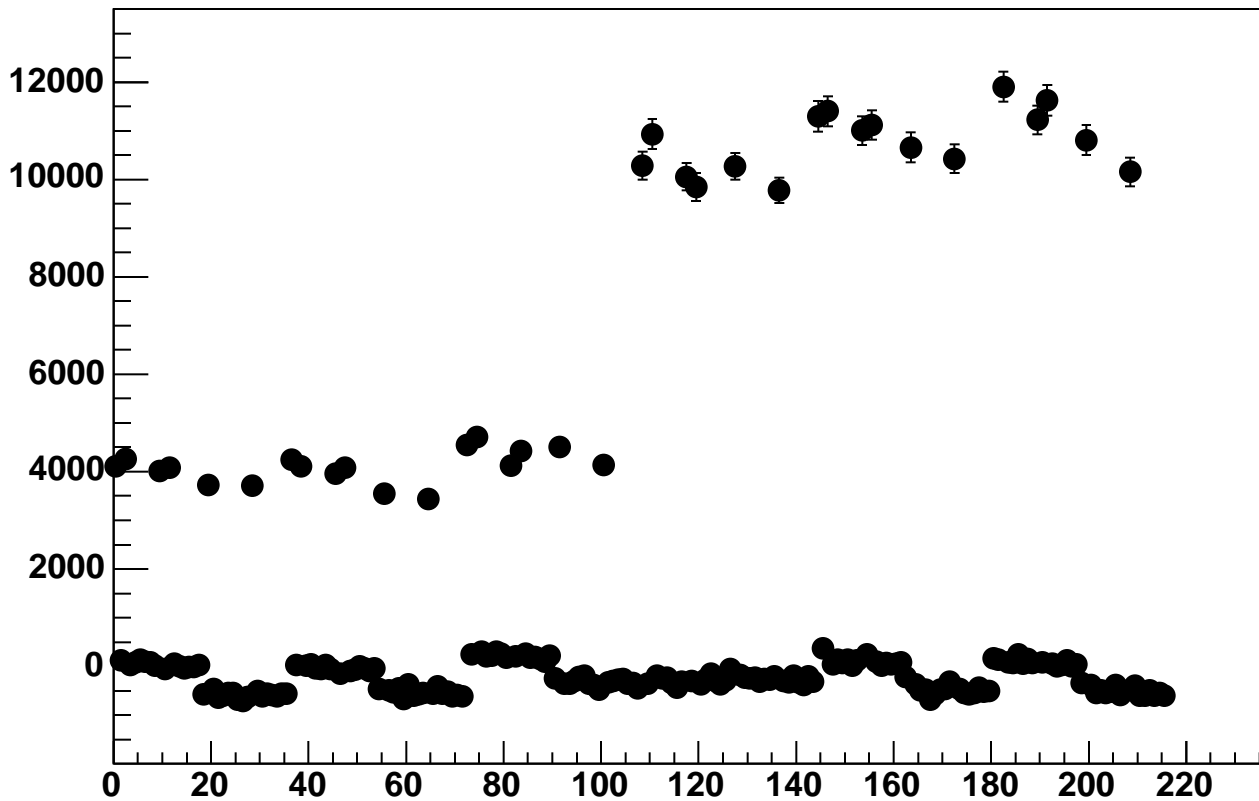
Enable 0, DAC=4000, Hold=5, ADC Mean vs 18*Chip+Chan



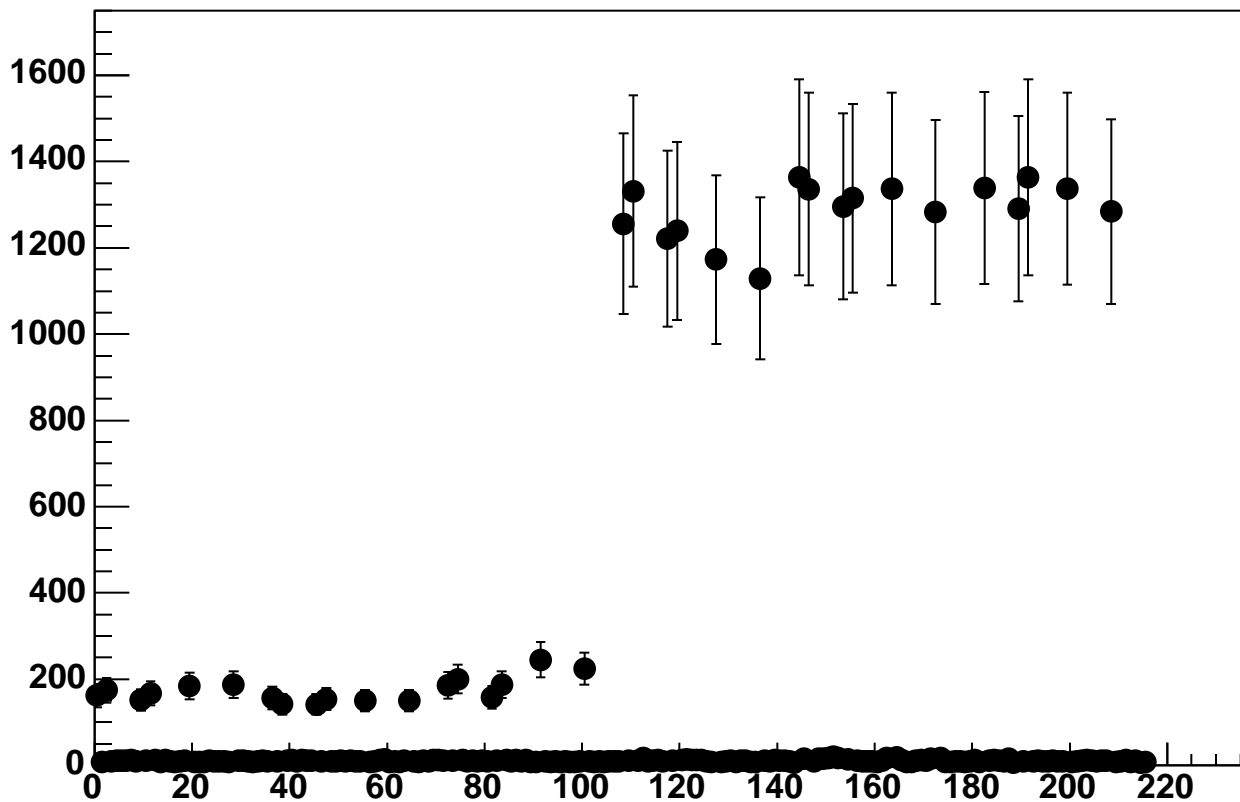
Enable 0, DAC=4000, Hold=5, ADC Noise vs 18*Chip+Chan



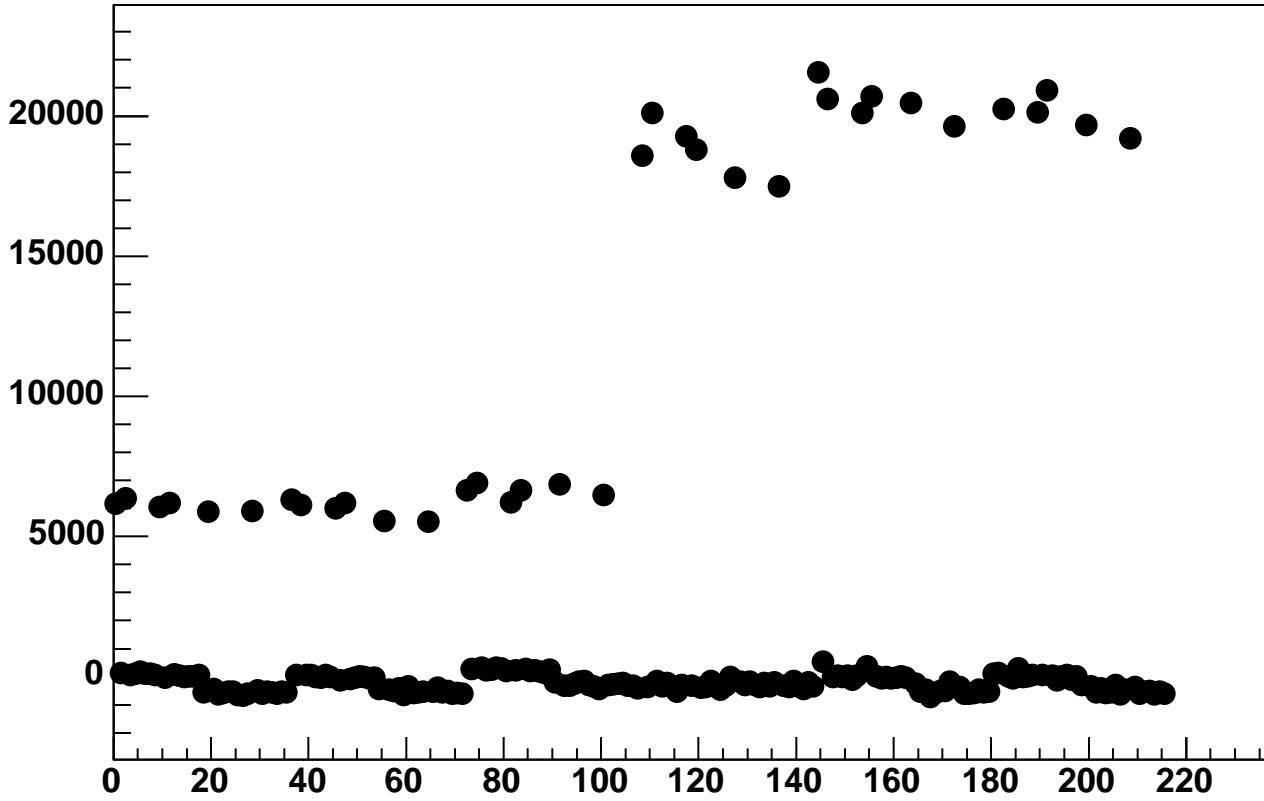
Enable 0, DAC=4000, Hold=10, ADC Mean vs 18*Chip+Chan



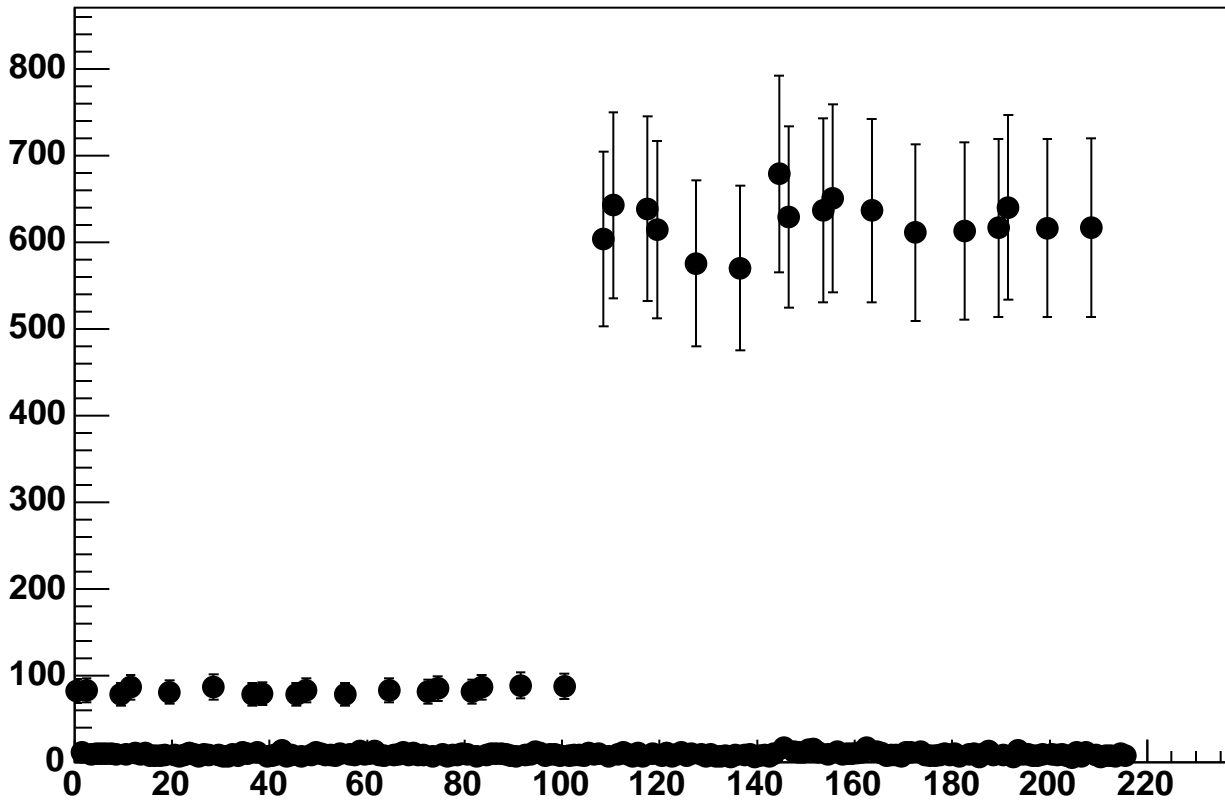
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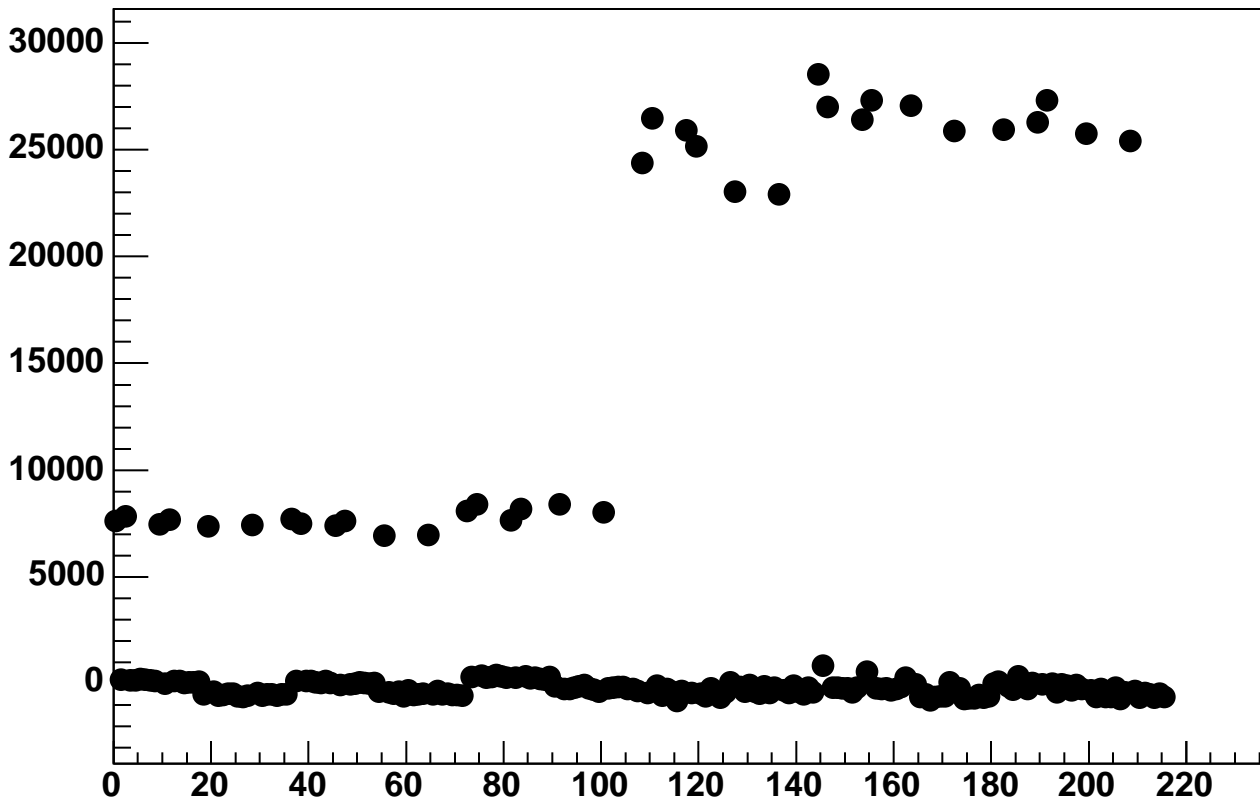
Enable 0, DAC=4000, Hold=15, ADC Mean vs 18*Chip+Chan



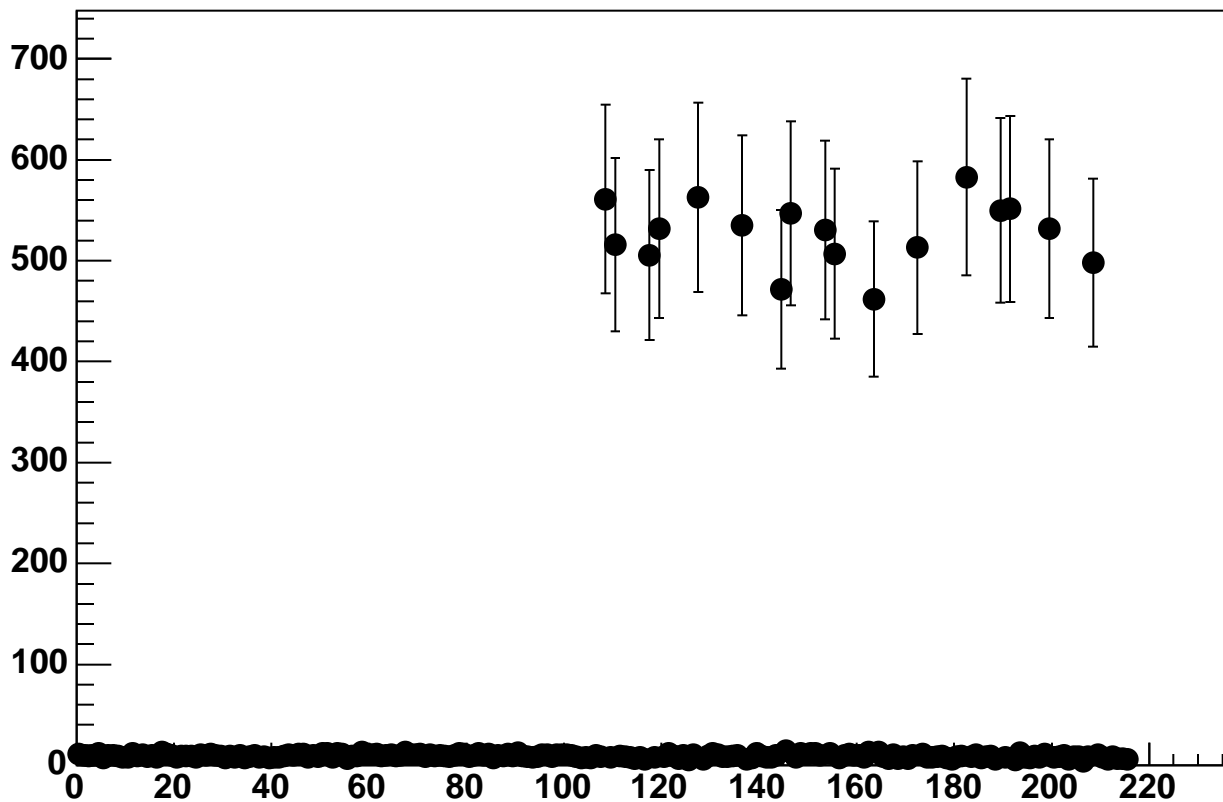
Enable 0, DAC=4000, Hold=15, ADC Noise vs 18*Chip+Chan



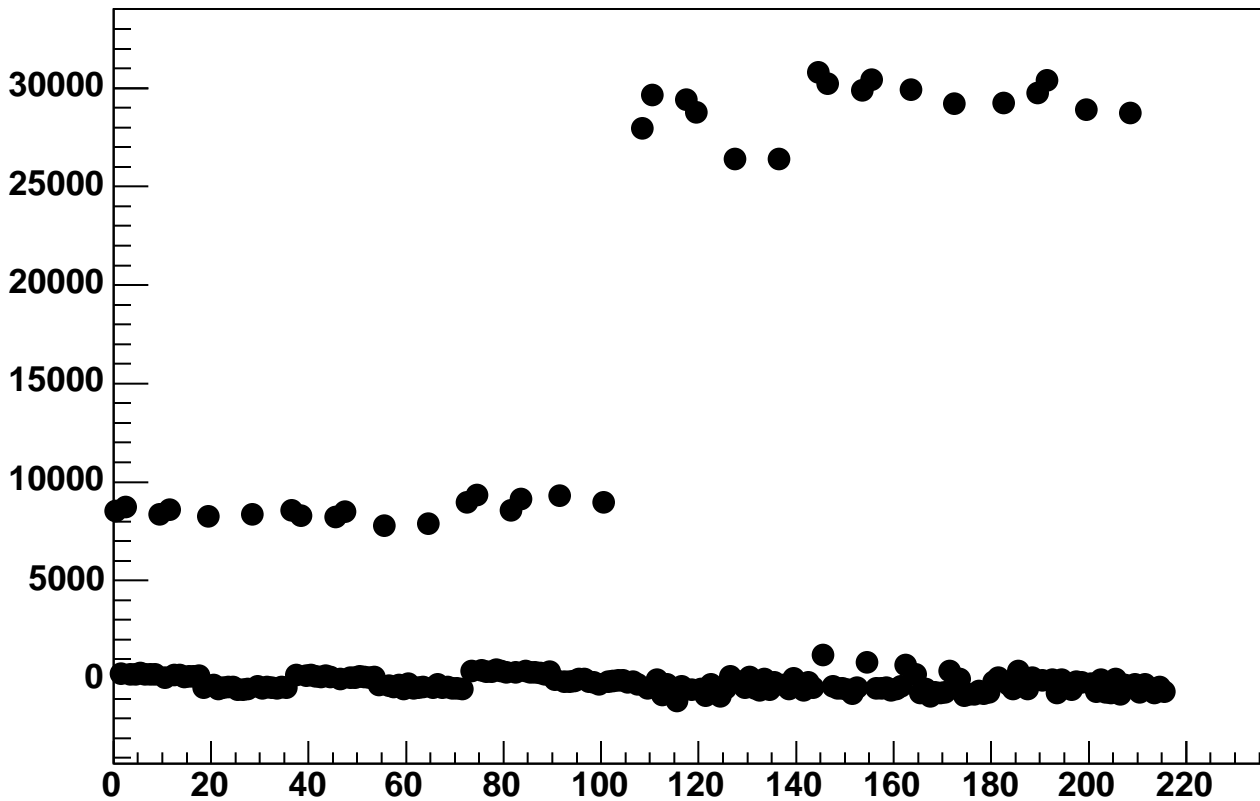
Enable 0, DAC=4000, Hold=20, ADC Mean vs 18*Chip+Chan



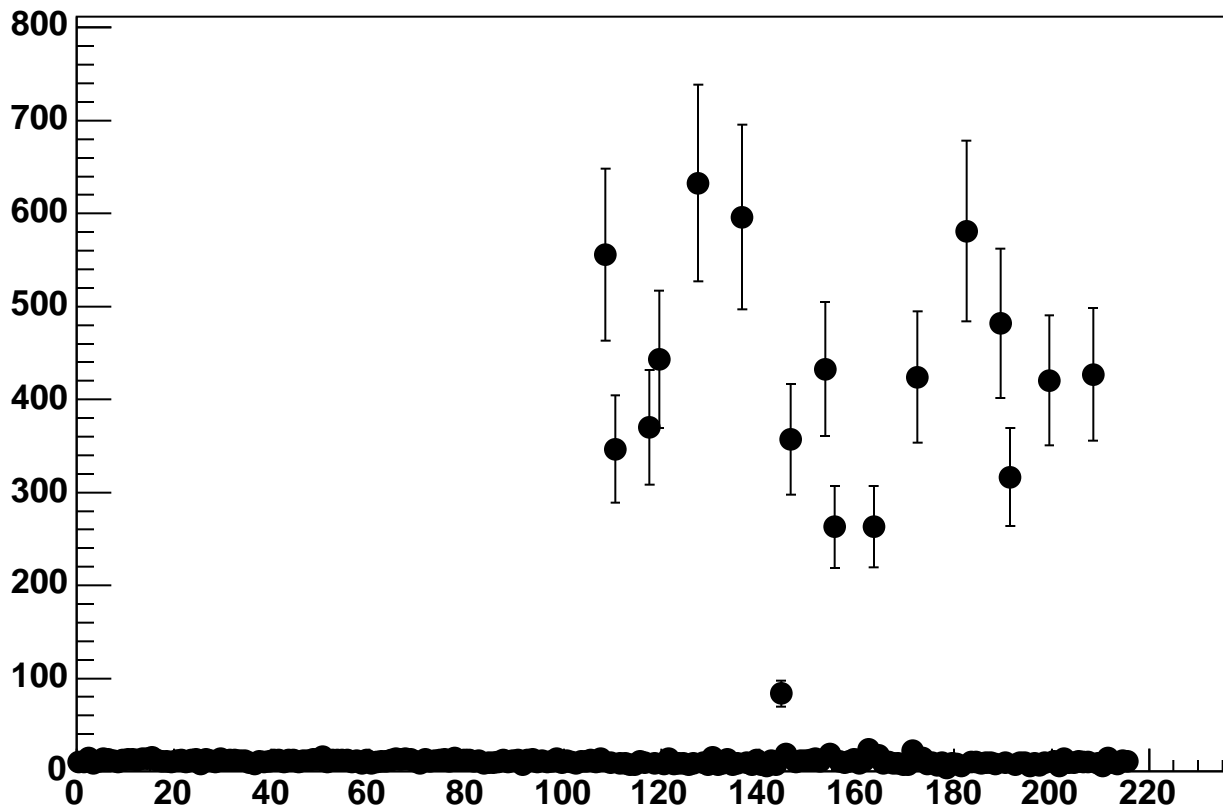
Enable 0, DAC=4000, Hold=20, ADC Noise vs 18*Chip+Chan



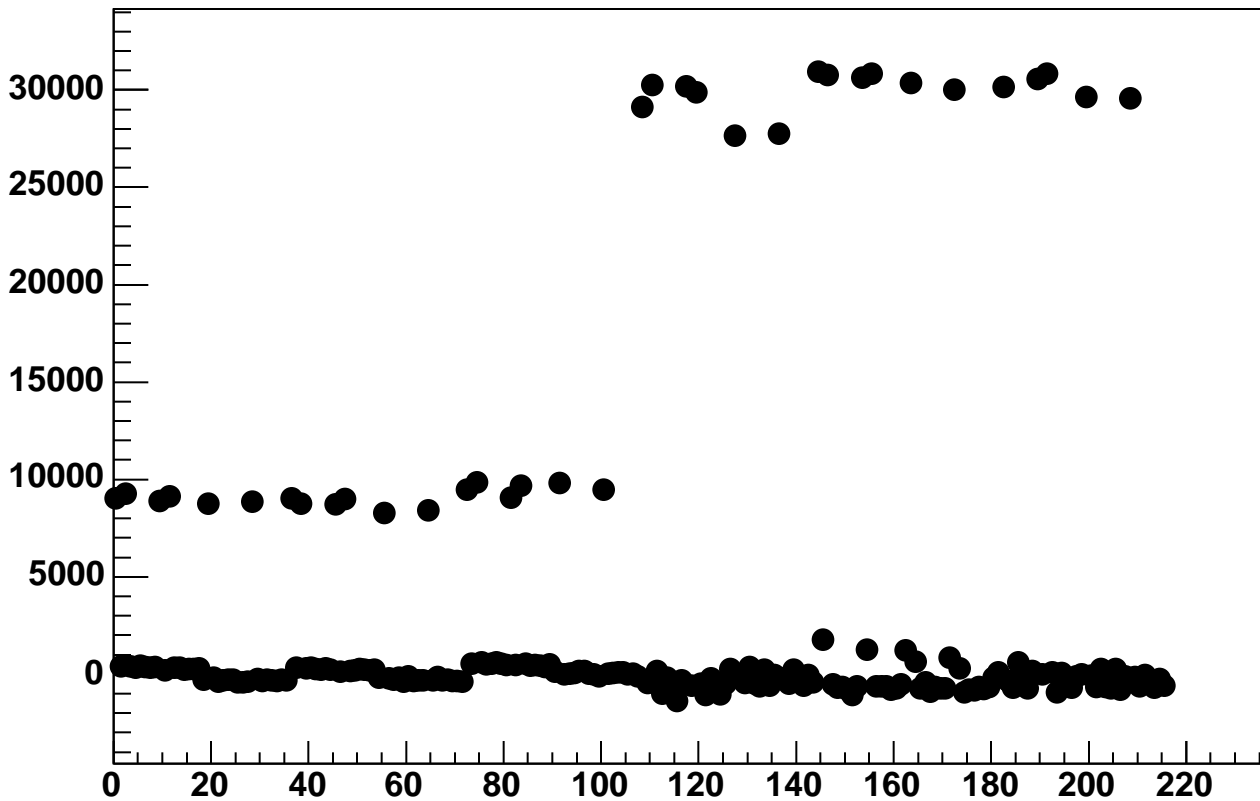
Enable 0, DAC=4000, Hold=25, ADC Mean vs 18*Chip+Chan



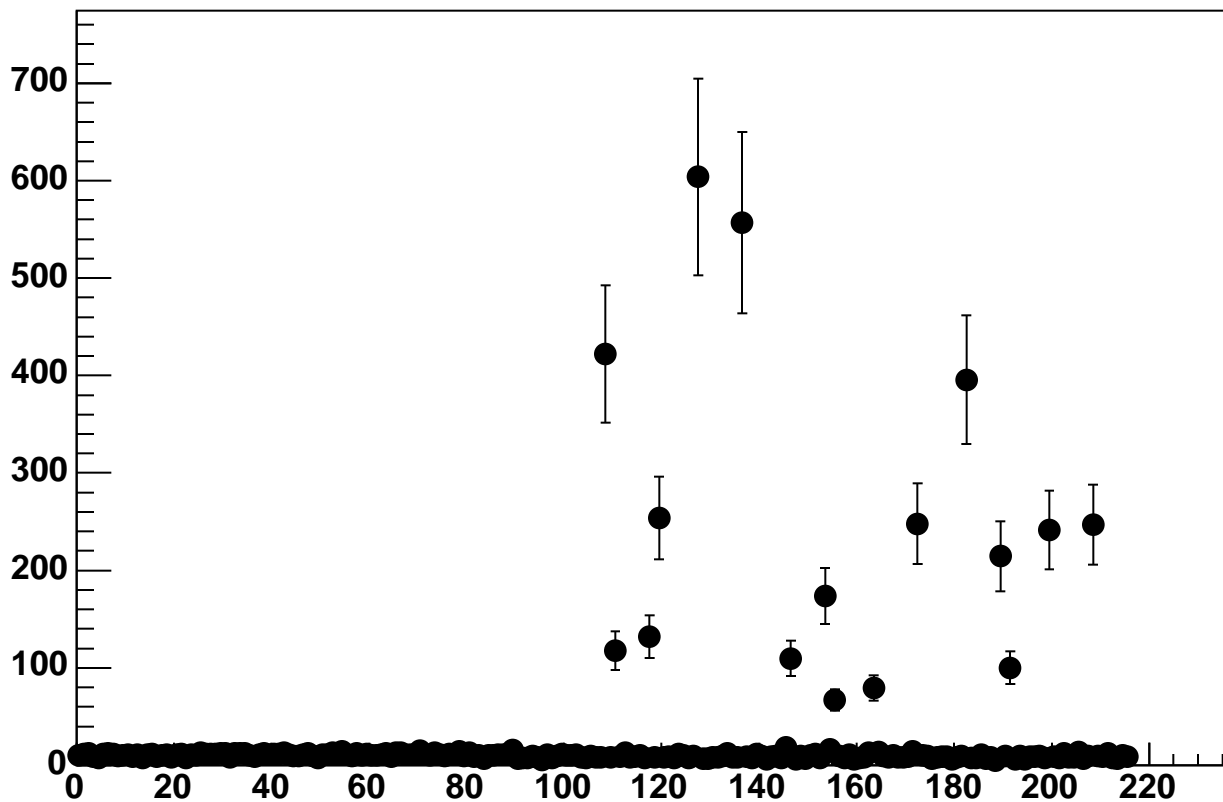
Enable 0, DAC=4000, Hold=25, ADC Noise vs 18*Chip+Chan



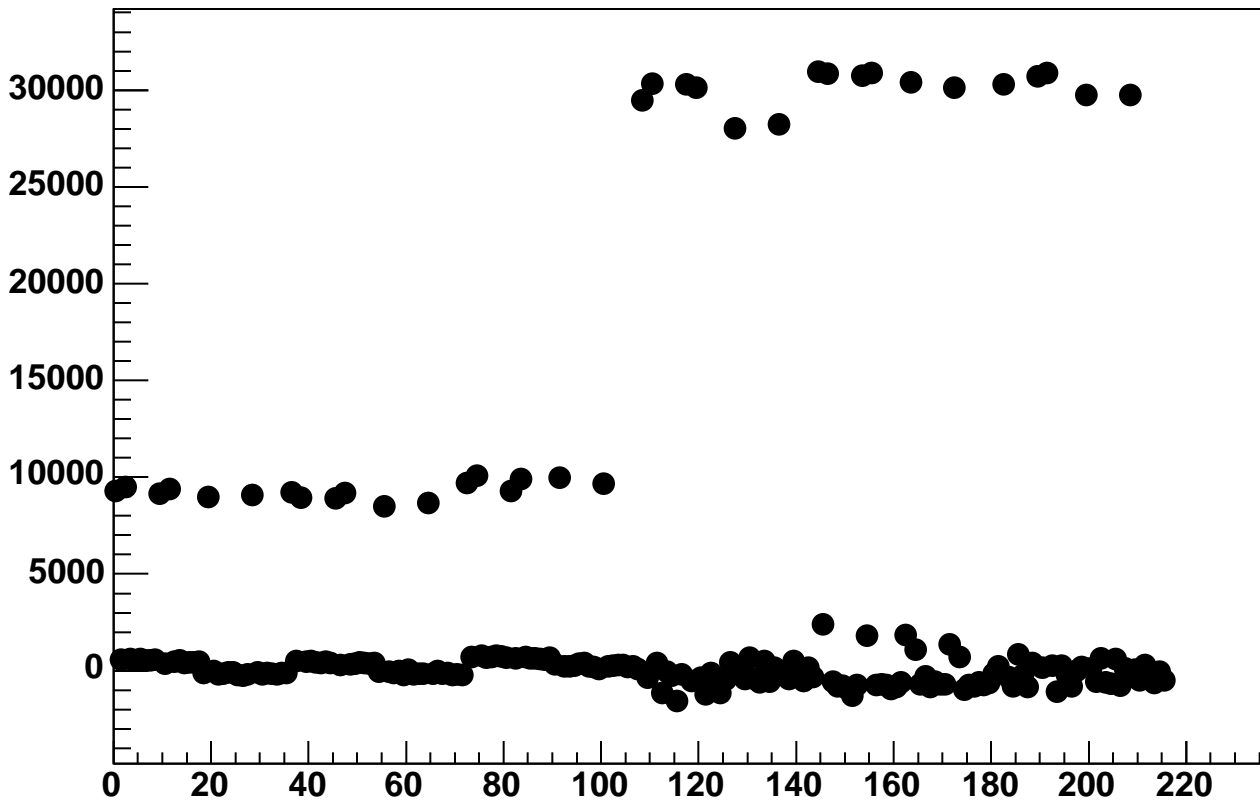
Enable 0, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



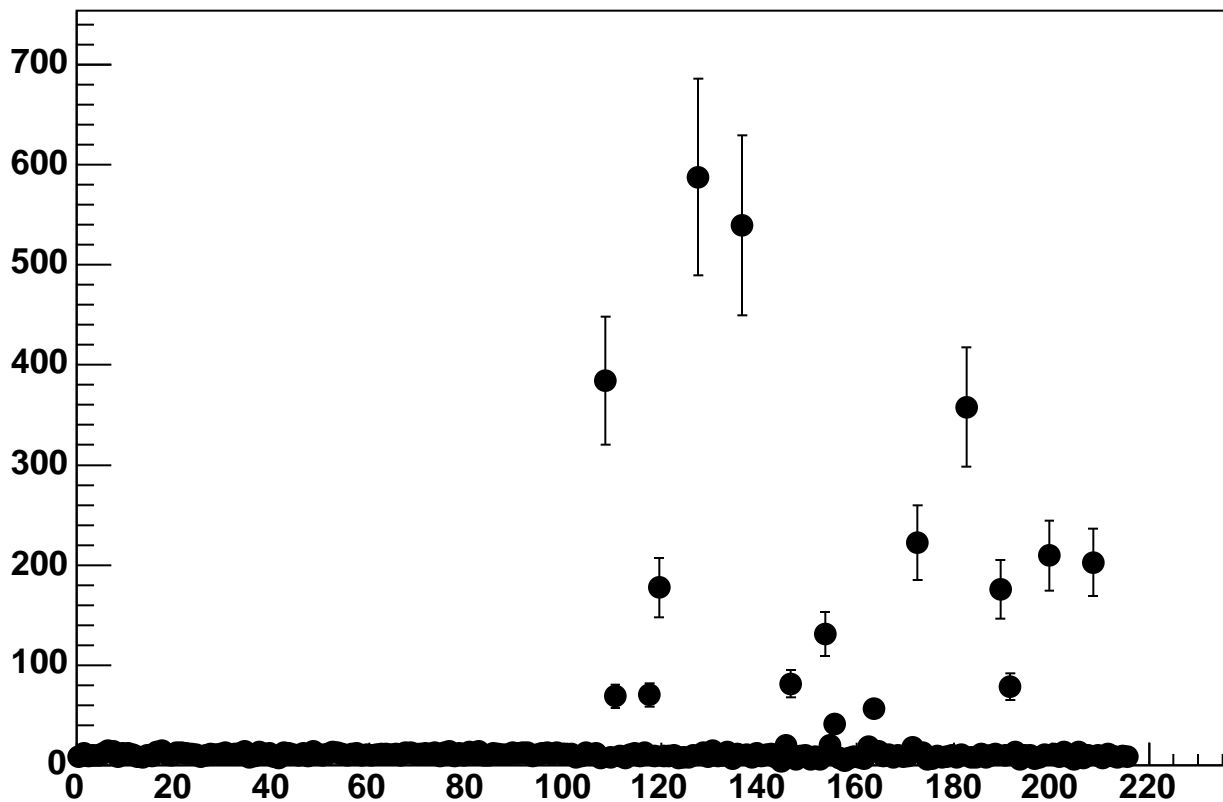
Enable 0, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



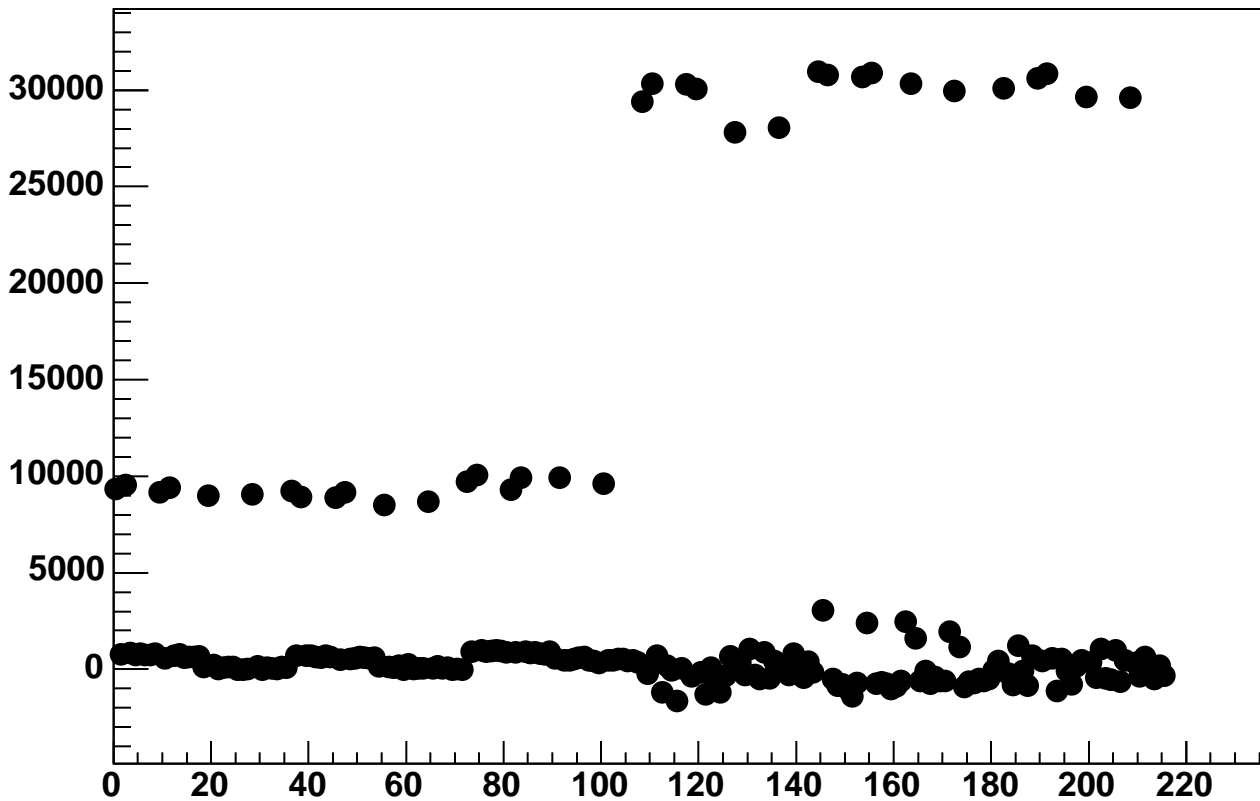
Enable 0, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



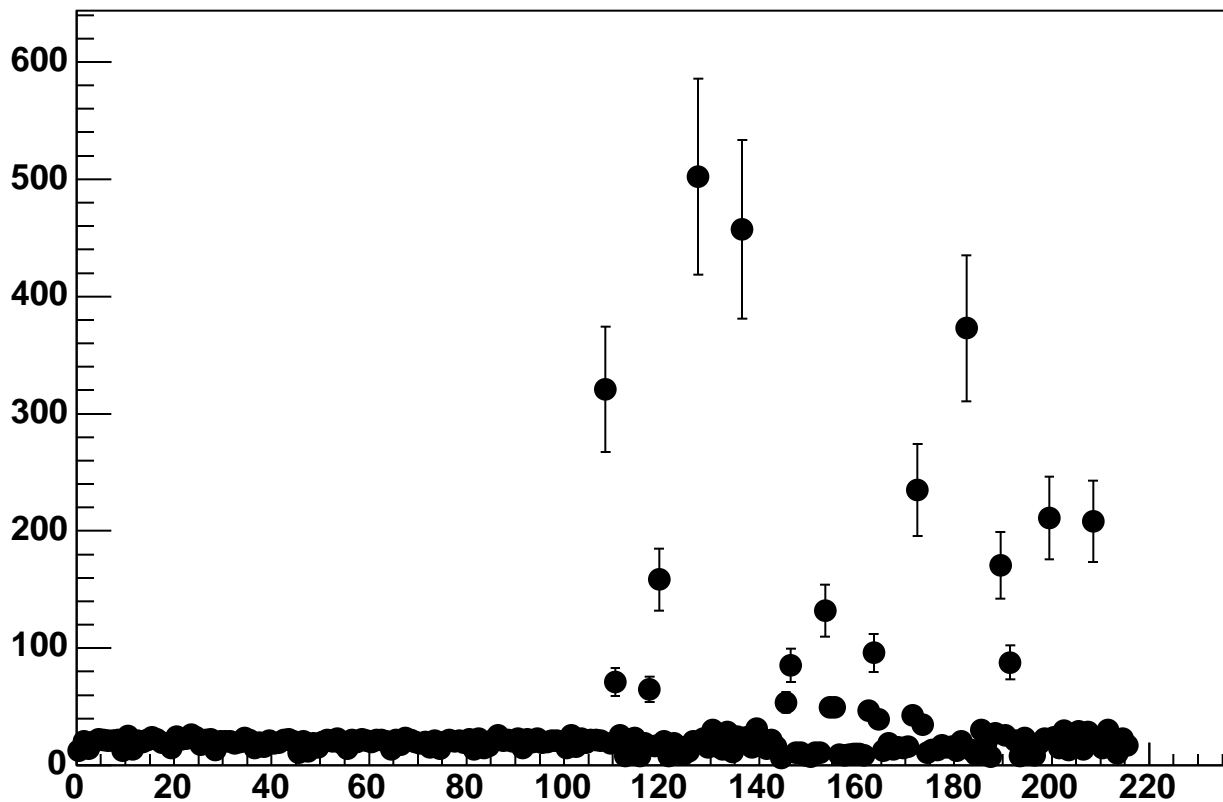
Enable 0, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



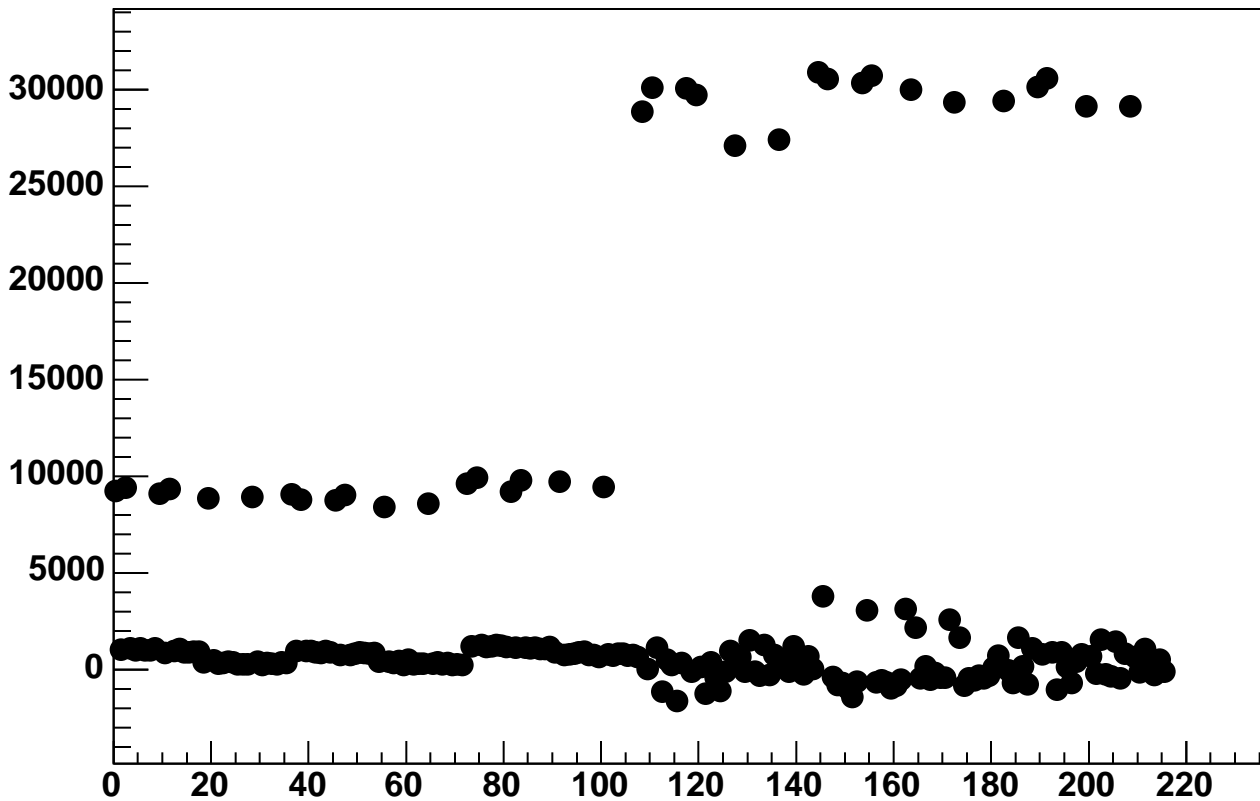
Enable 0, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



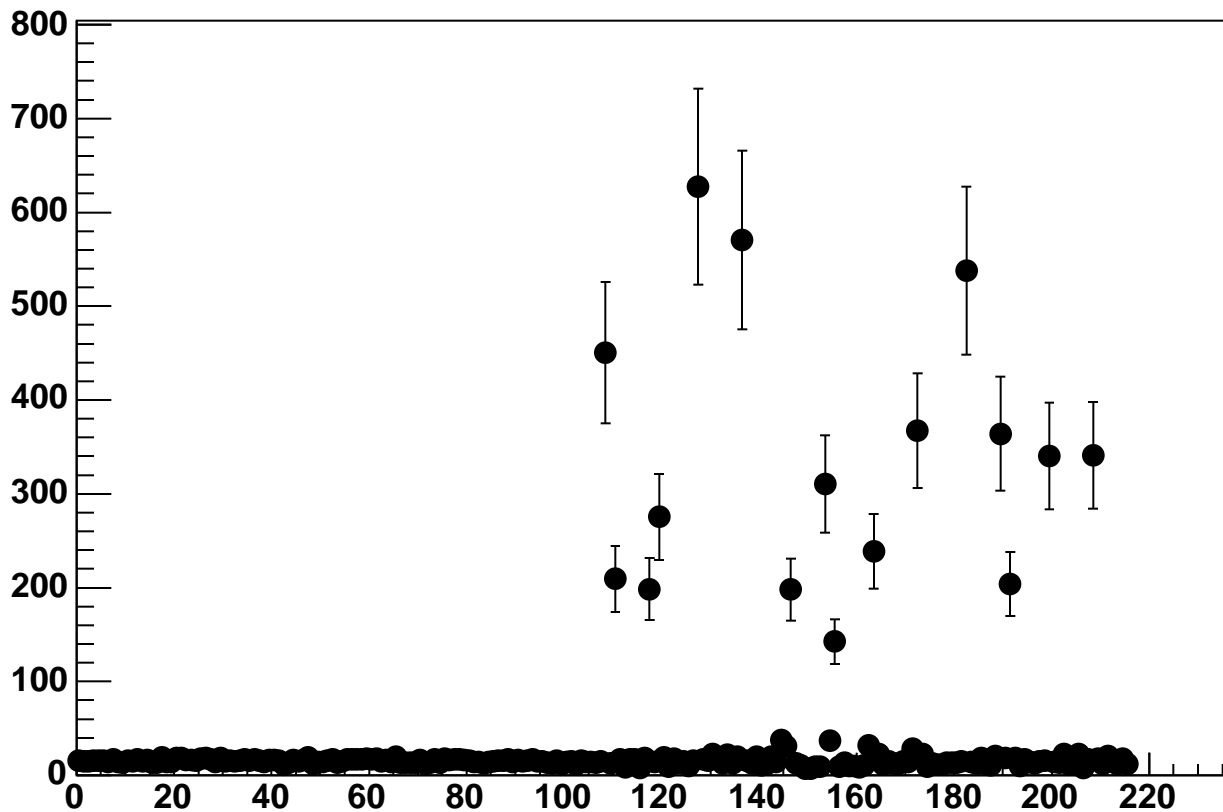
Enable 0, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



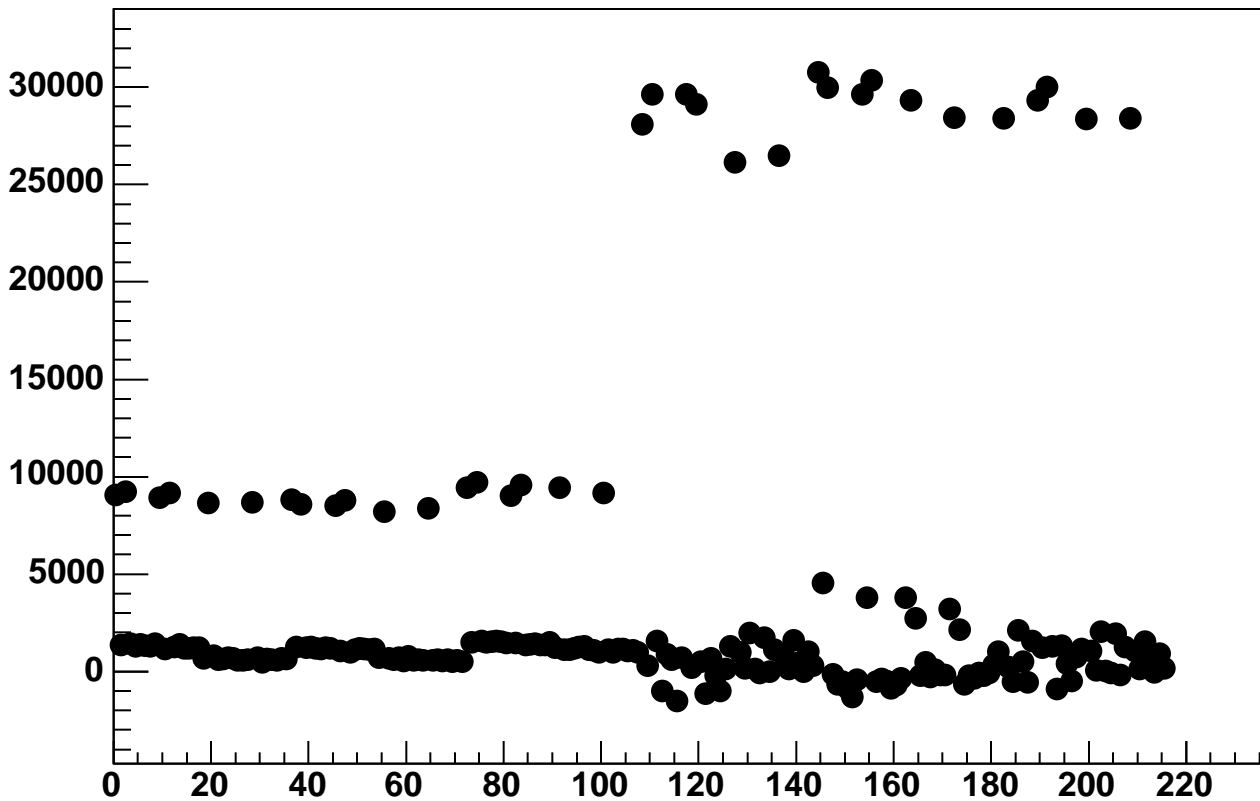
Enable 0, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



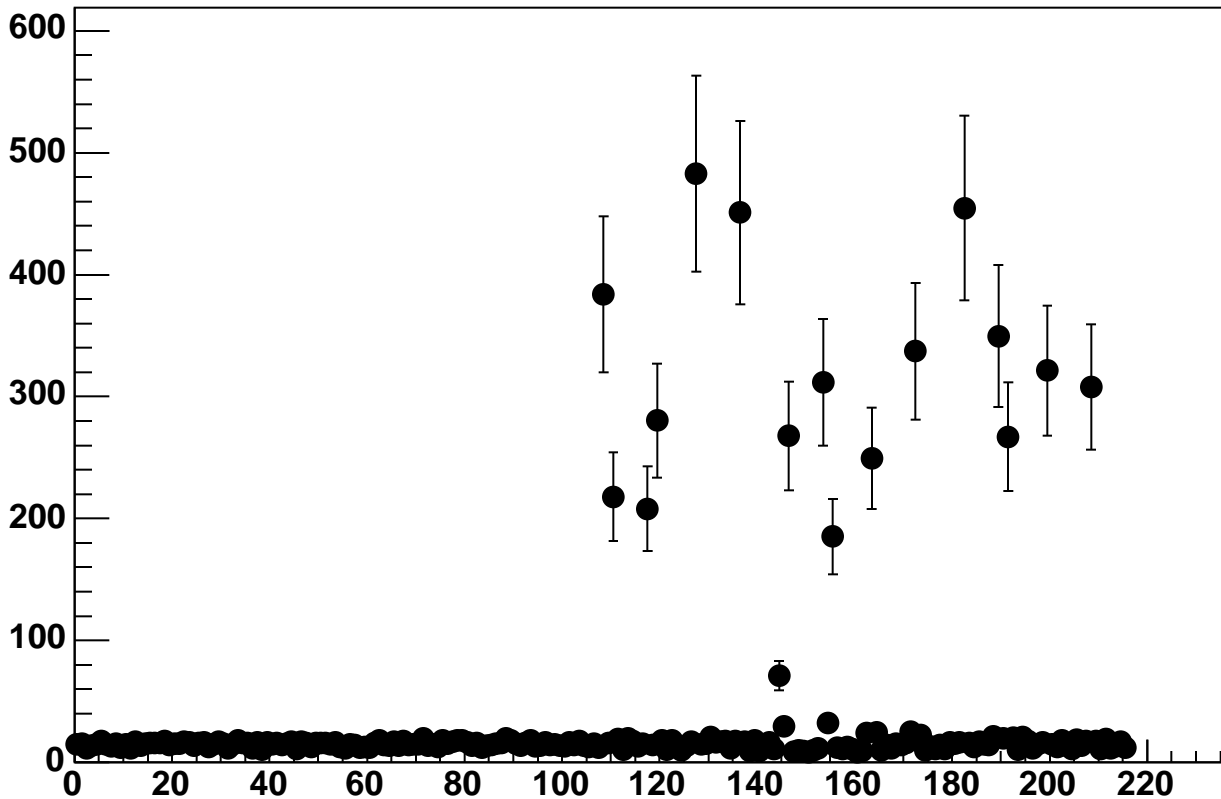
Enable 0, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



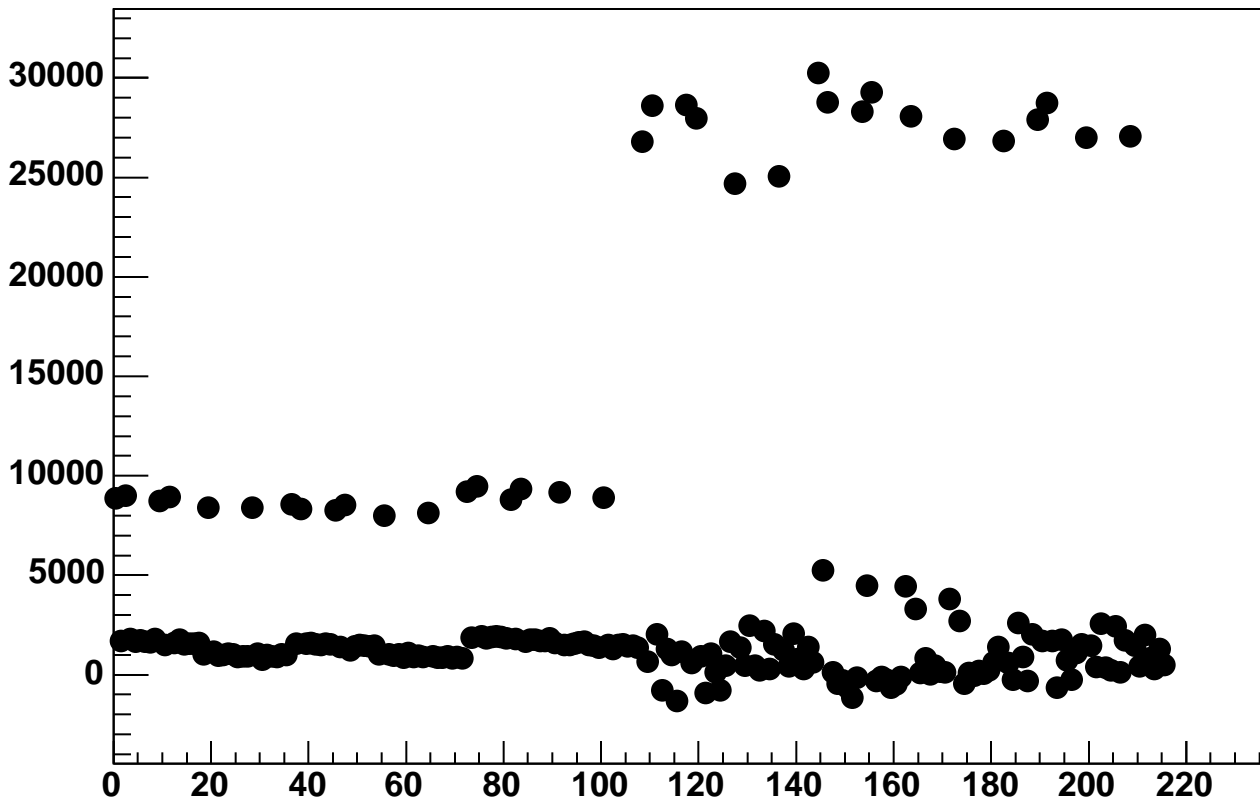
Enable 0, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



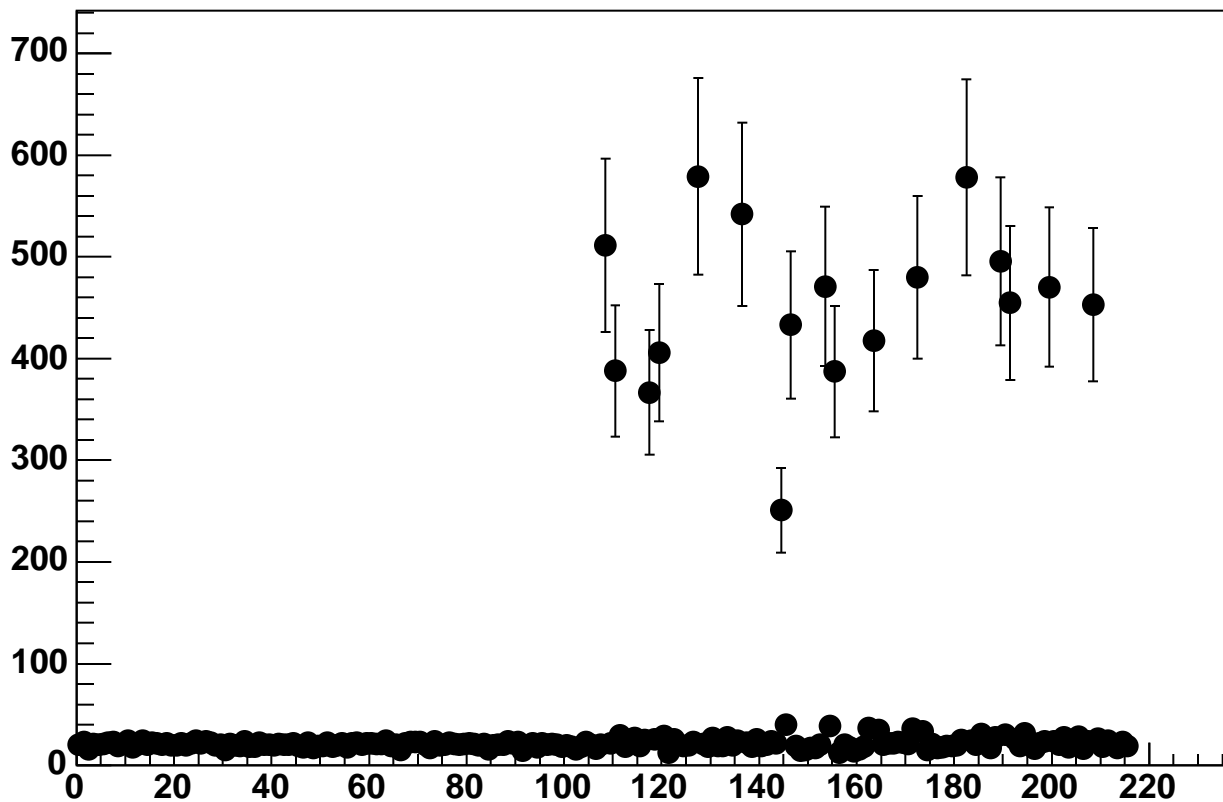
Enable 0, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



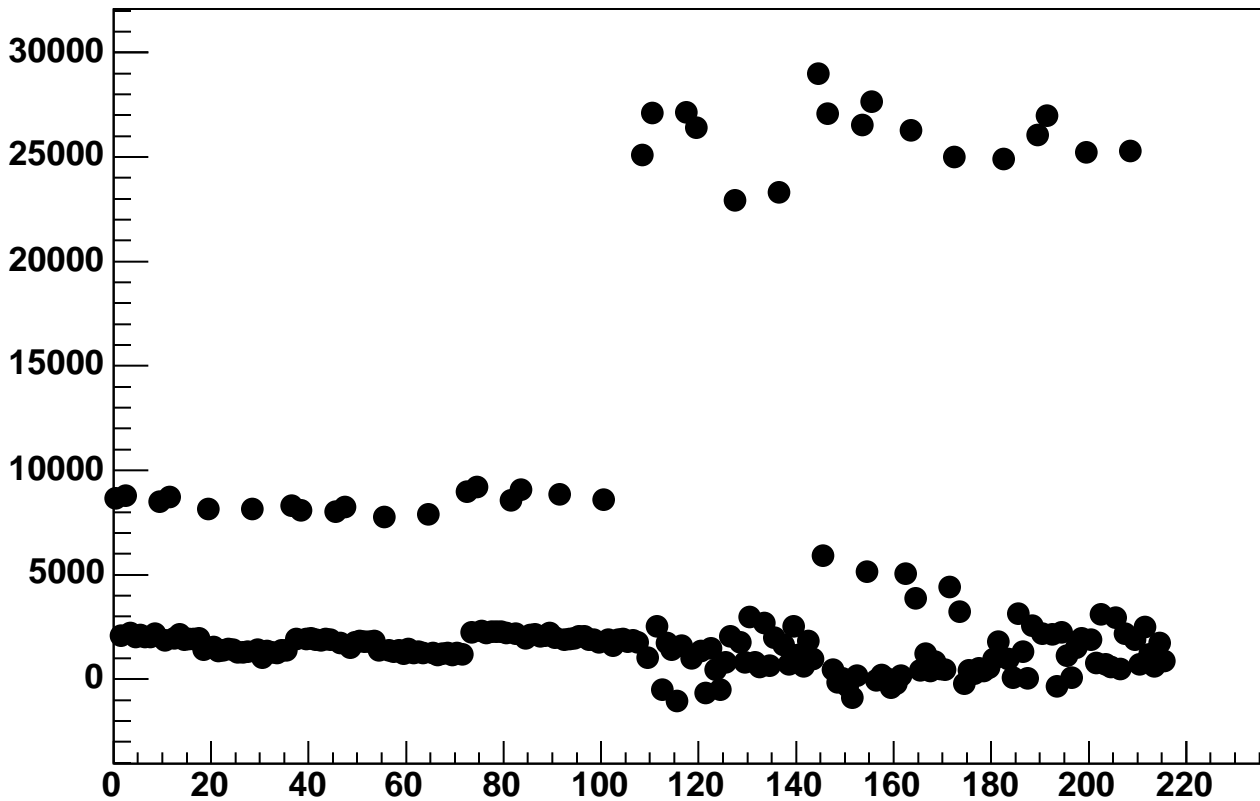
Enable 0, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



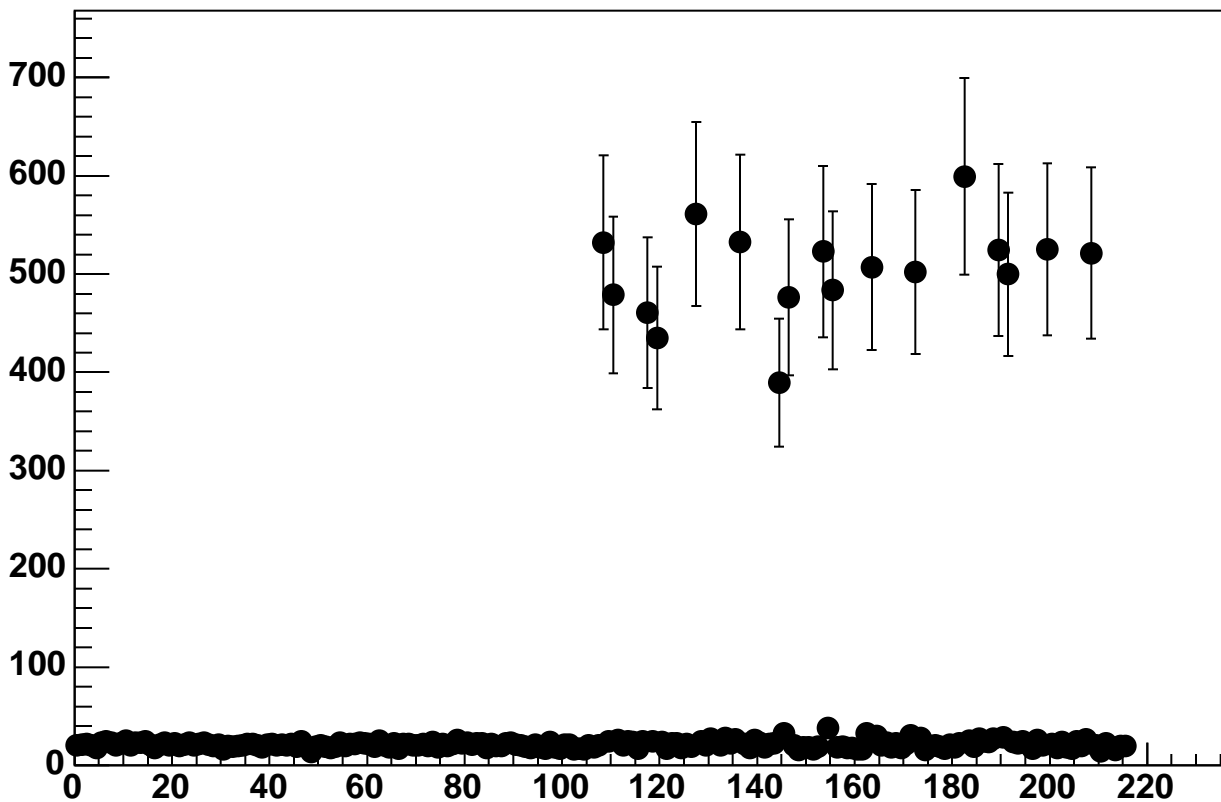
Enable 0, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



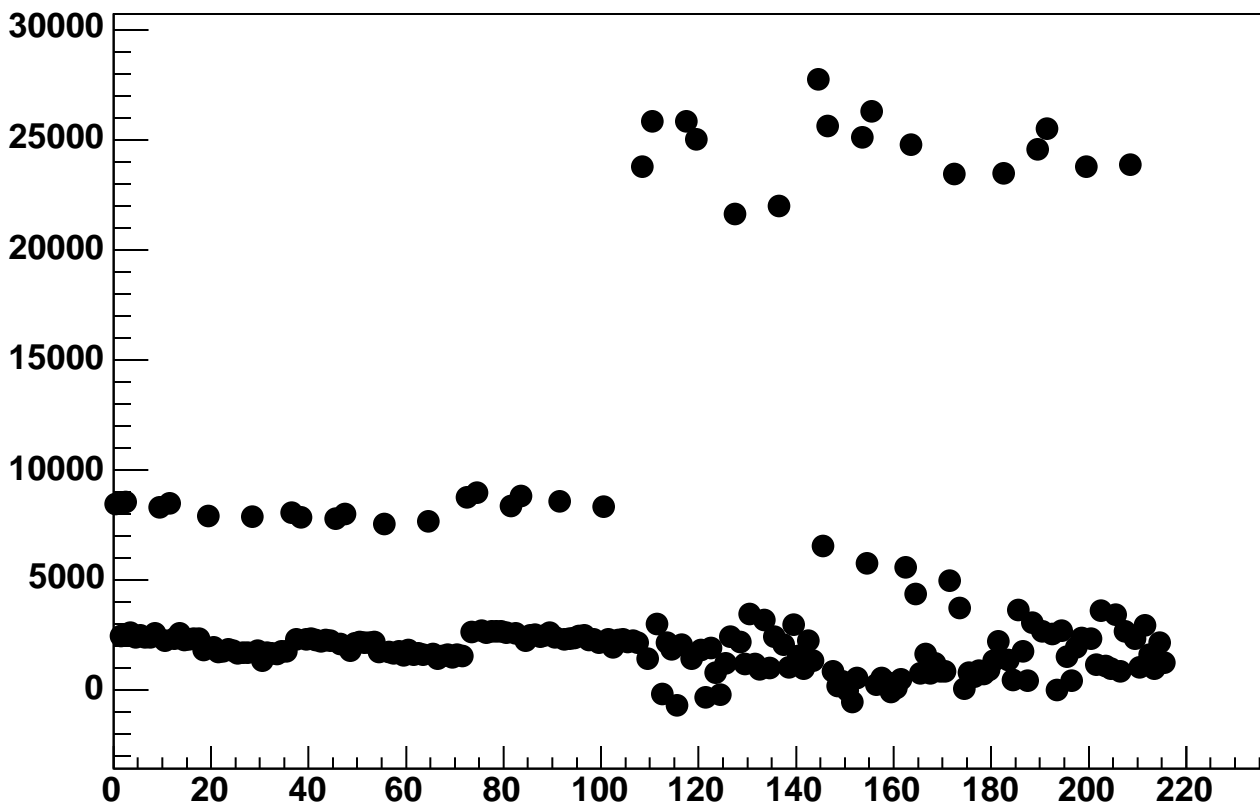
Enable 0, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



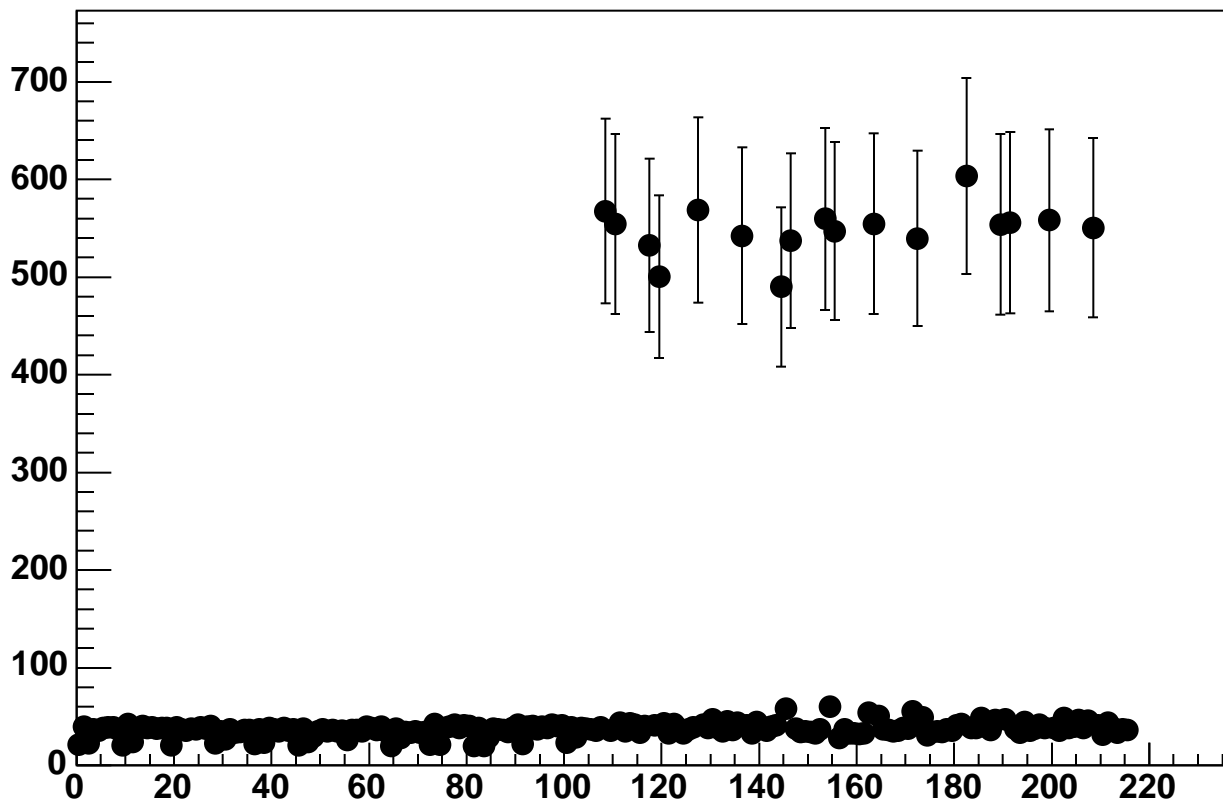
Enable 0, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



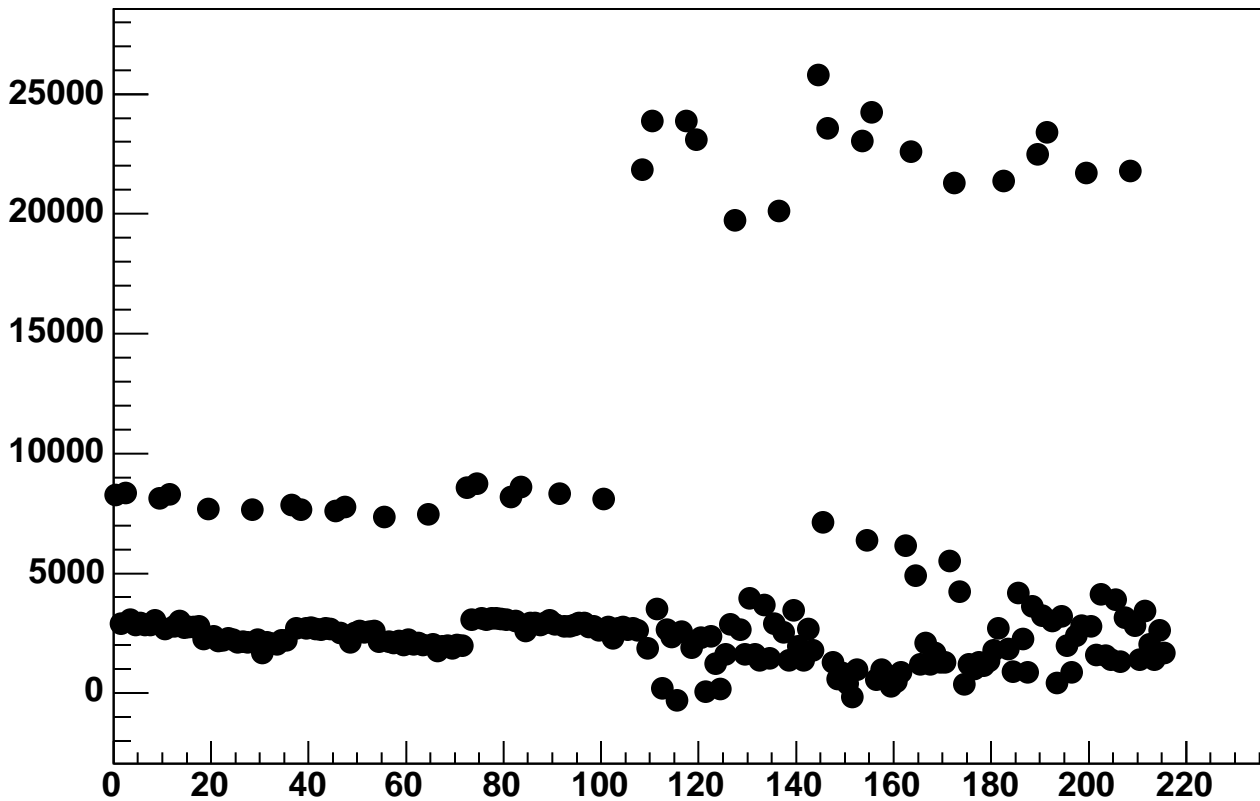
Enable 0, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



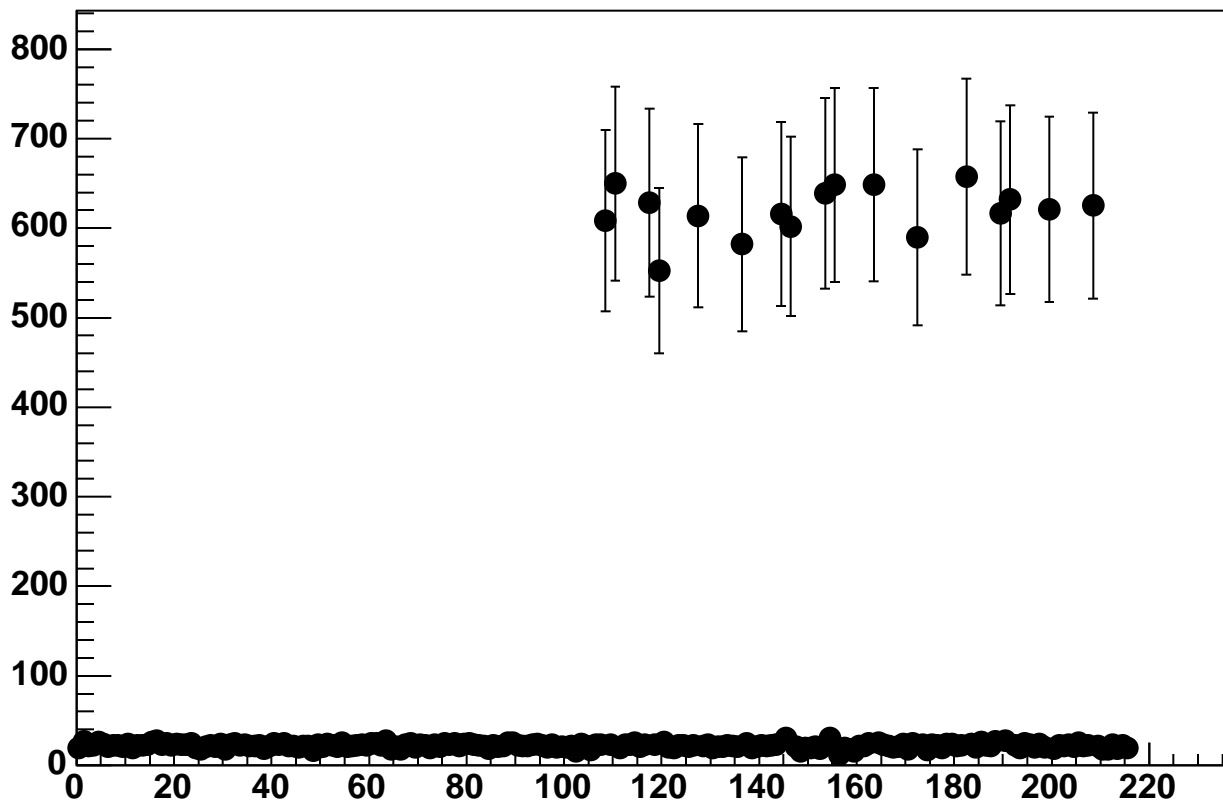
Enable 0, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



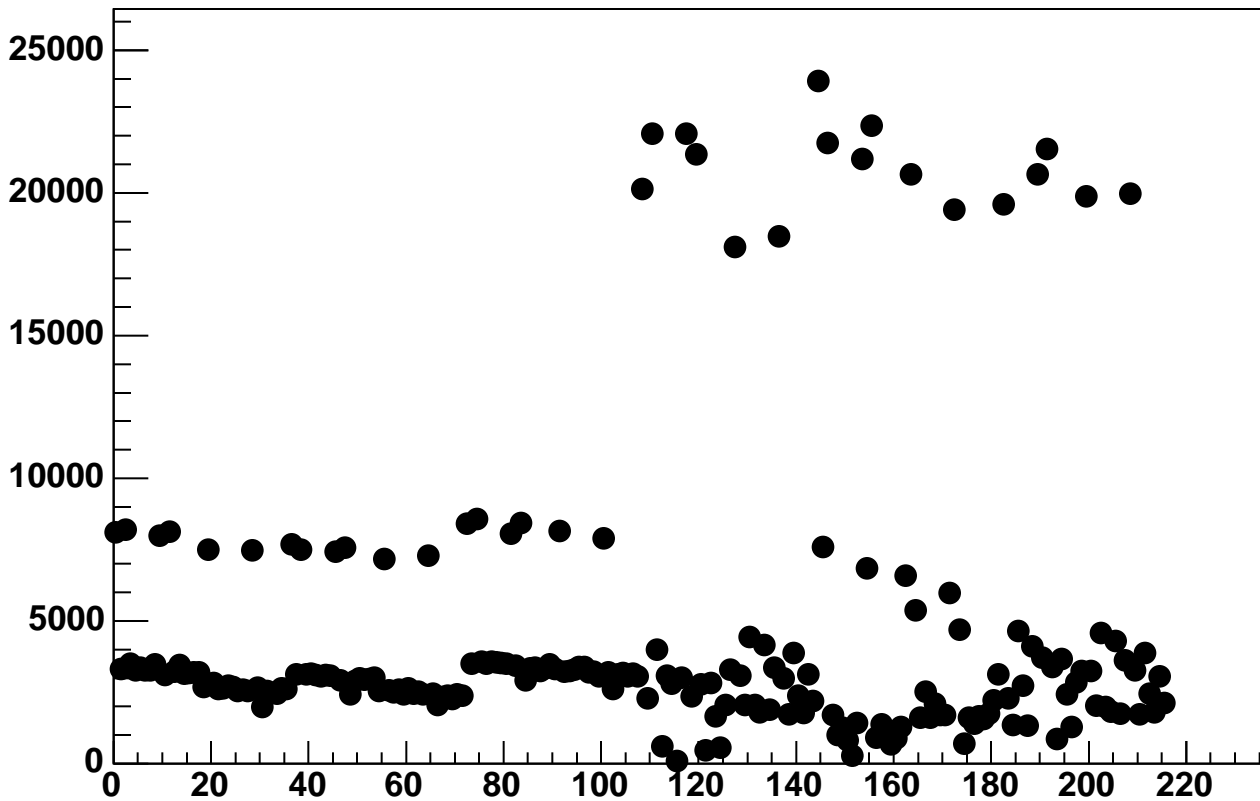
Enable 0, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



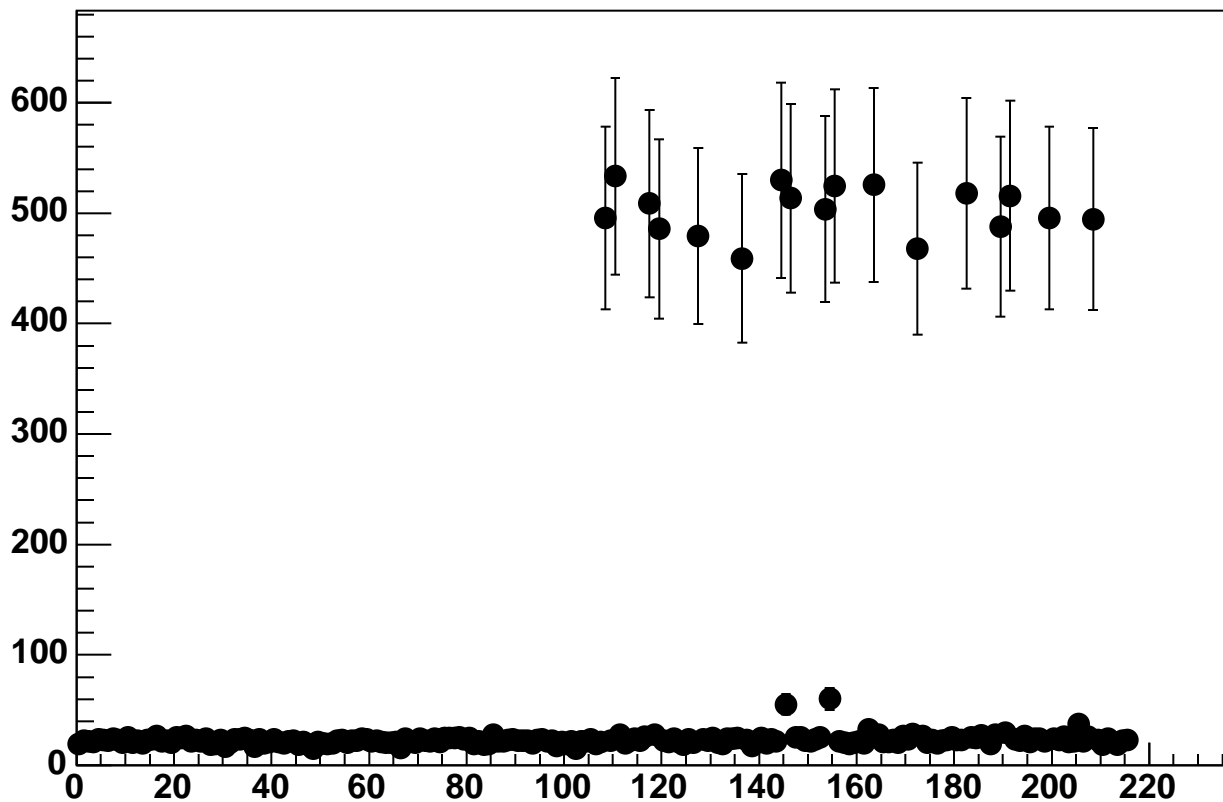
Enable 0, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



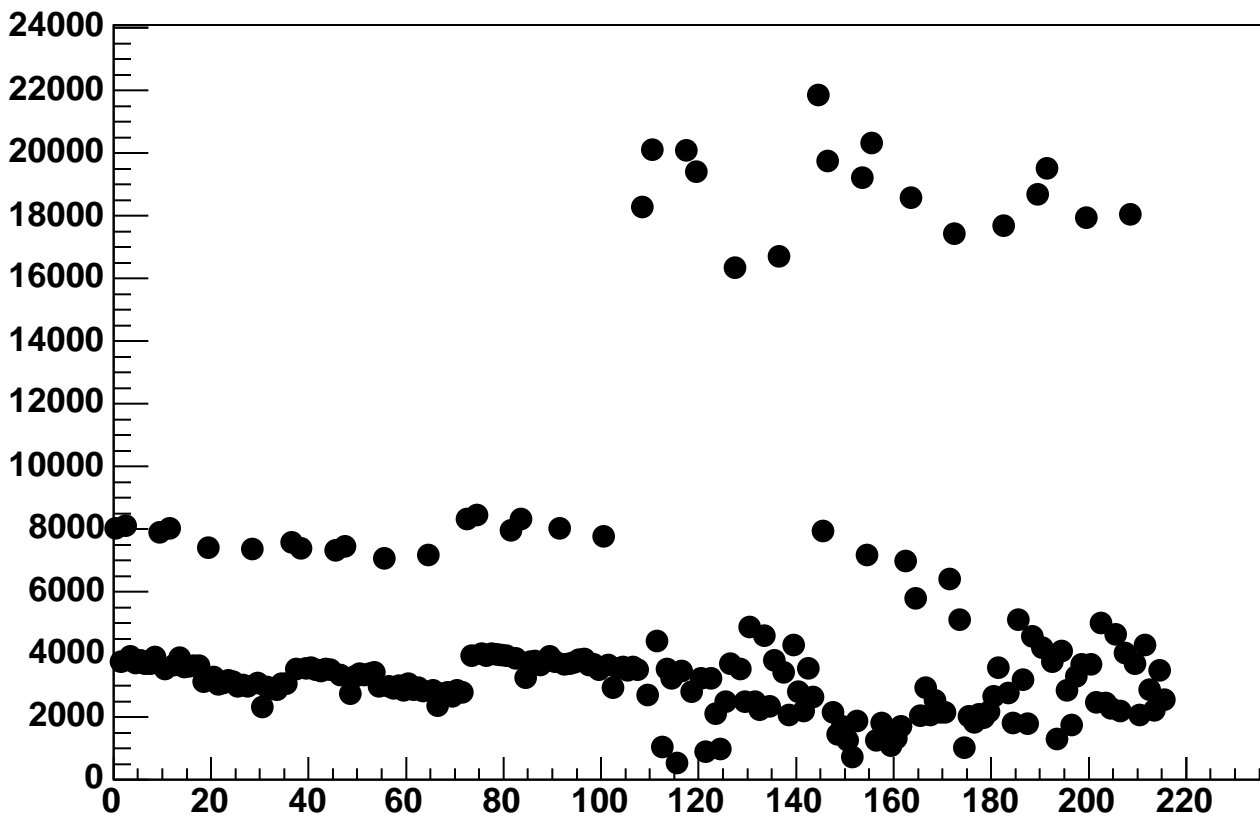
Enable 0, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



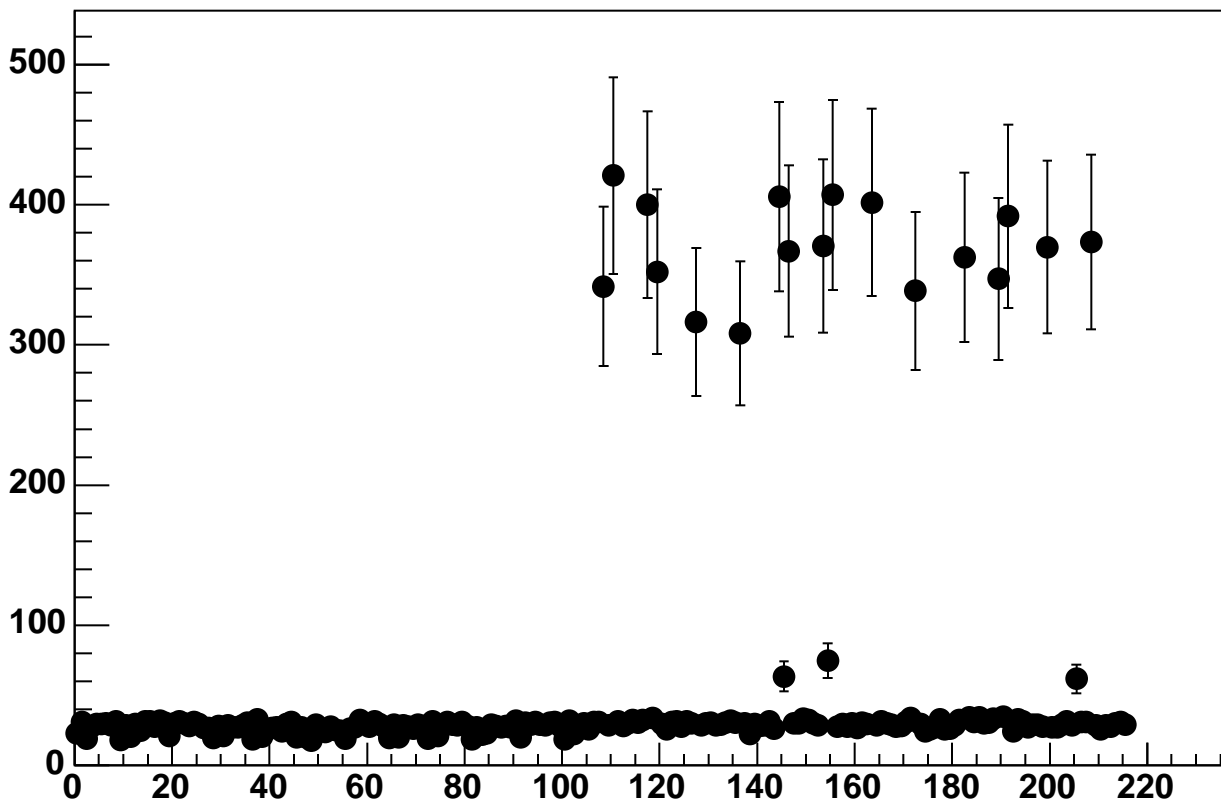
Enable 0, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



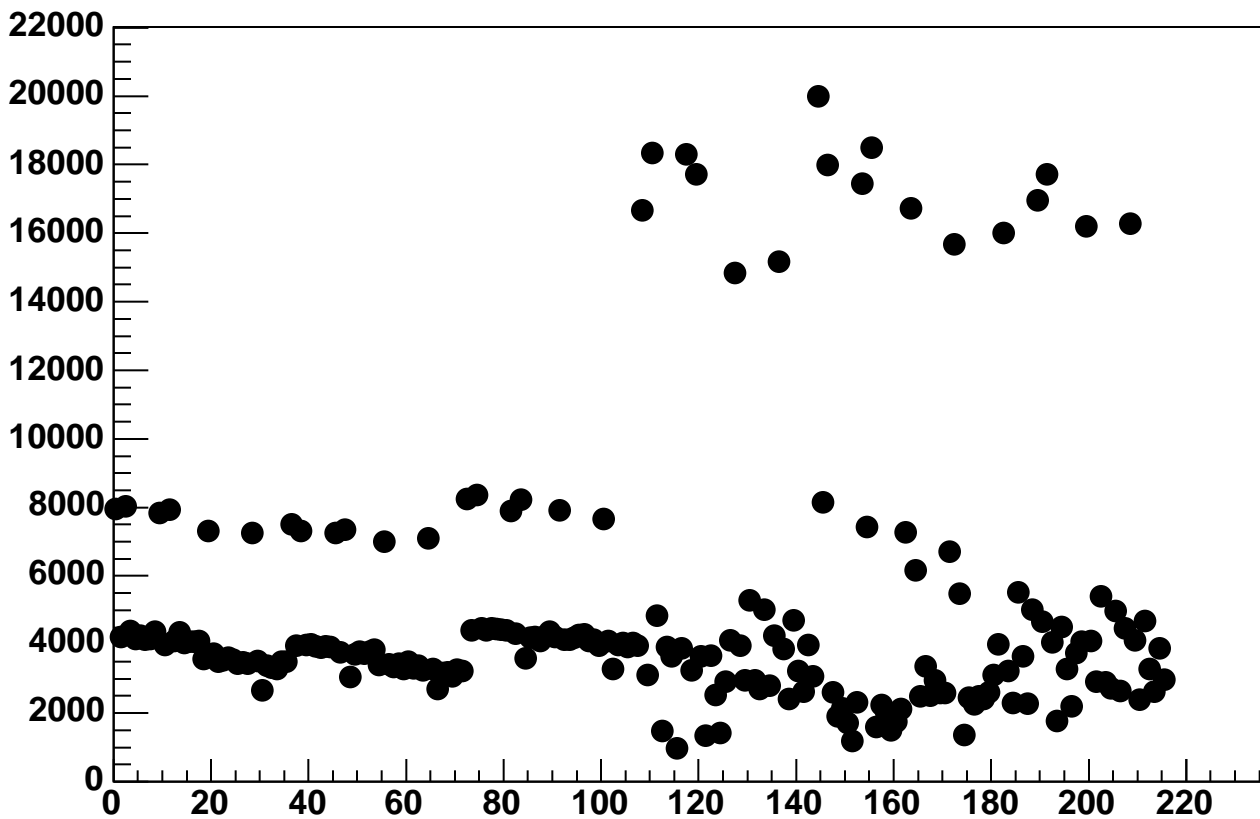
Enable 0, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



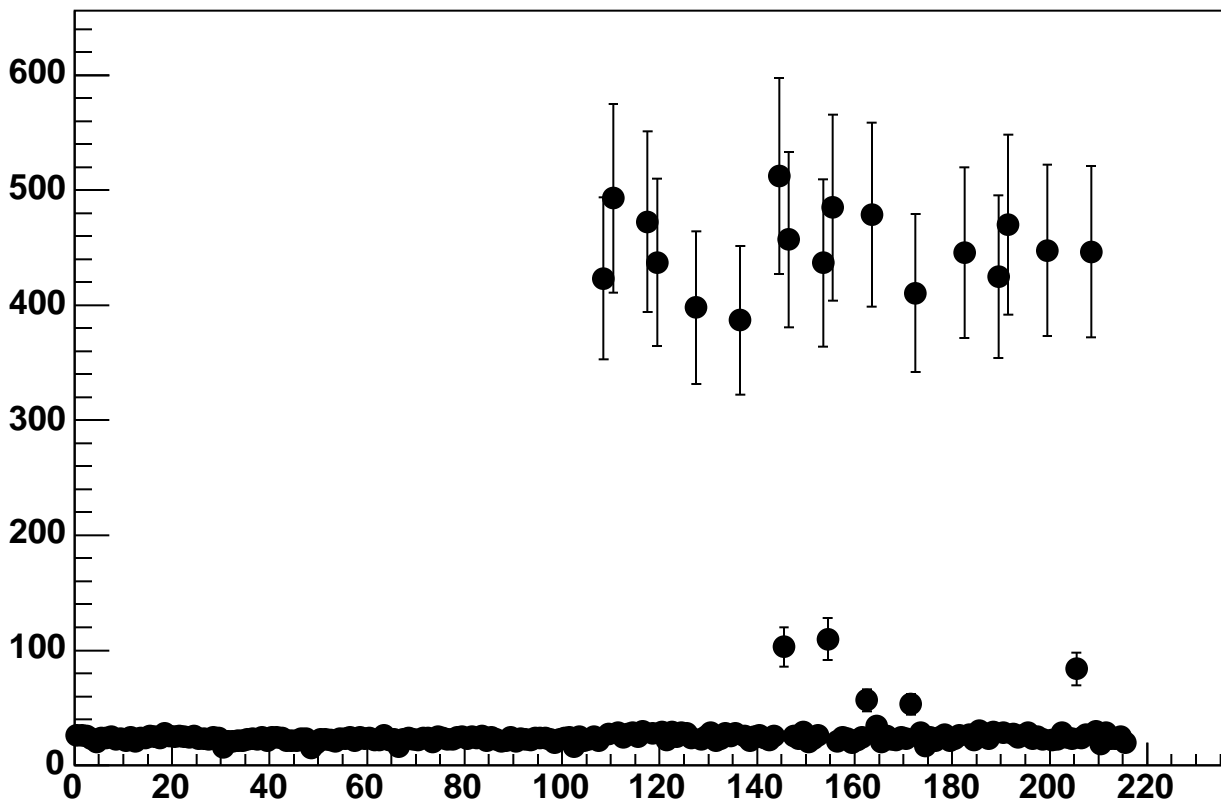
Enable 0, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



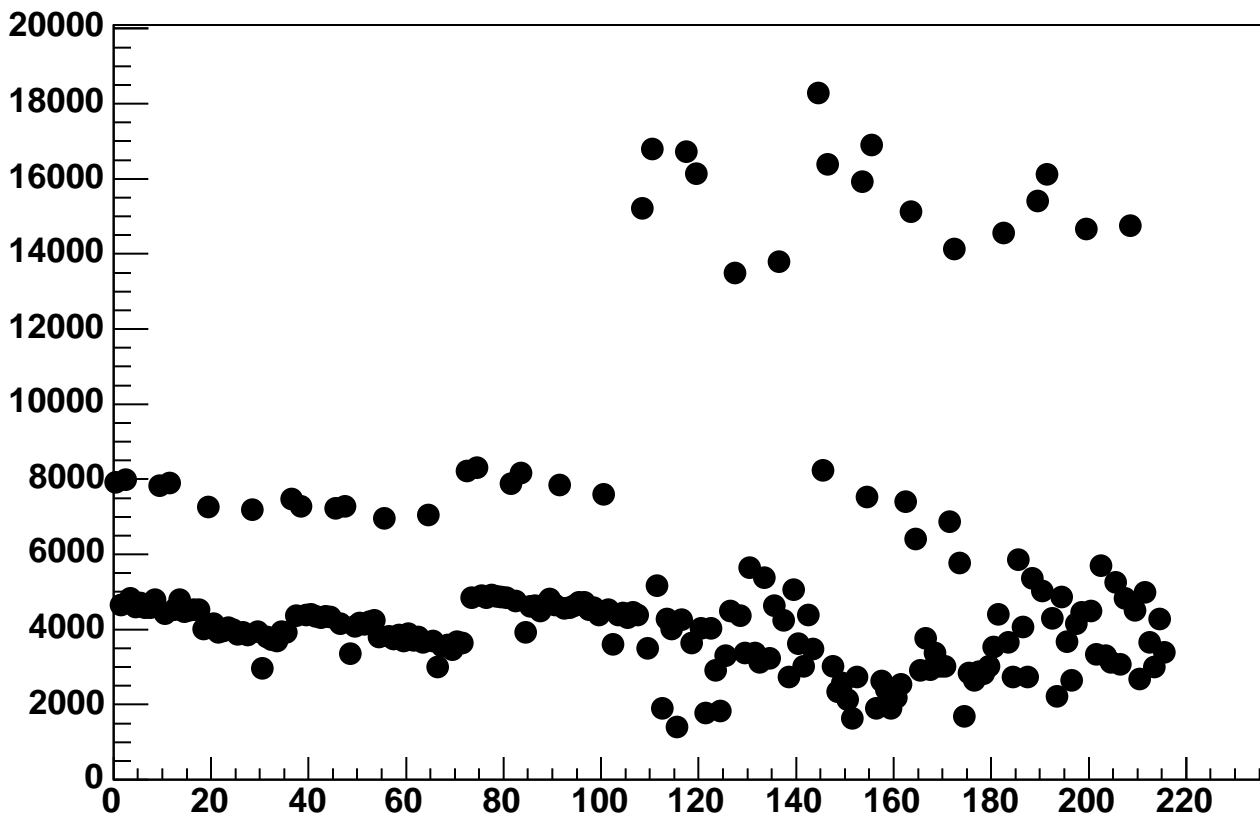
Enable 0, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



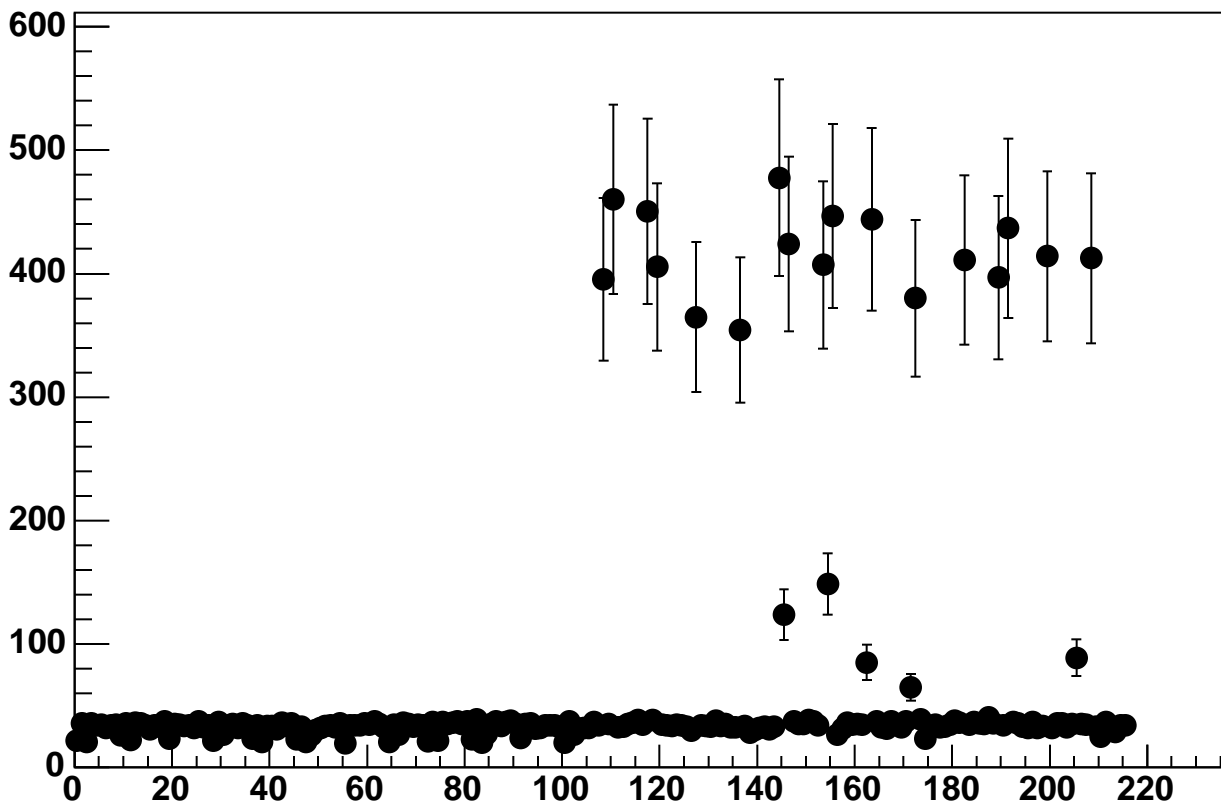
Enable 0, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



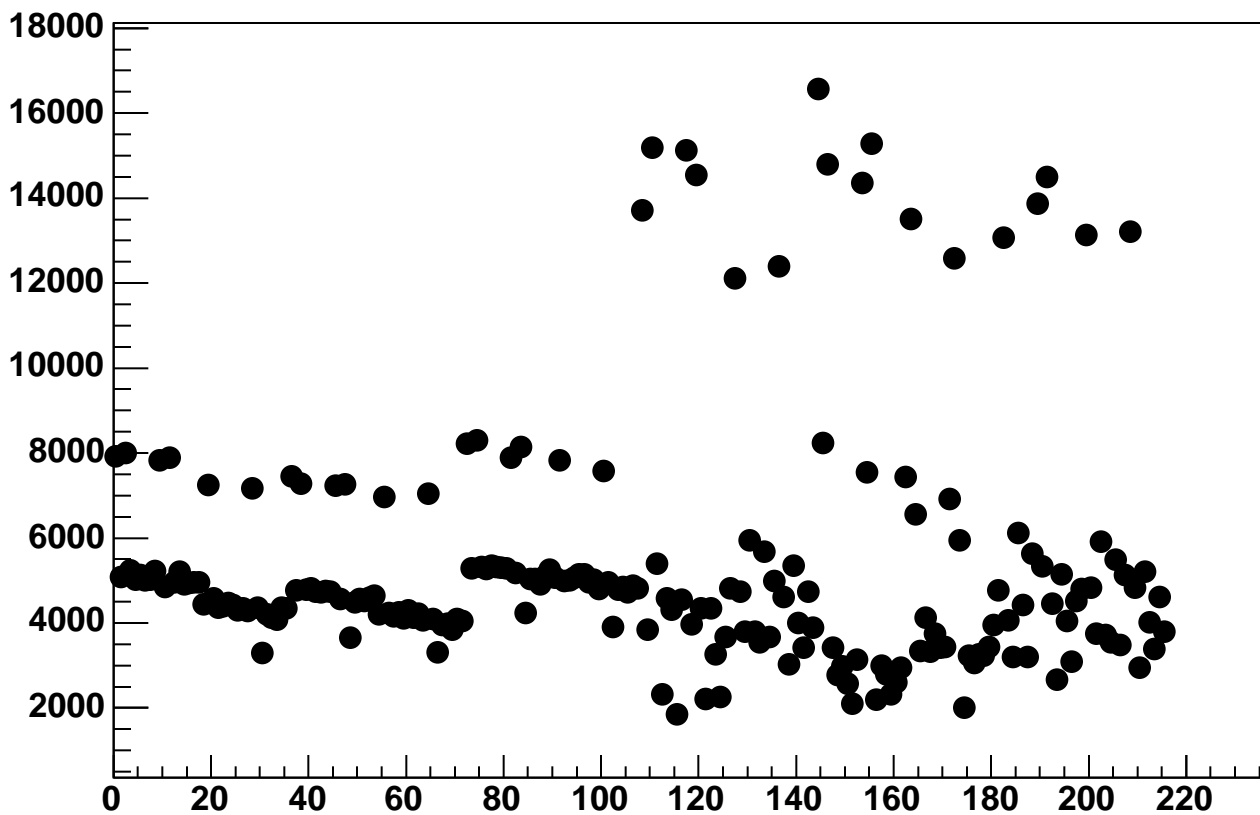
Enable 0, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



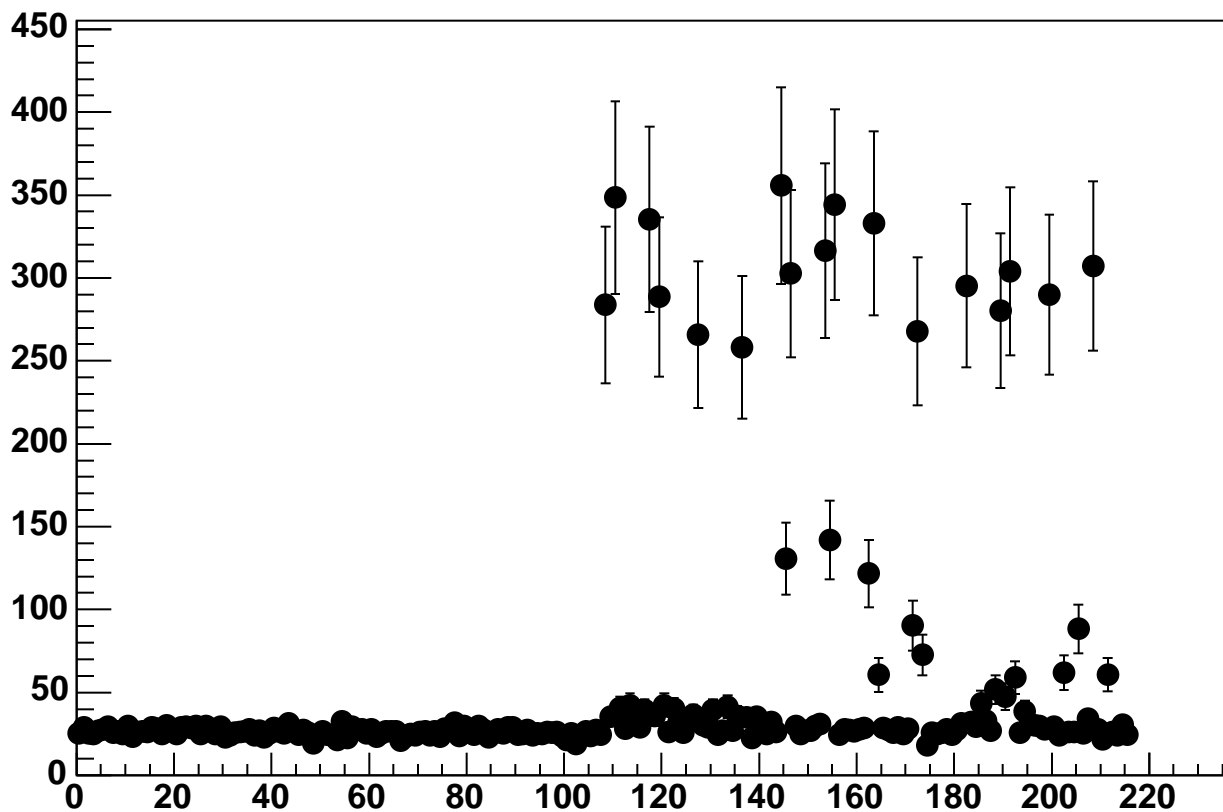
Enable 0, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



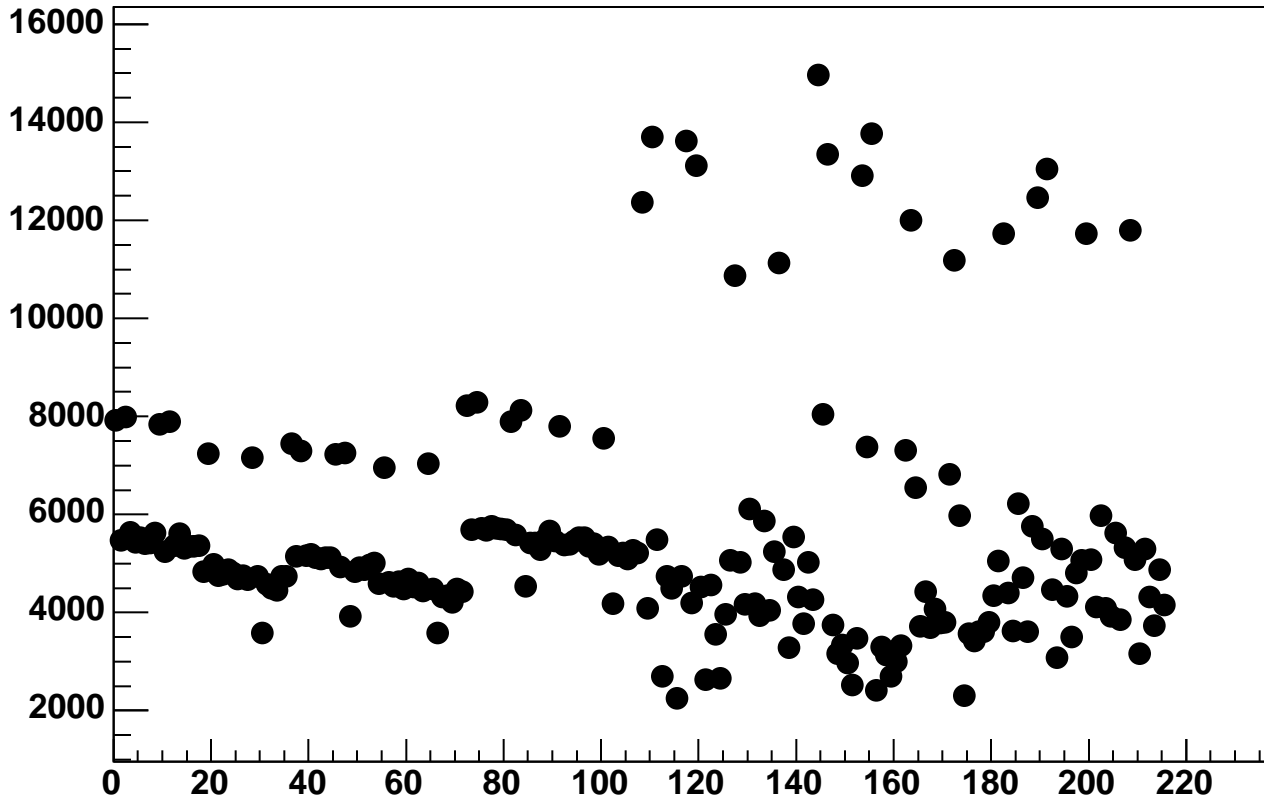
Enable 0, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



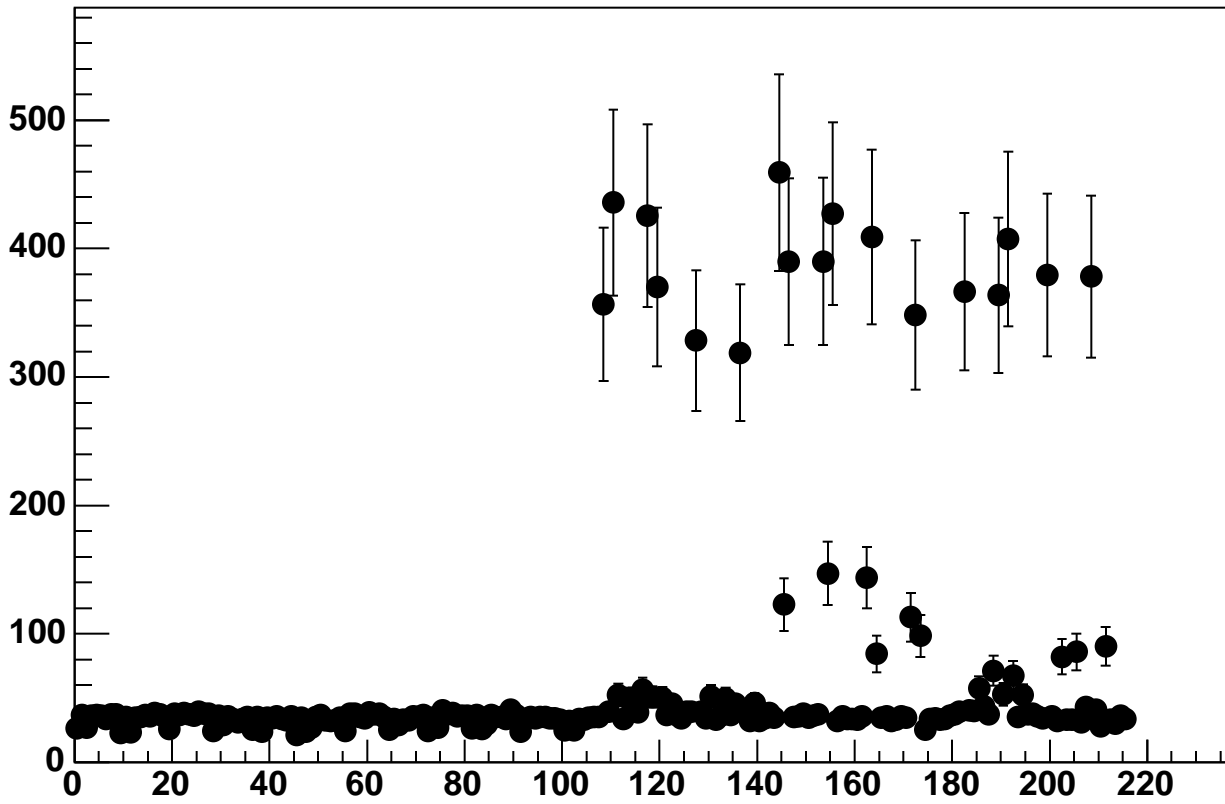
Enable 0, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



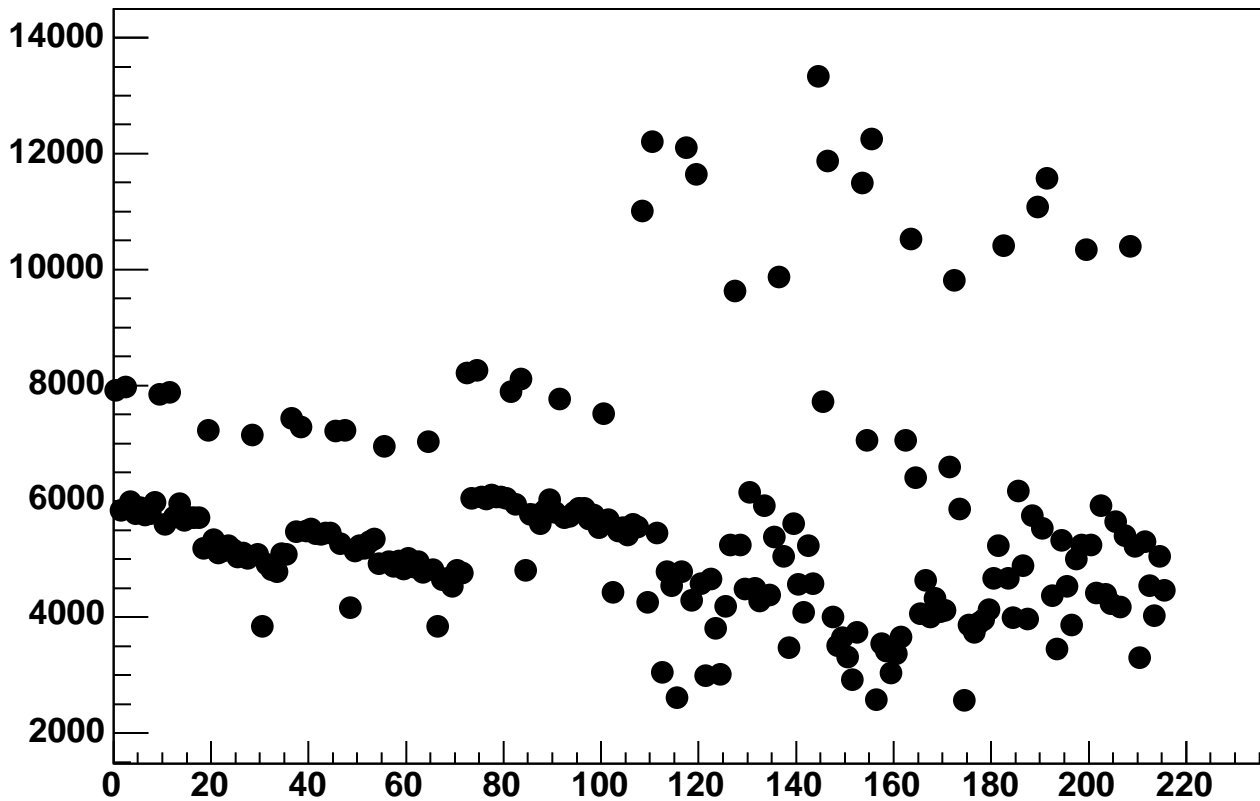
Enable 0, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



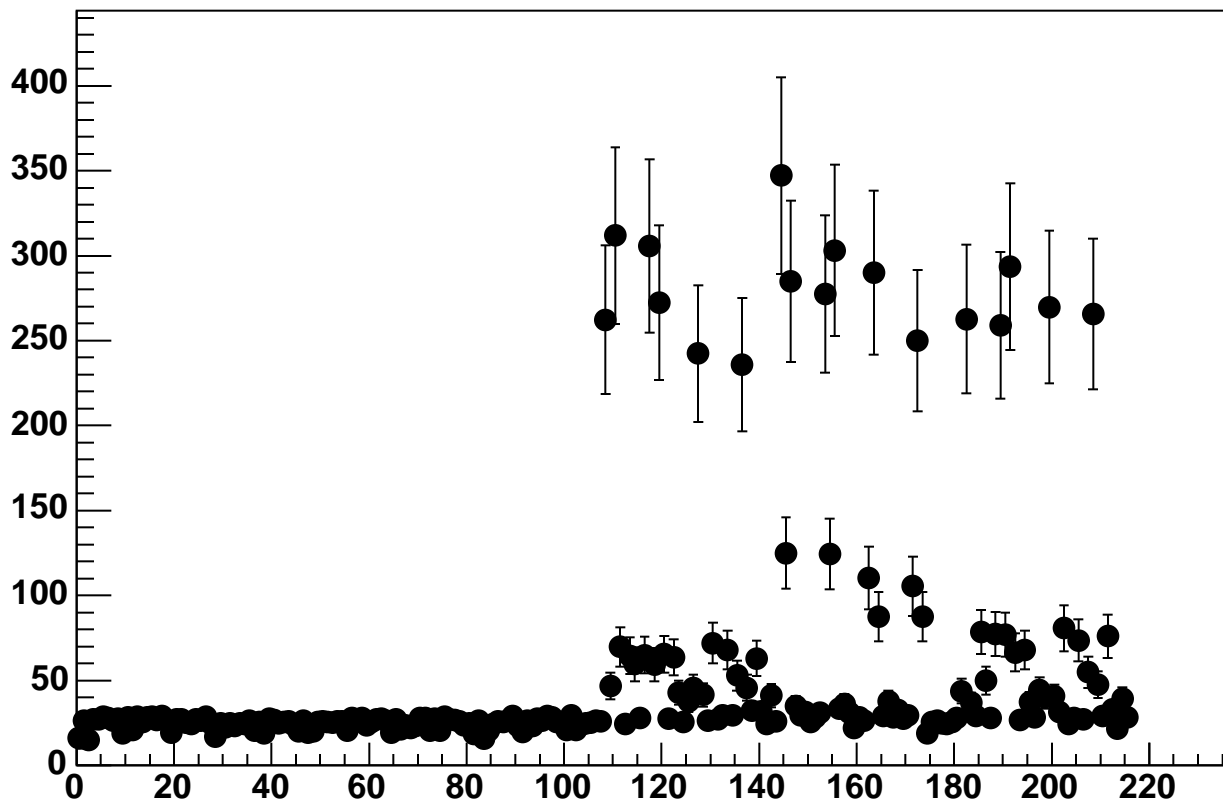
Enable 0, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



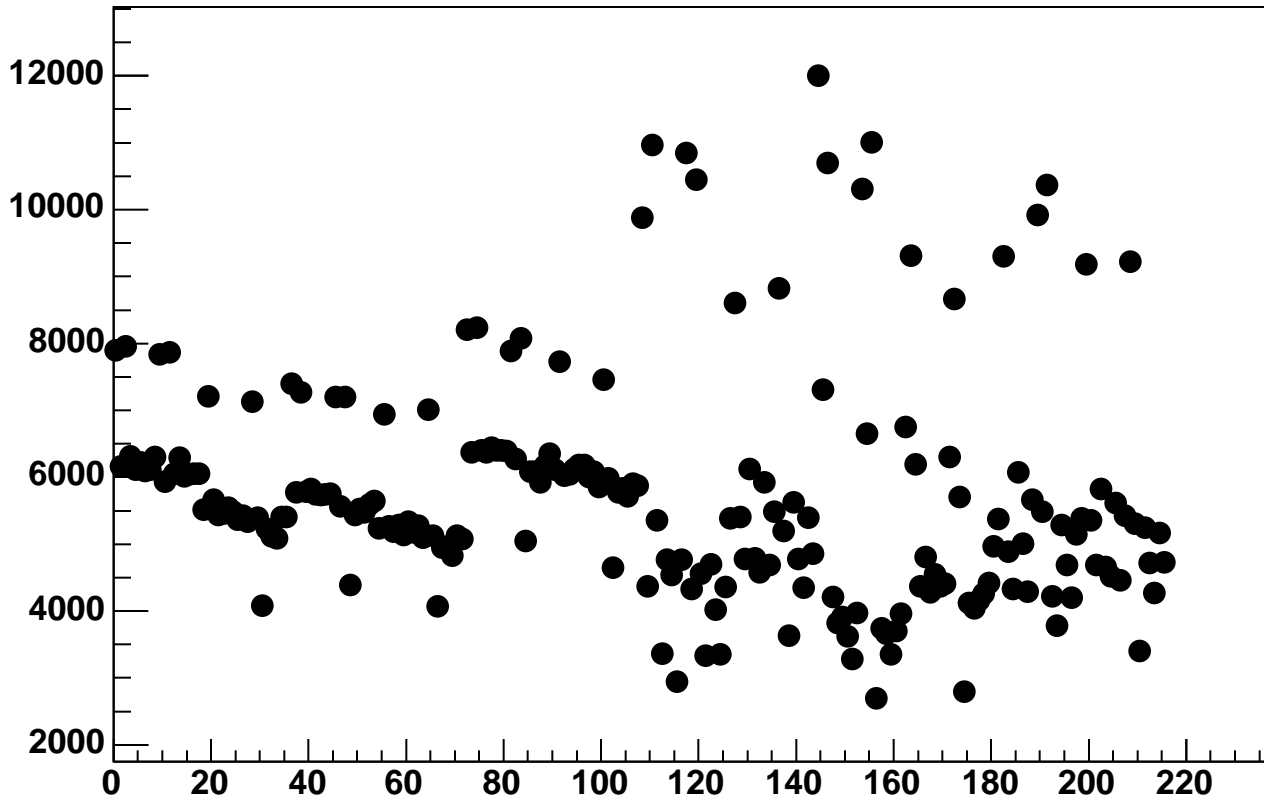
Enable 0, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



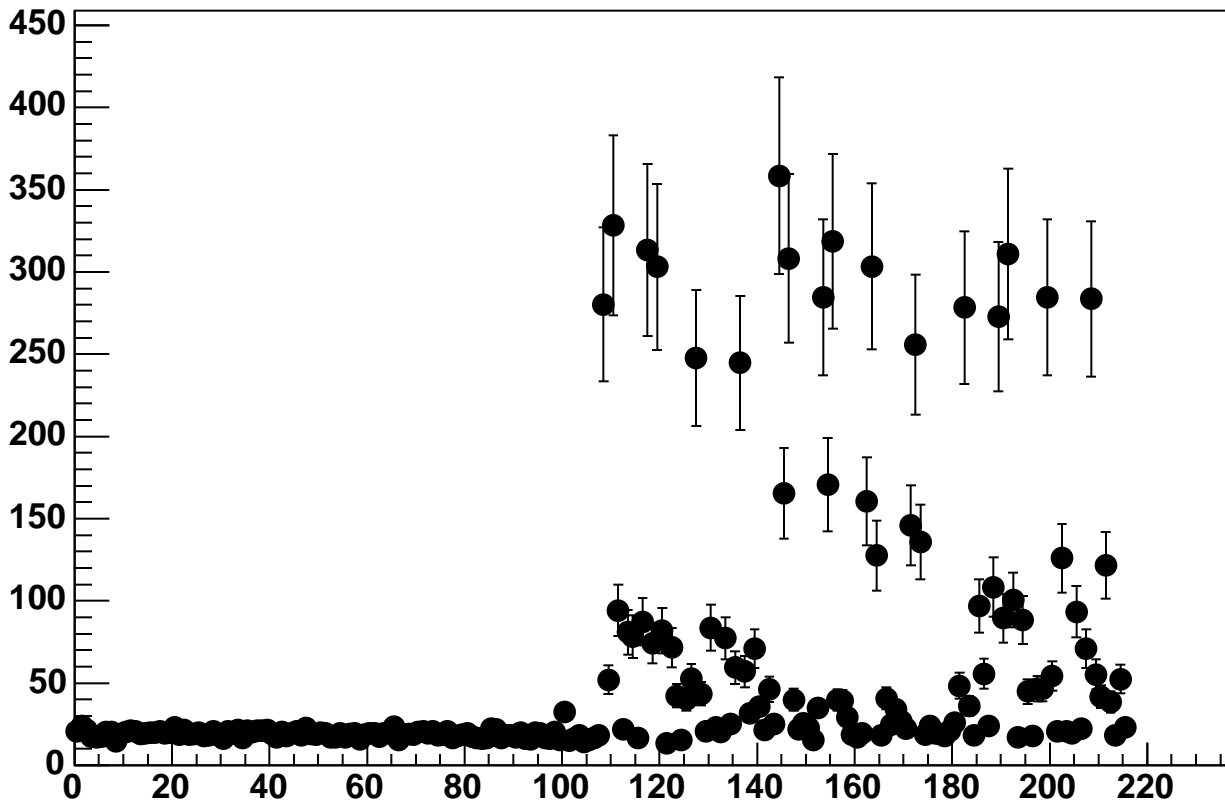
Enable 0, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



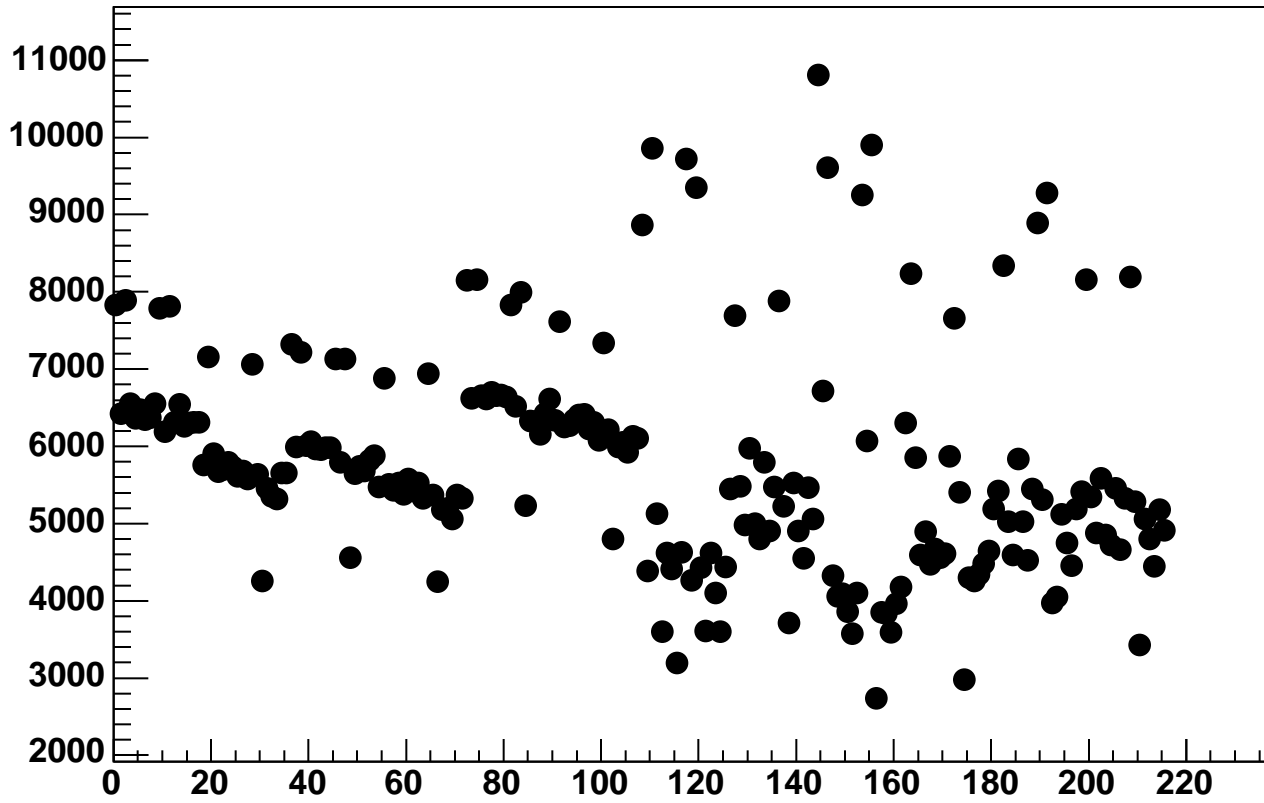
Enable 0, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



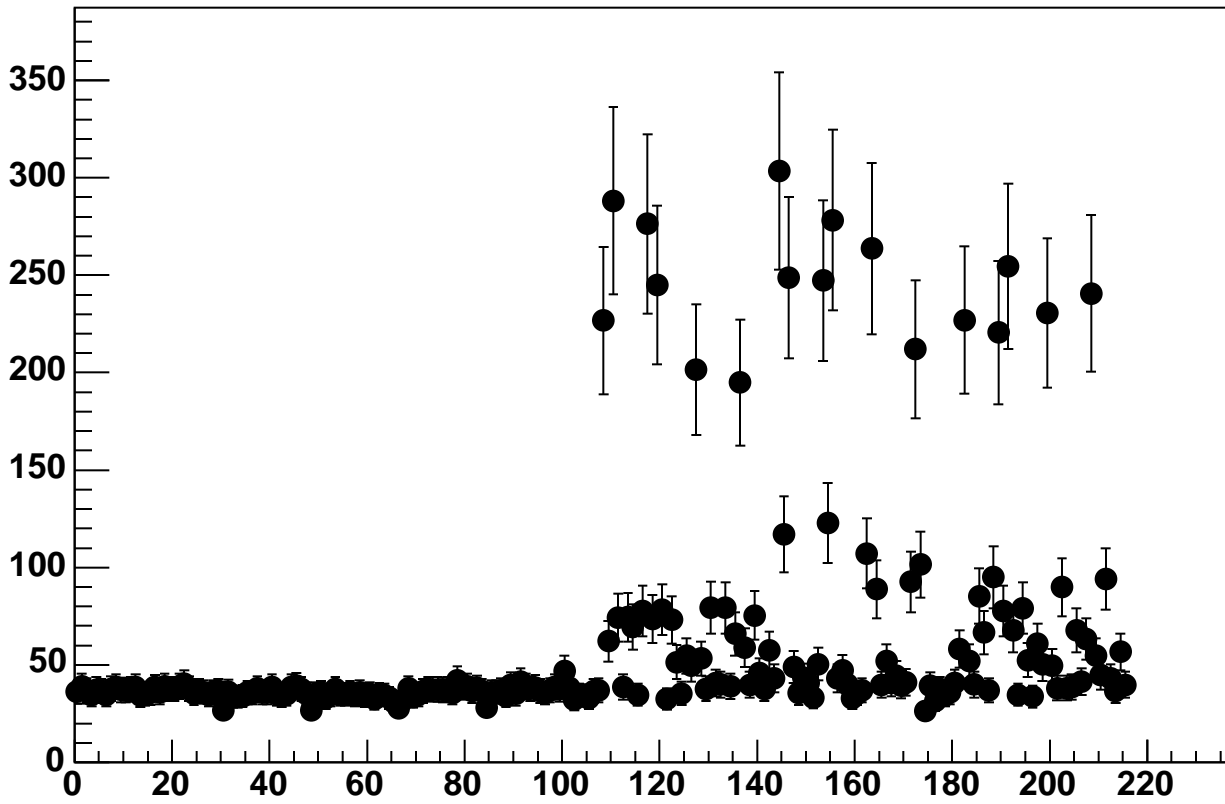
Enable 0, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



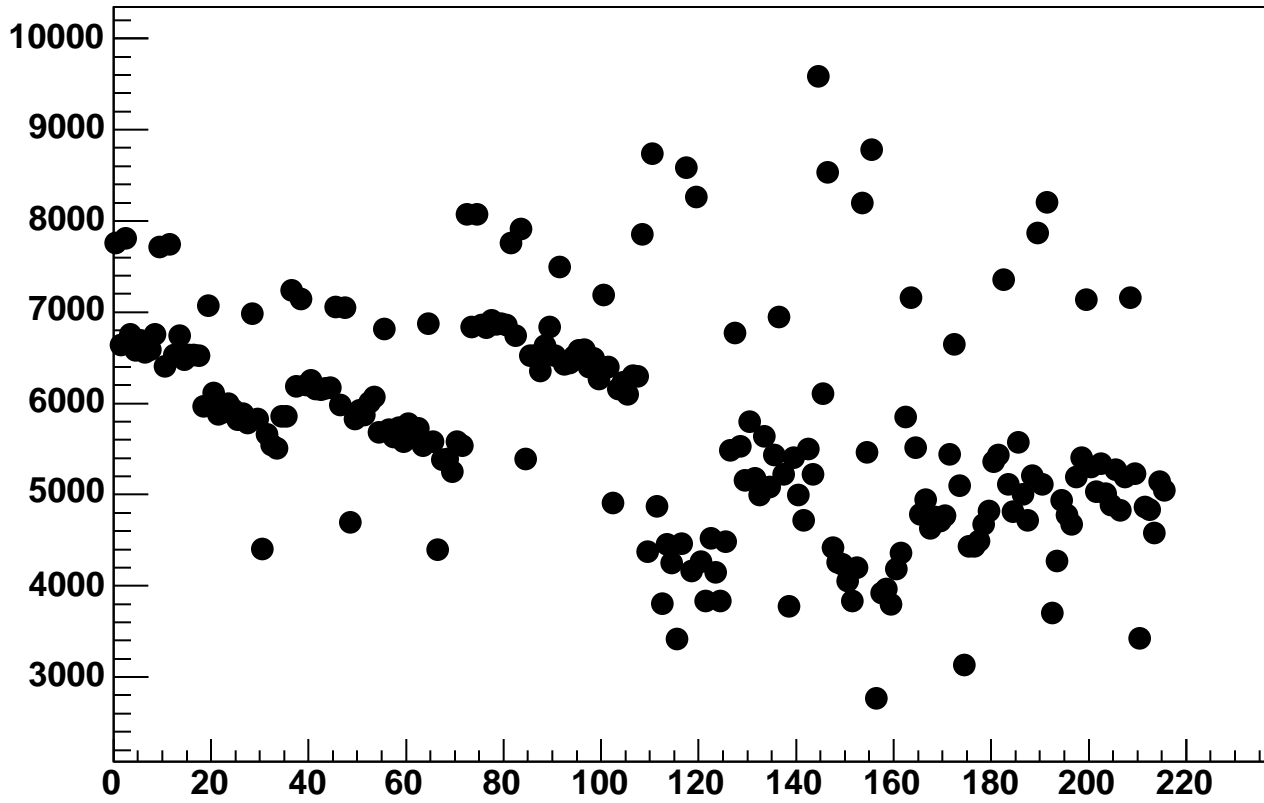
Enable 0, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



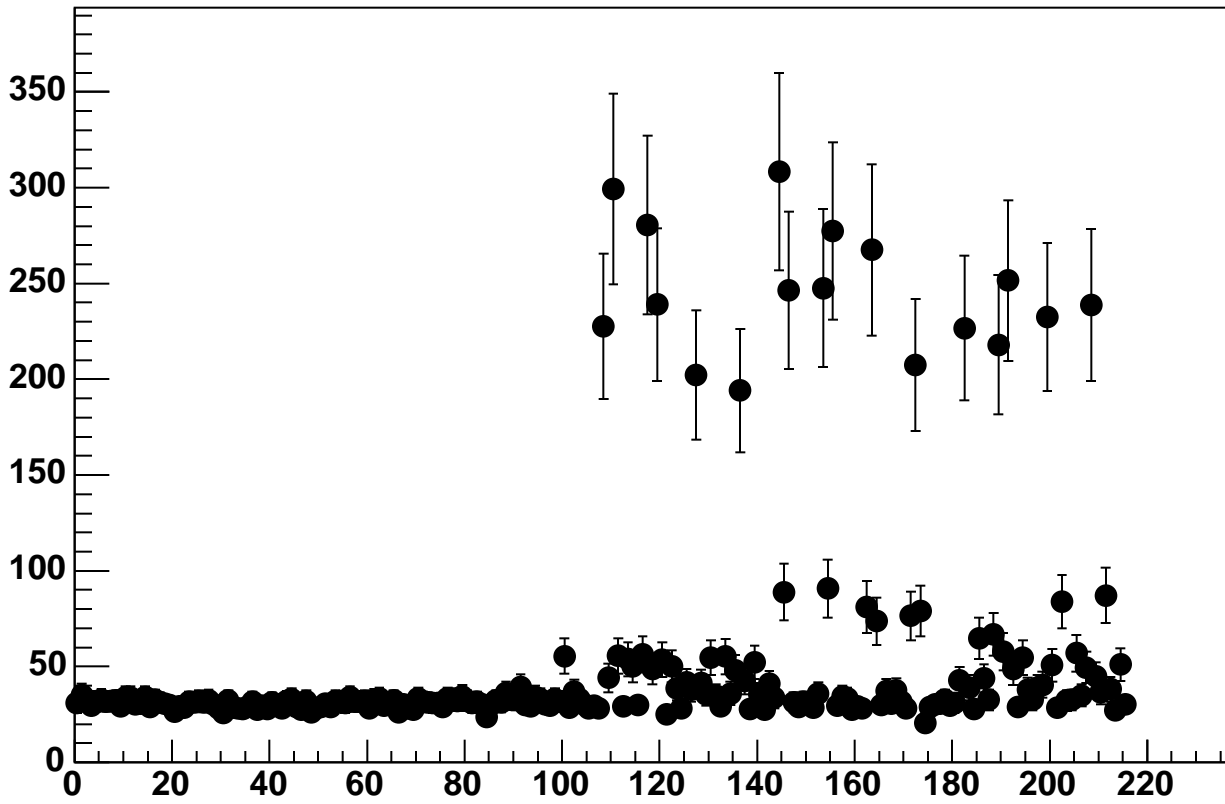
Enable 0, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



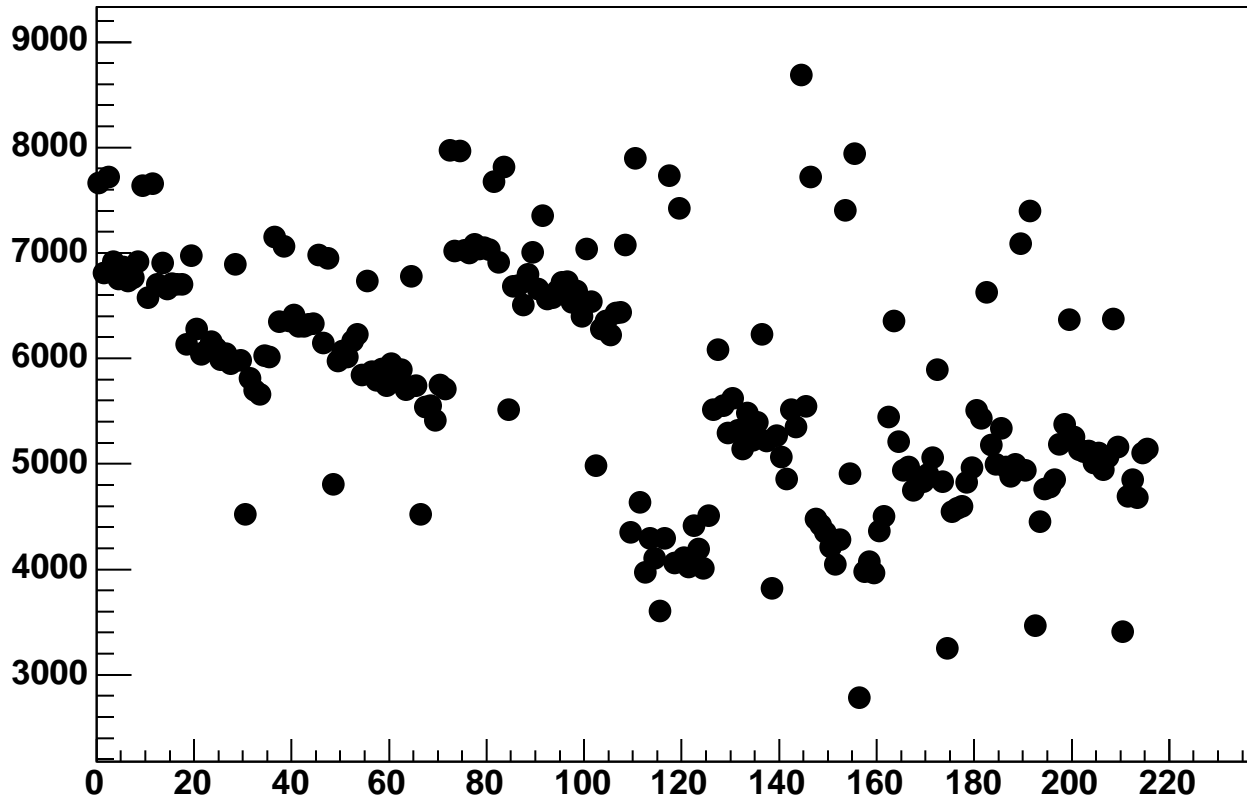
Enable 0, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



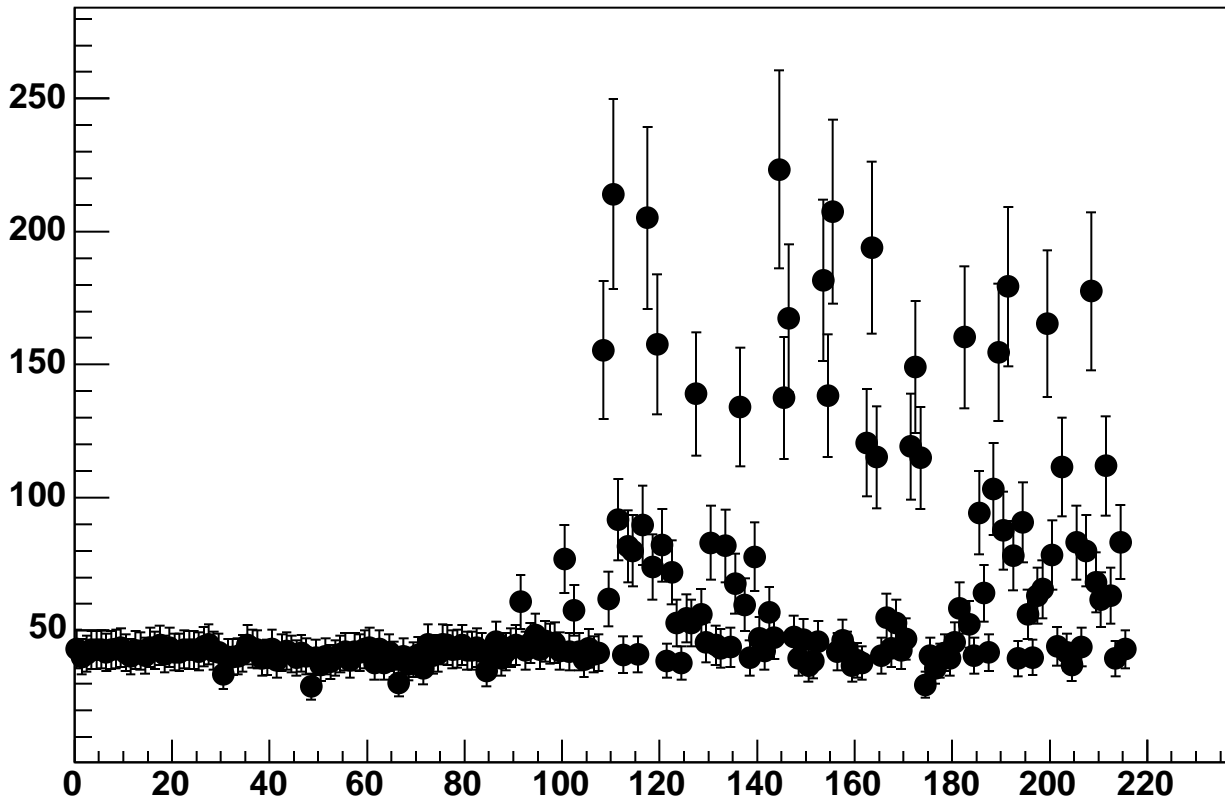
Enable 0, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



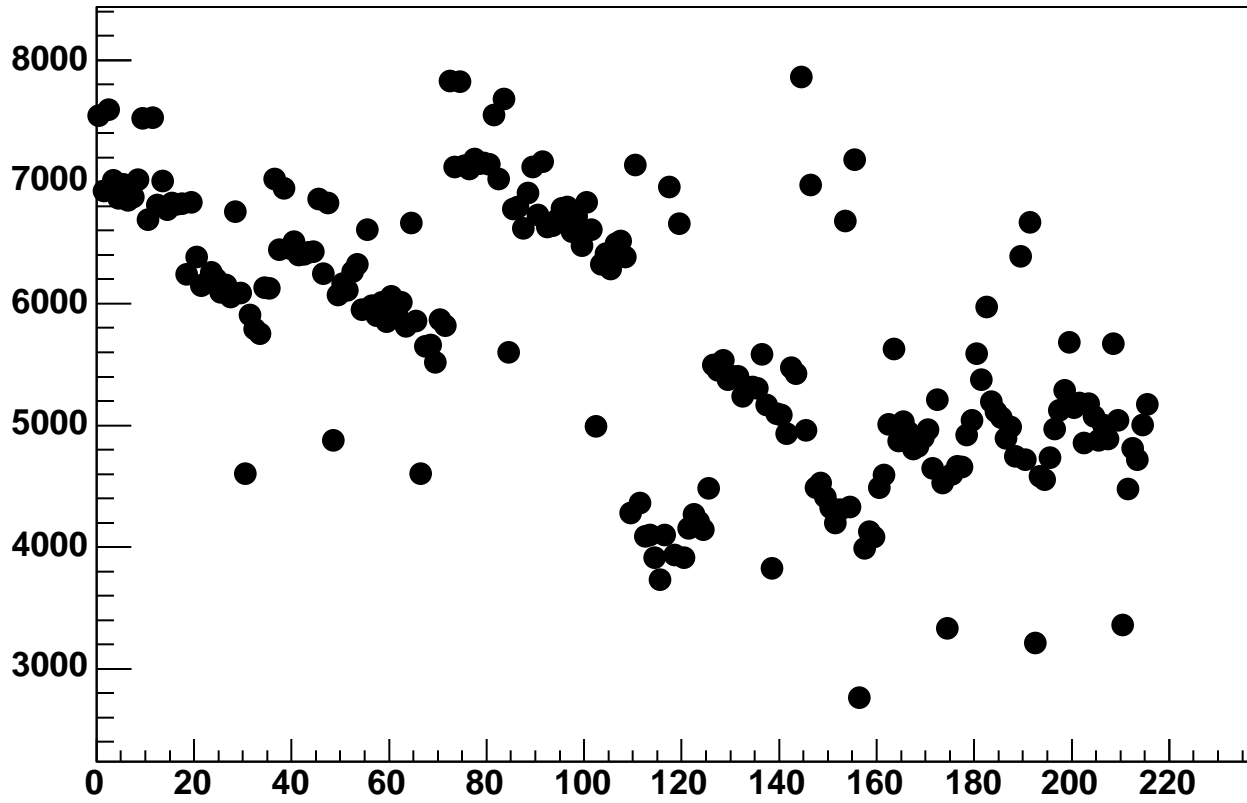
Enable 0, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



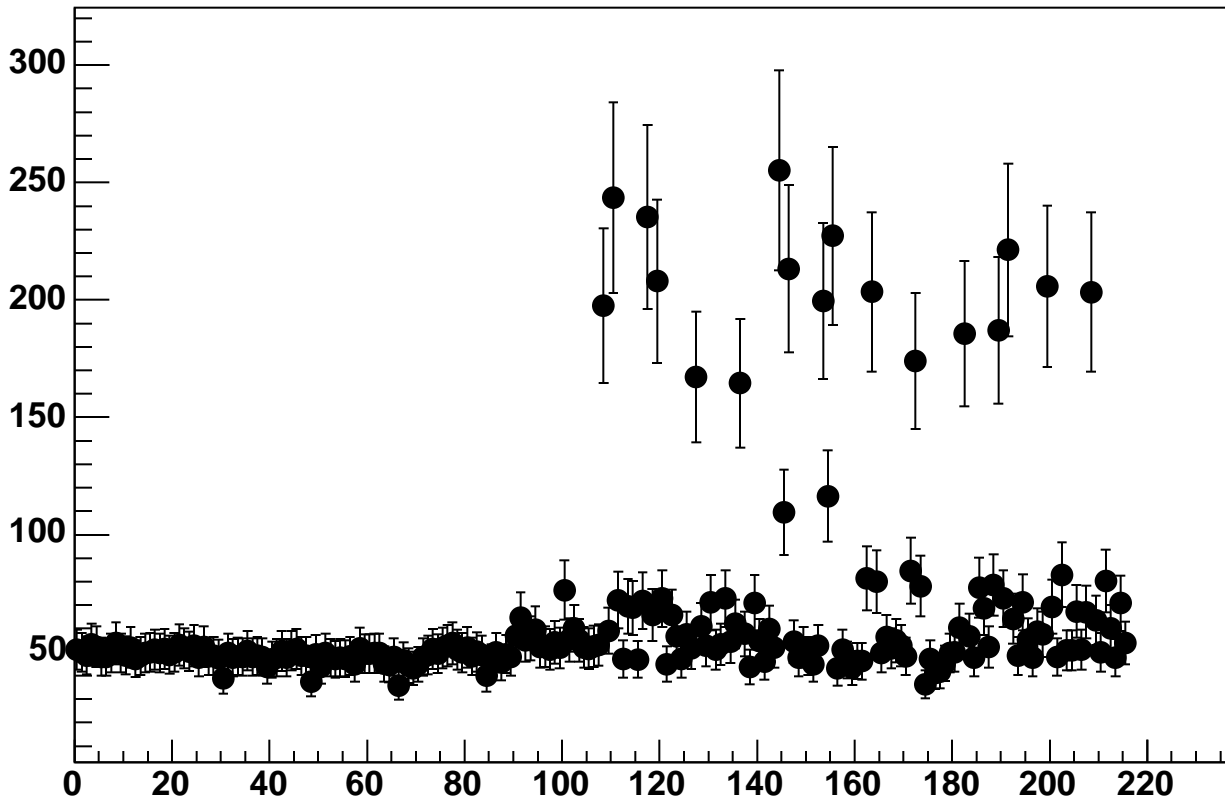
Enable 0, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



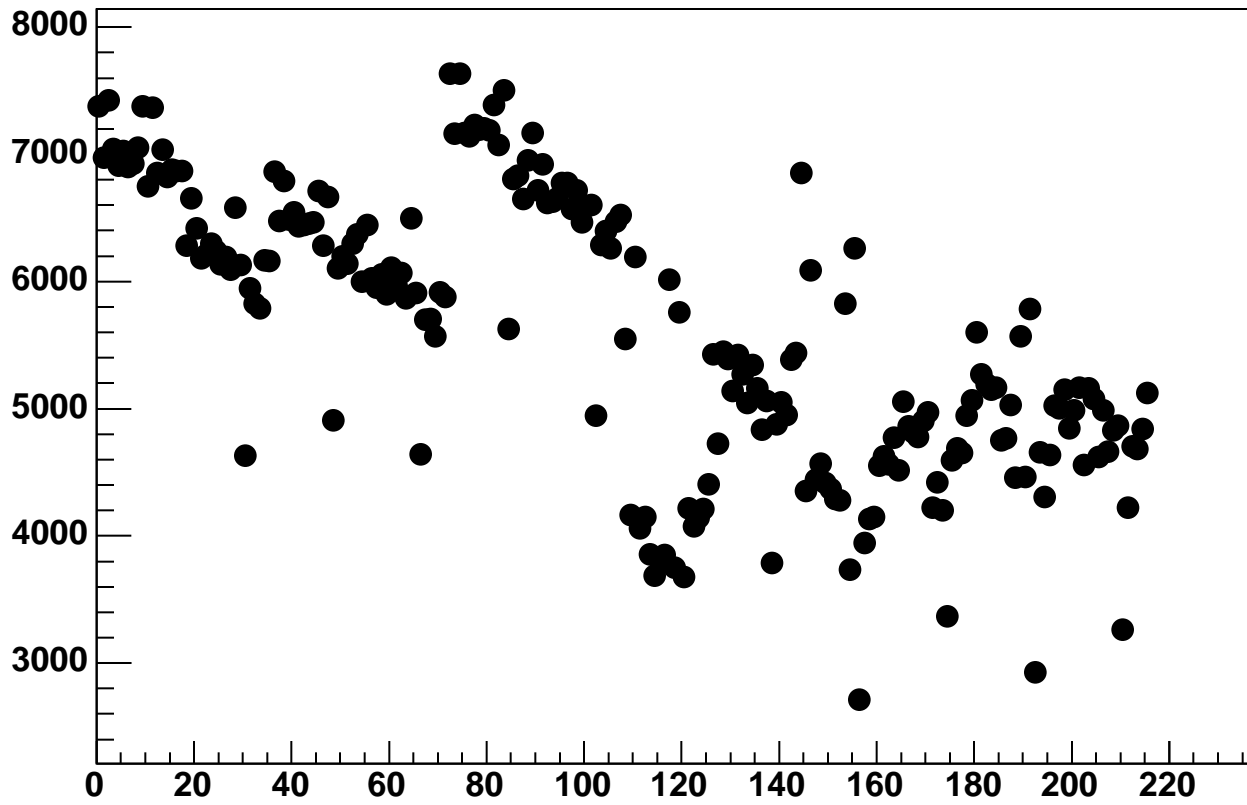
Enable 0, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



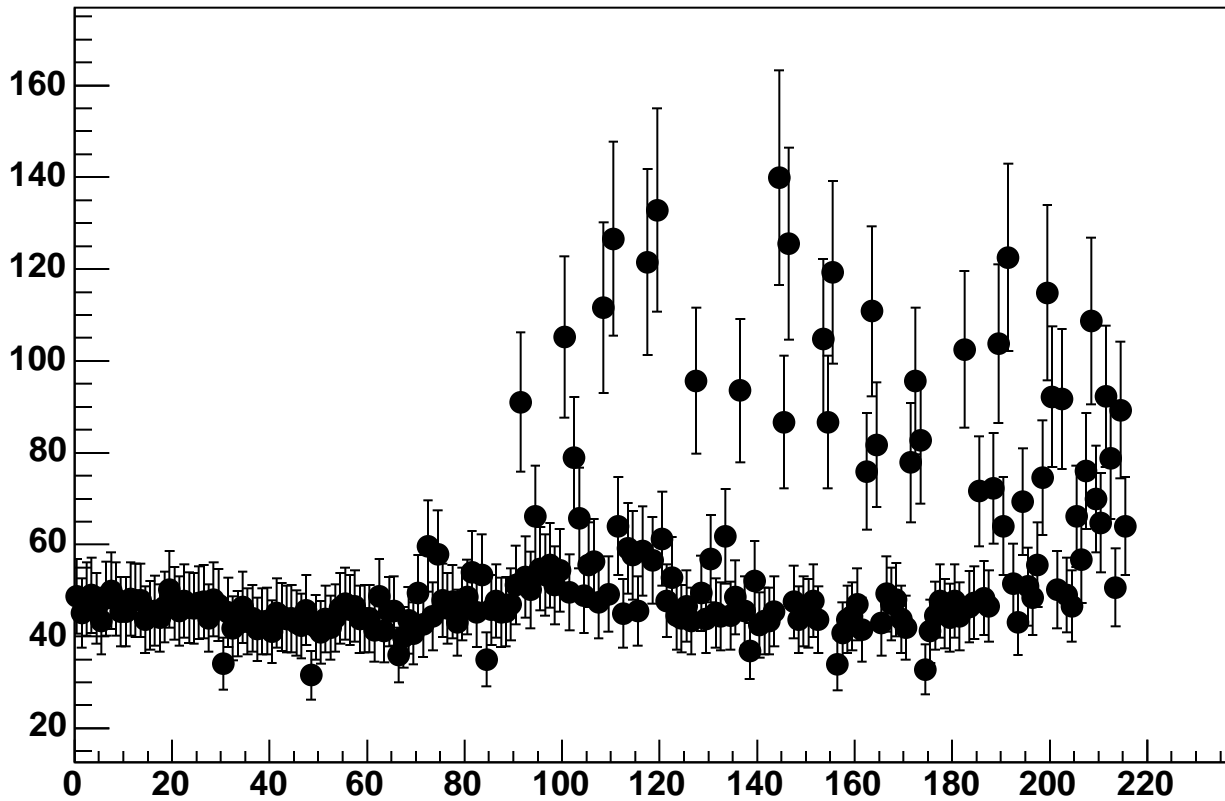
Enable 0, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



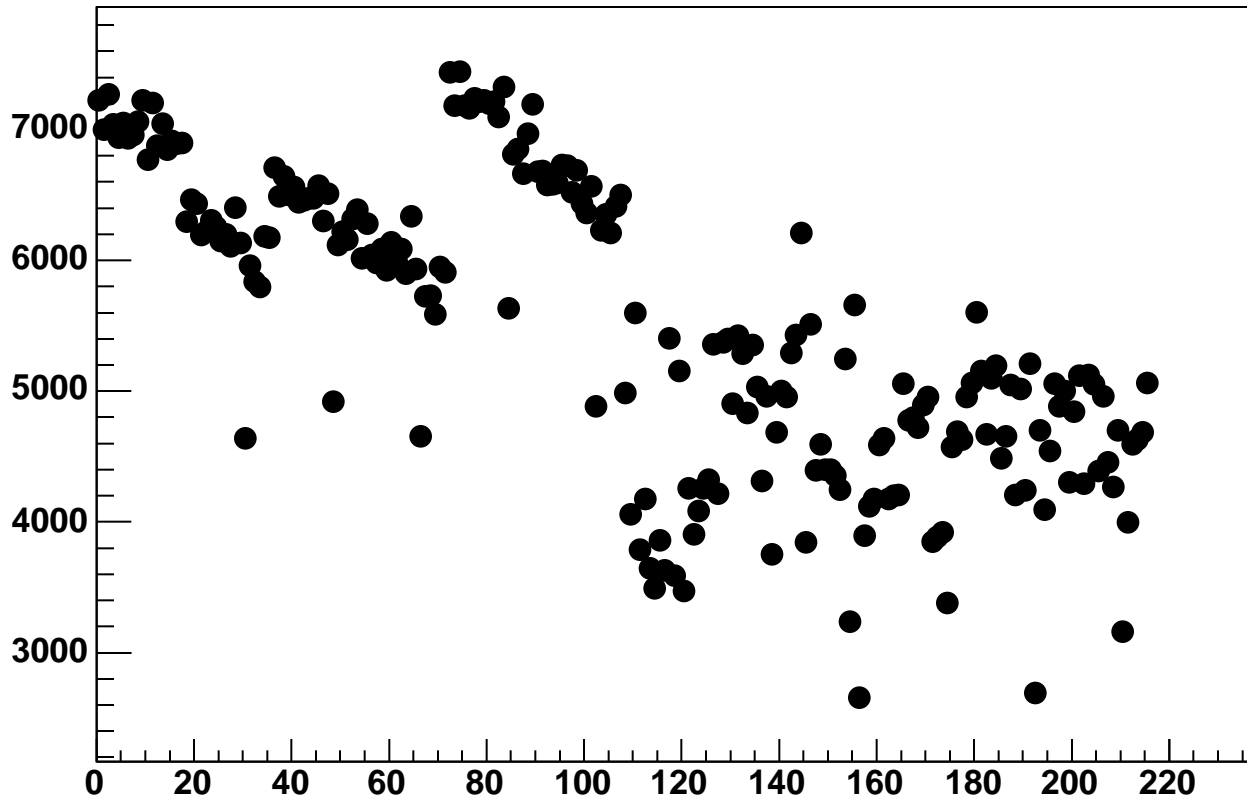
Enable 0, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



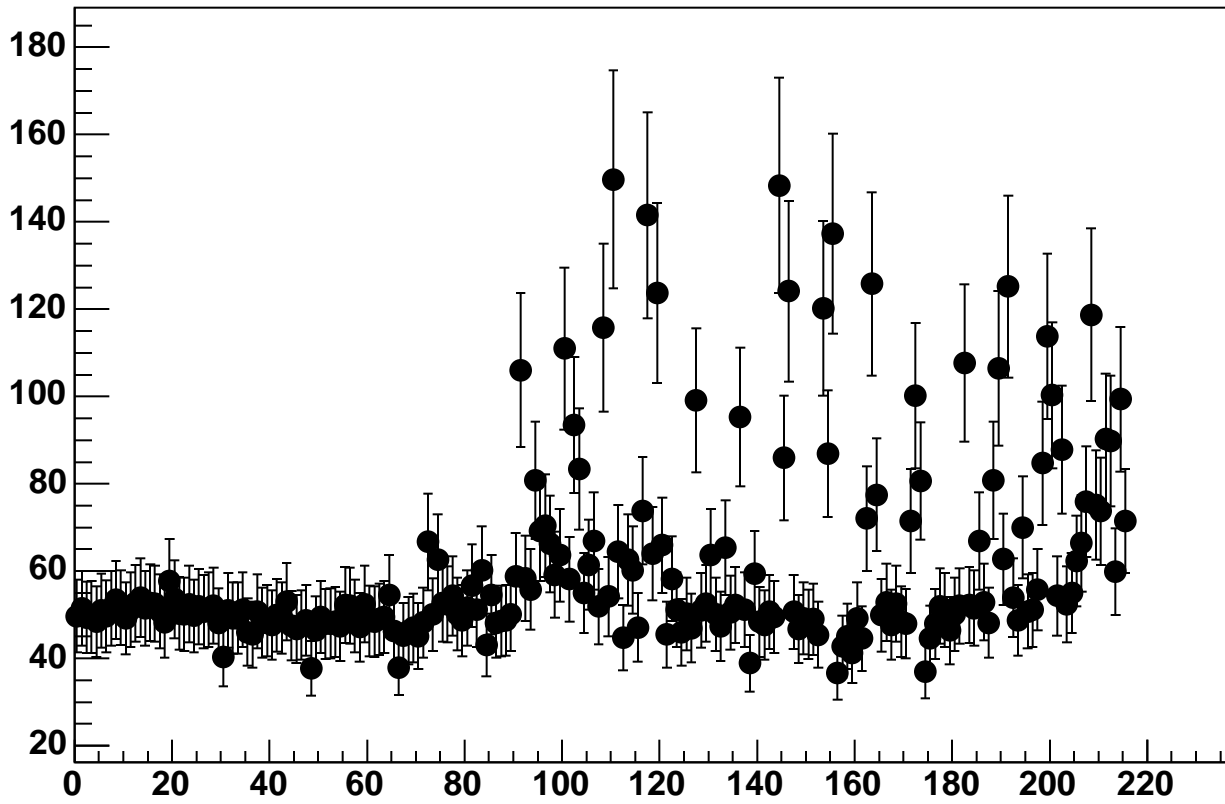
Enable 0, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



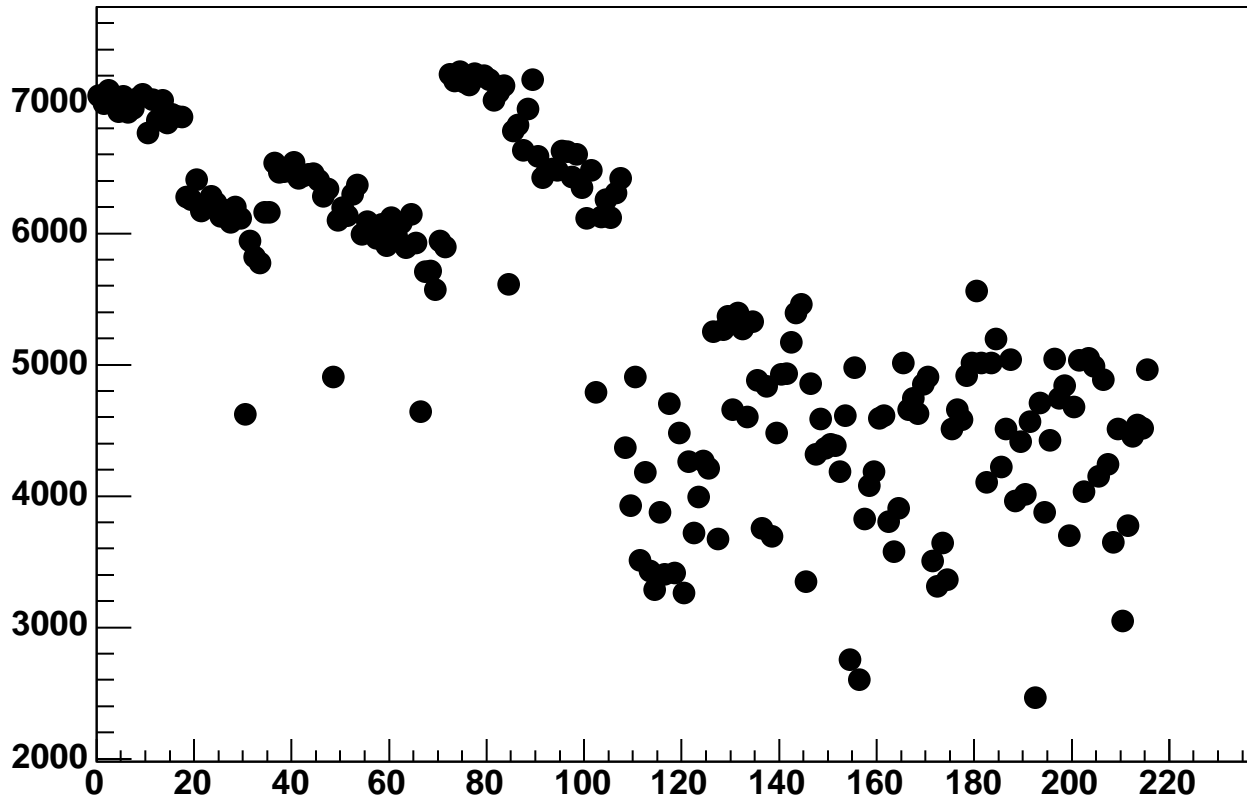
Enable 0, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



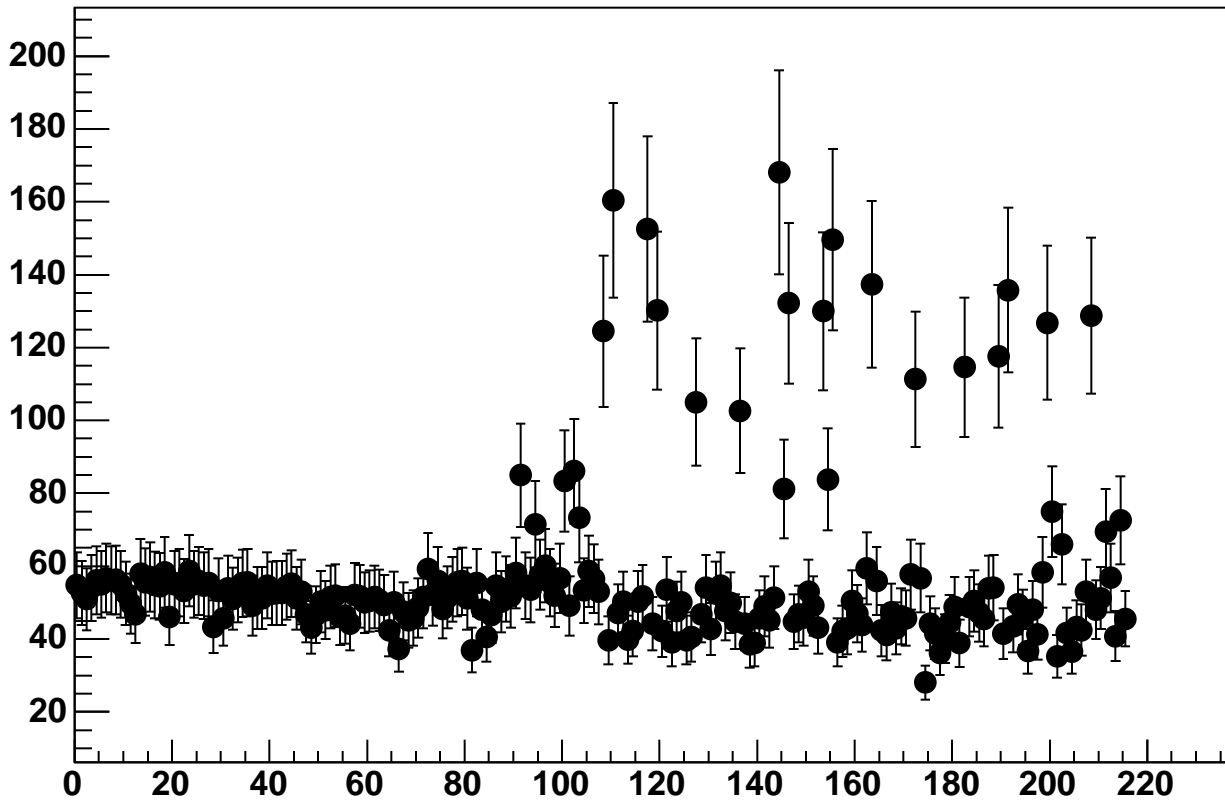
Enable 0, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



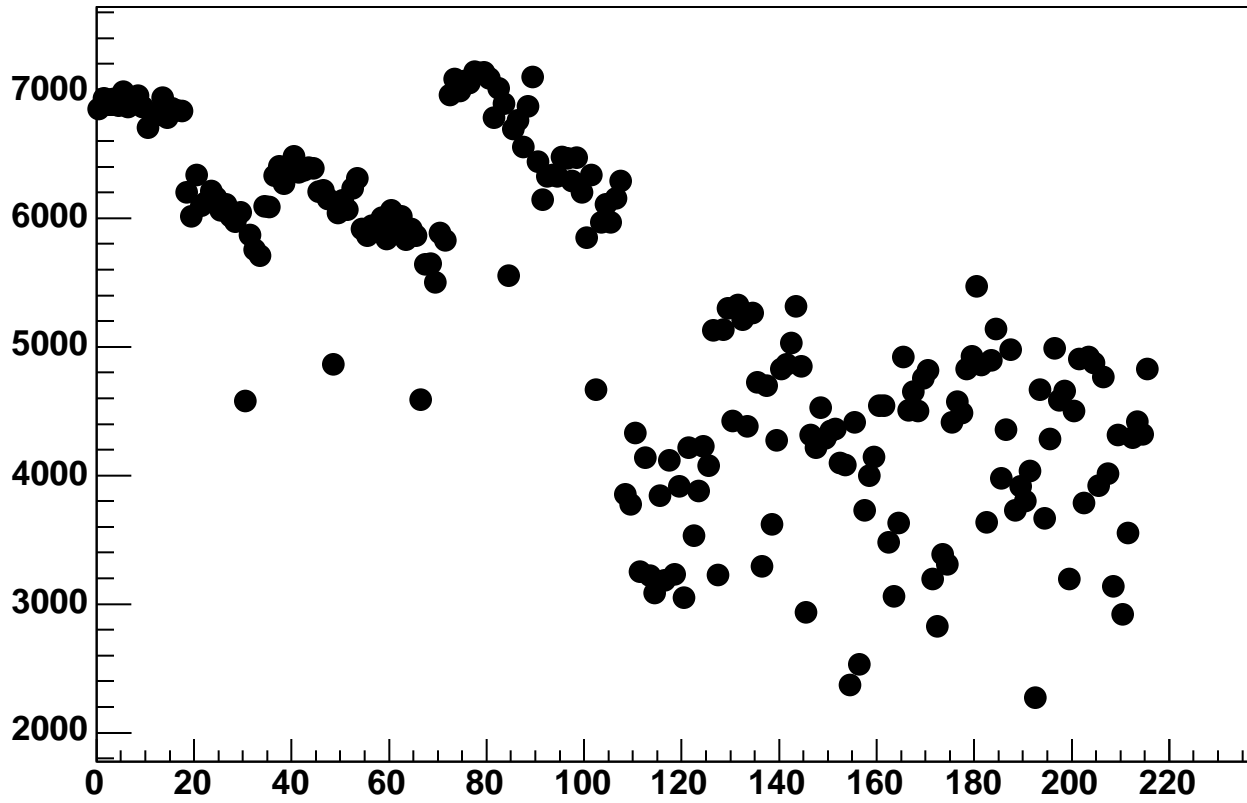
Enable 0, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



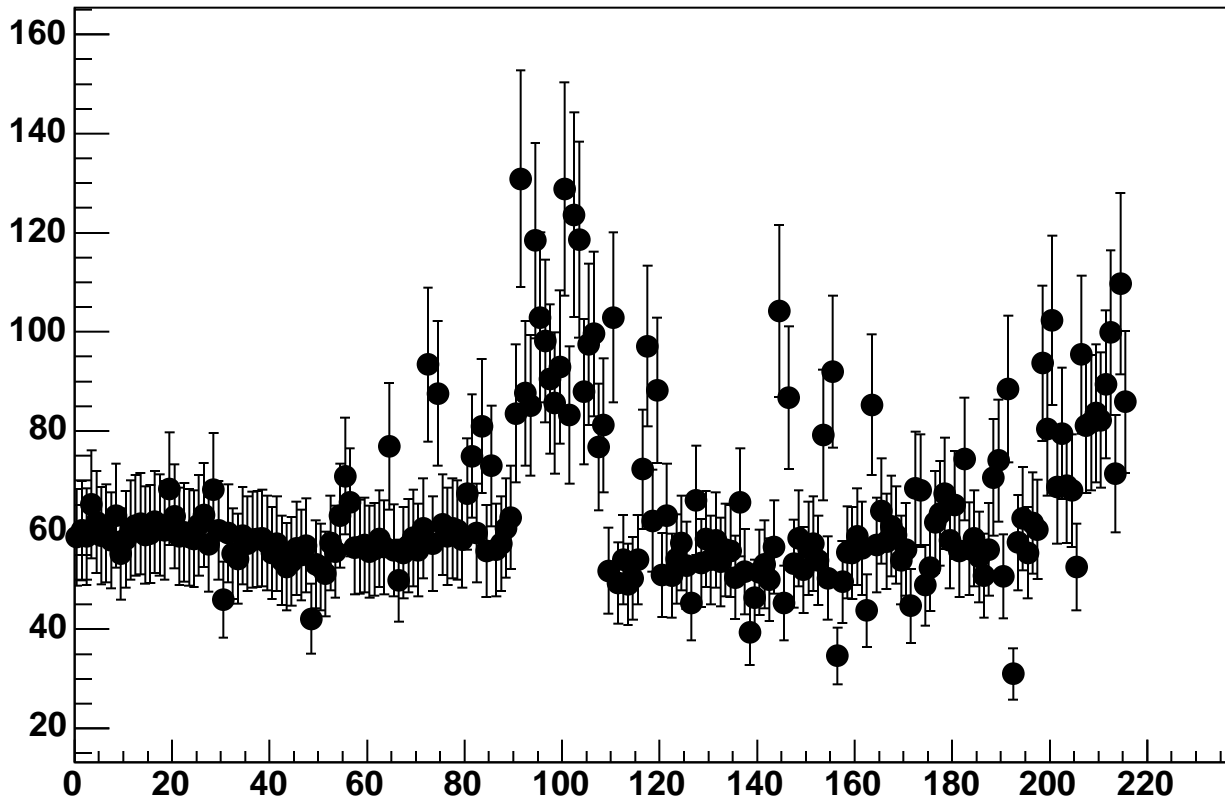
Enable 0, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



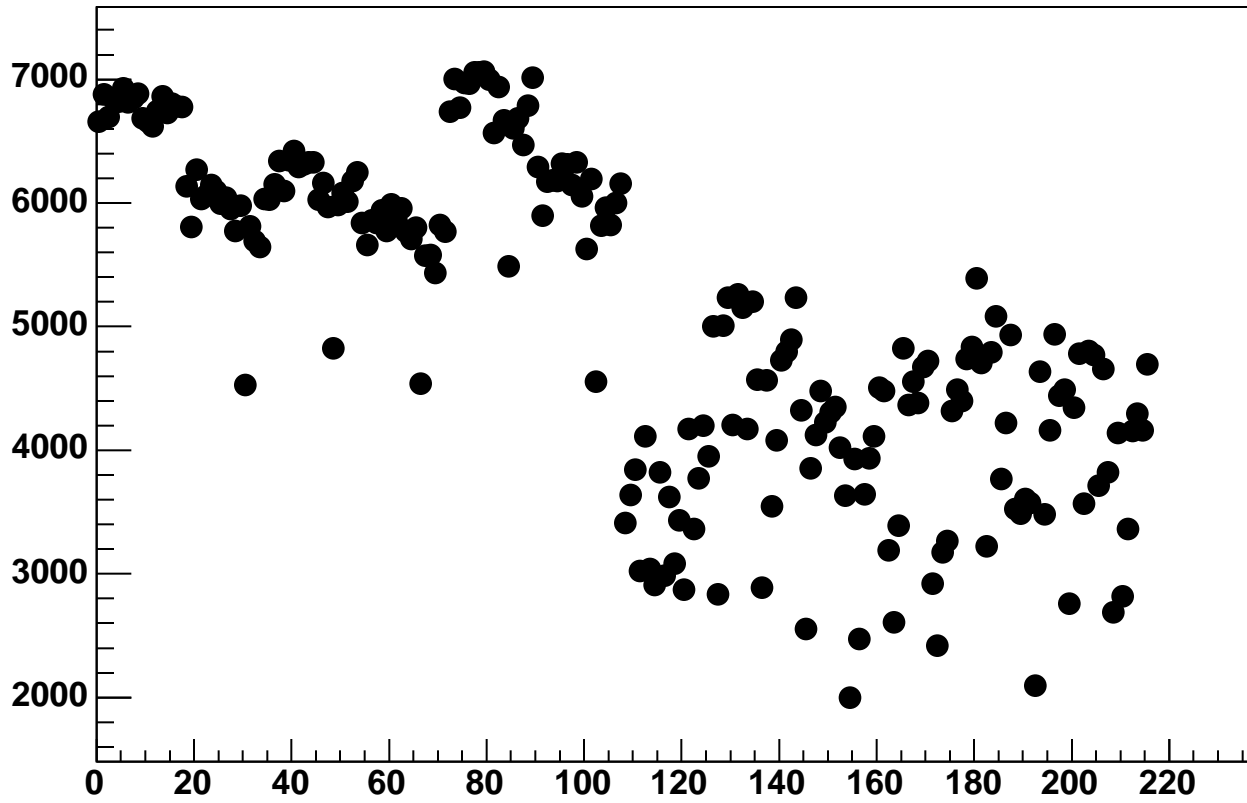
Enable 0, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



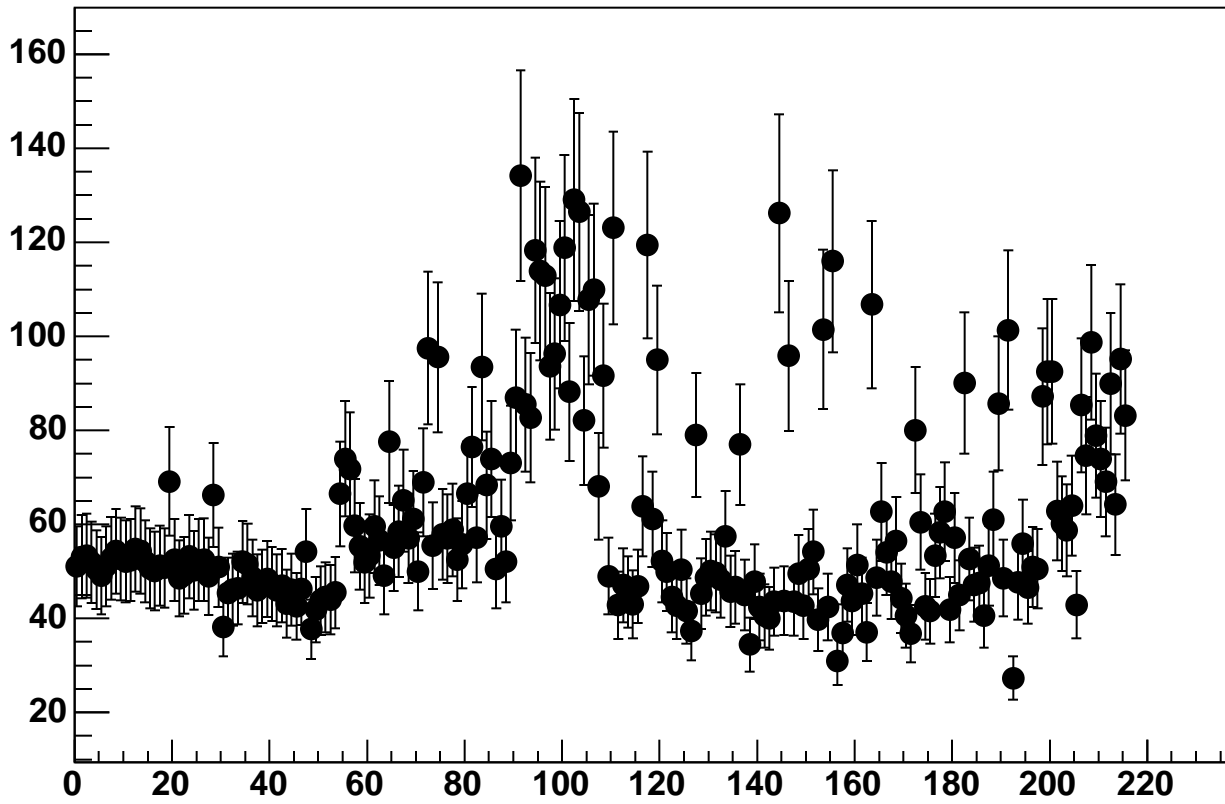
Enable 0, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



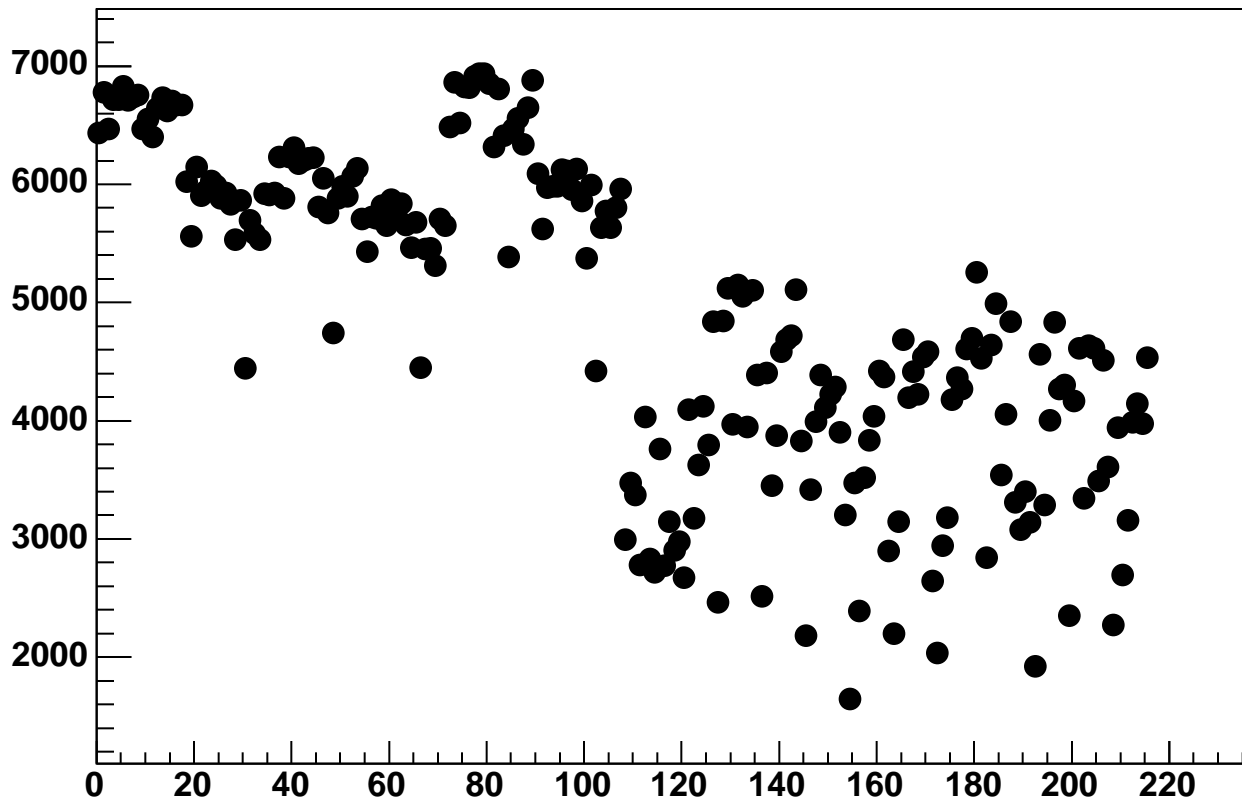
Enable 0, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



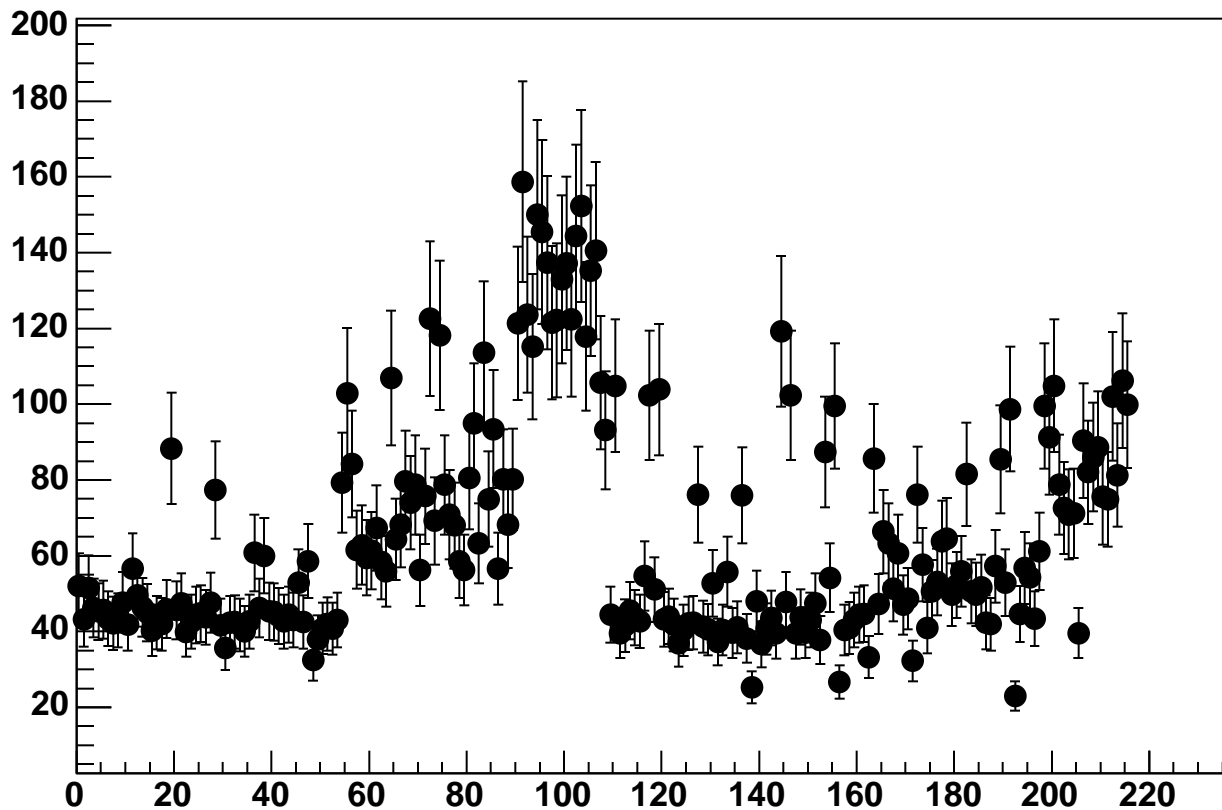
Enable 0, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



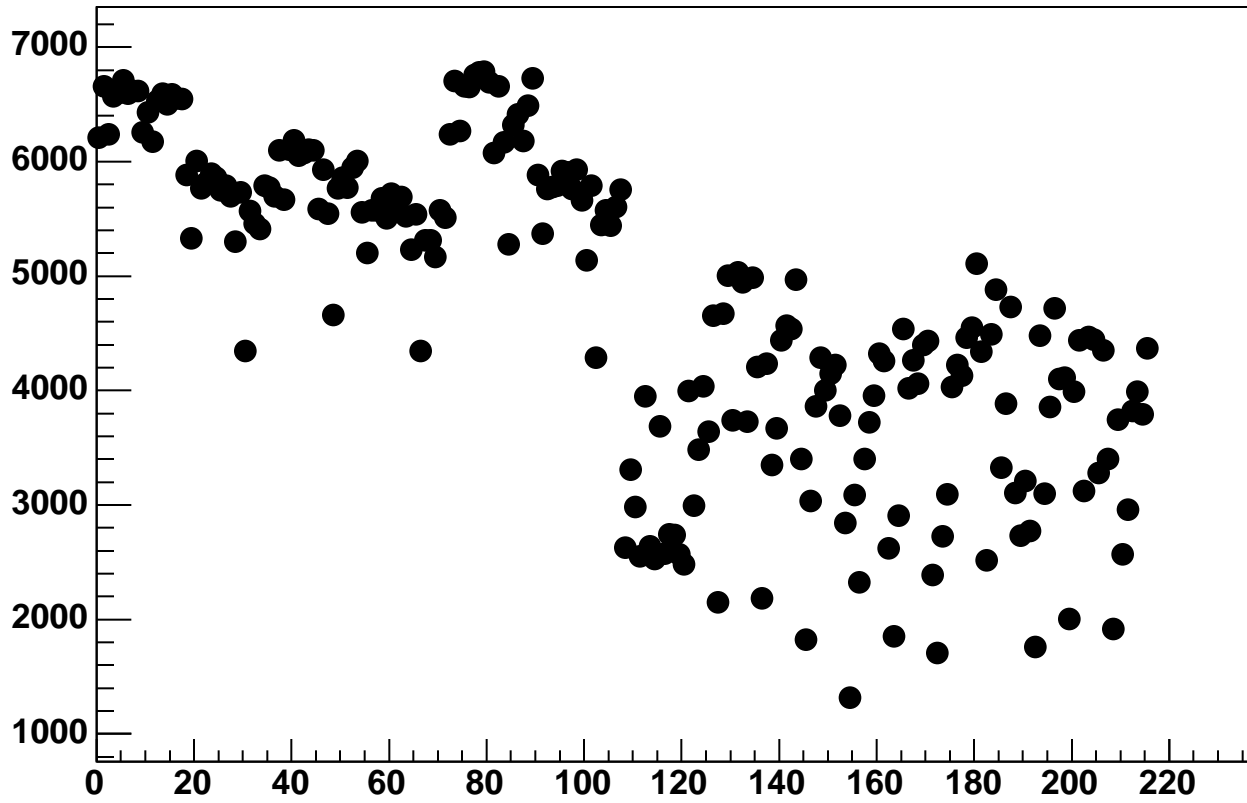
Enable 0, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



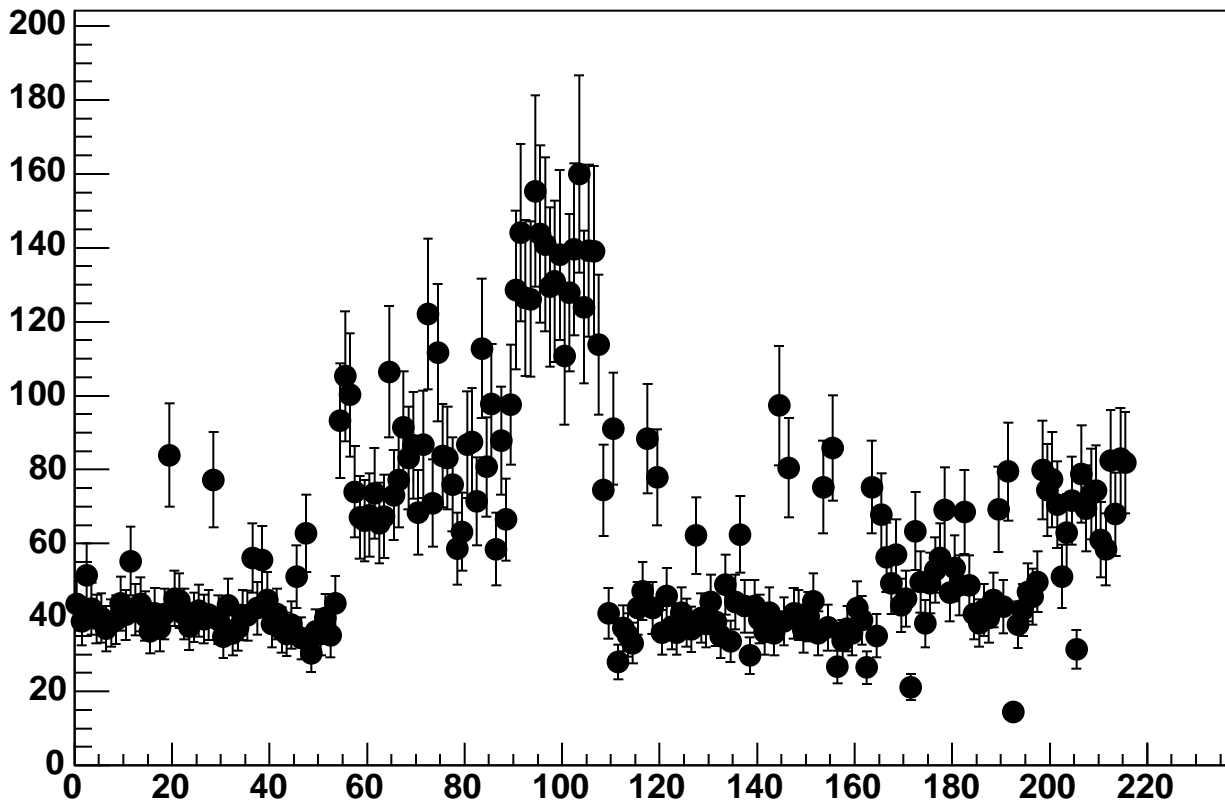
Enable 0, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



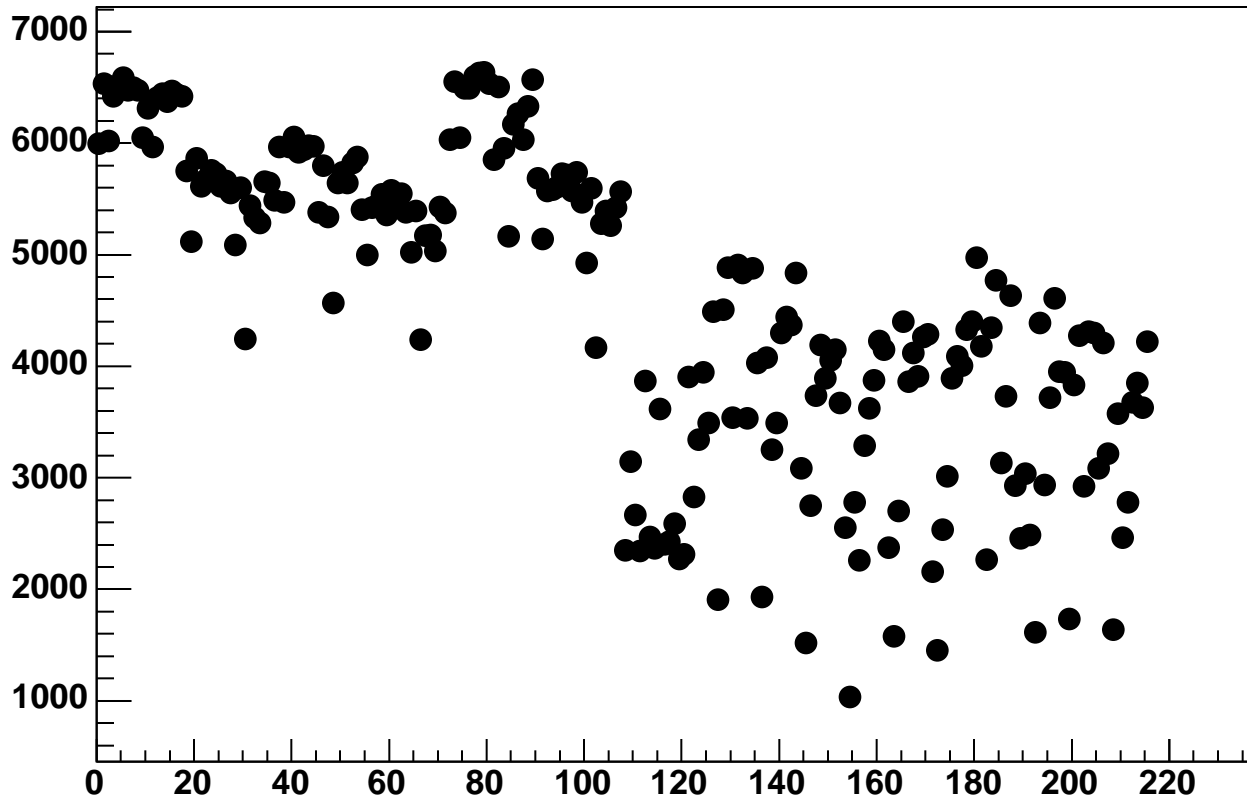
Enable 0, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



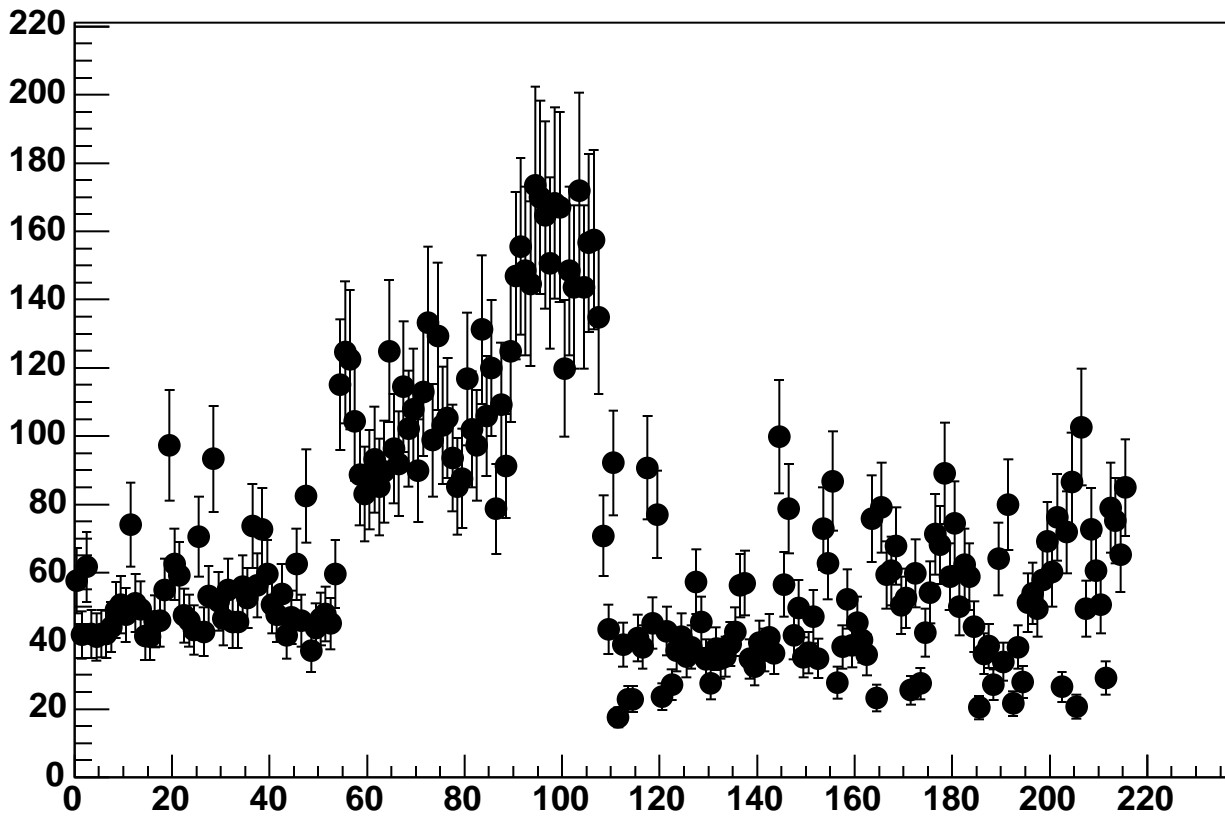
Enable 0, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



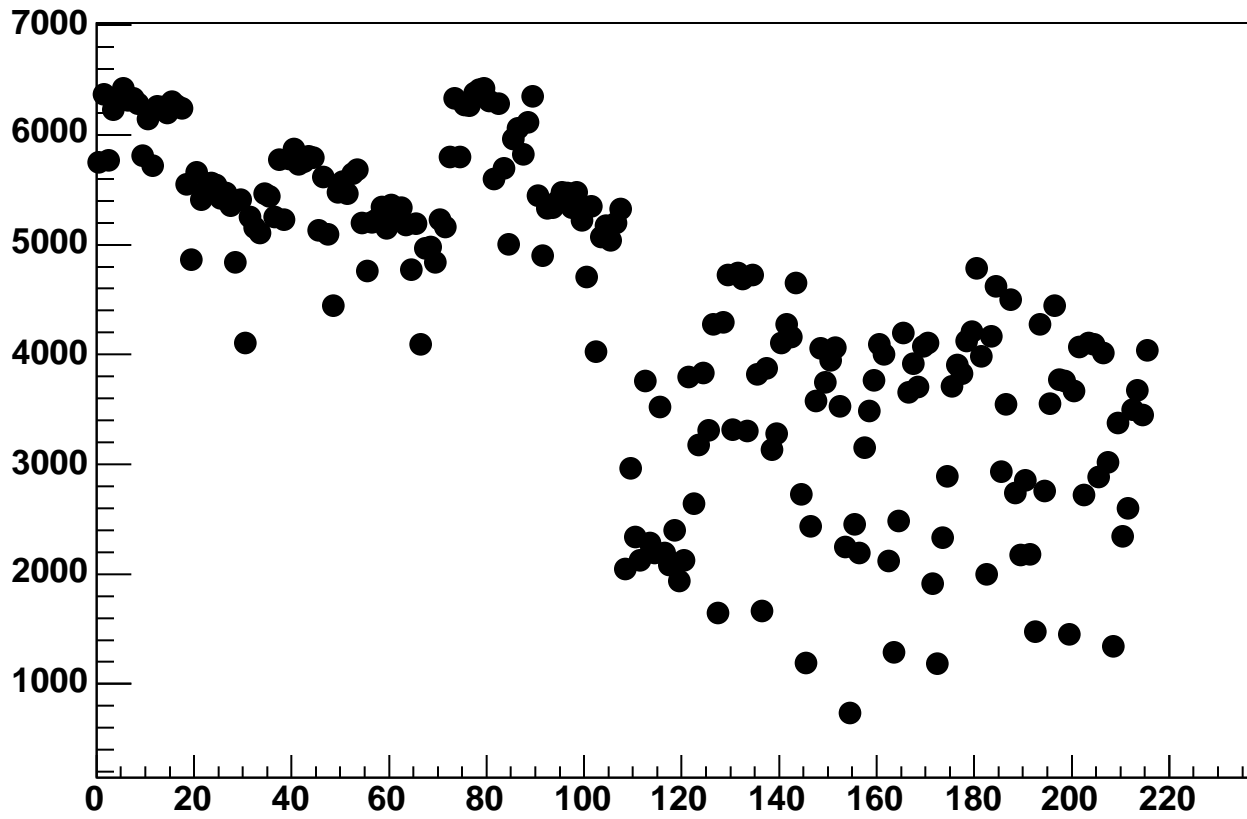
Enable 0, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



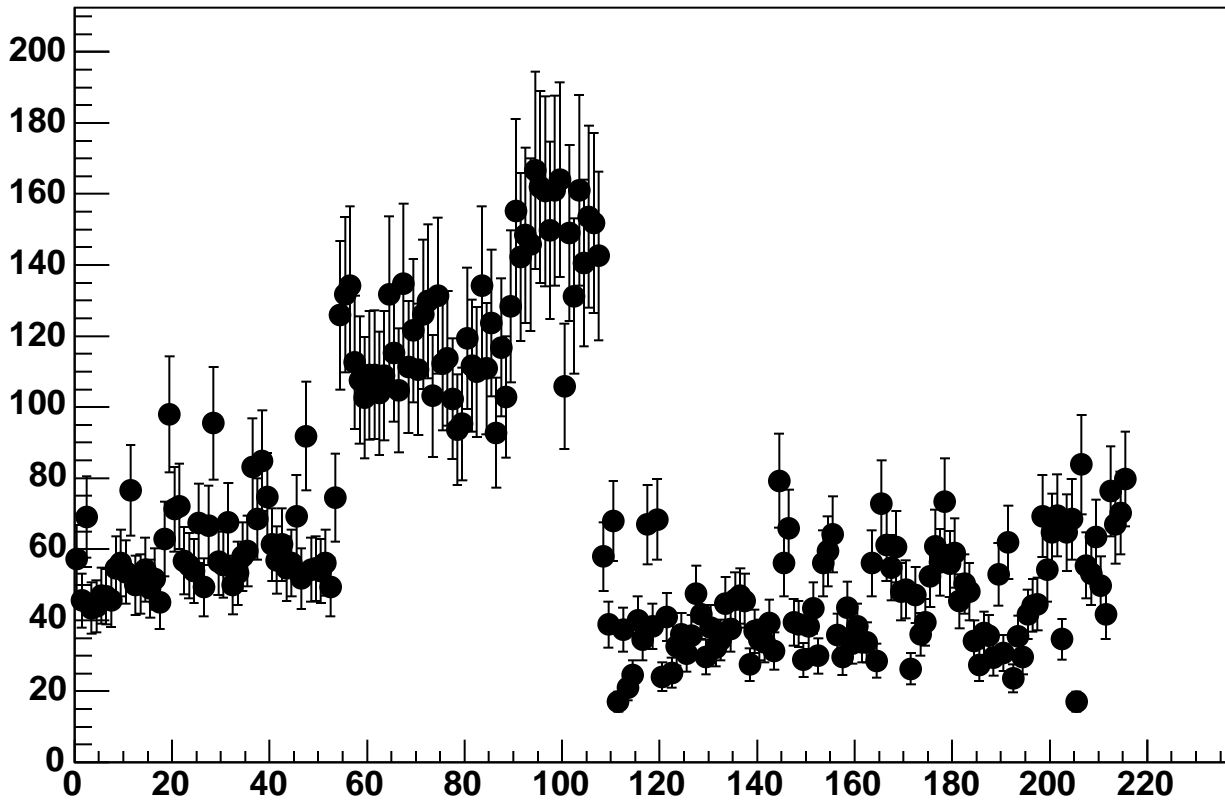
Enable 0, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



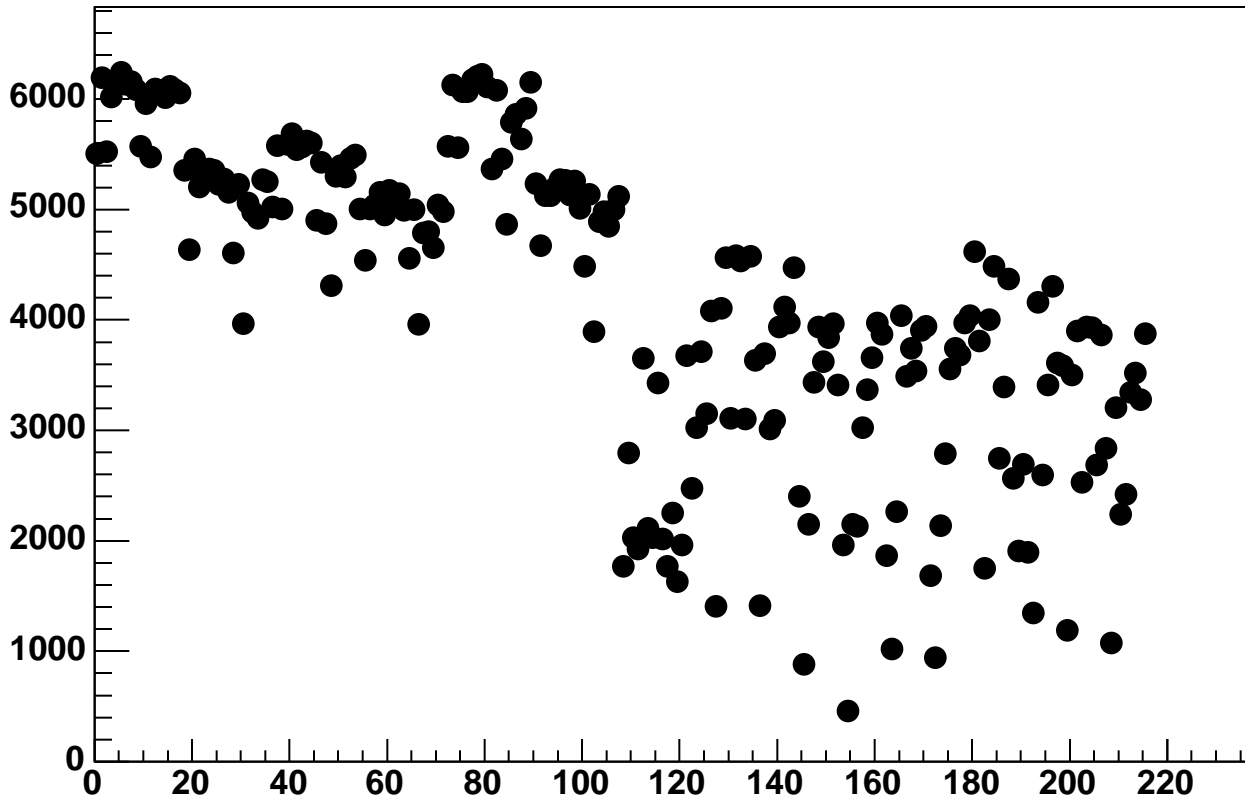
Enable 0, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



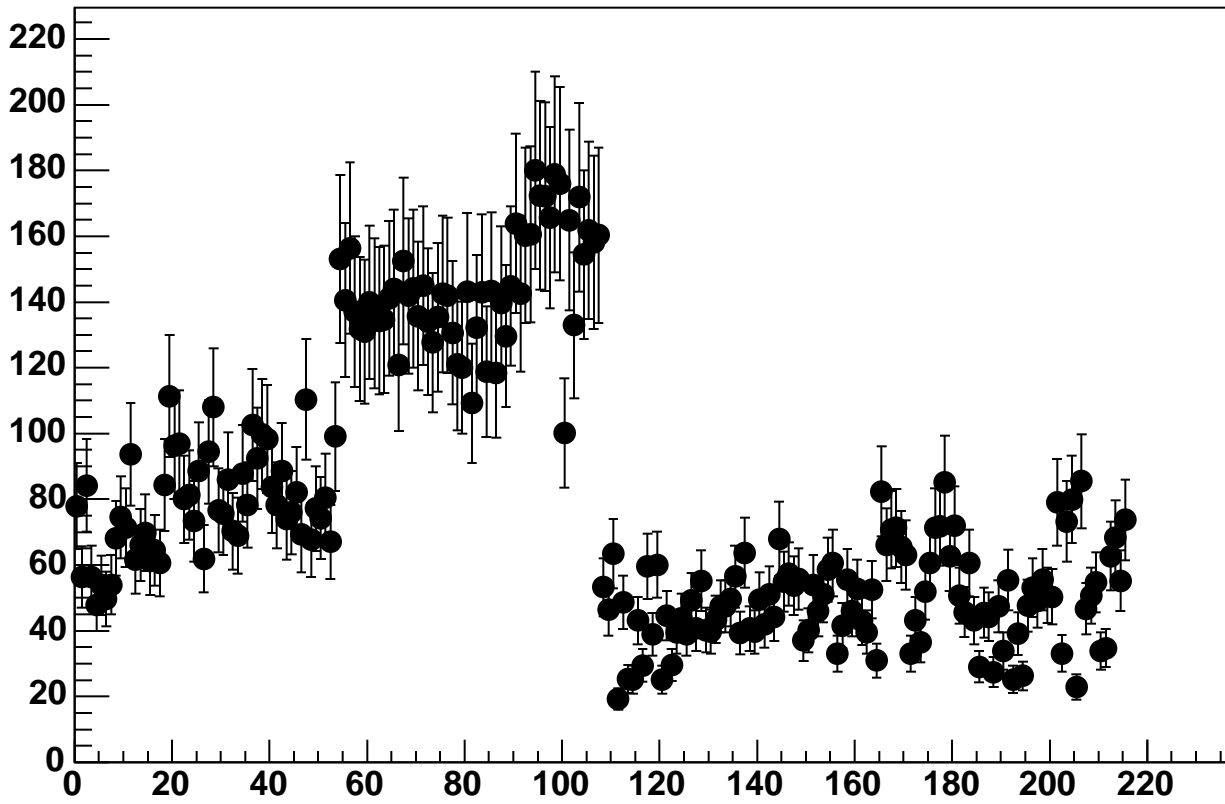
Enable 0, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



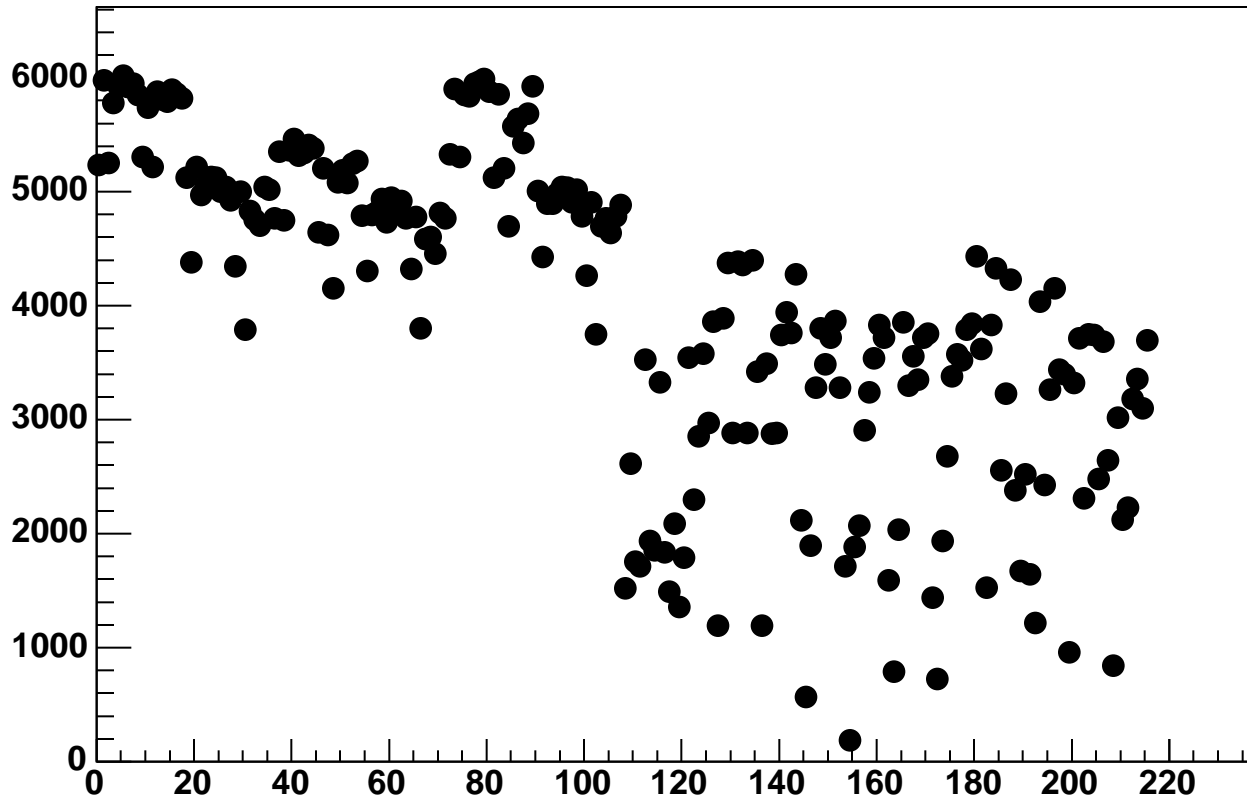
Enable 0, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



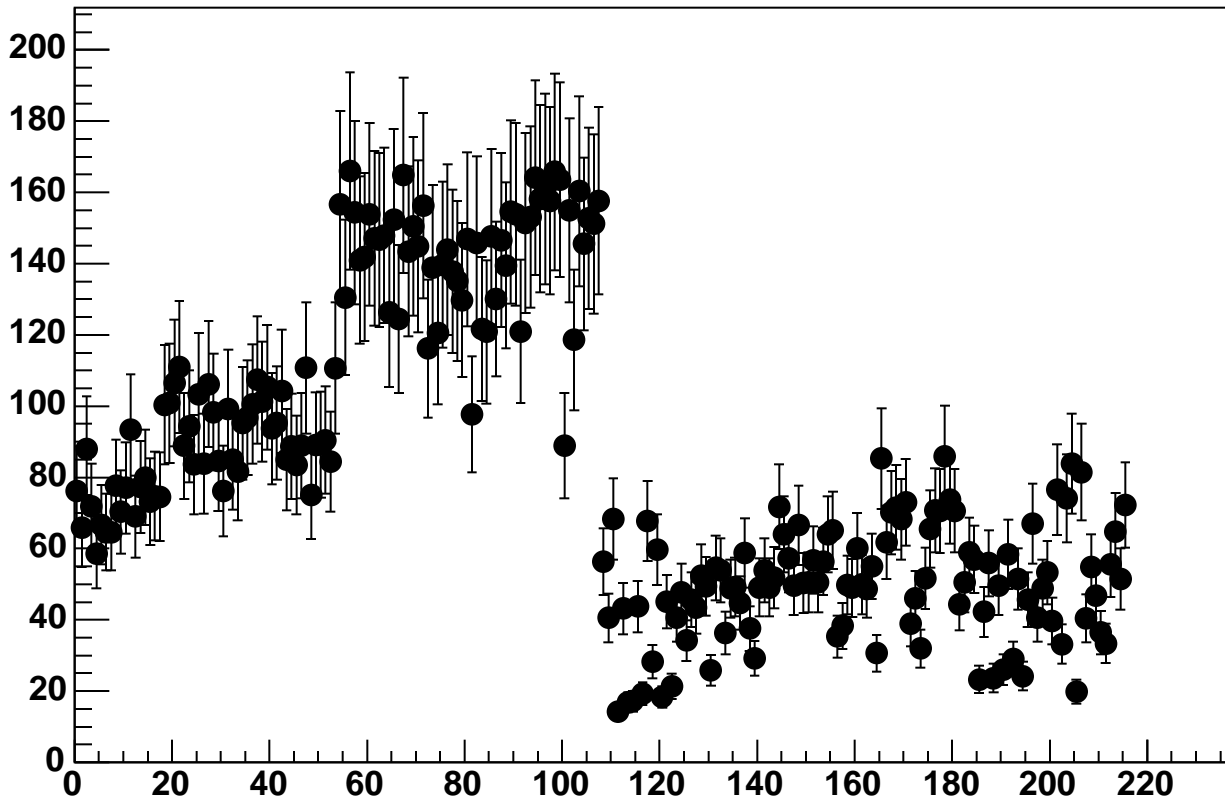
Enable 0, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



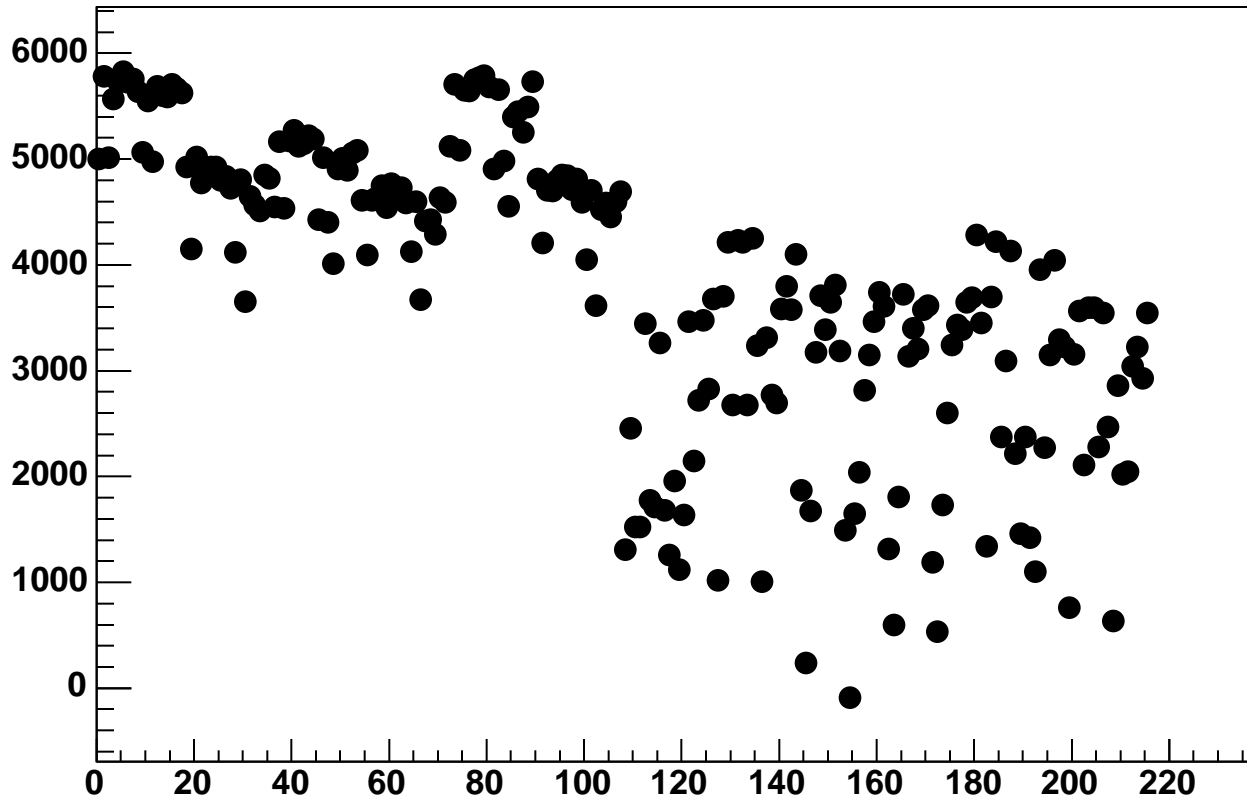
Enable 0, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



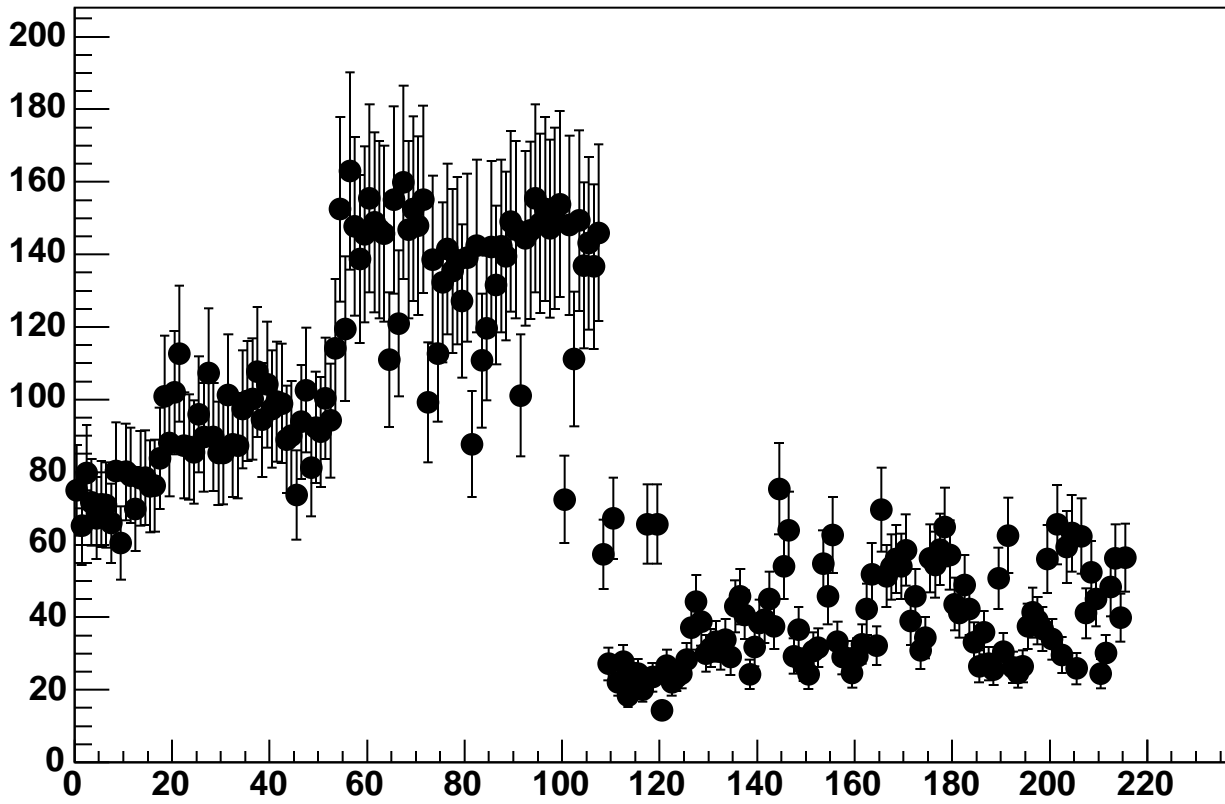
Enable 0, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



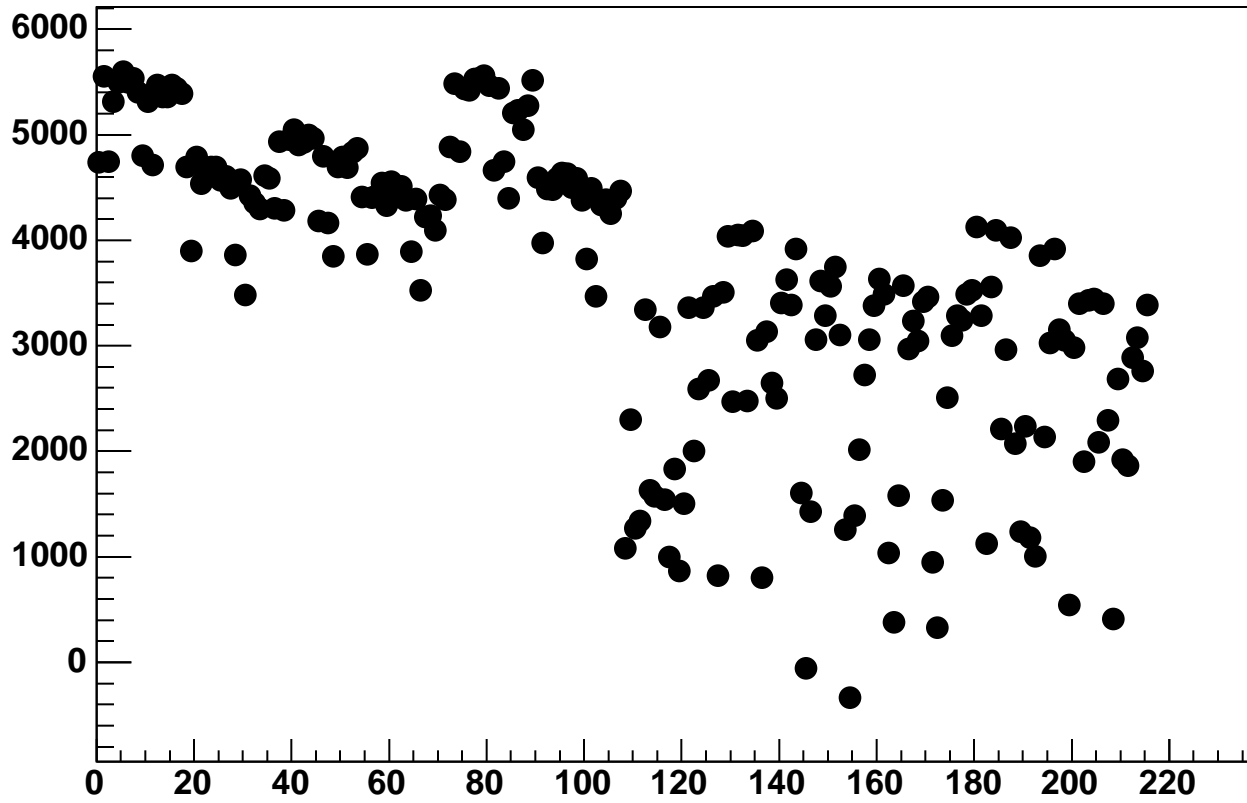
Enable 0, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



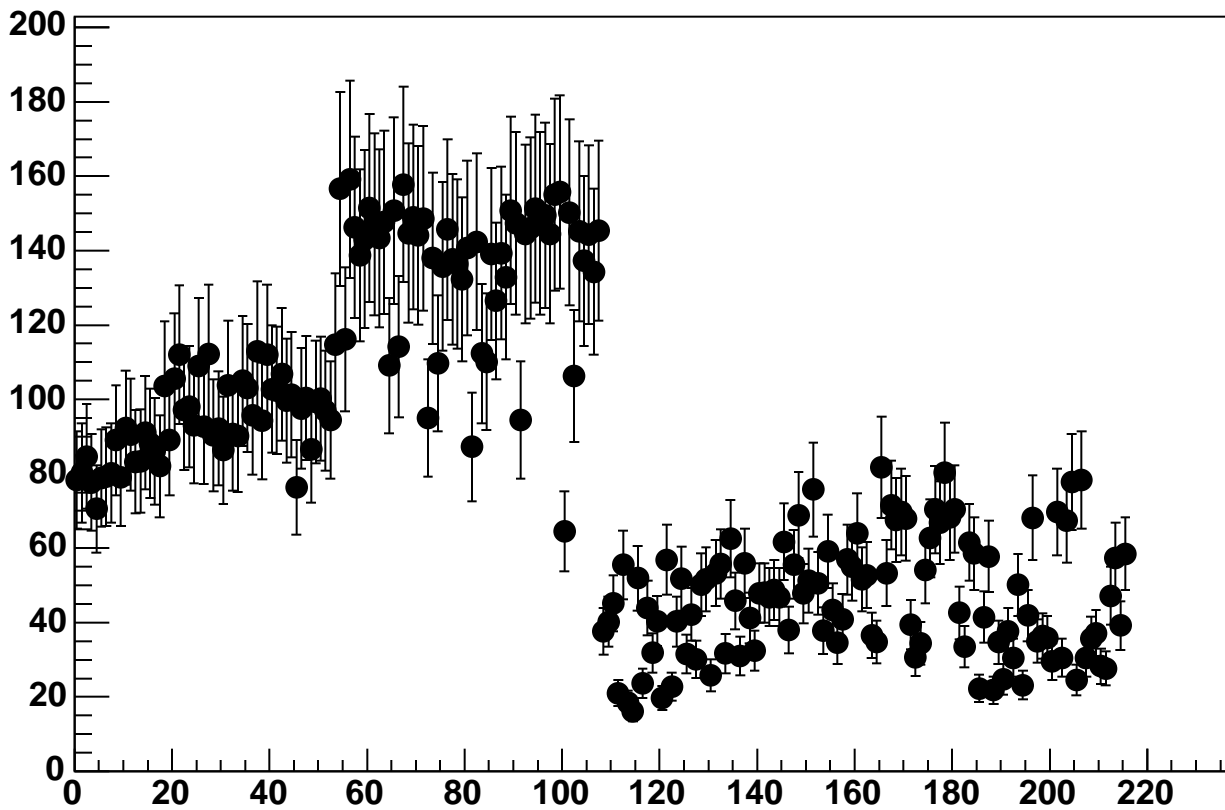
Enable 0, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



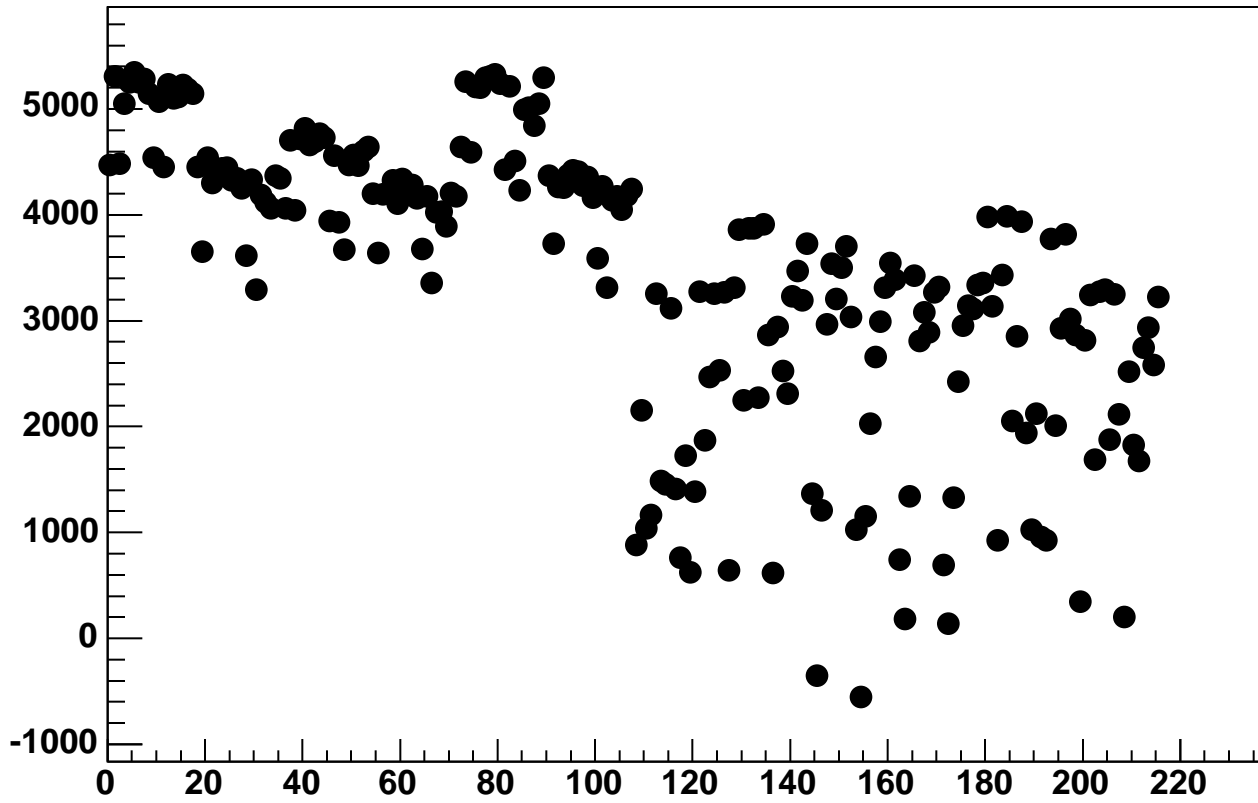
Enable 0, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



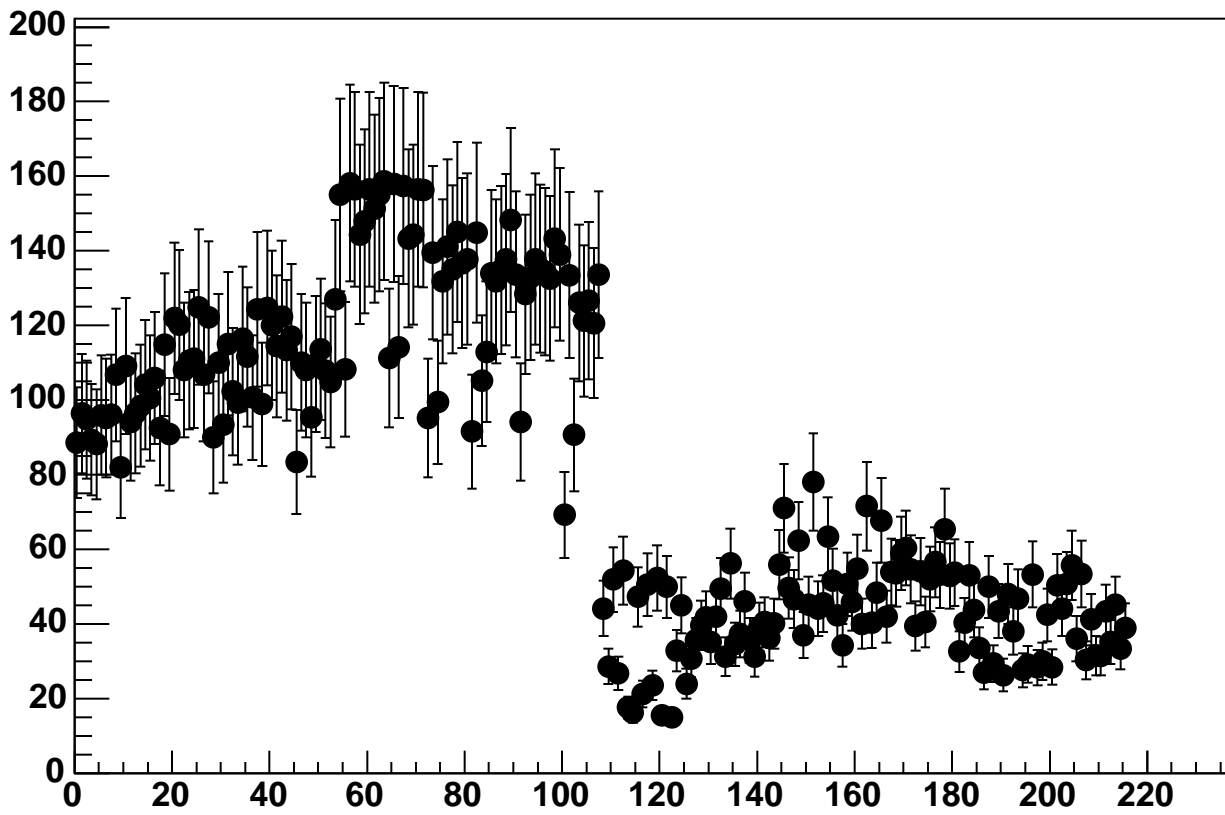
Enable 0, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



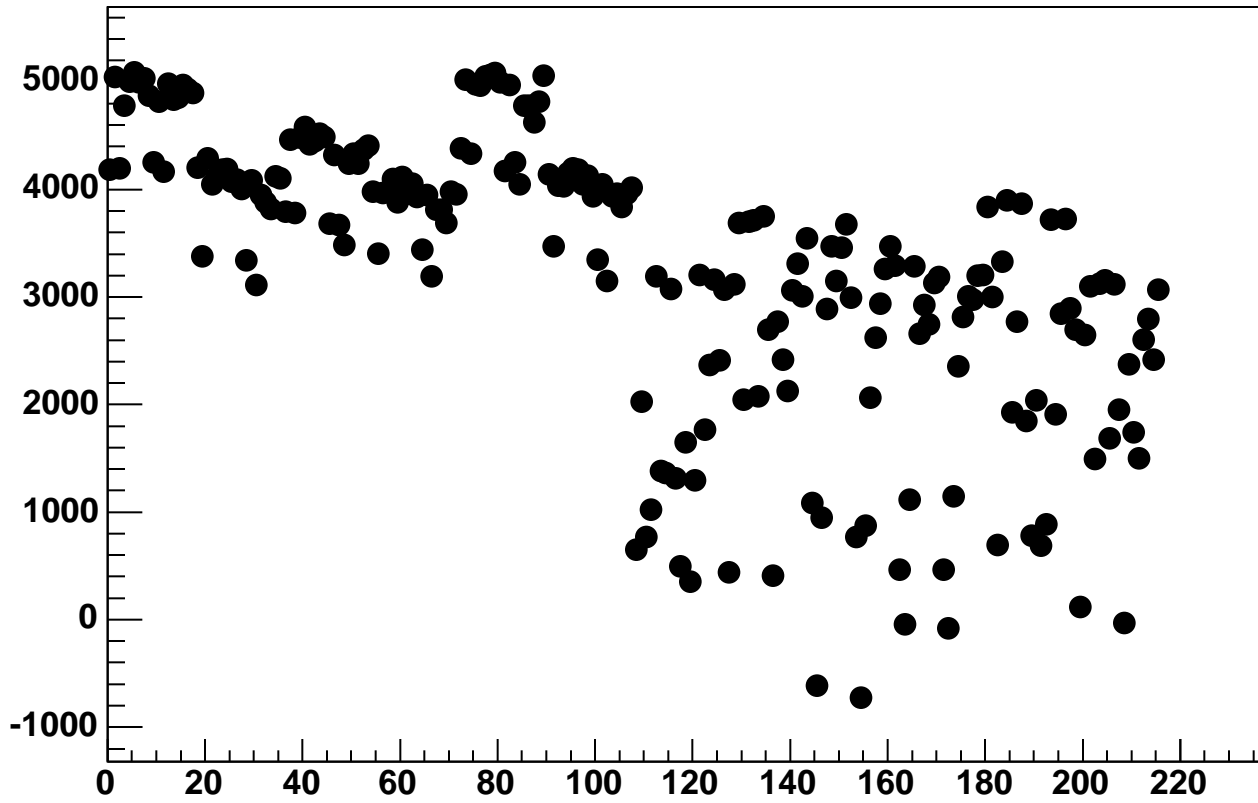
Enable 0, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



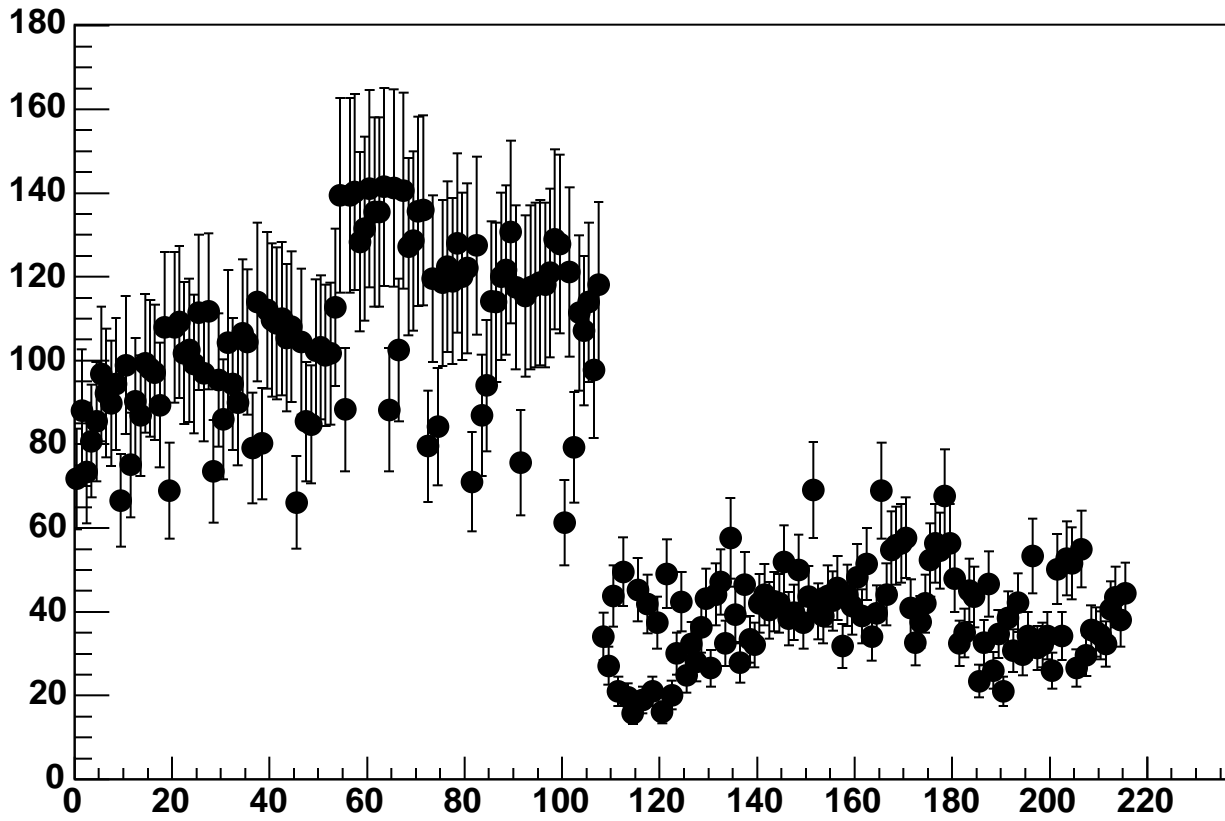
Enable 0, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



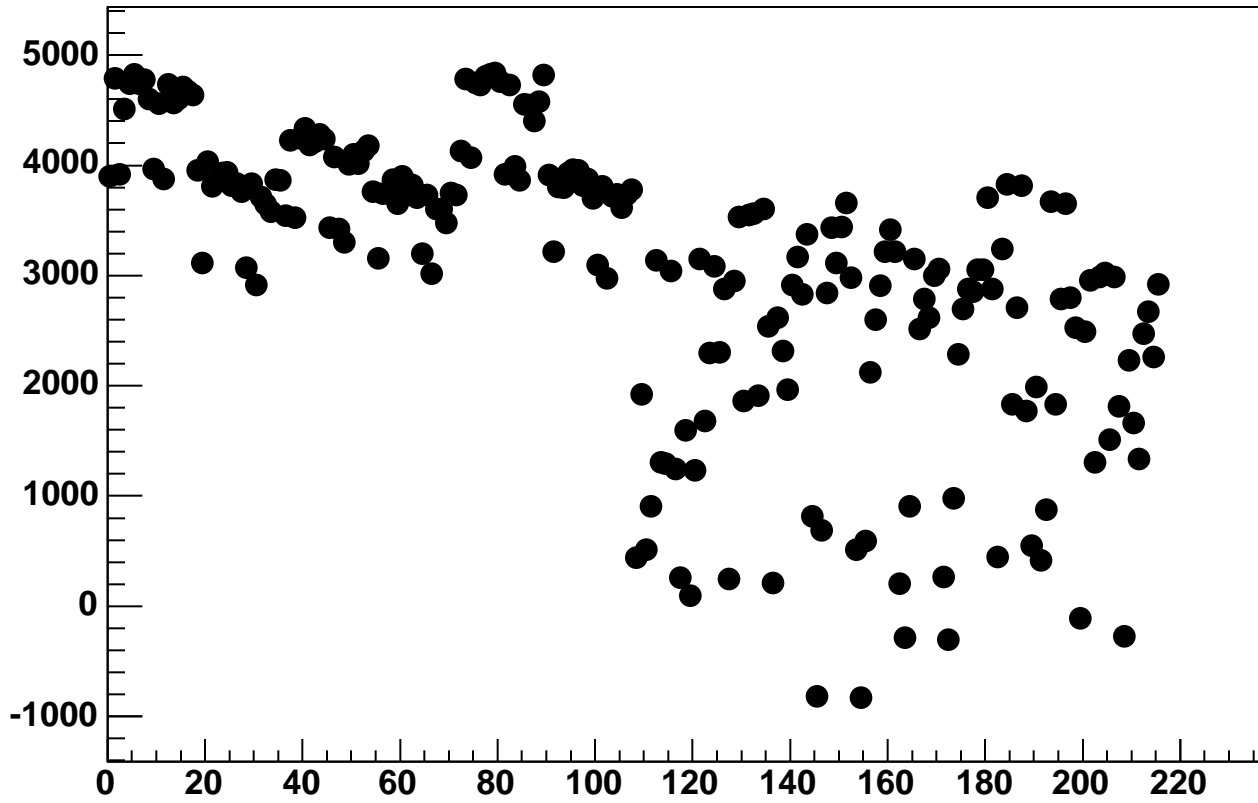
Enable 0, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



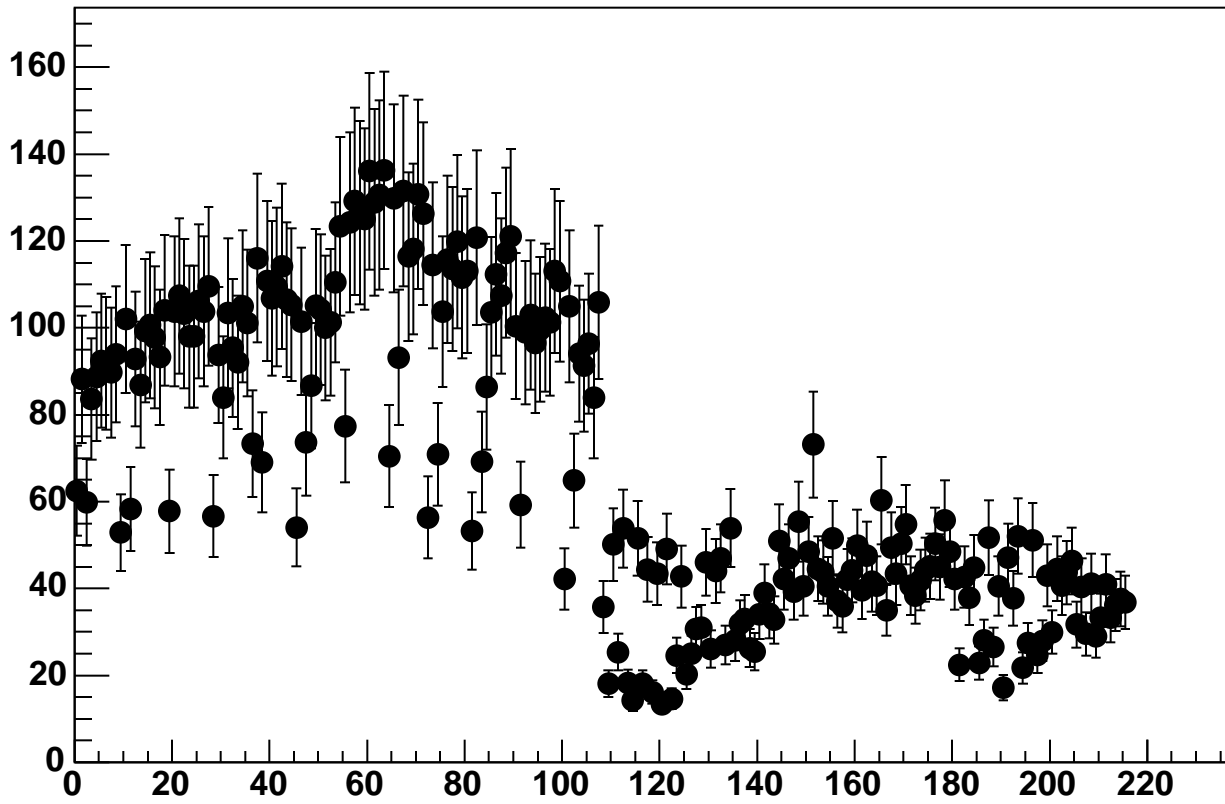
Enable 0, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



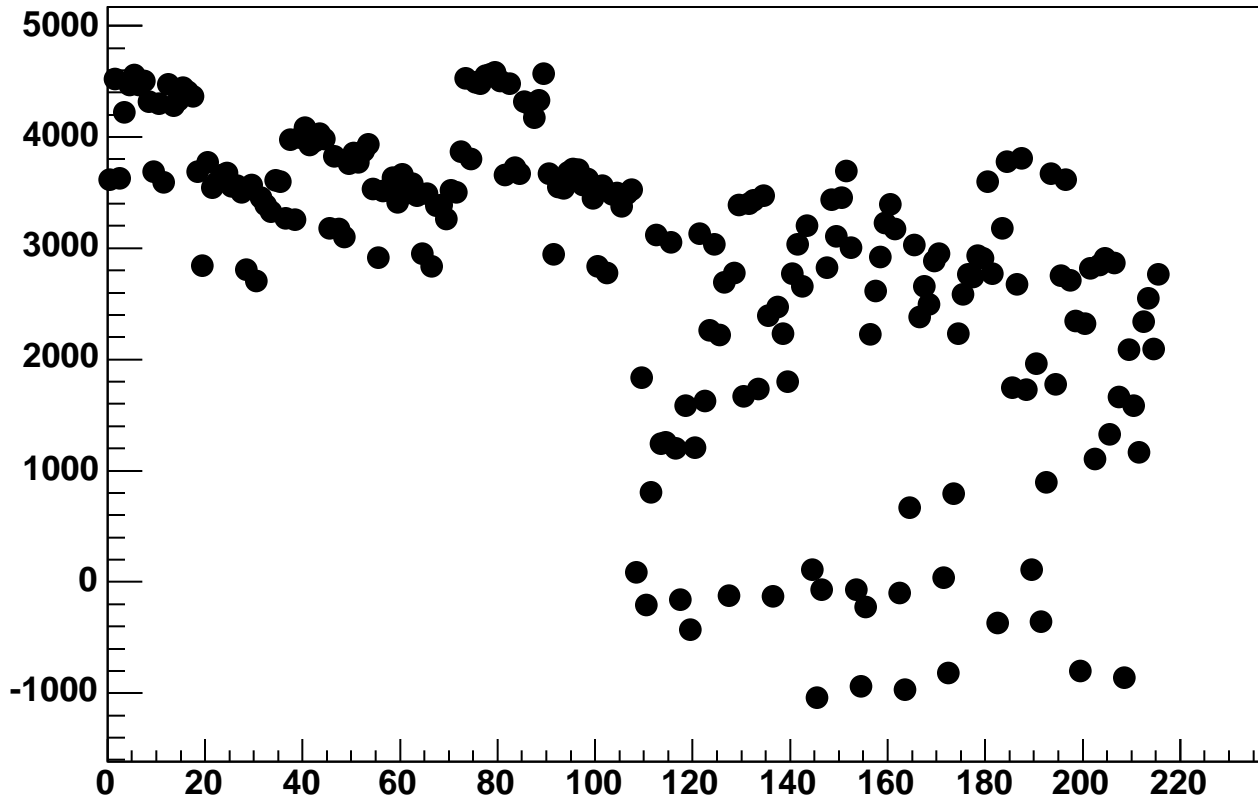
Enable 0, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



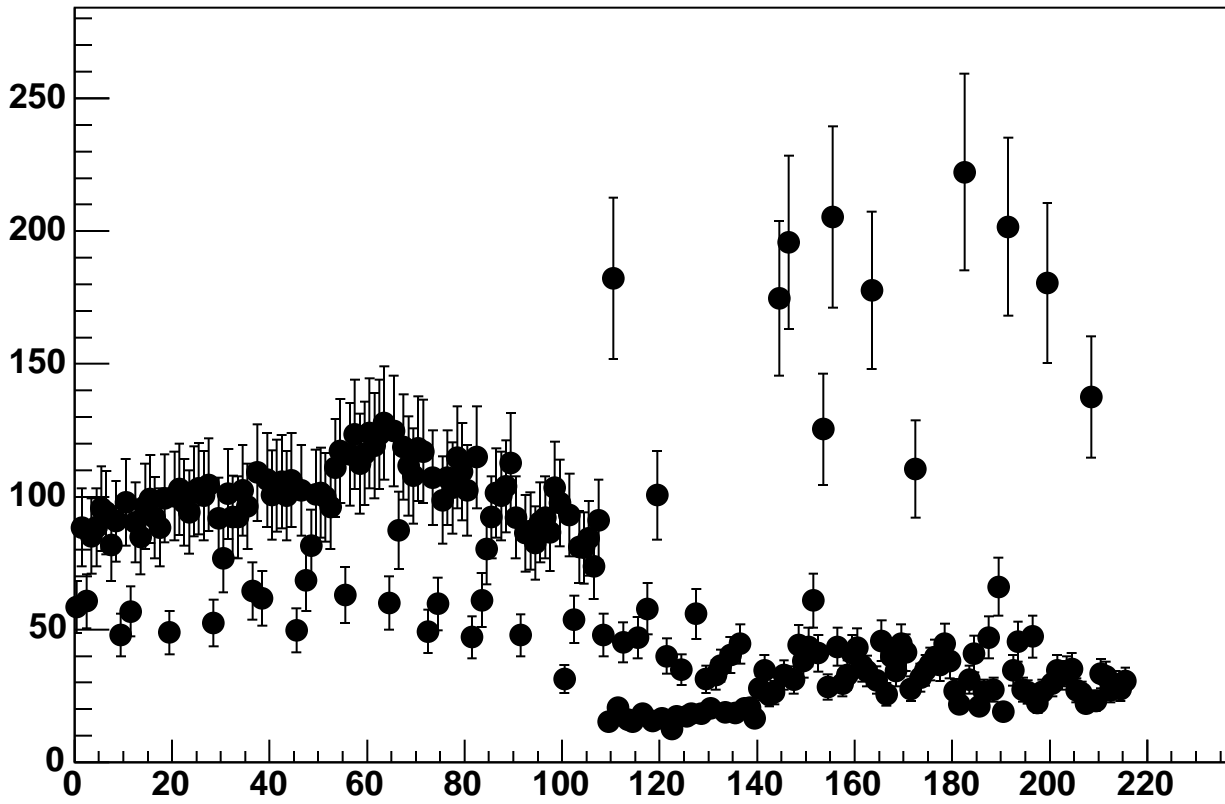
Enable 0, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



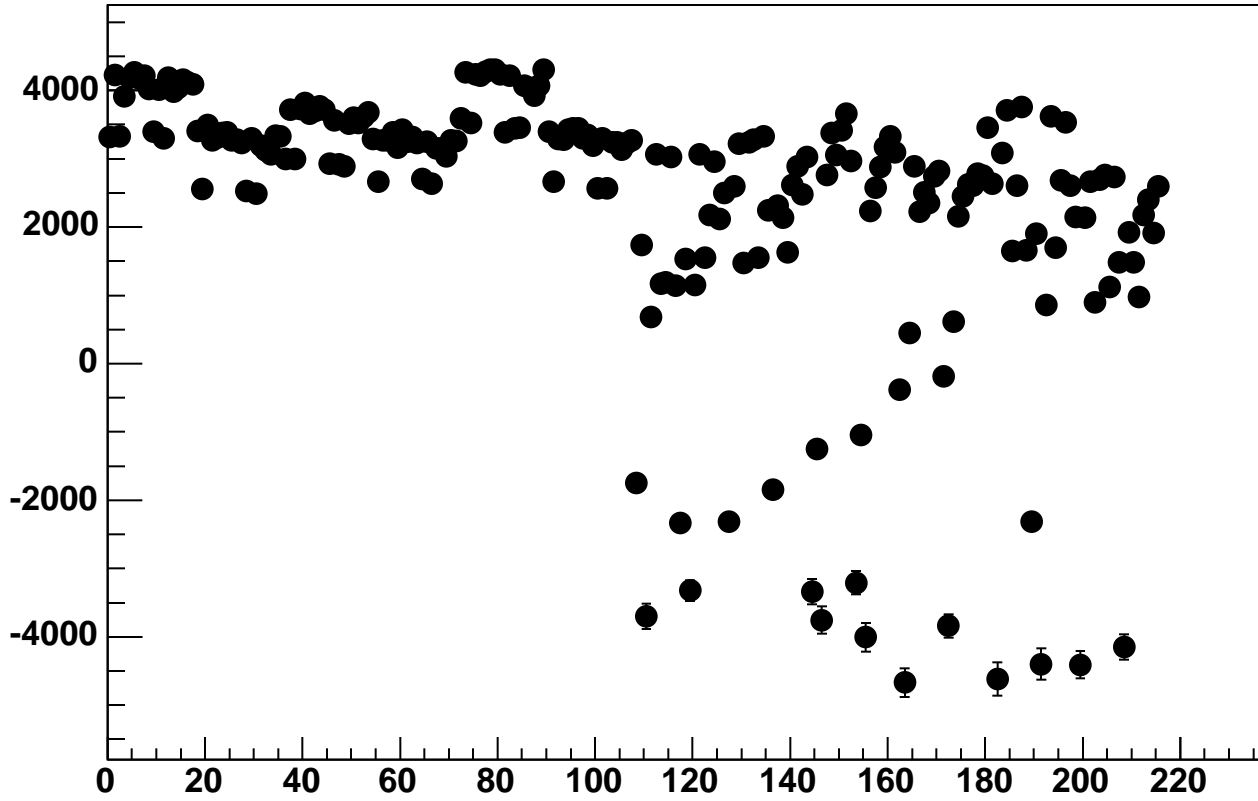
Enable 0, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



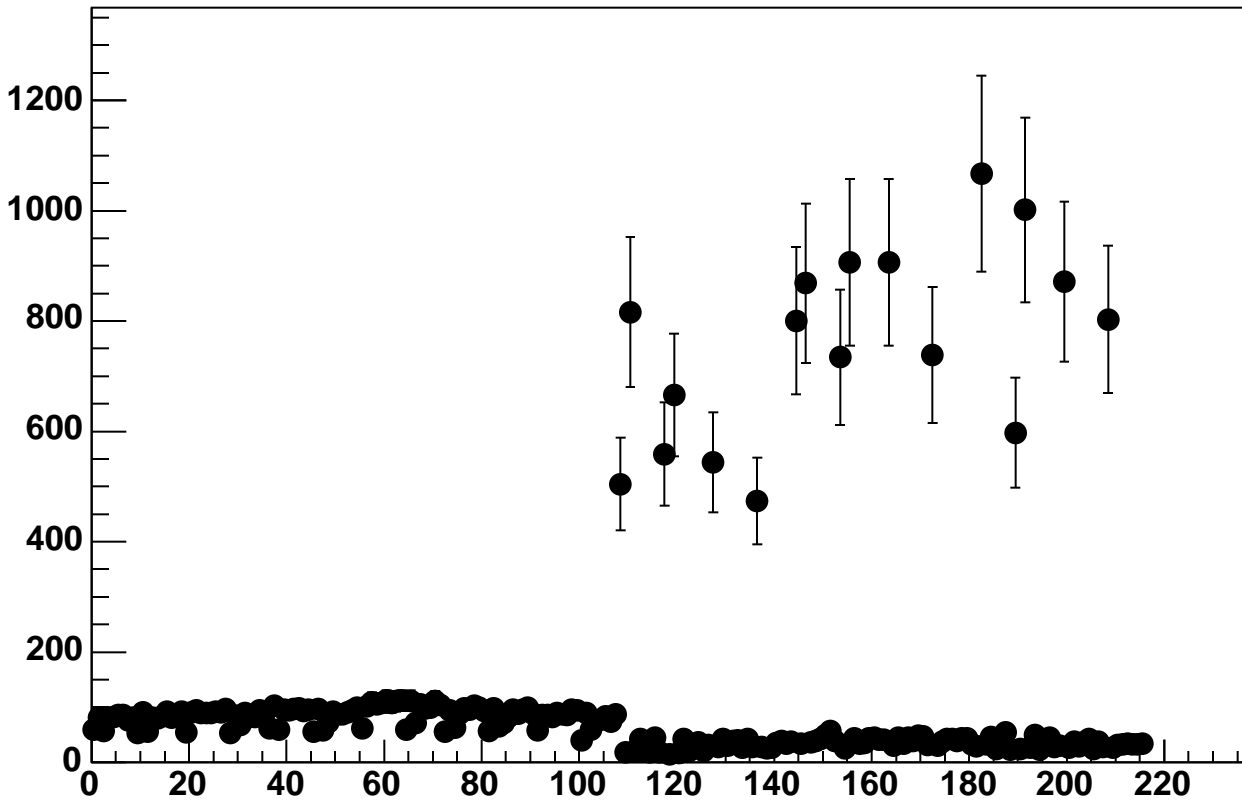
Enable 0, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



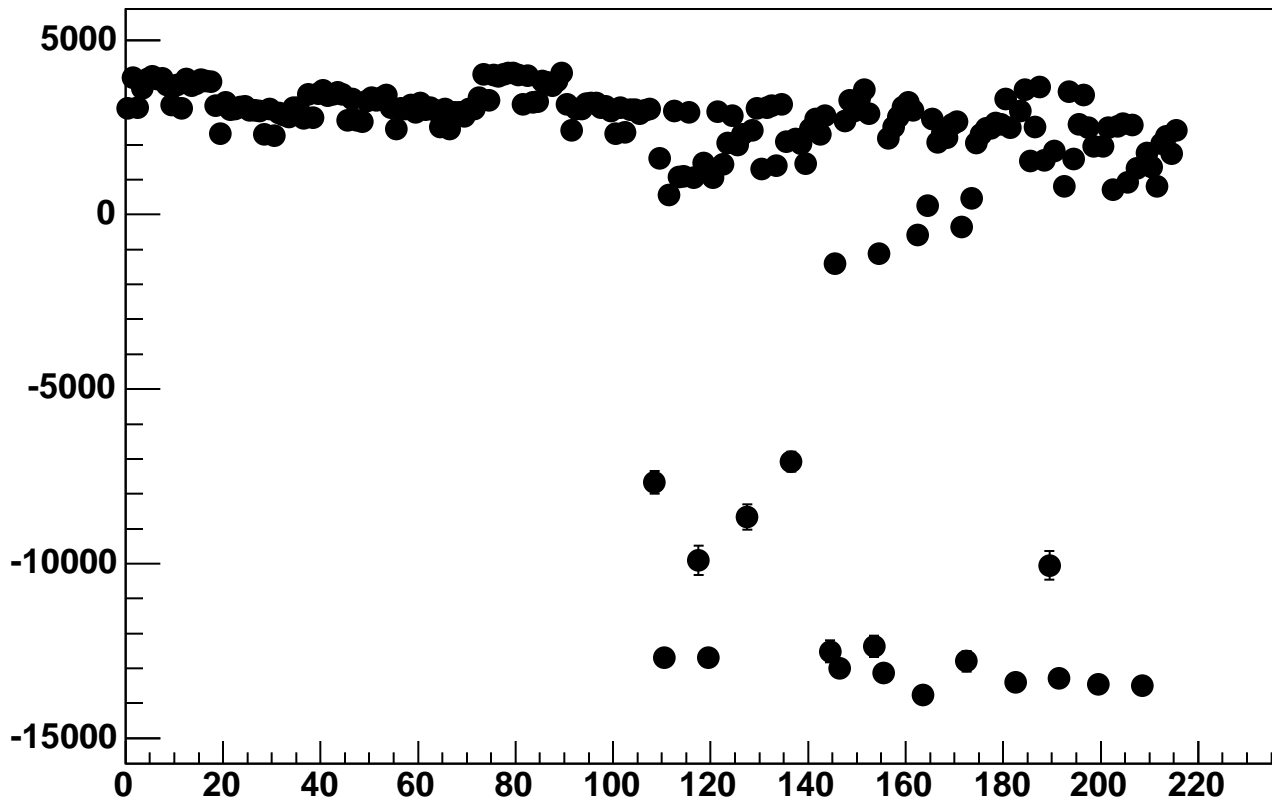
Enable 0, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



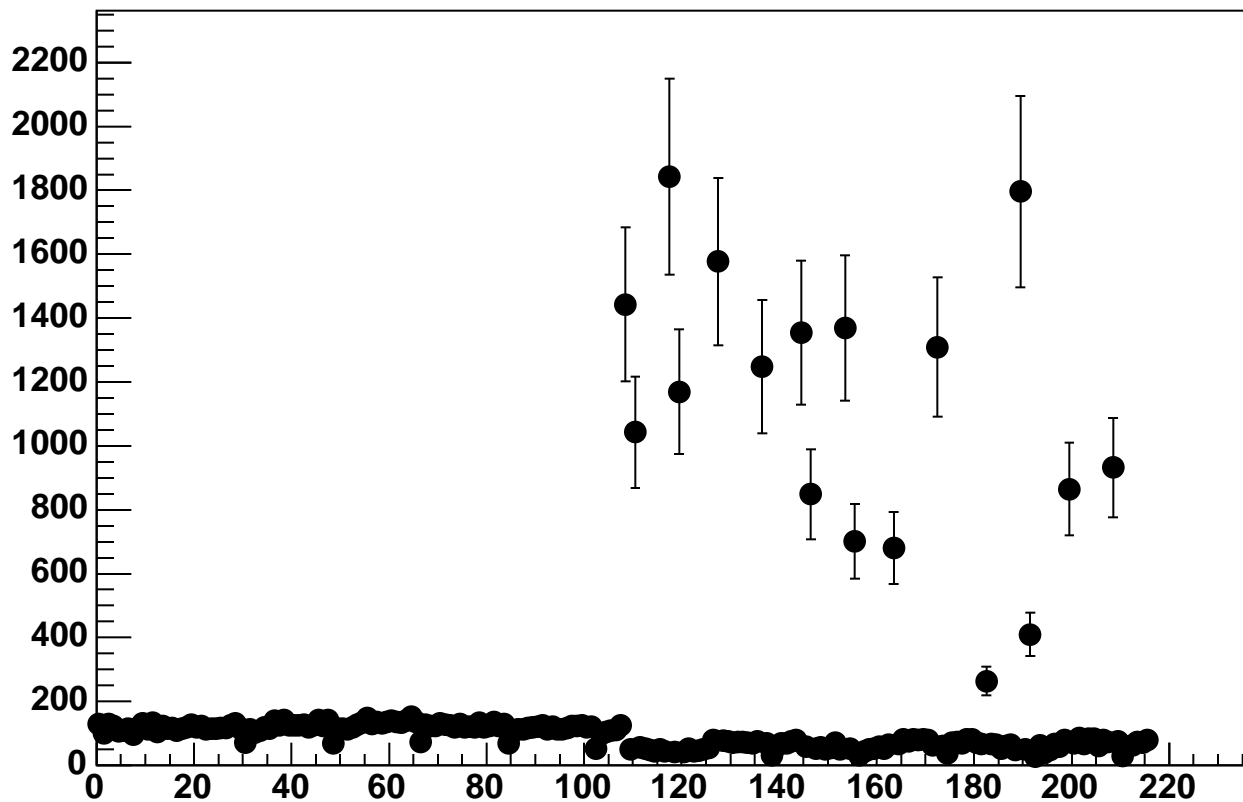
Enable 0, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



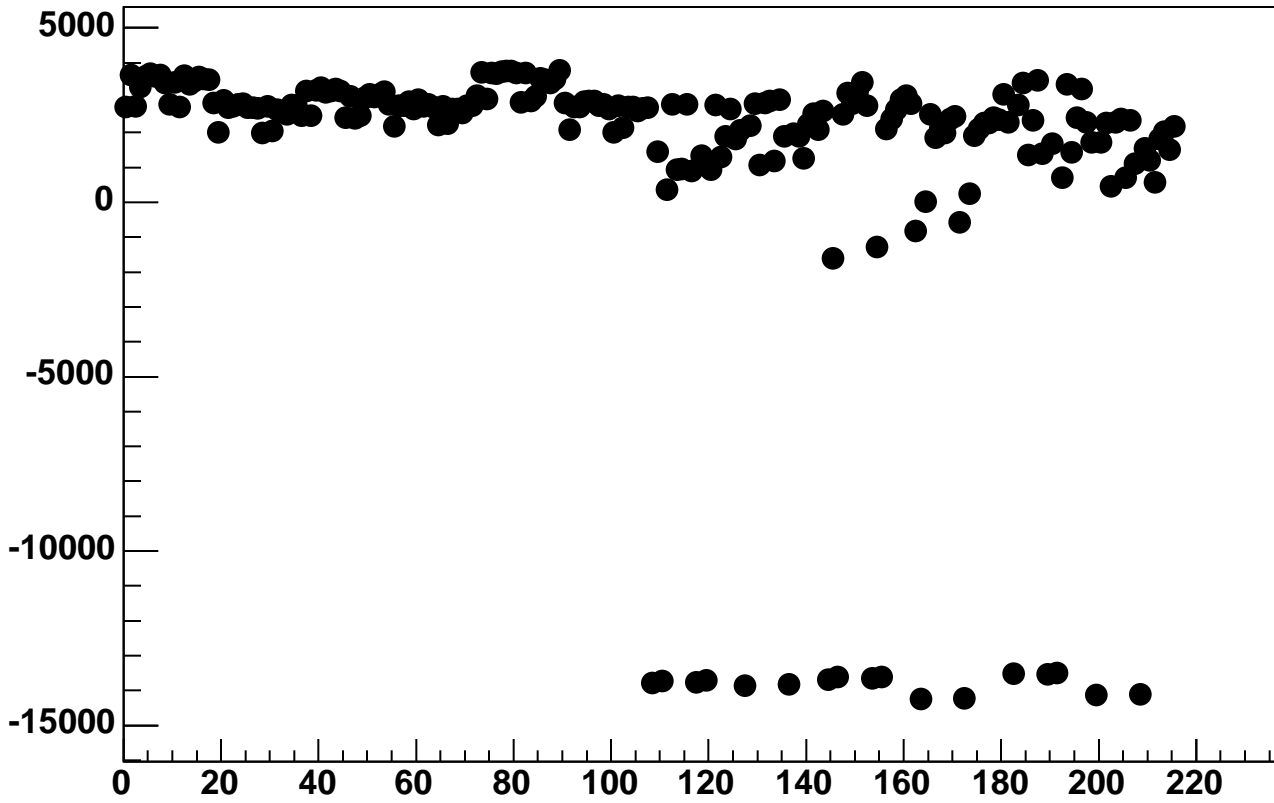
Enable 0, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



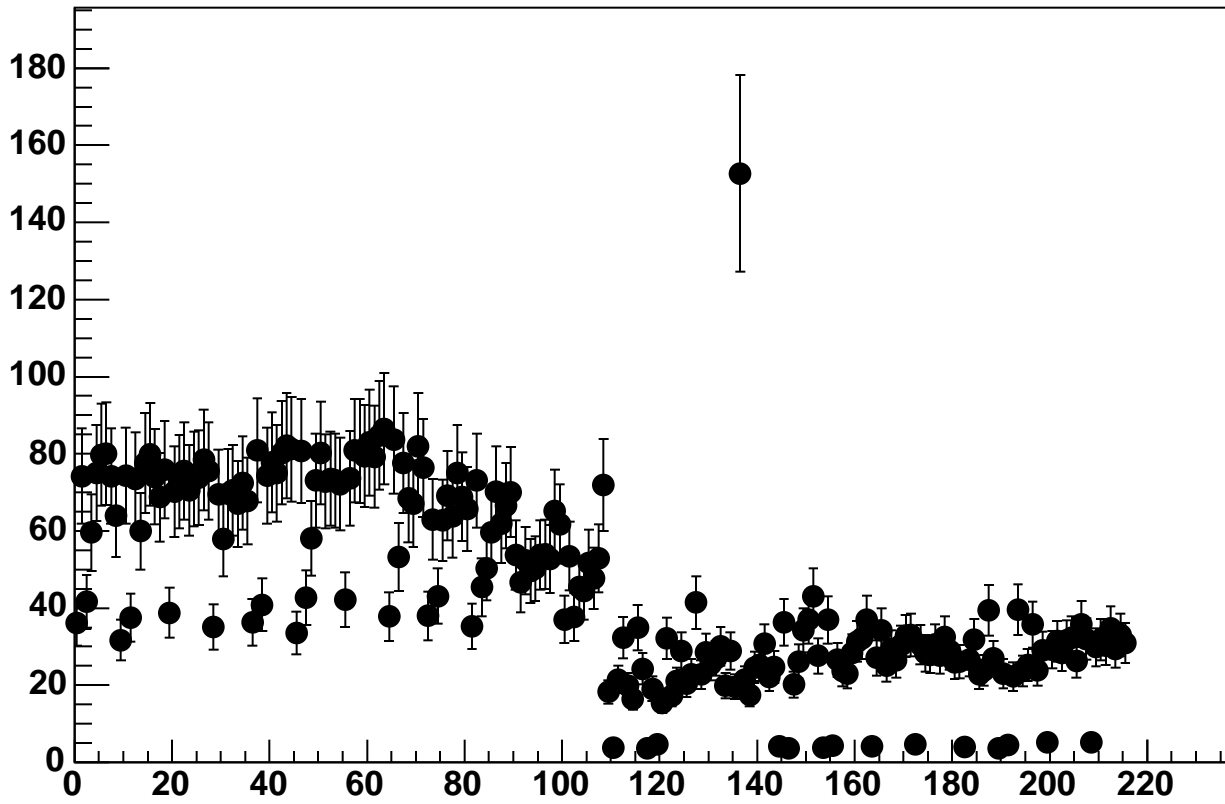
Enable 0, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



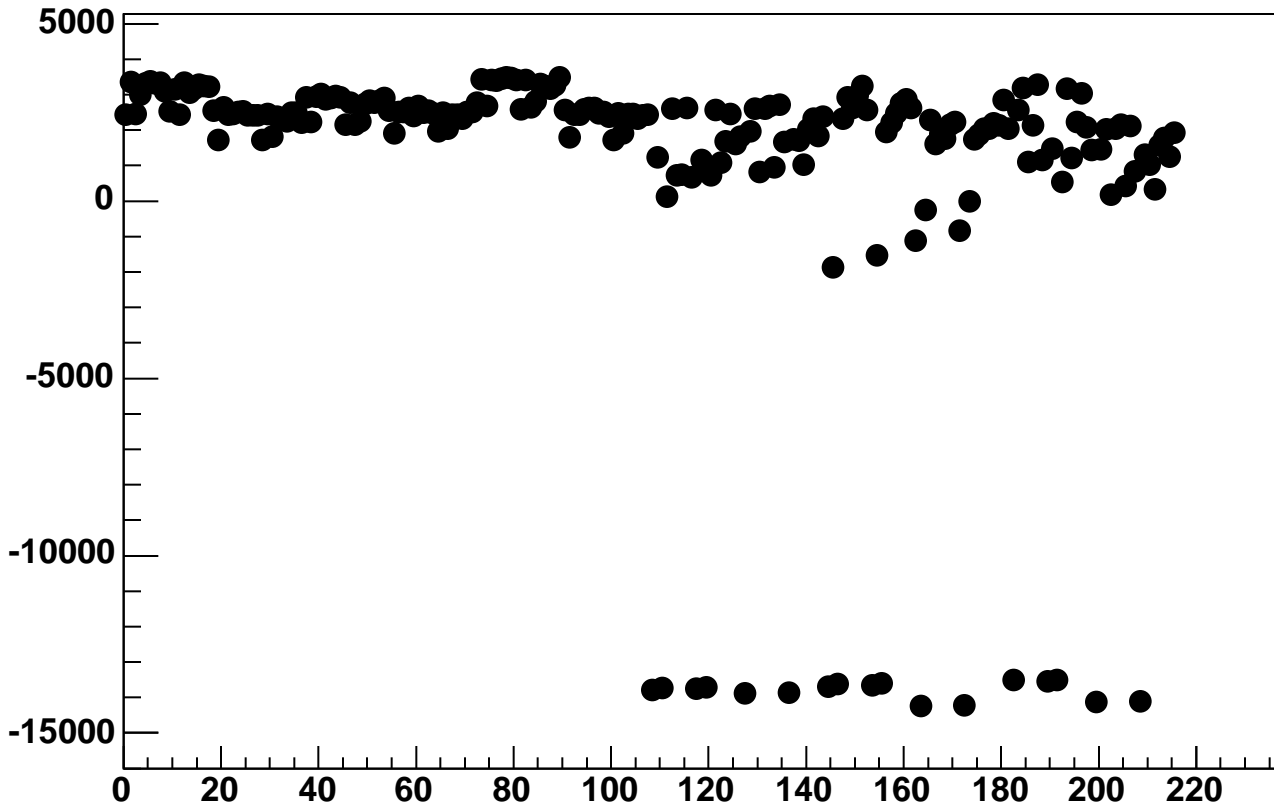
Enable 0, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



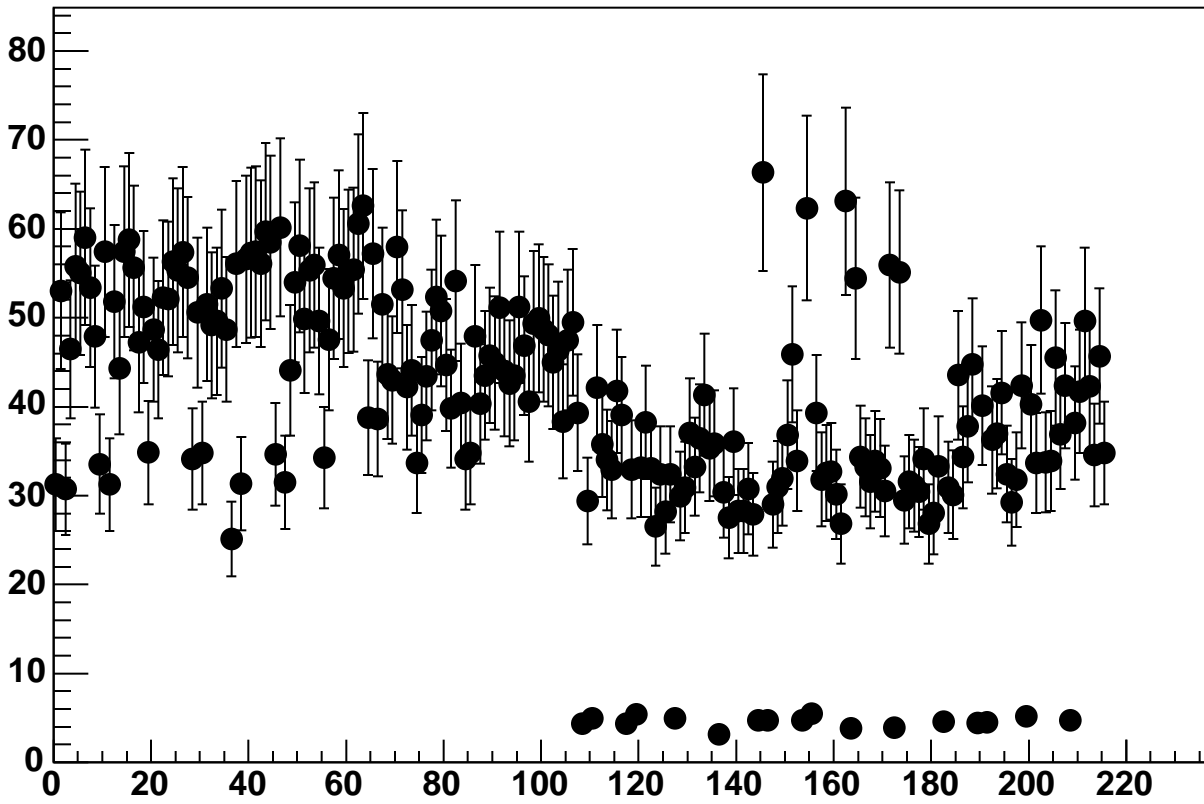
Enable 0, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



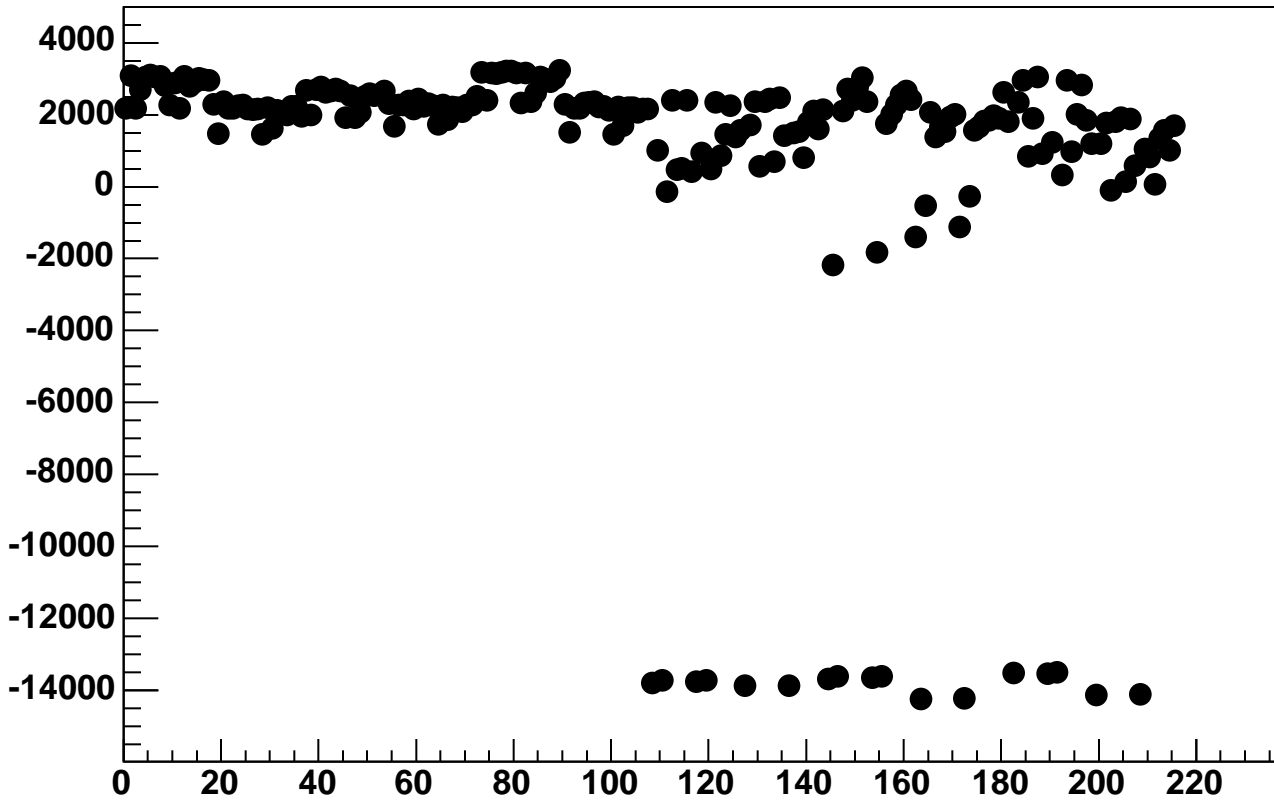
Enable 0, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



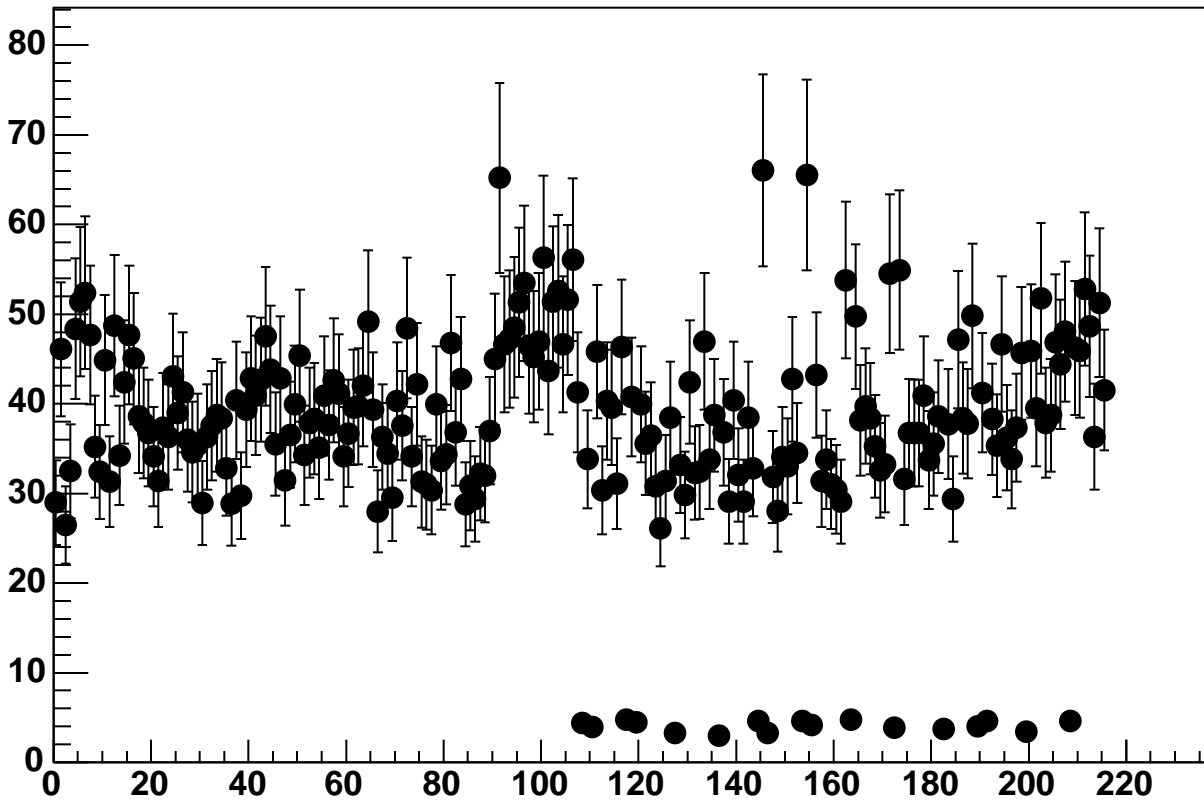
Enable 0, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



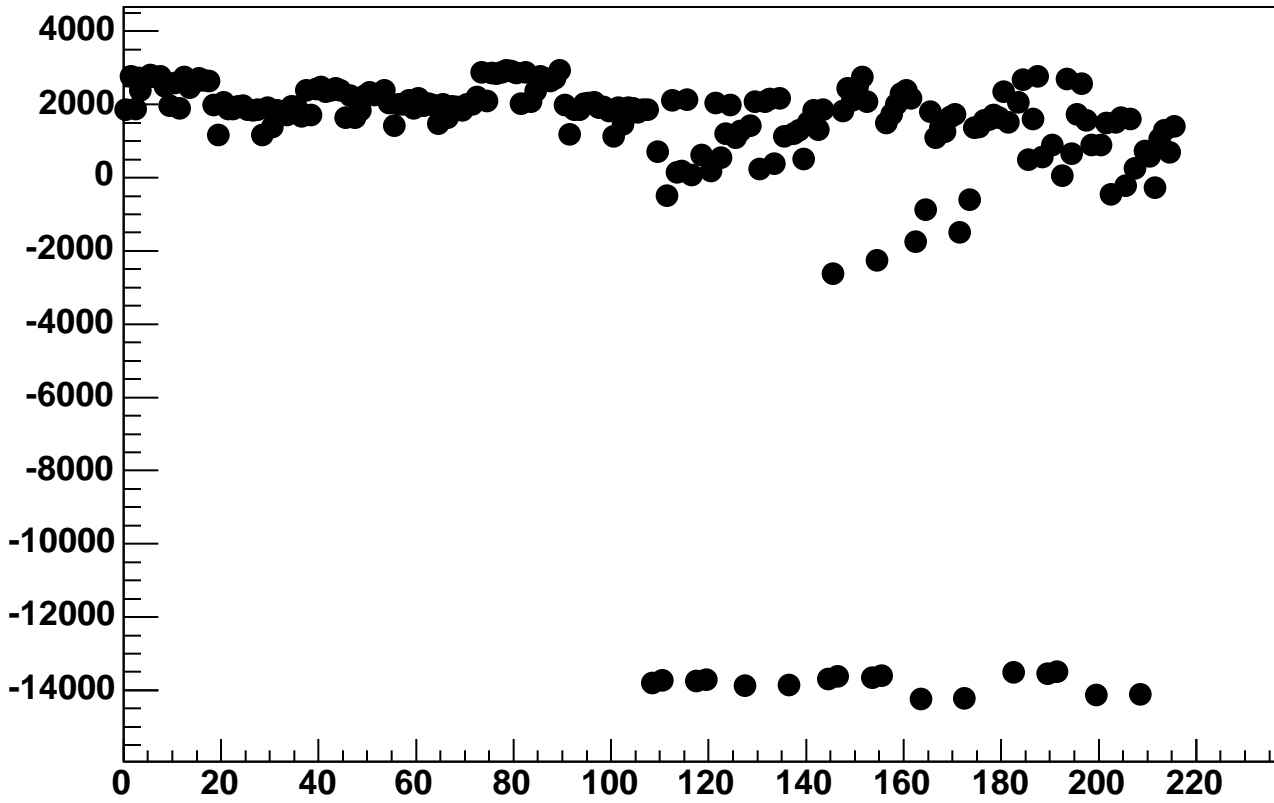
Enable 0, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



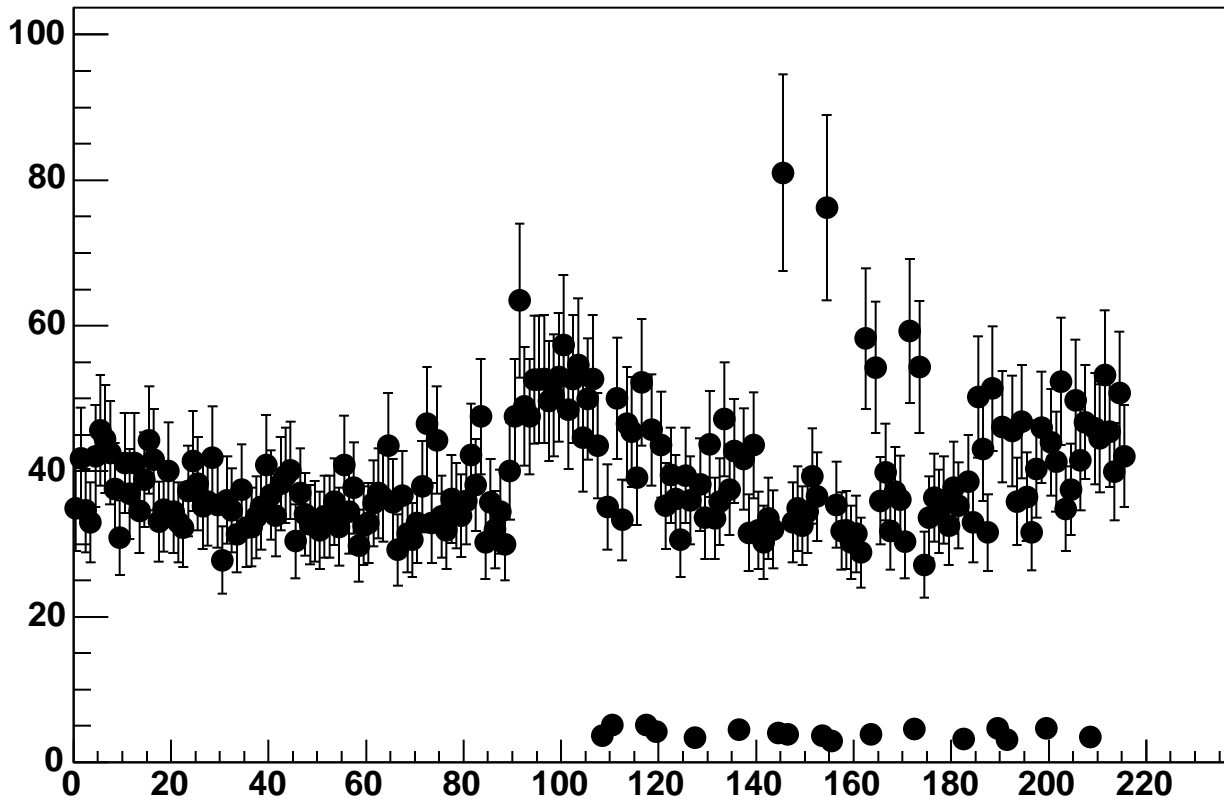
Enable 0, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan



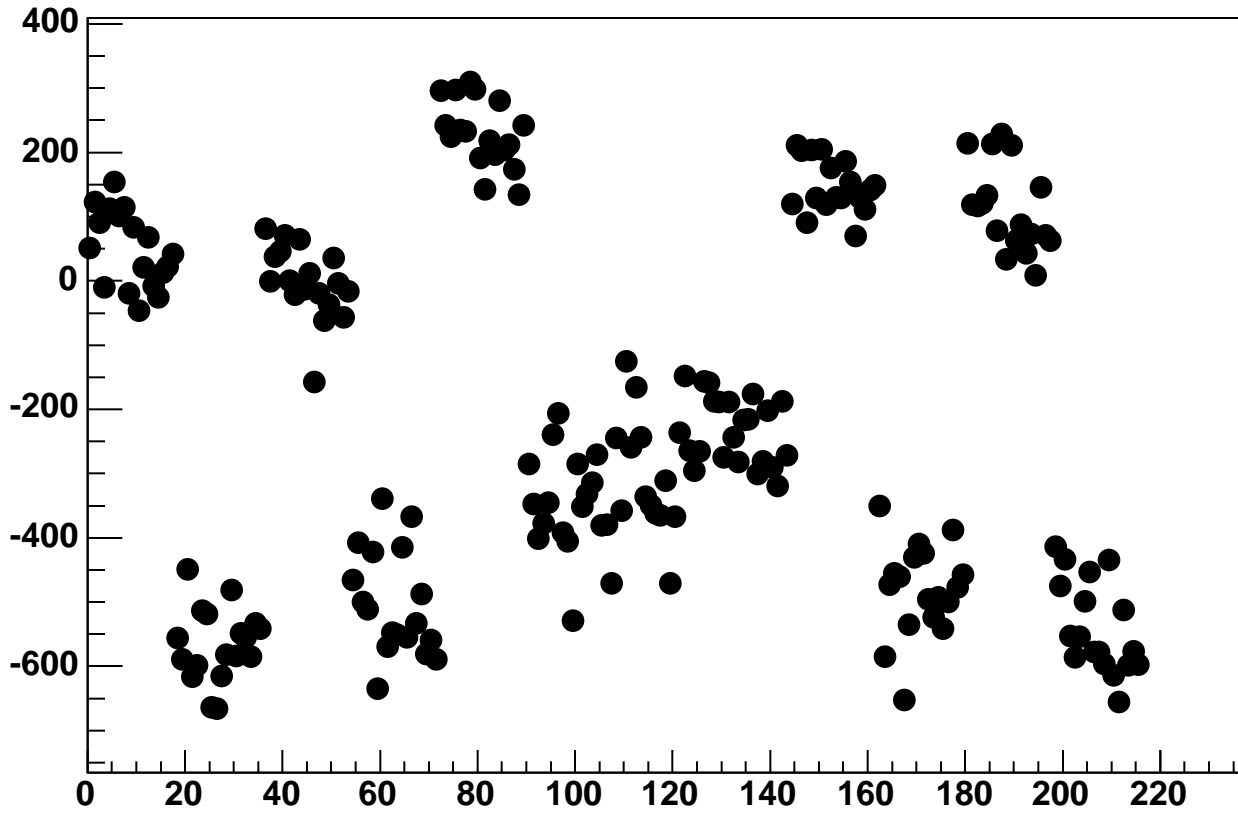
Enable 0, DAC=4000, Hold=245, ADC Mean vs 18*Chip+Chan



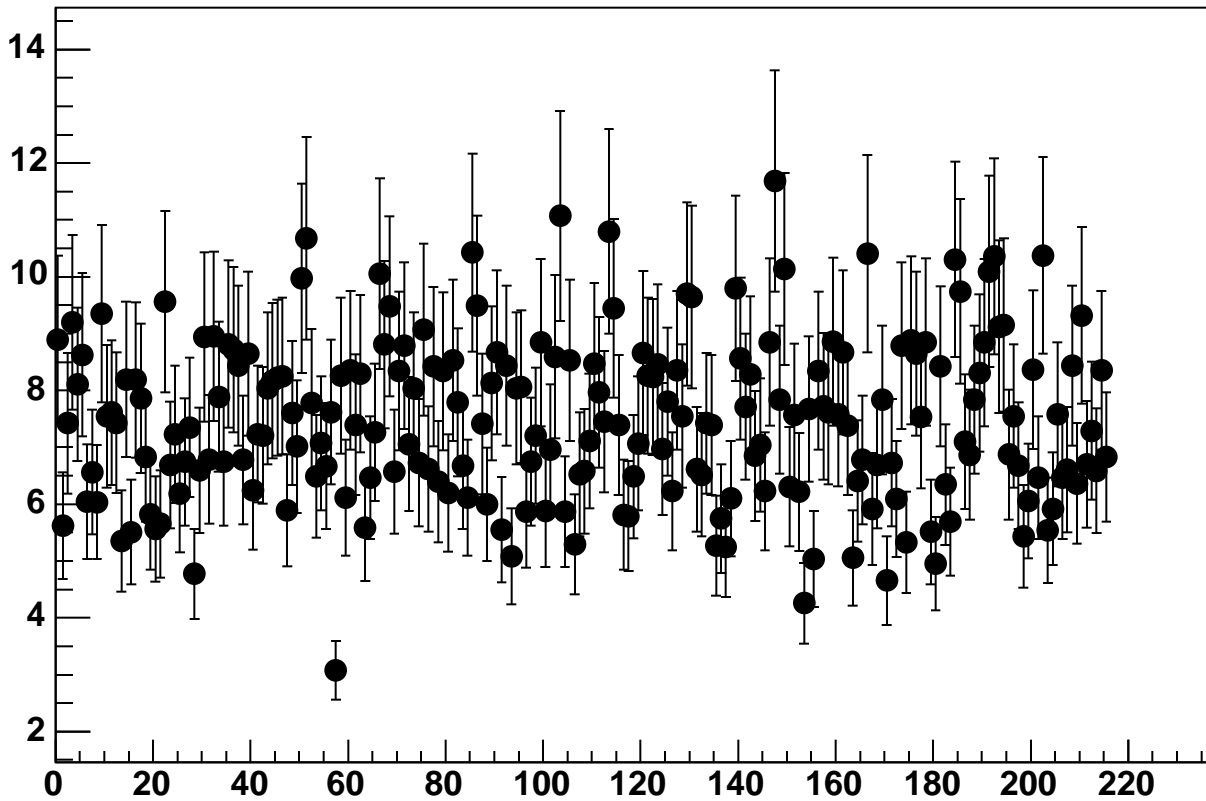
Enable 0, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



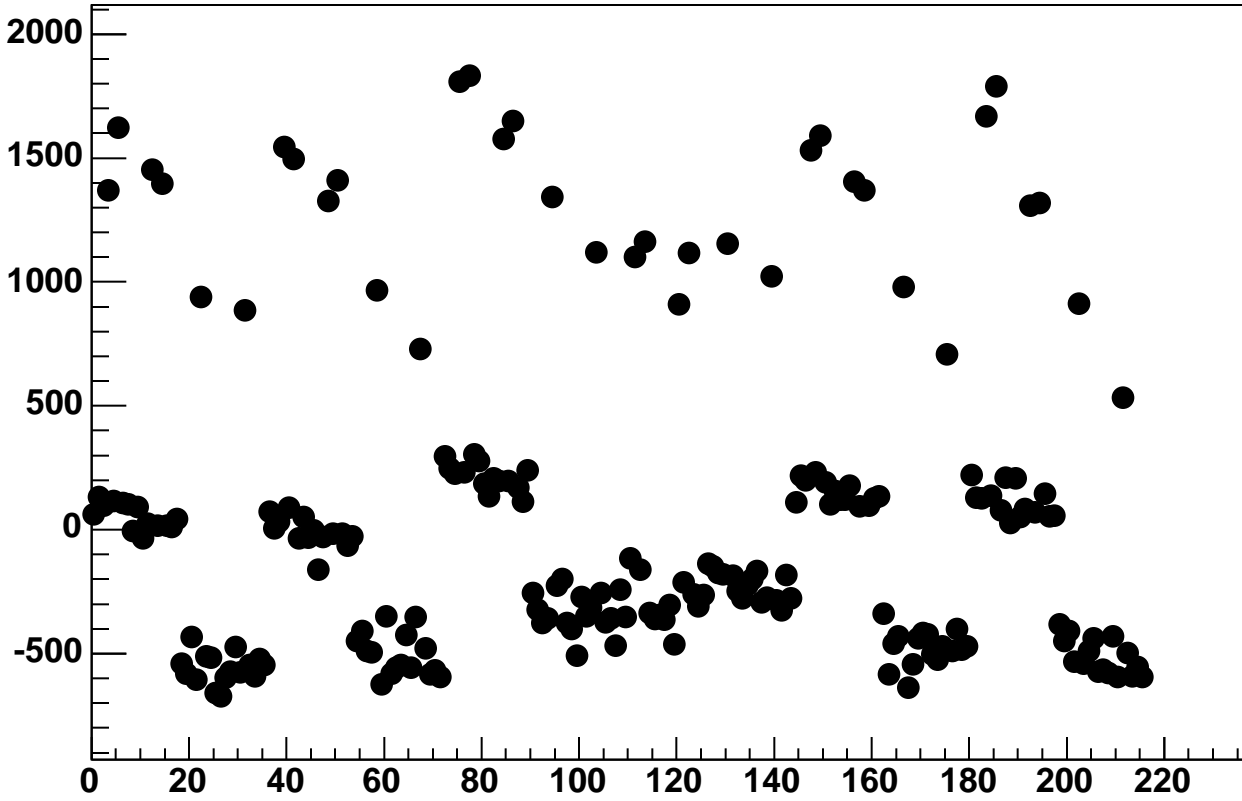
Enable 1, DAC=4000, Hold=0, ADC Mean vs 18*Chip+Chan



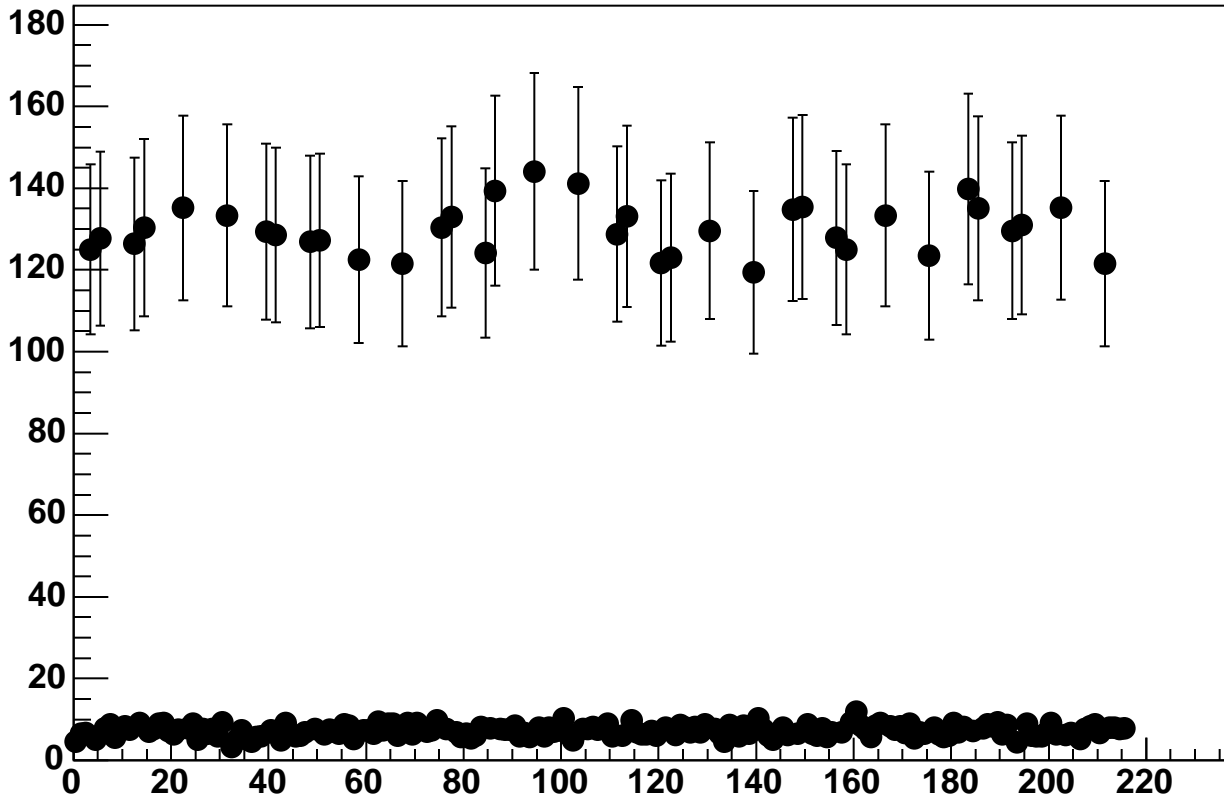
Enable 1, DAC=4000, Hold=0, ADC Noise vs 18*Chip+Chan



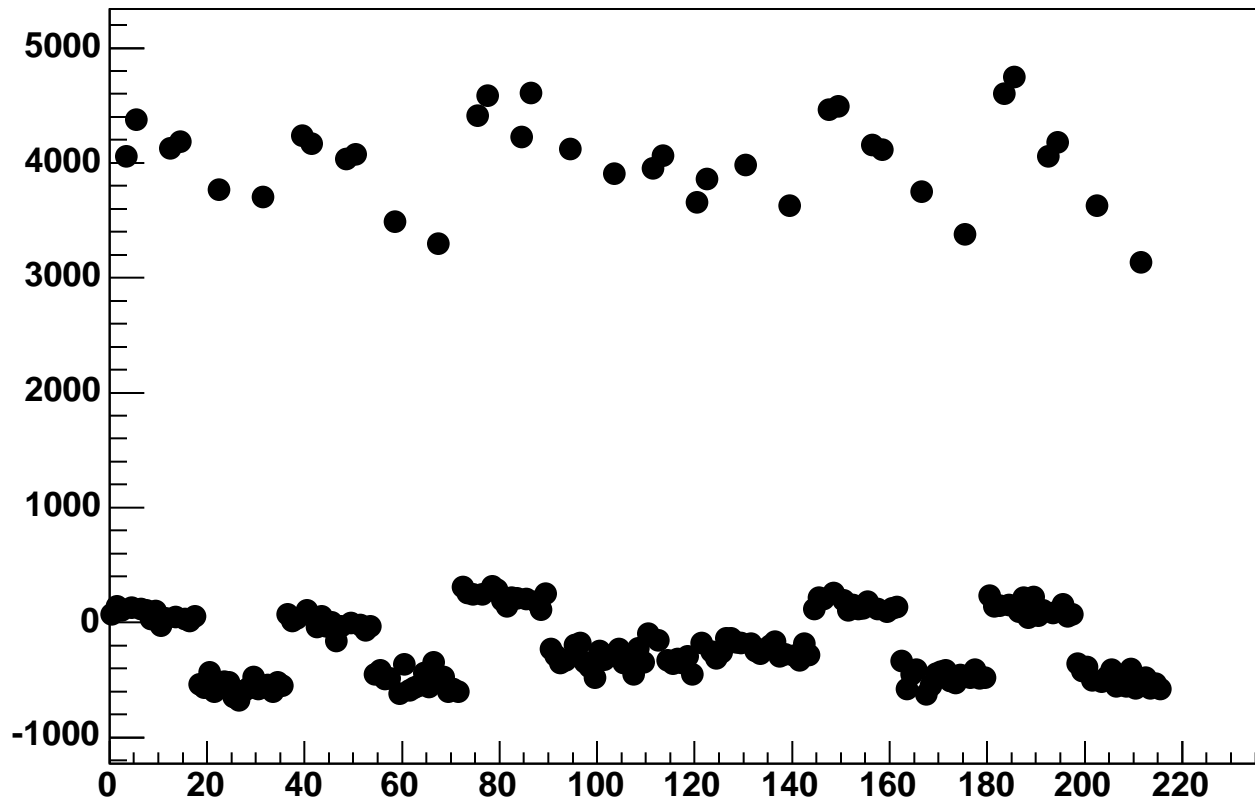
Enable 1, DAC=4000, Hold=5, ADC Mean vs 18*Chip+Chan



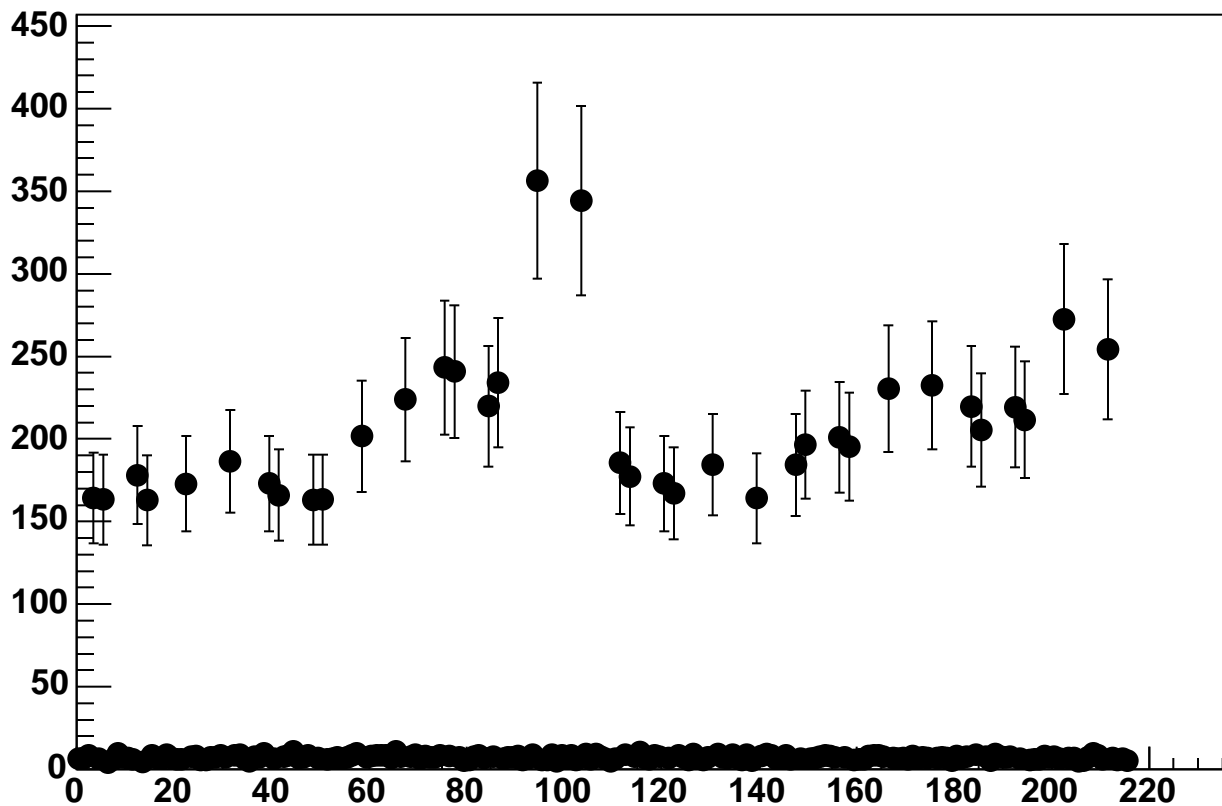
Enable 1, DAC=4000, Hold=5, ADC Noise vs 18*Chip+Chan



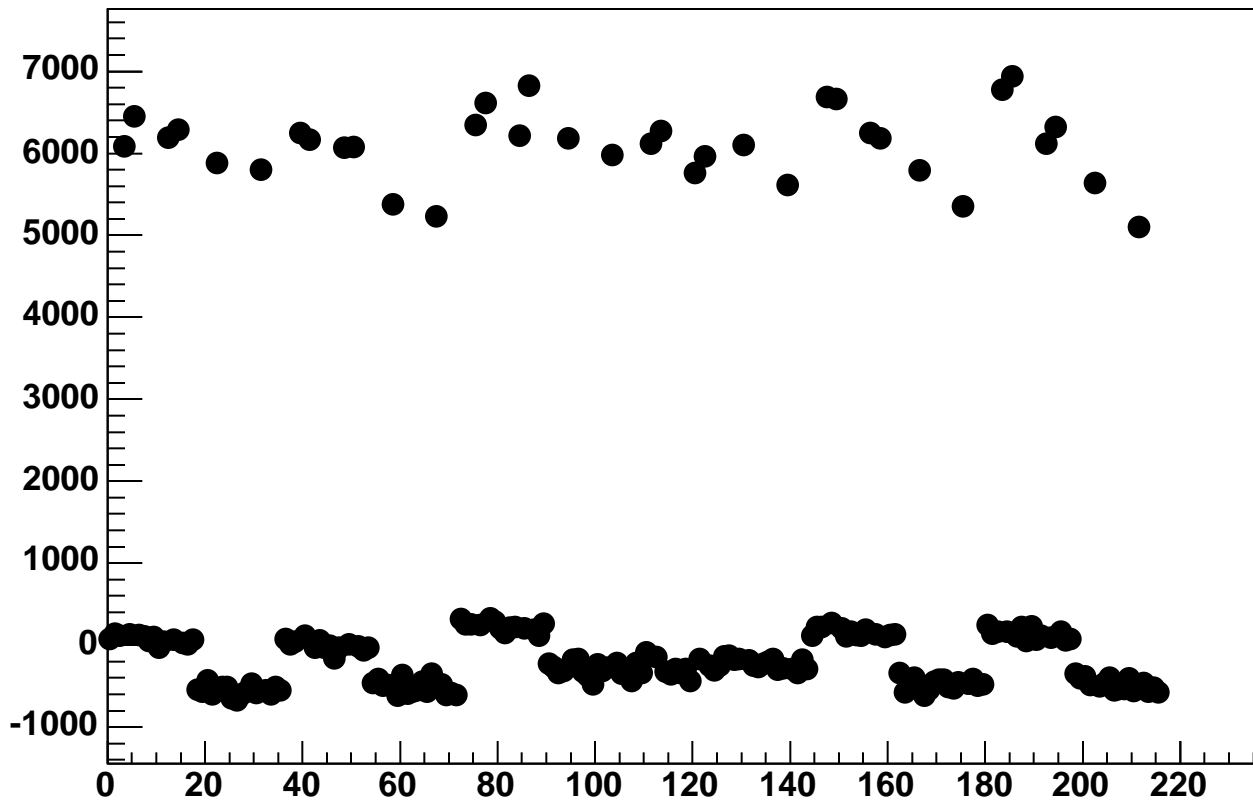
Enable 1, DAC=4000, Hold=10, ADC Mean vs 18*Chip+Chan



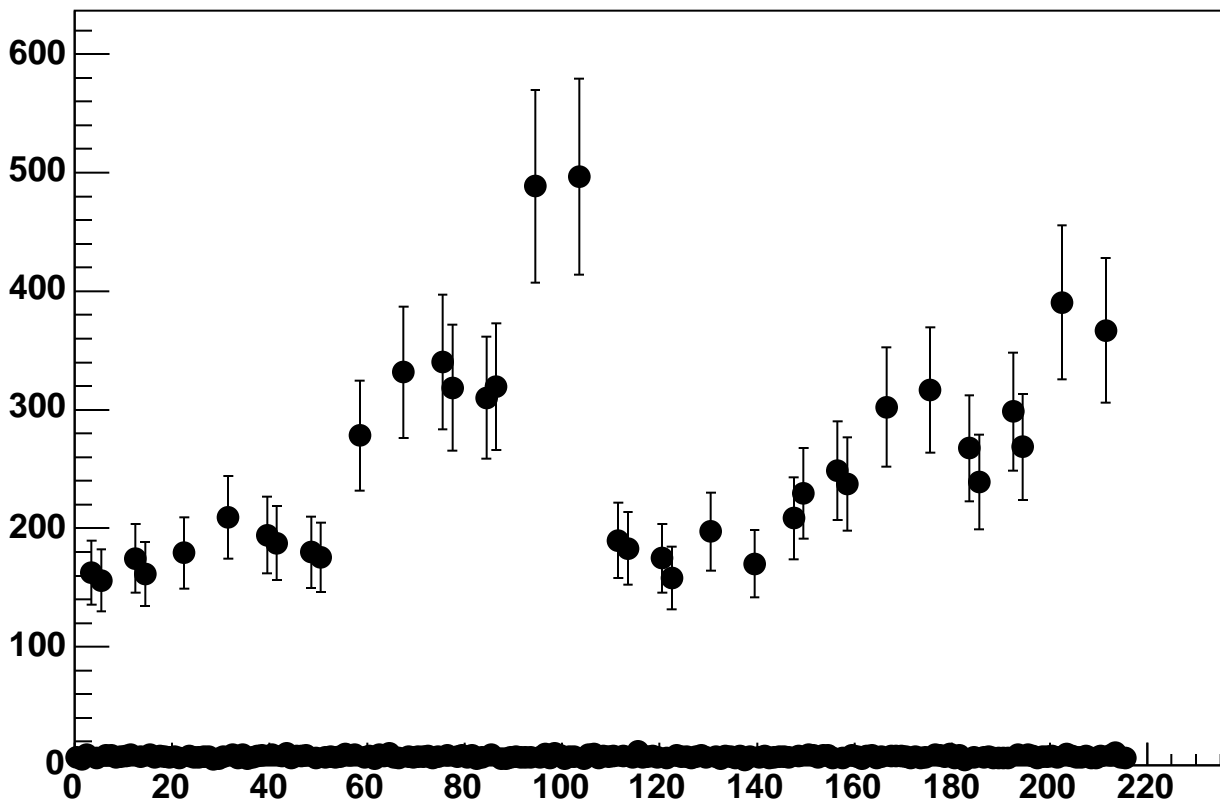
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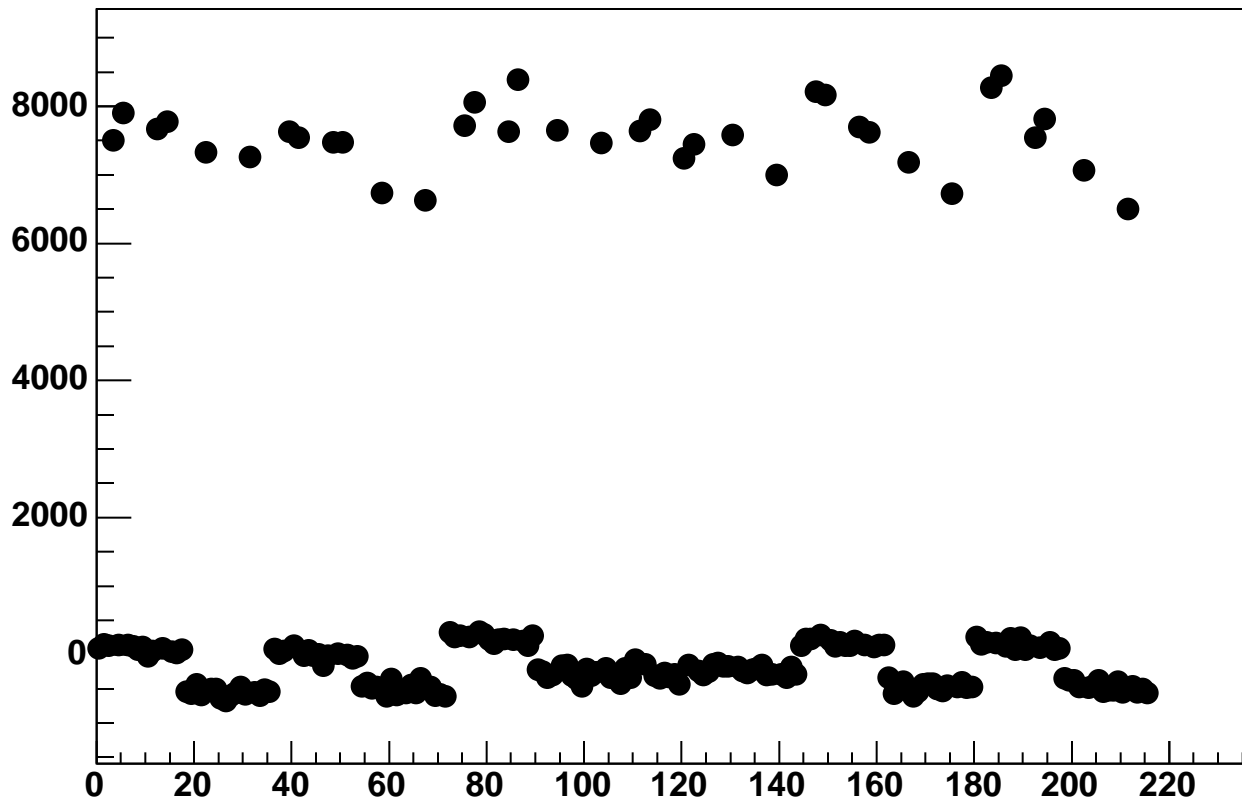
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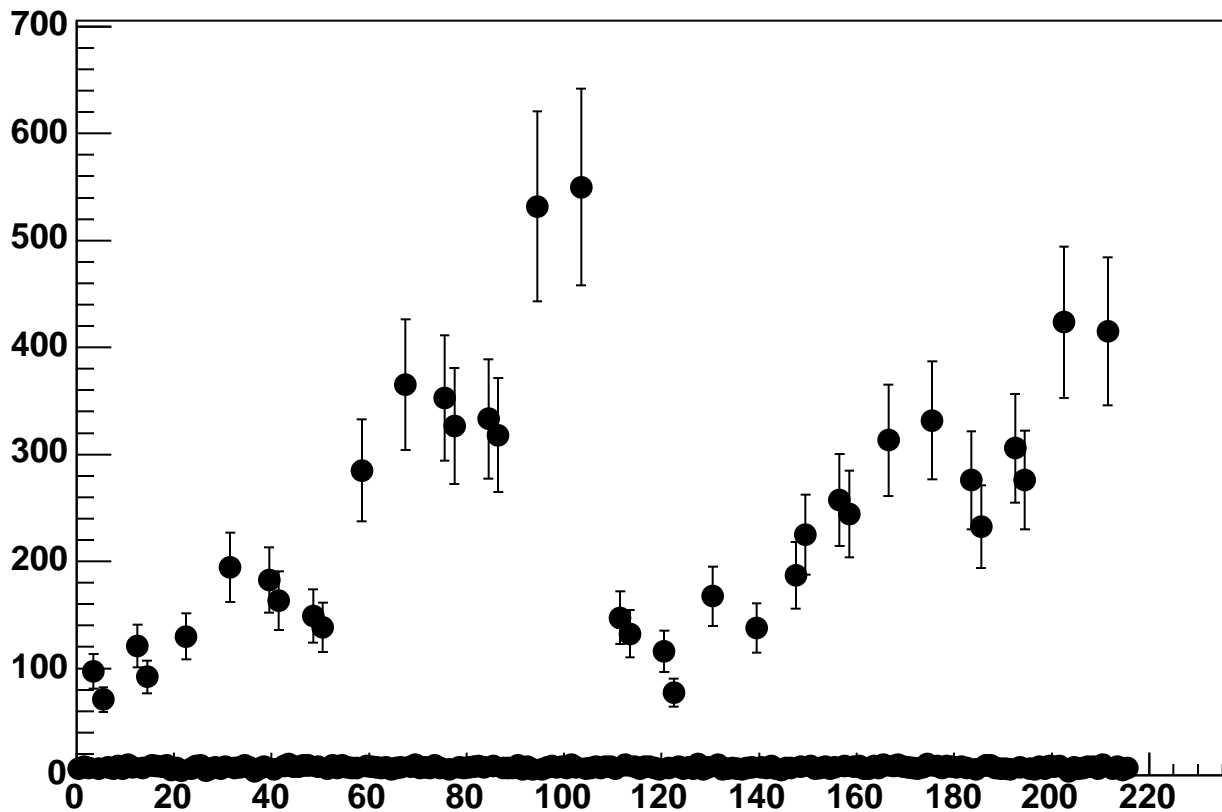
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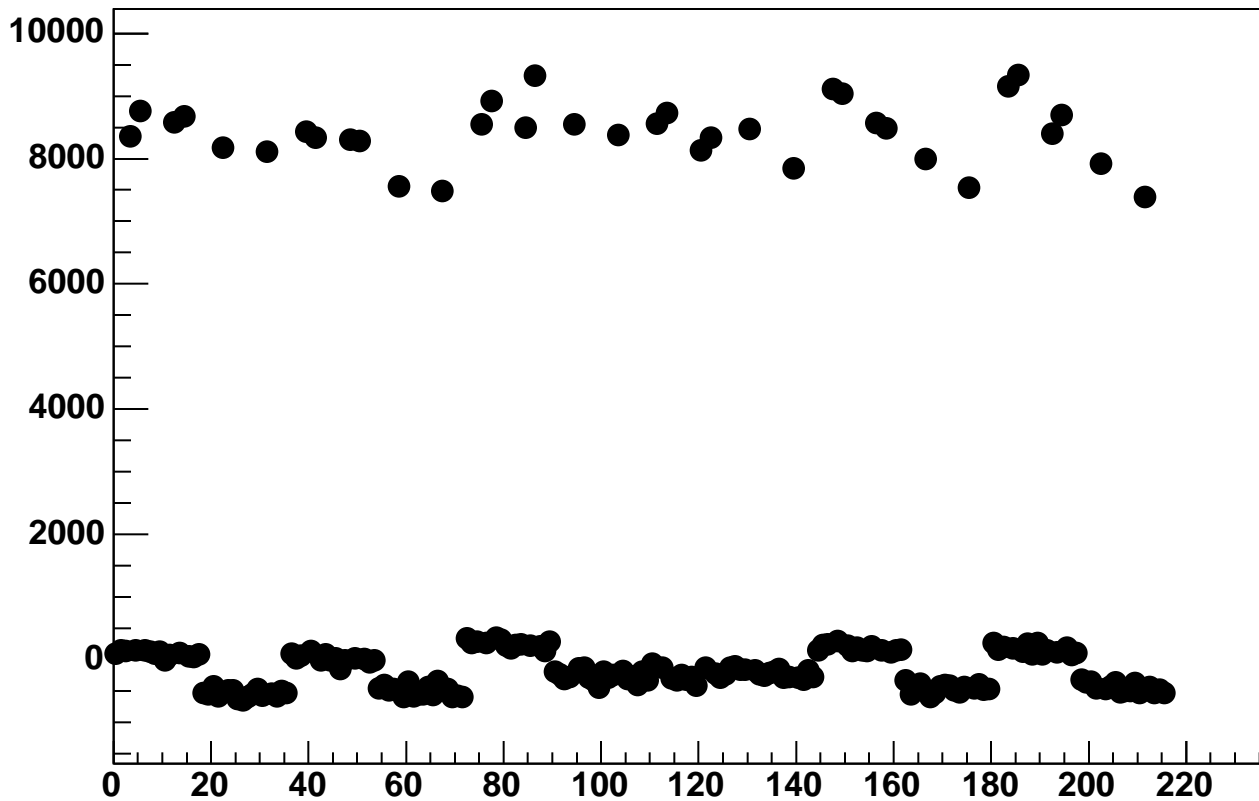
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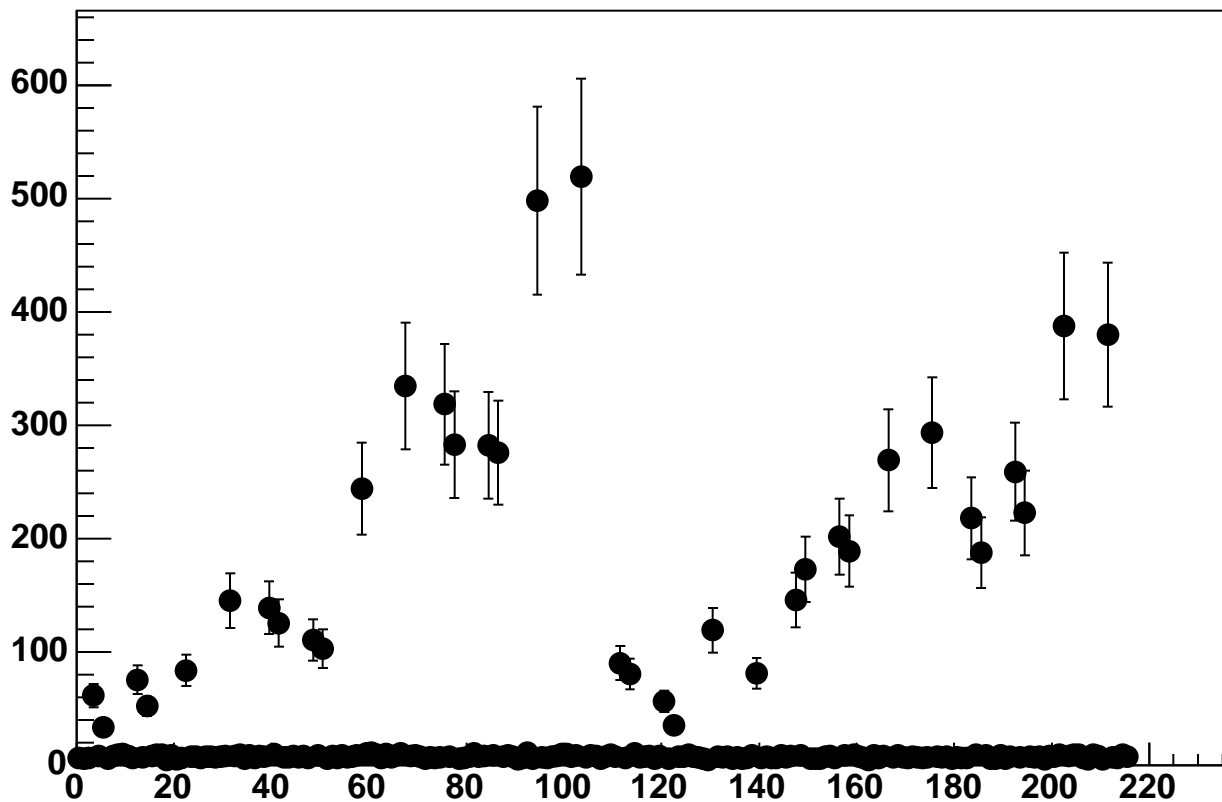
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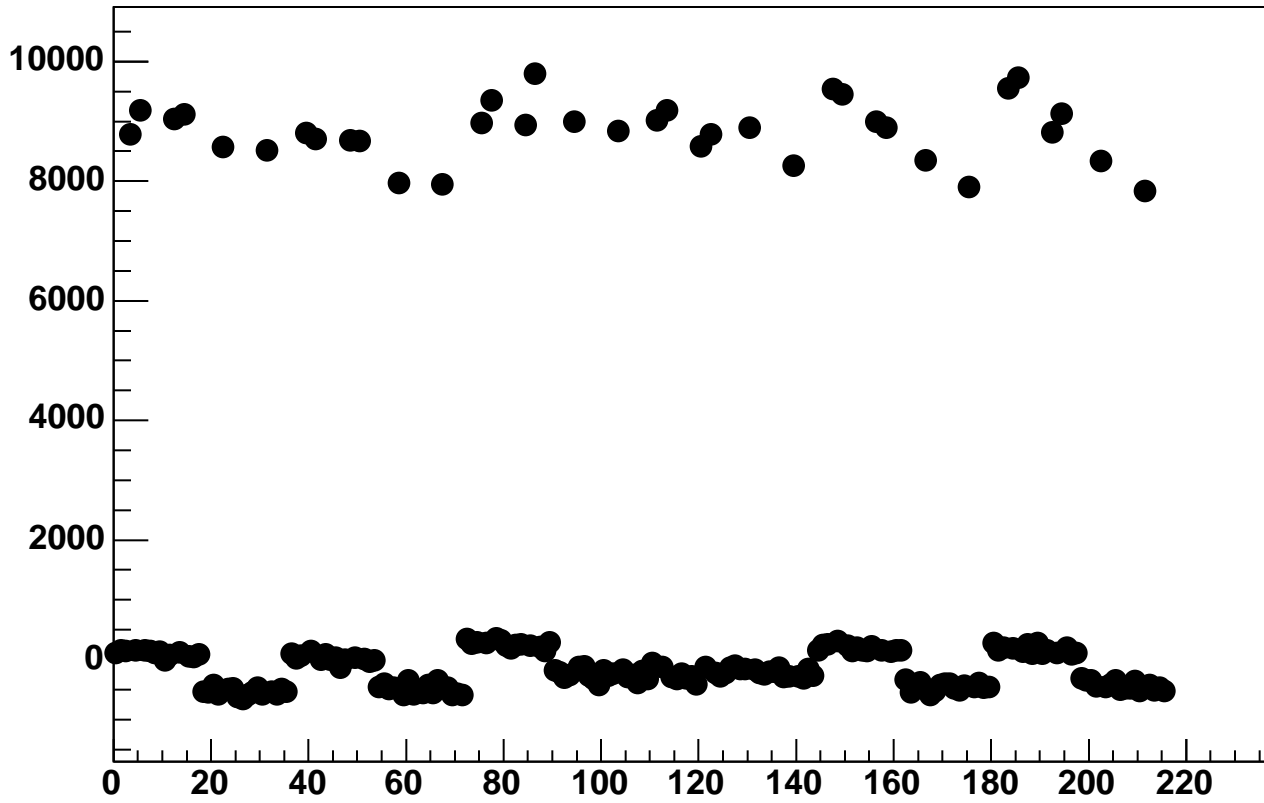
Enable 1, DAC=4000, Hold=25, ADC Mean vs 18*Chip+Chan



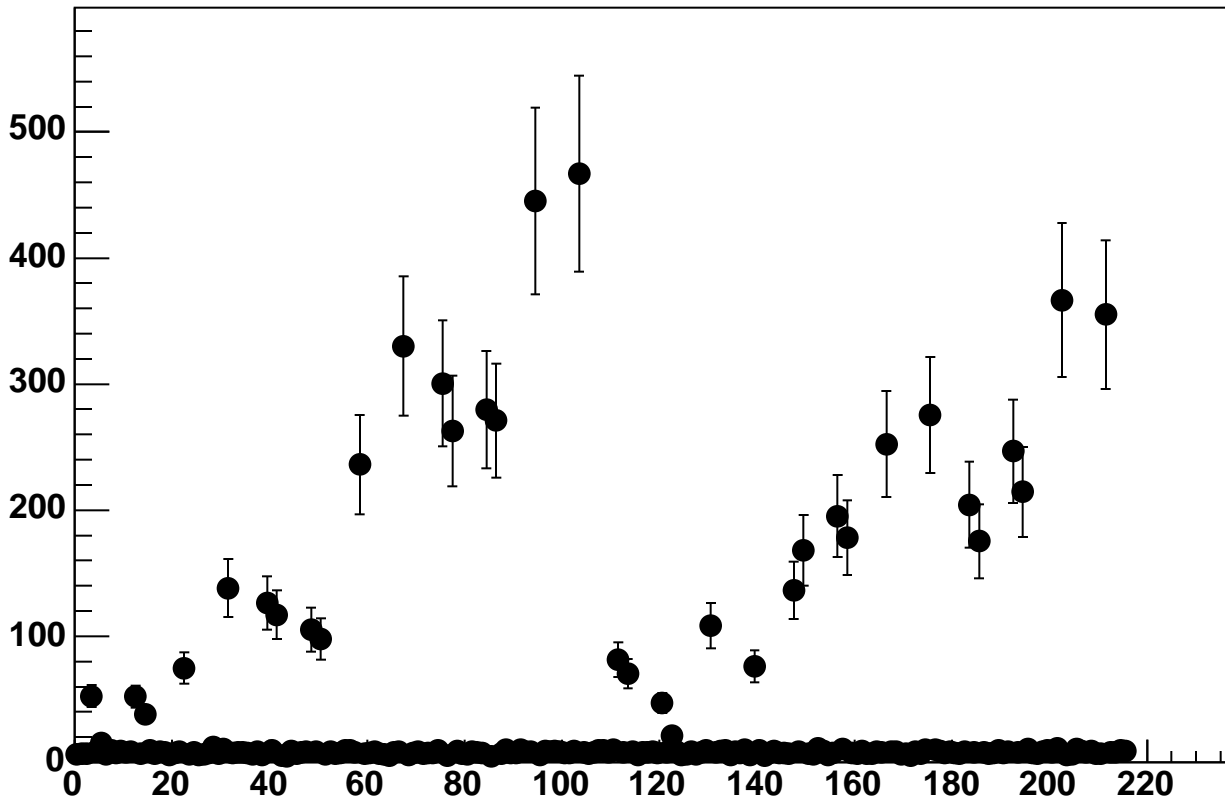
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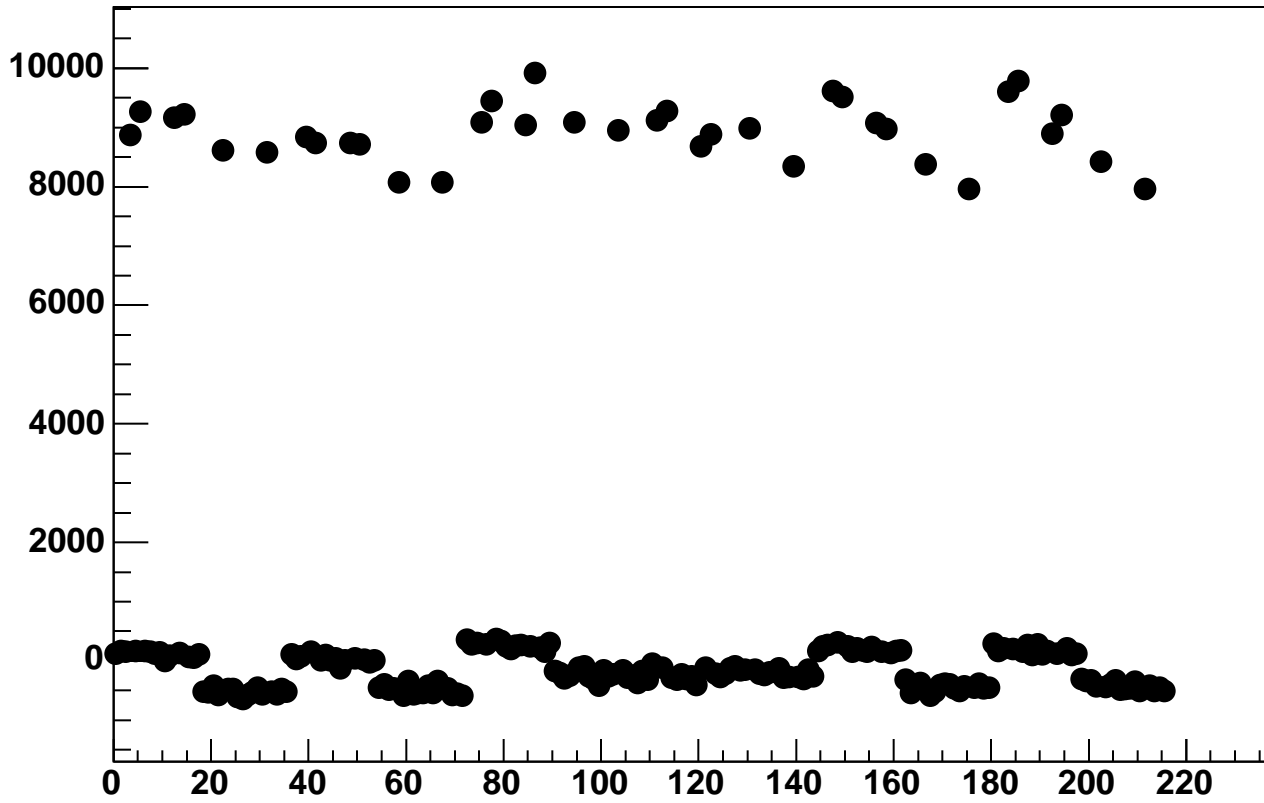
Enable 1, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



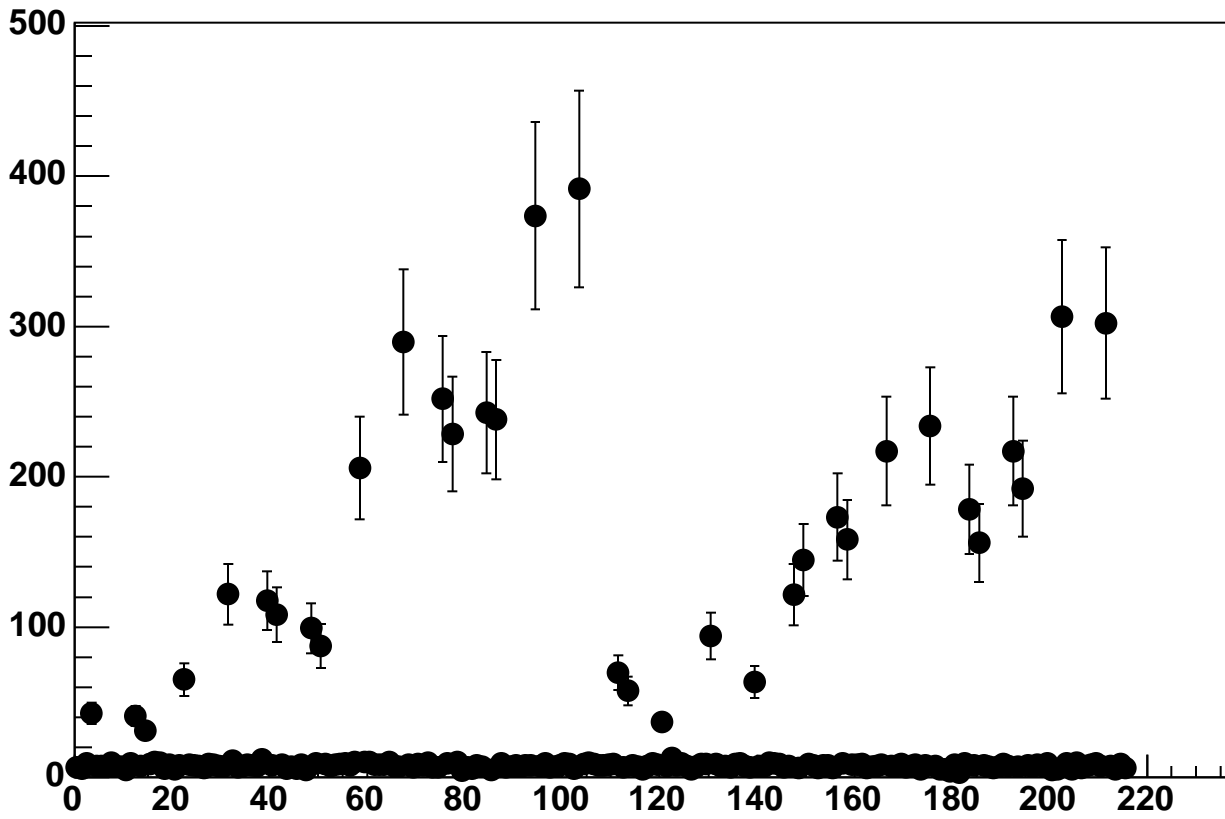
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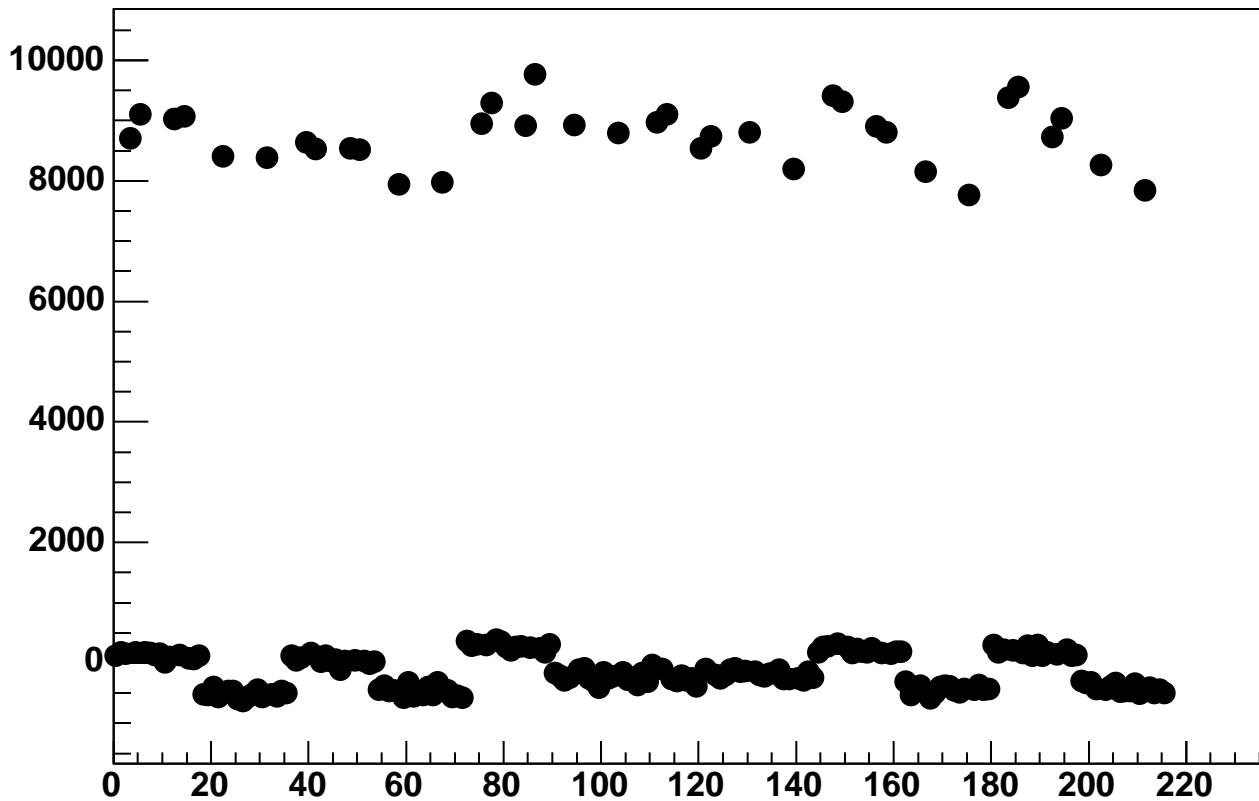
Enable 1, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



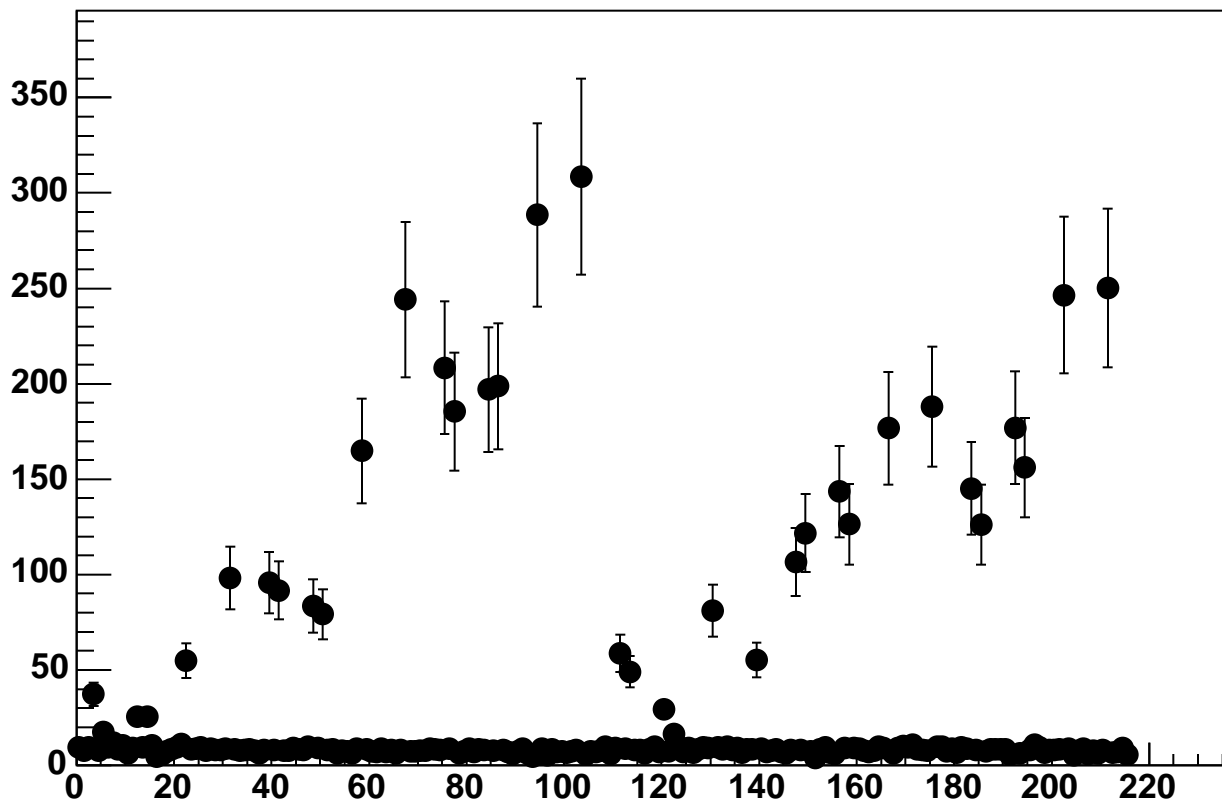
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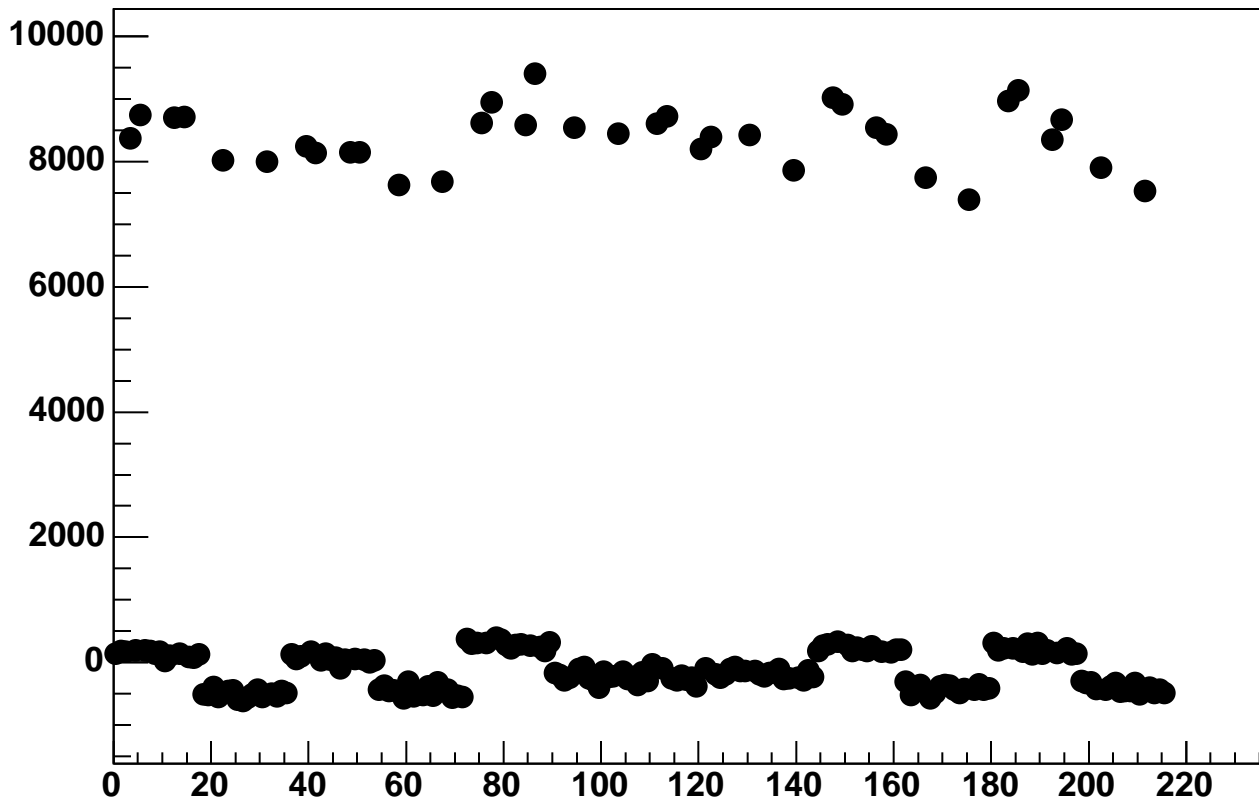
Enable 1, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



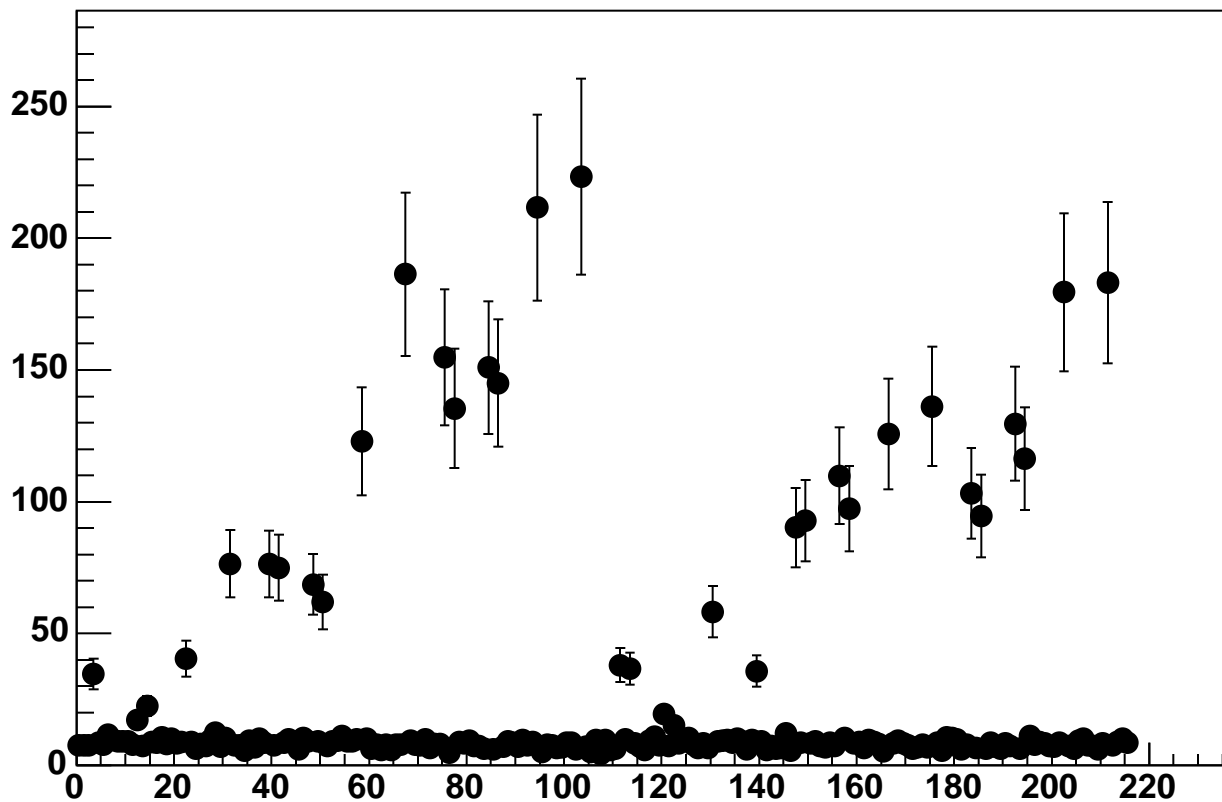
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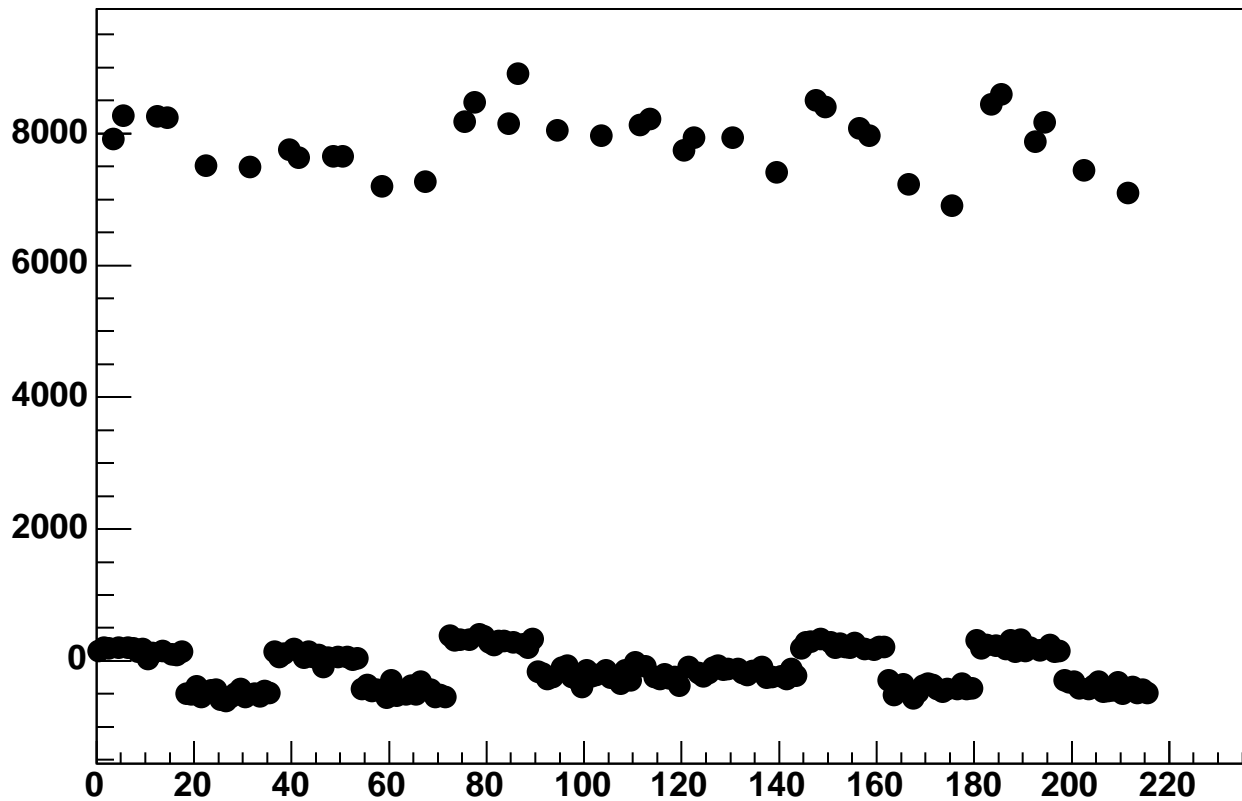
Enable 1, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



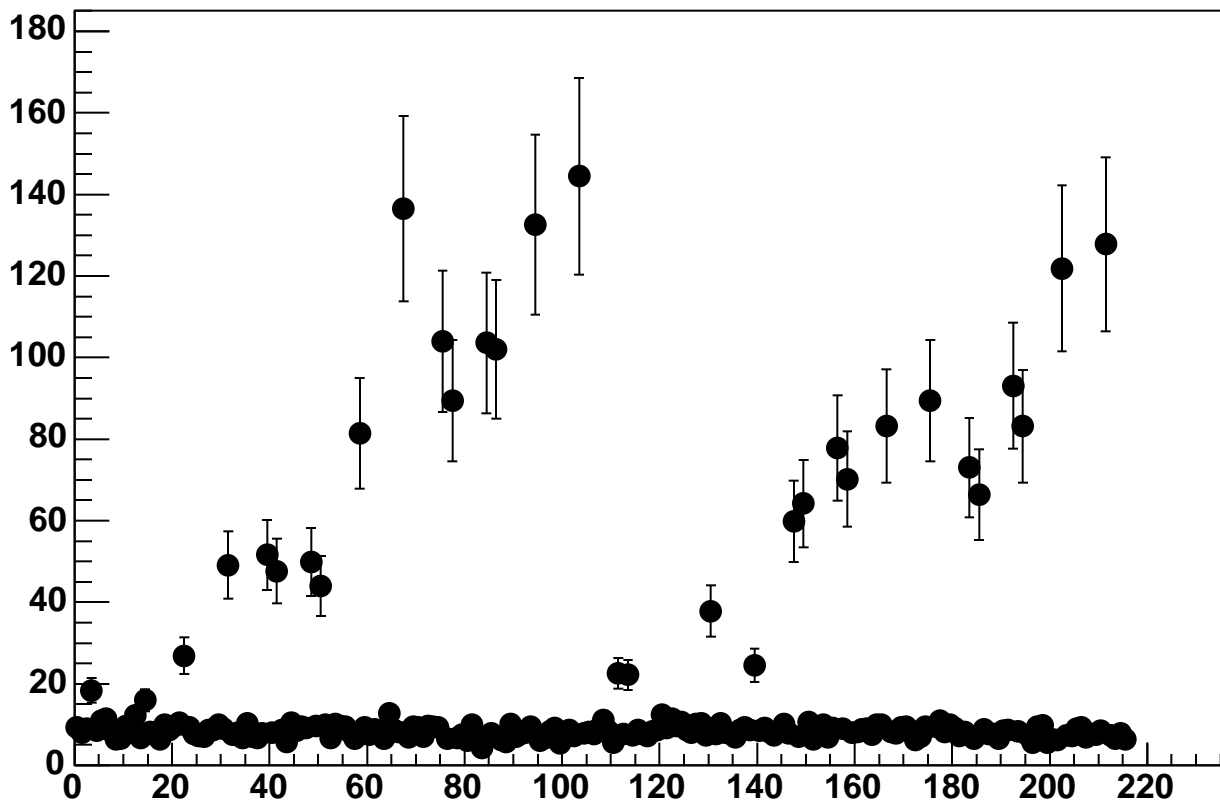
Enable 1, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



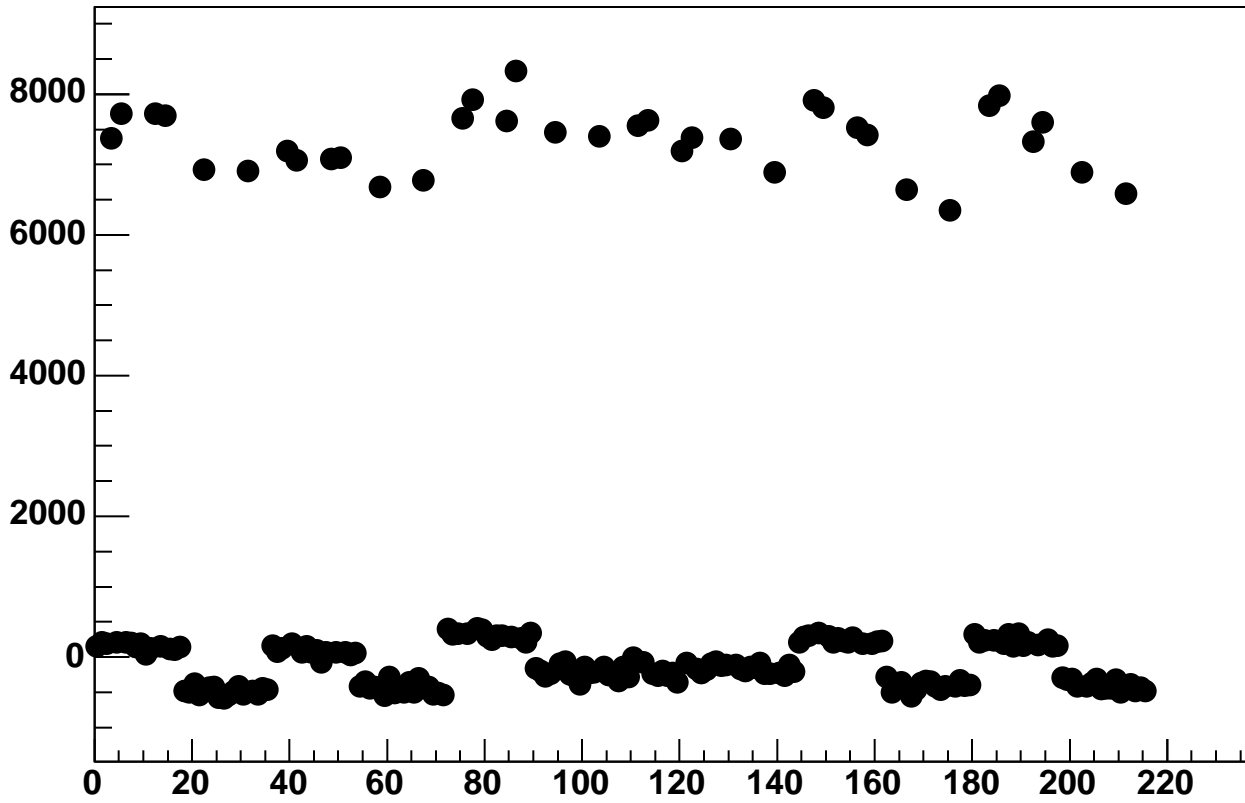
Enable 1, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



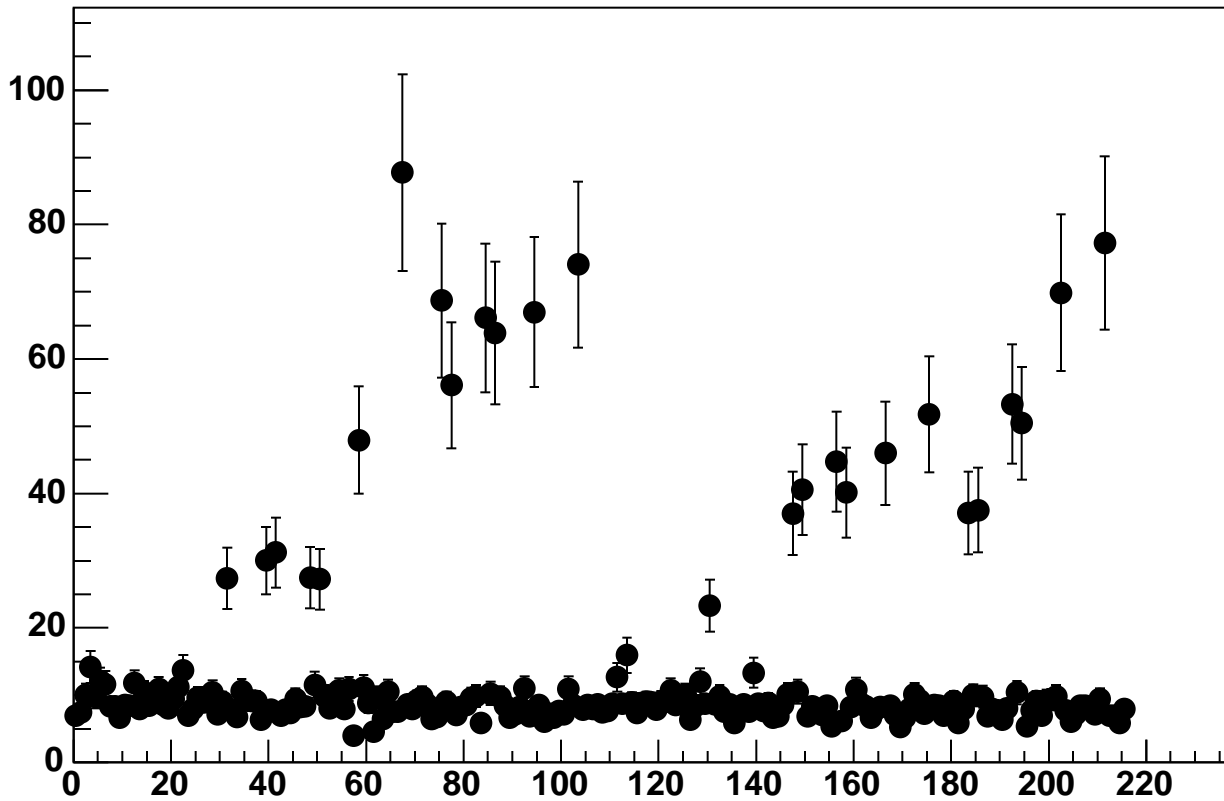
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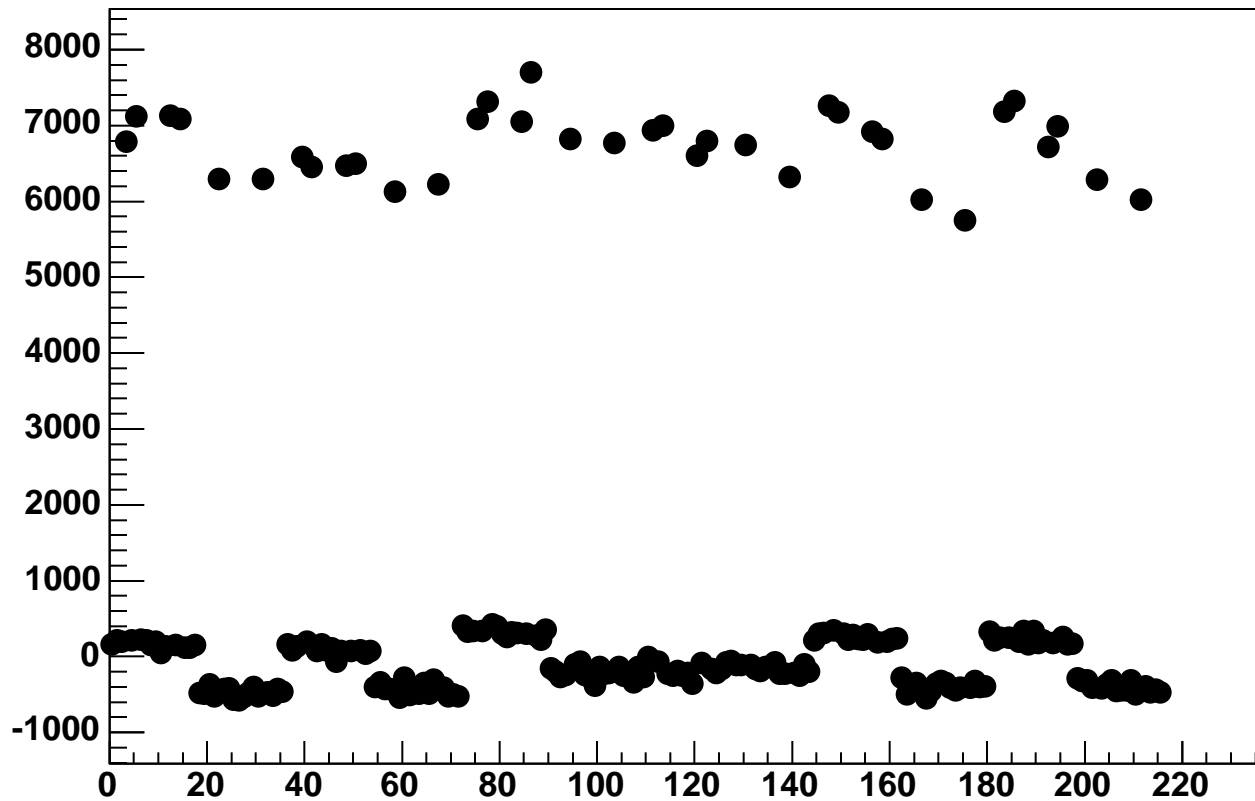
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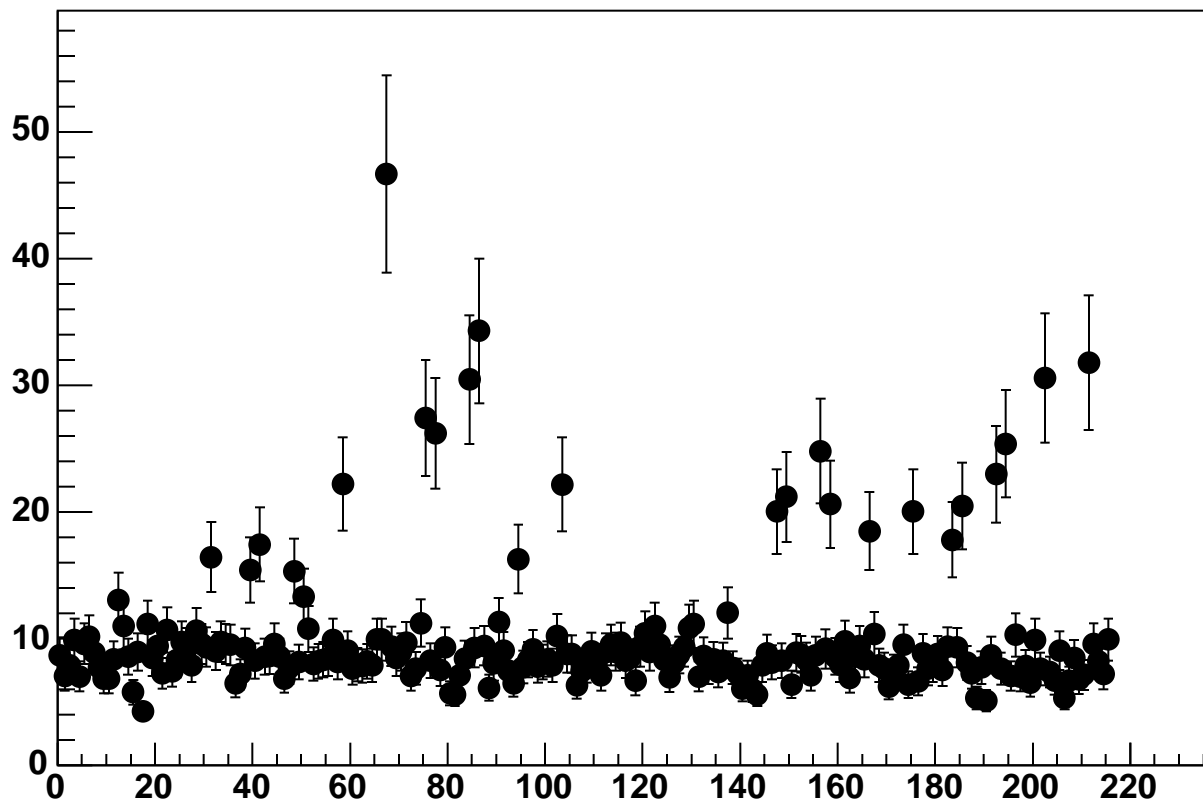
Enable 1, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



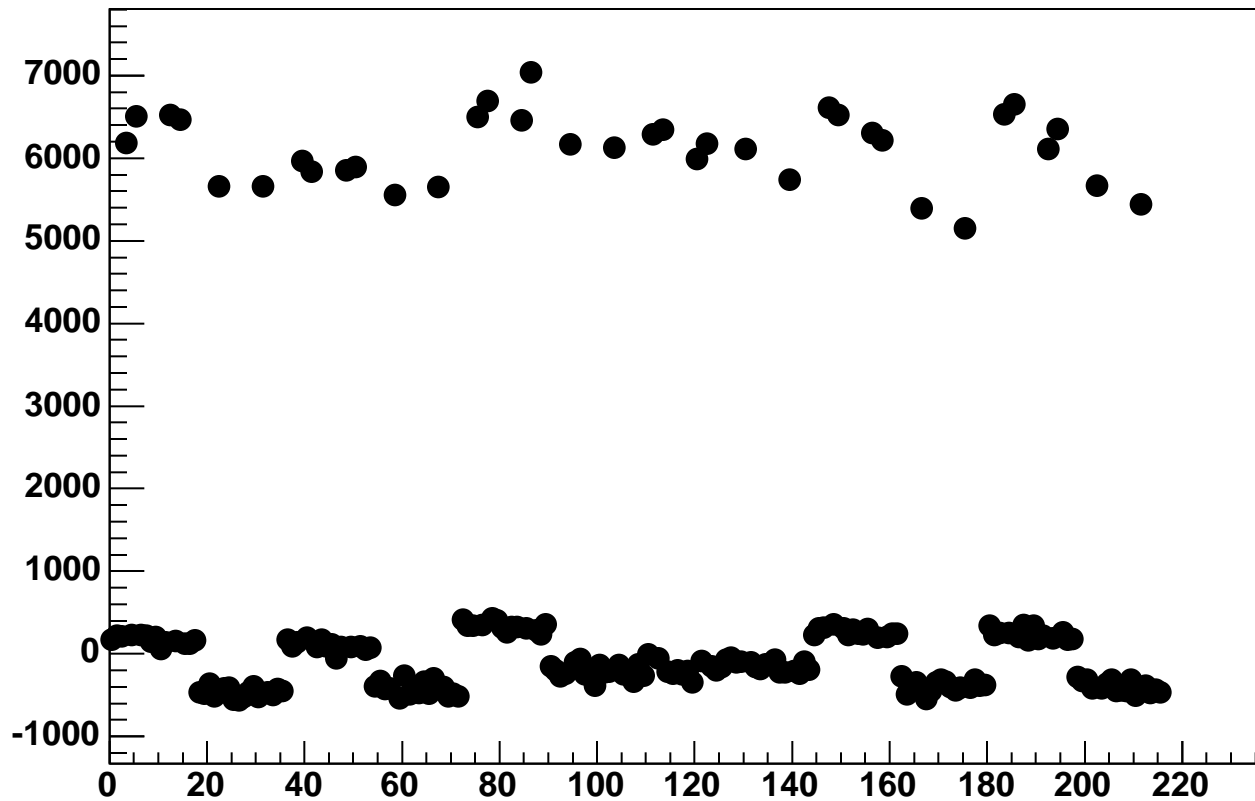
Enable 1, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



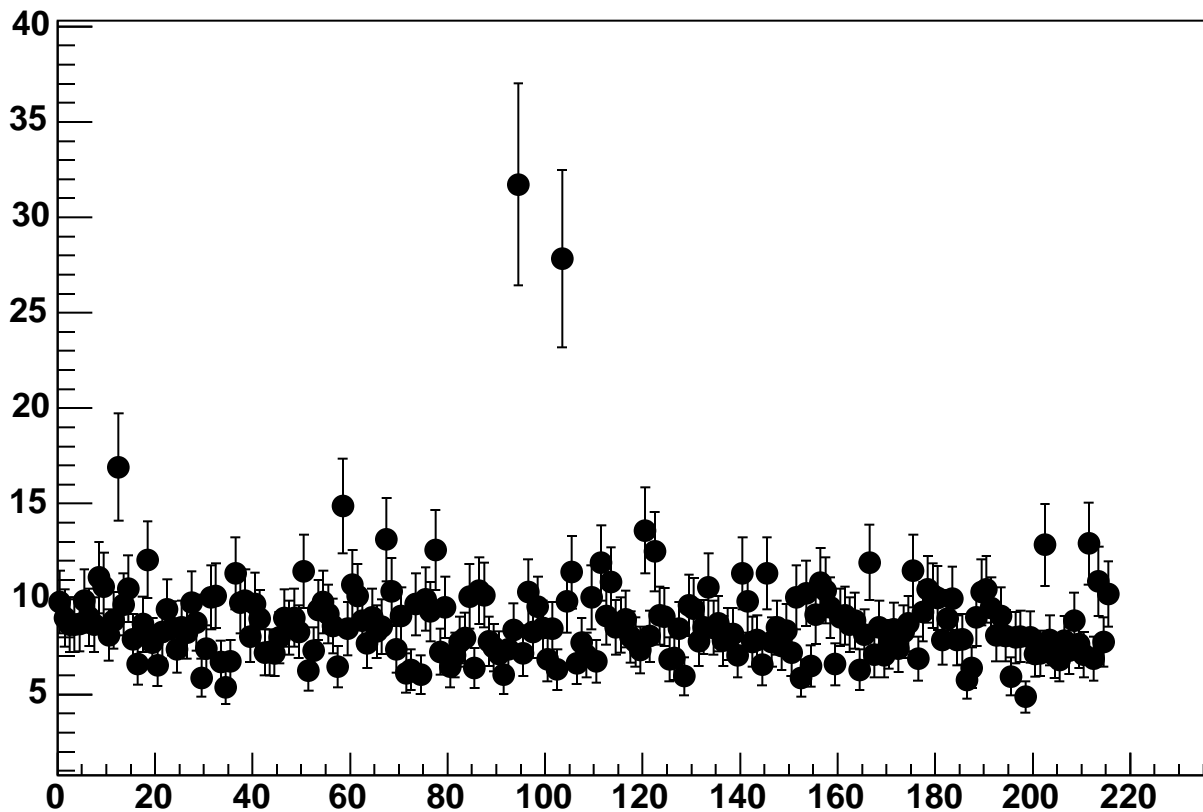
Enable 1, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



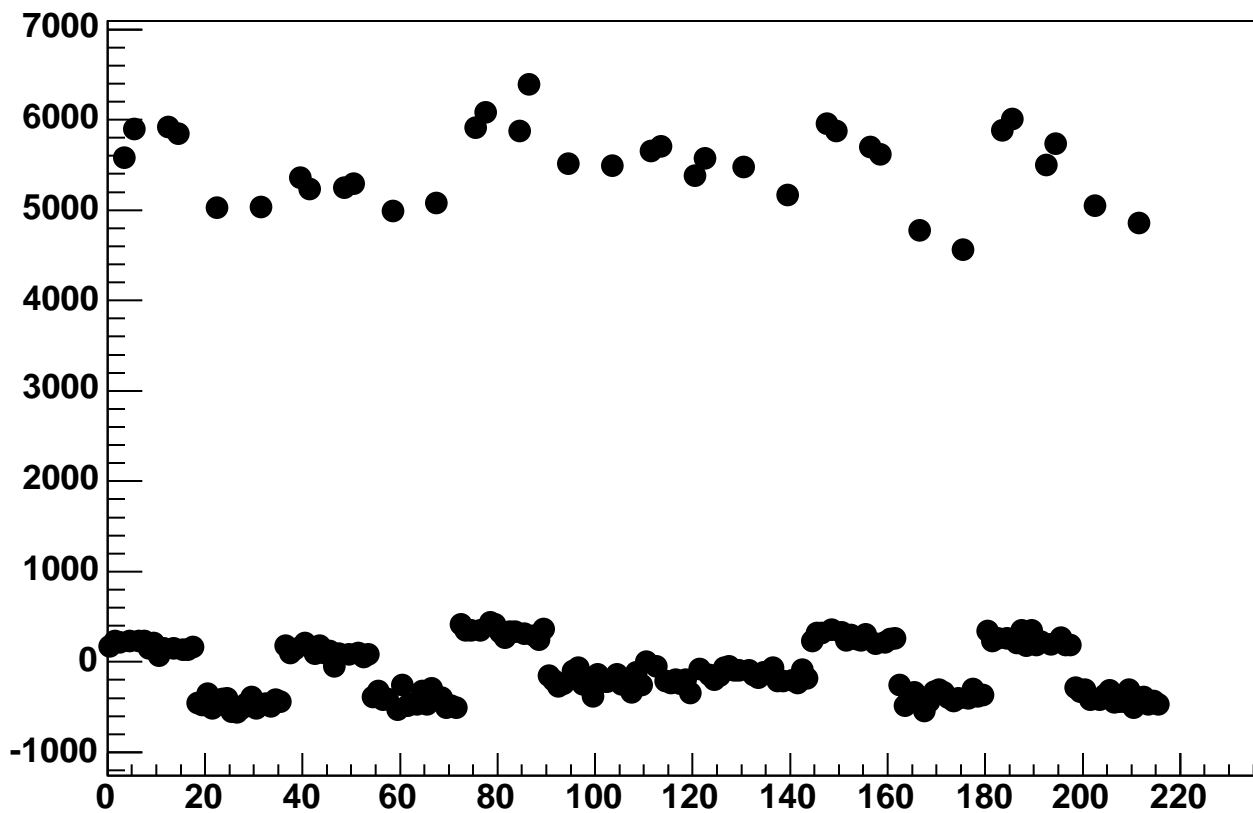
Enable 1, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



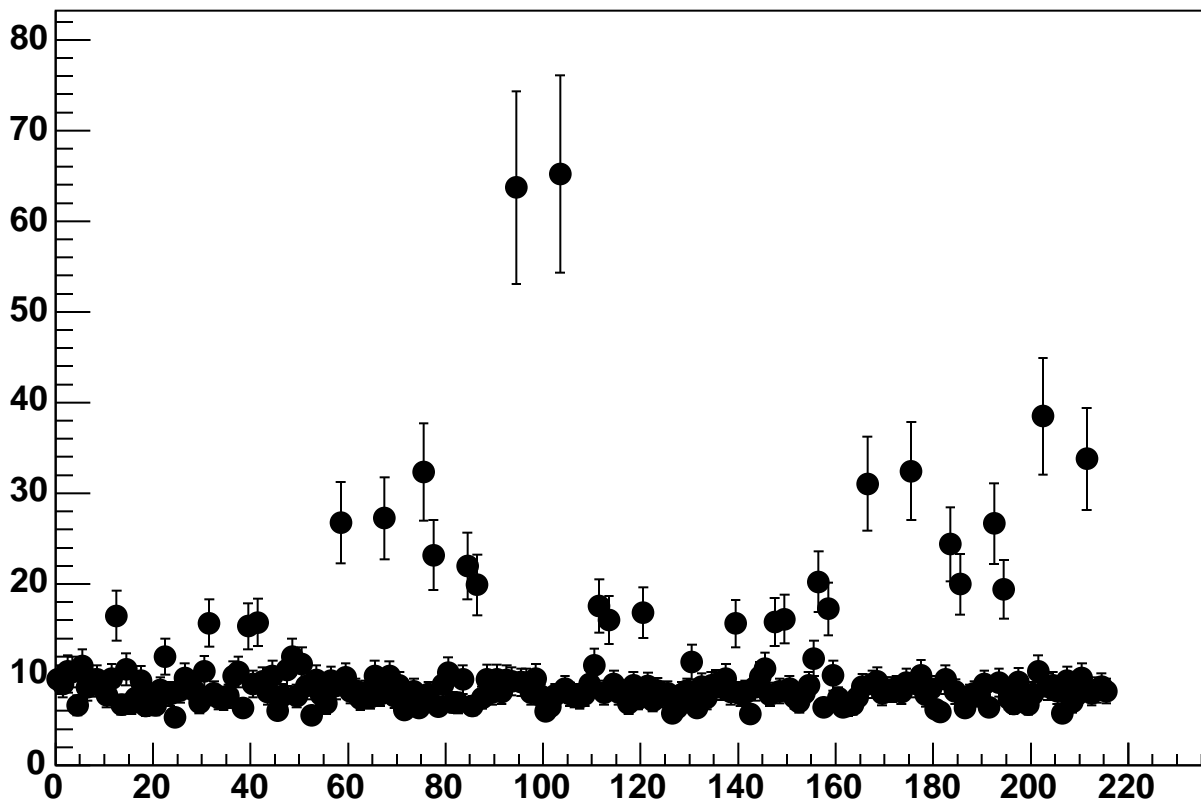
Enable 1, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



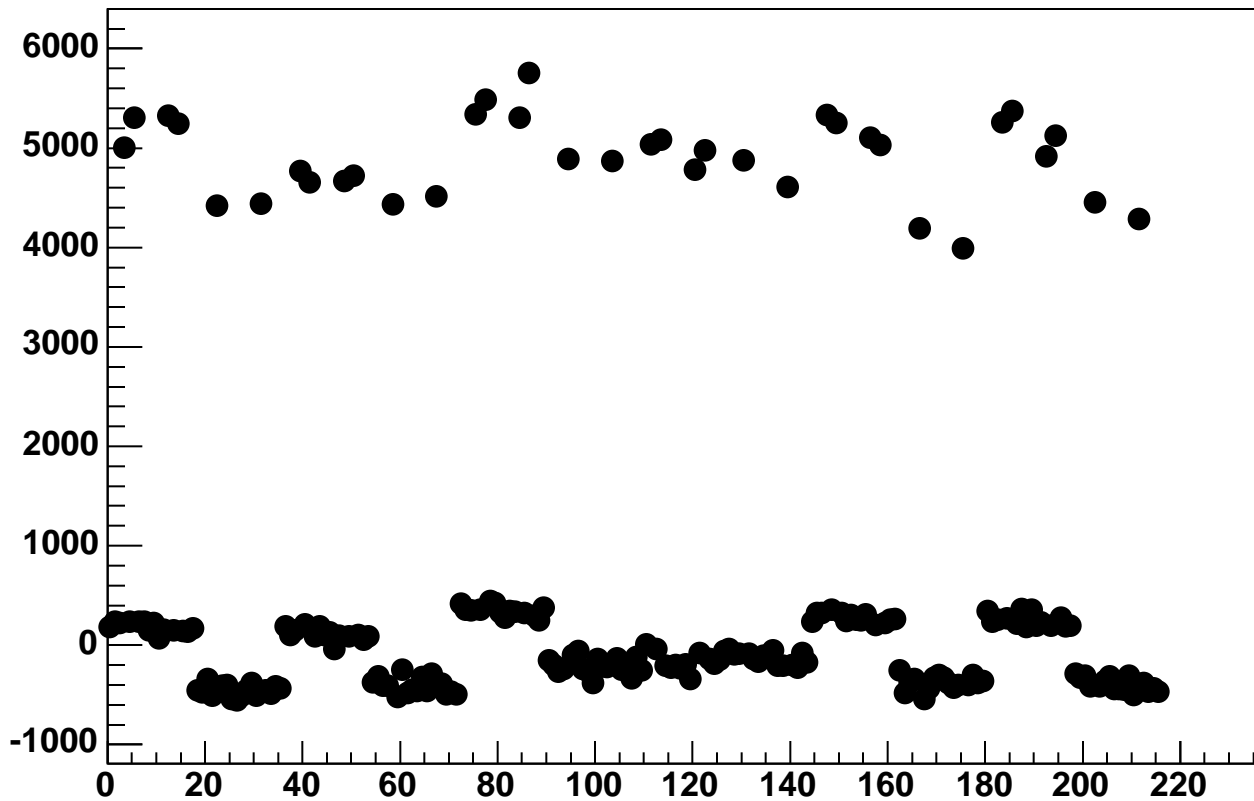
Enable 1, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



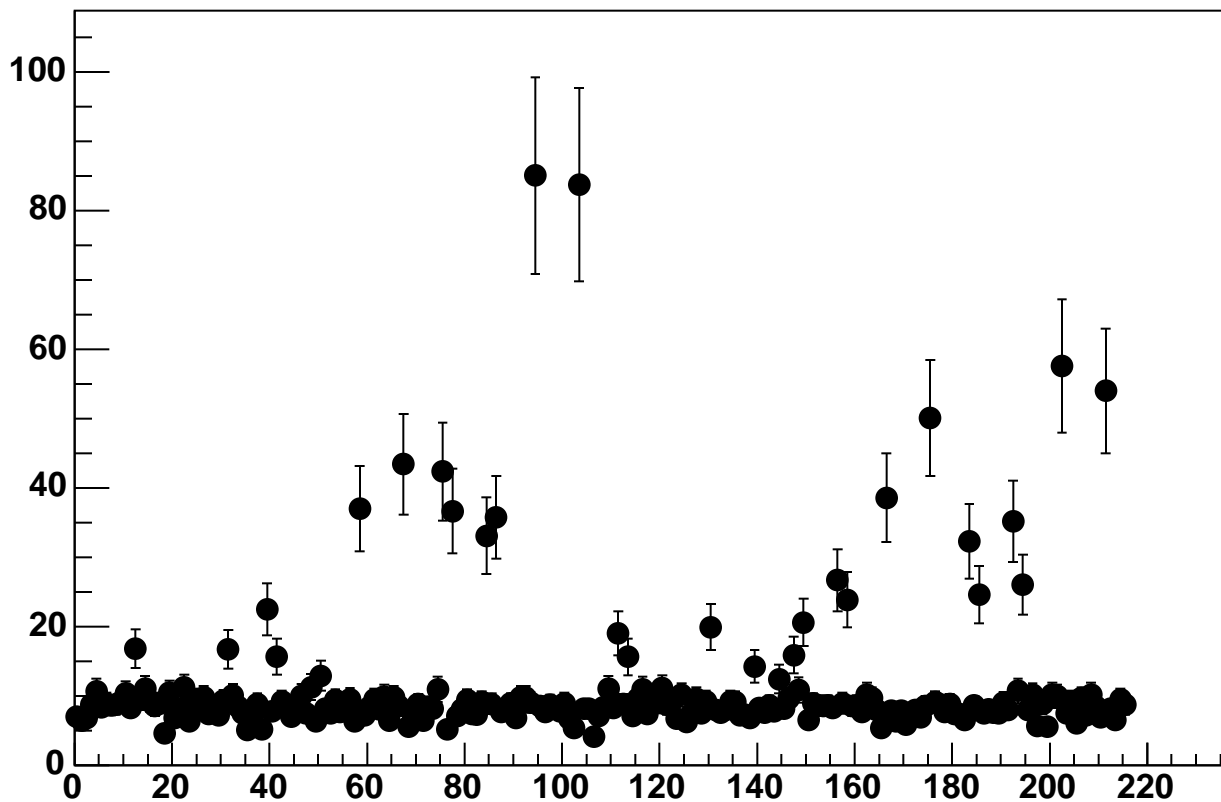
Enable 1, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



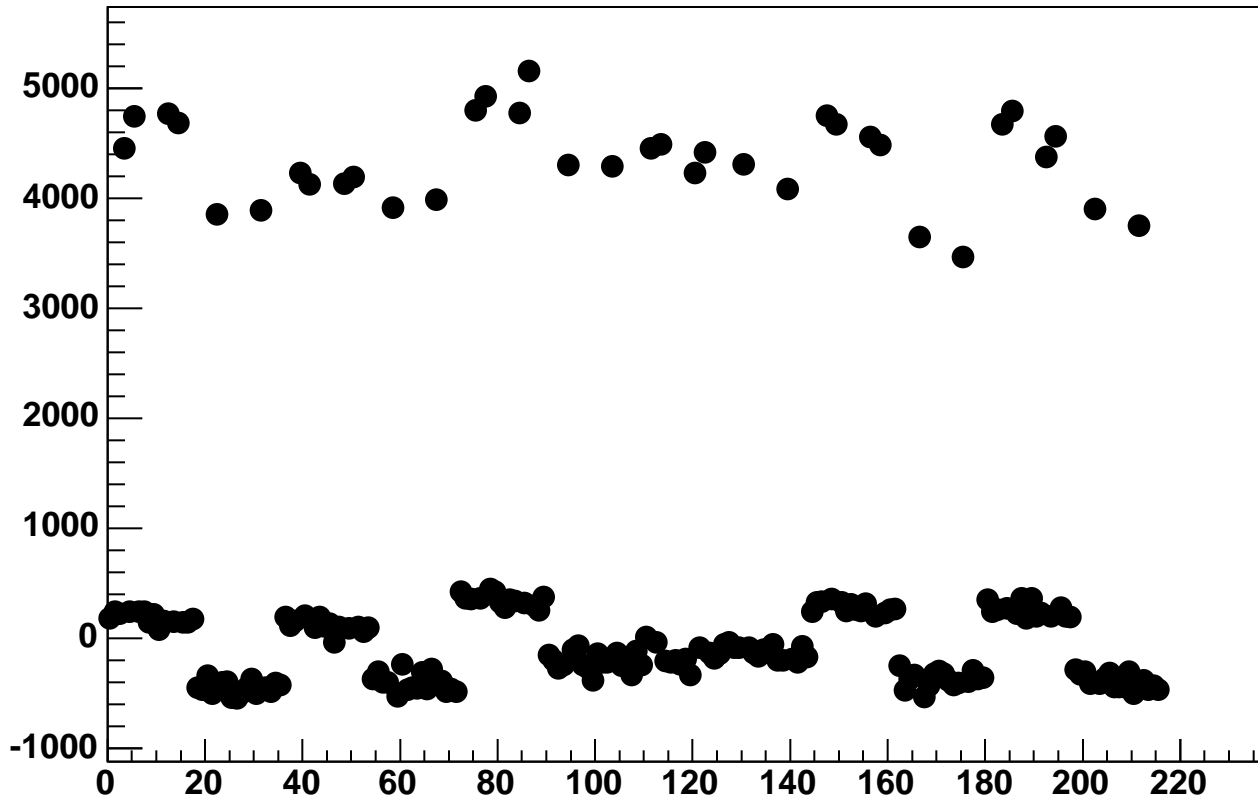
Enable 1, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



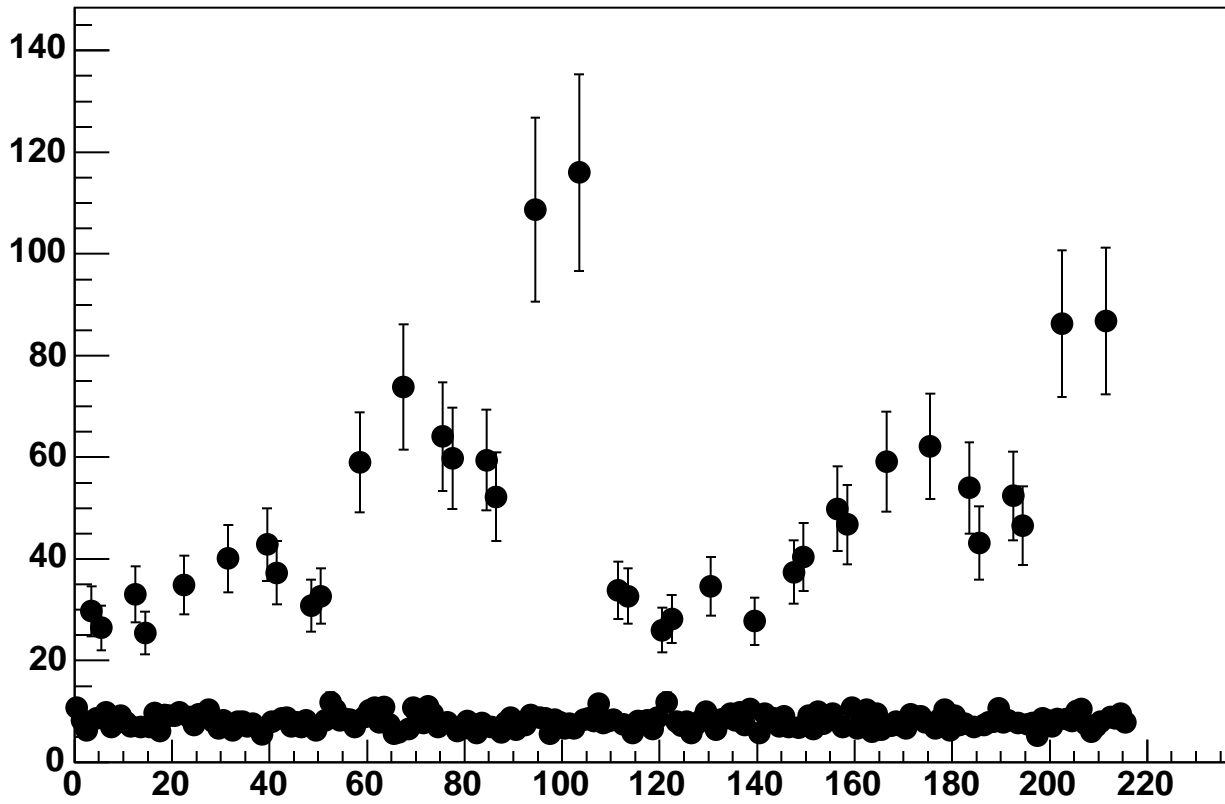
Enable 1, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



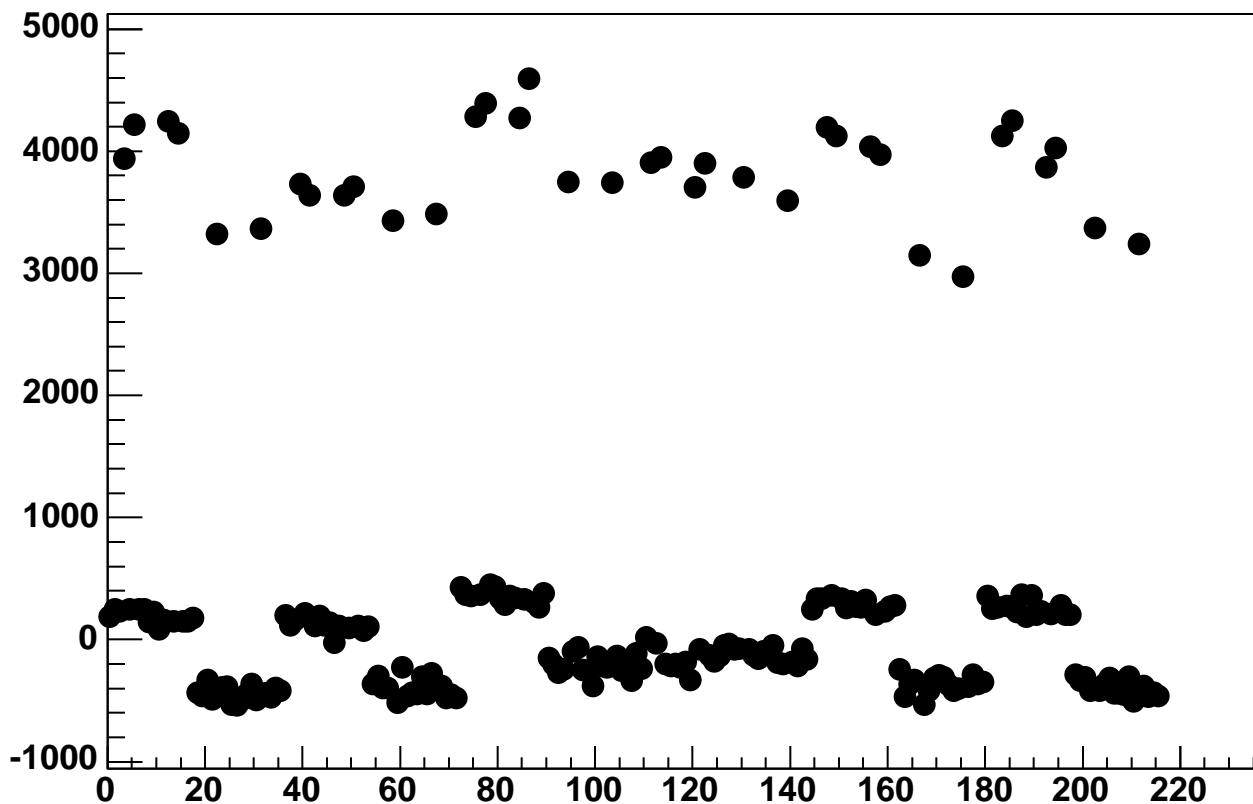
Enable 1, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



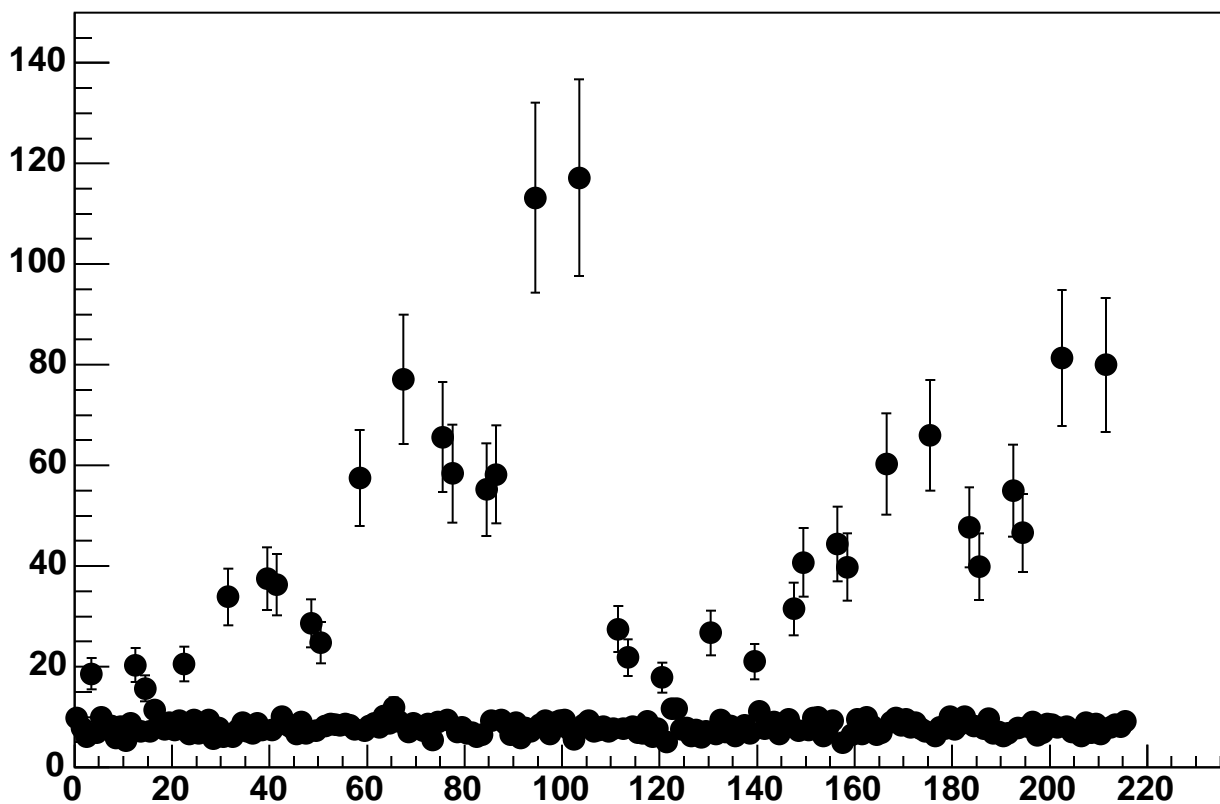
Enable 1, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



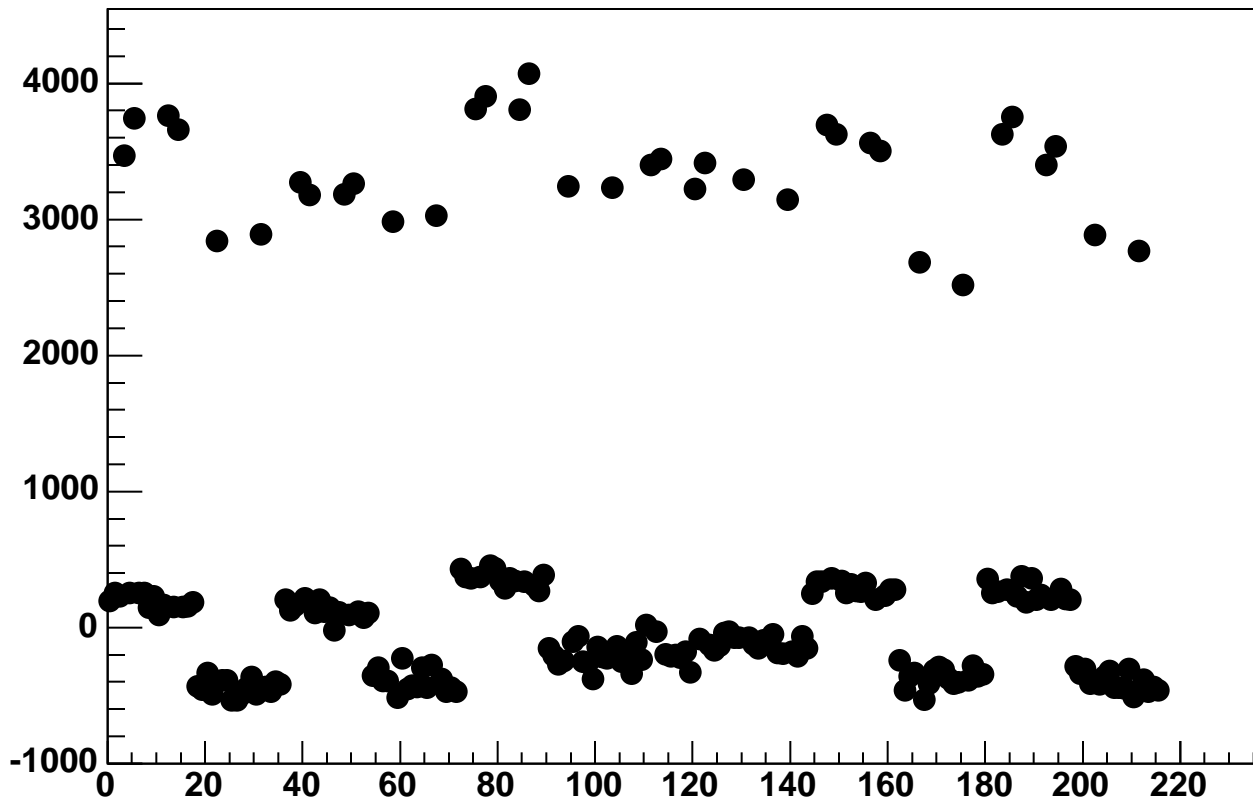
Enable 1, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



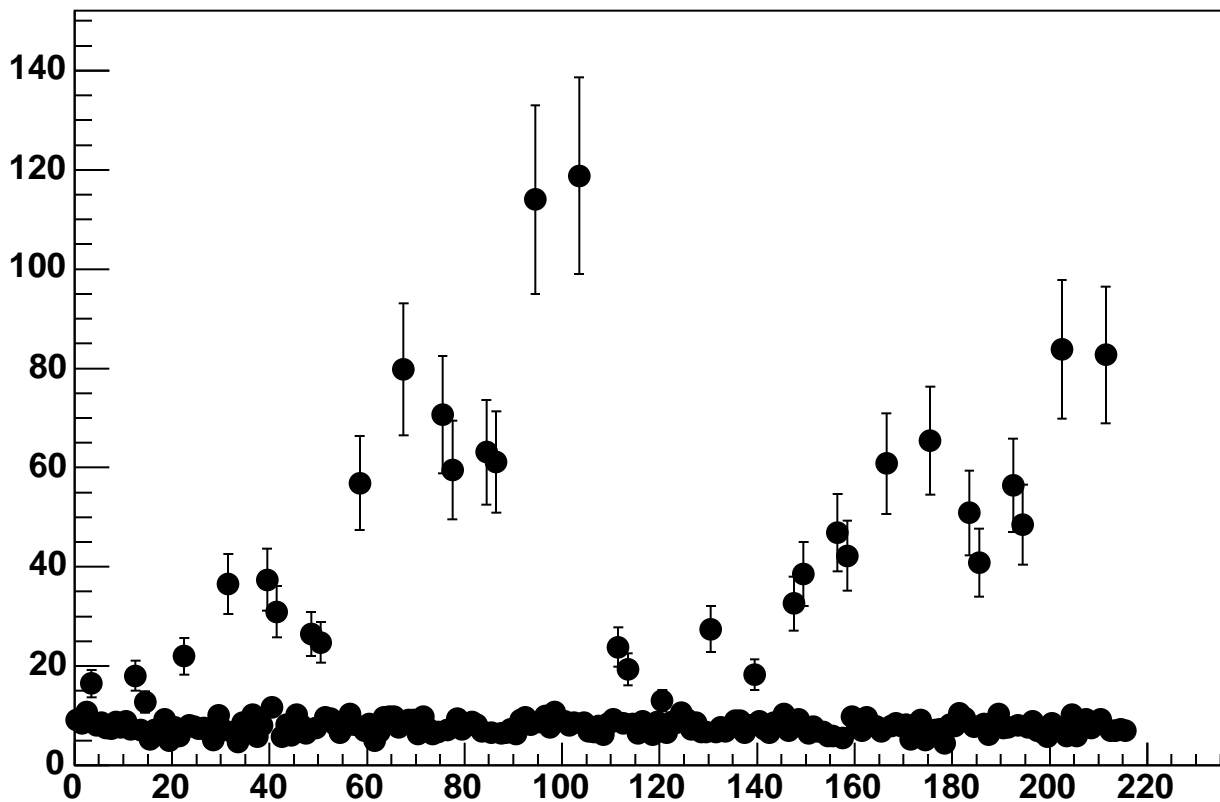
Enable 1, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



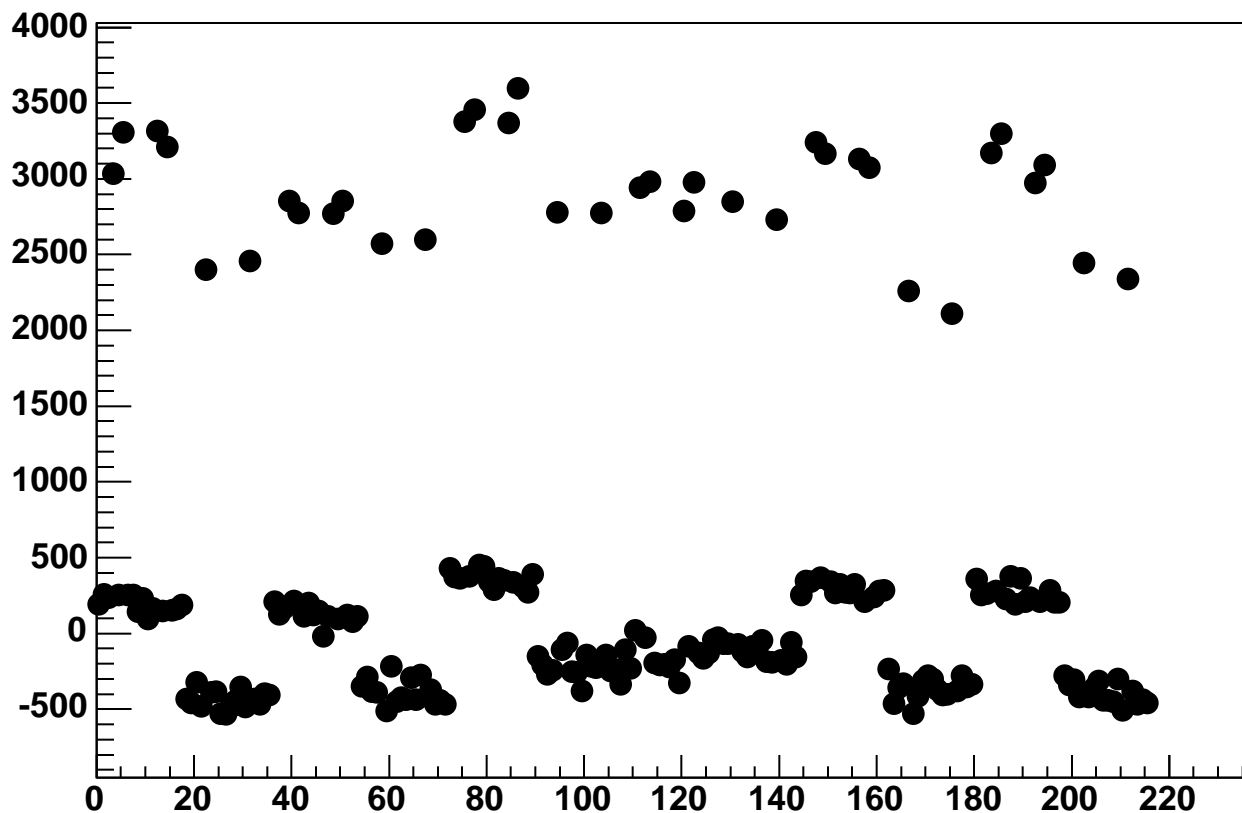
Enable 1, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



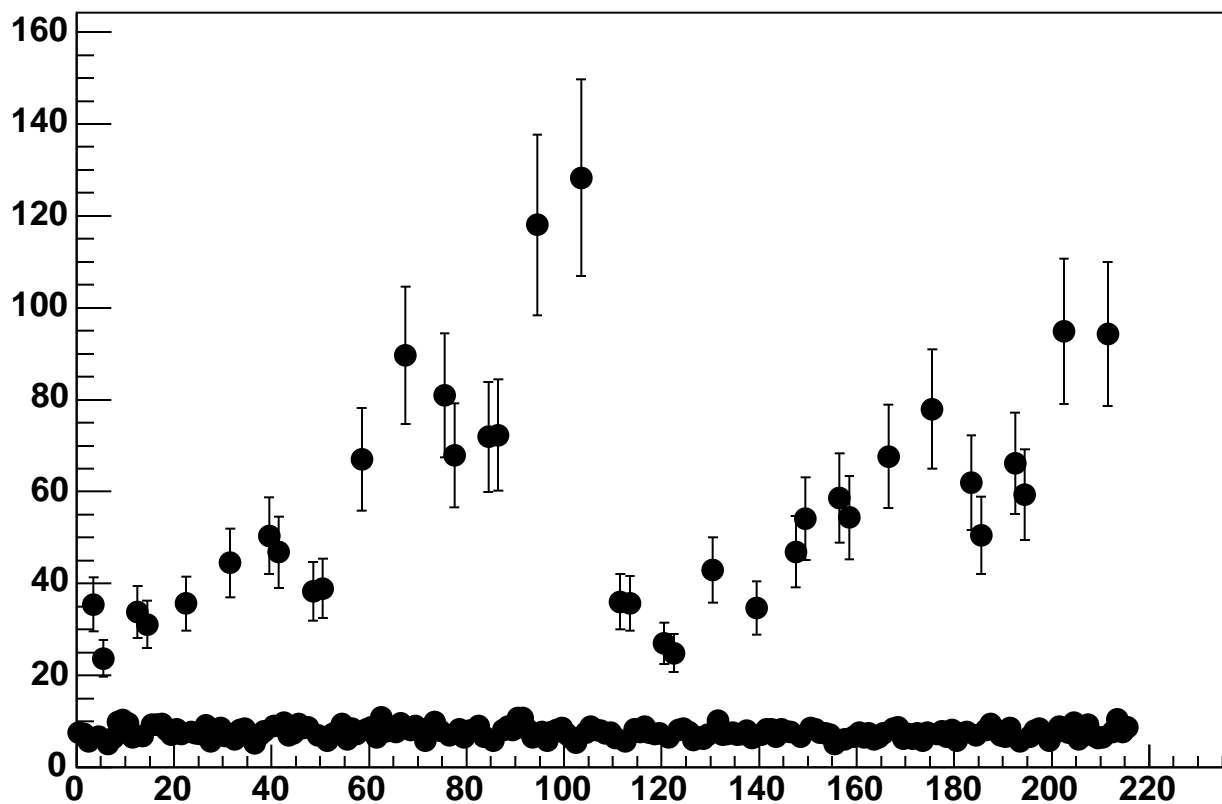
Enable 1, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



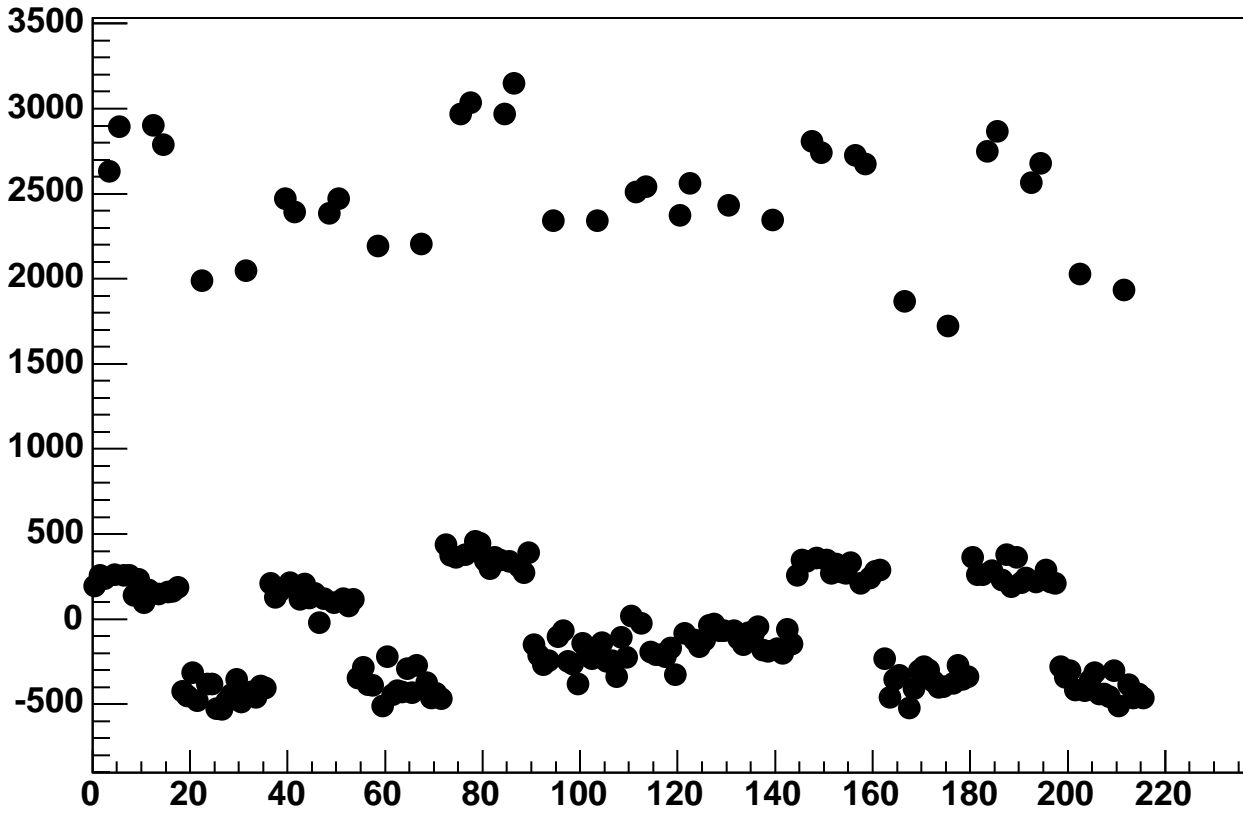
Enable 1, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



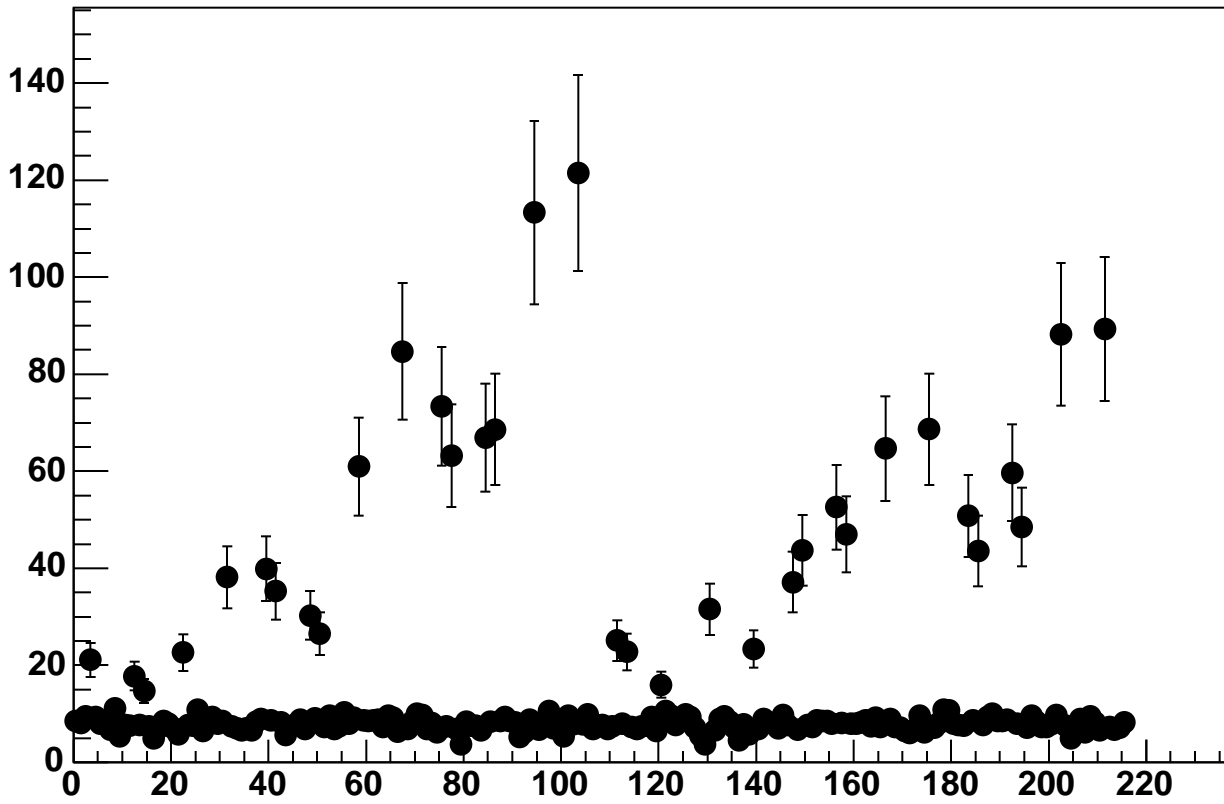
Enable 1, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



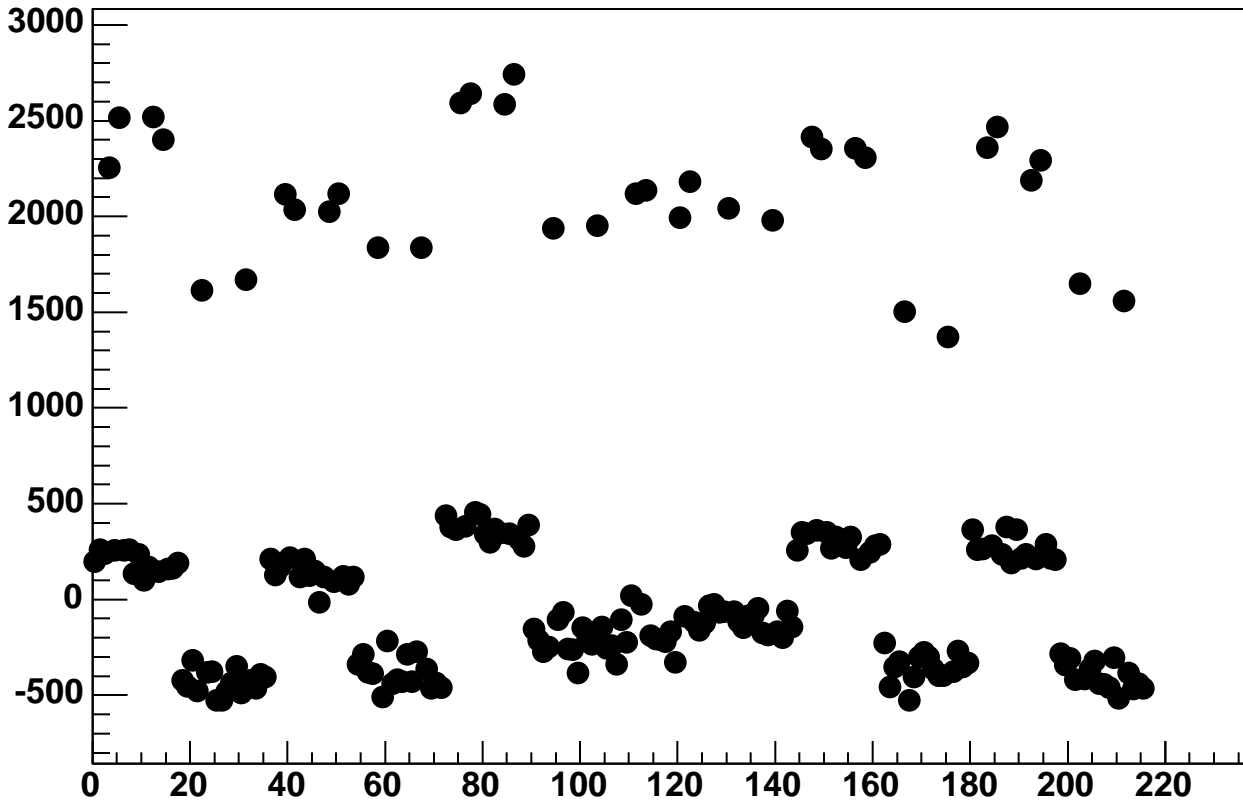
Enable 1, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



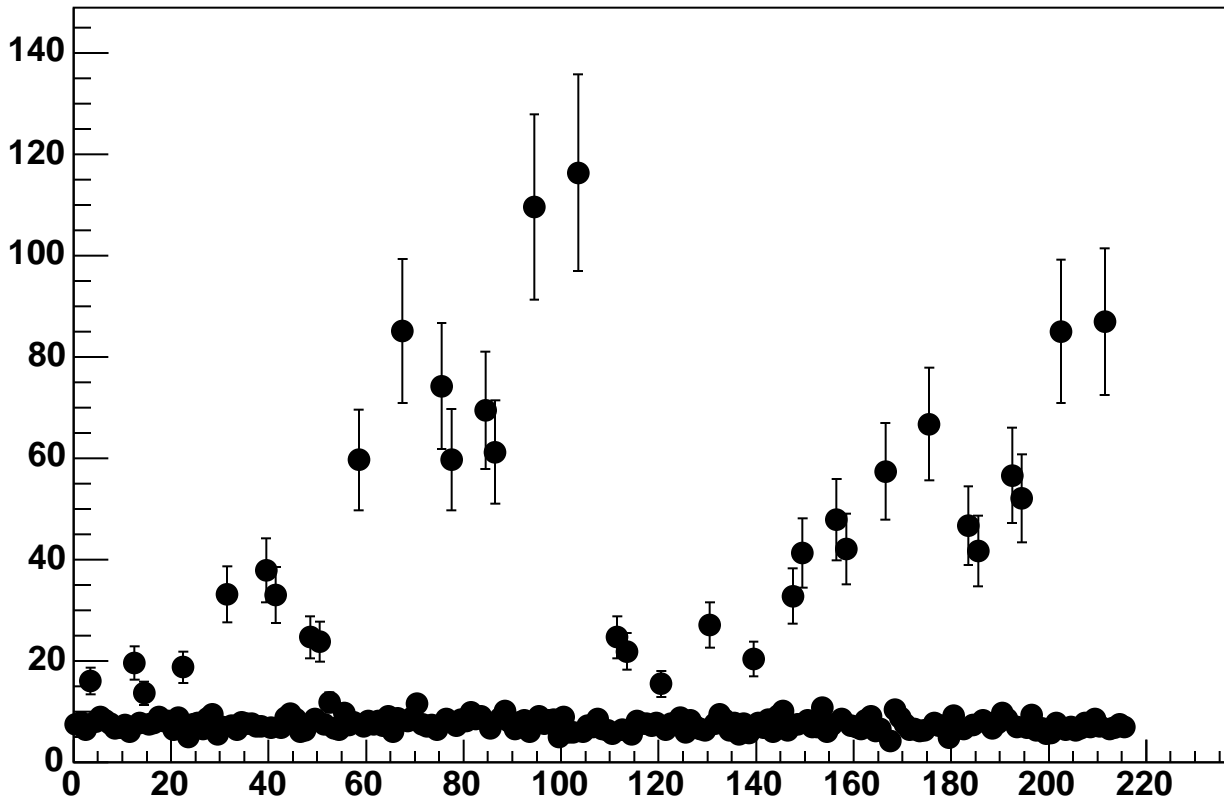
Enable 1, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



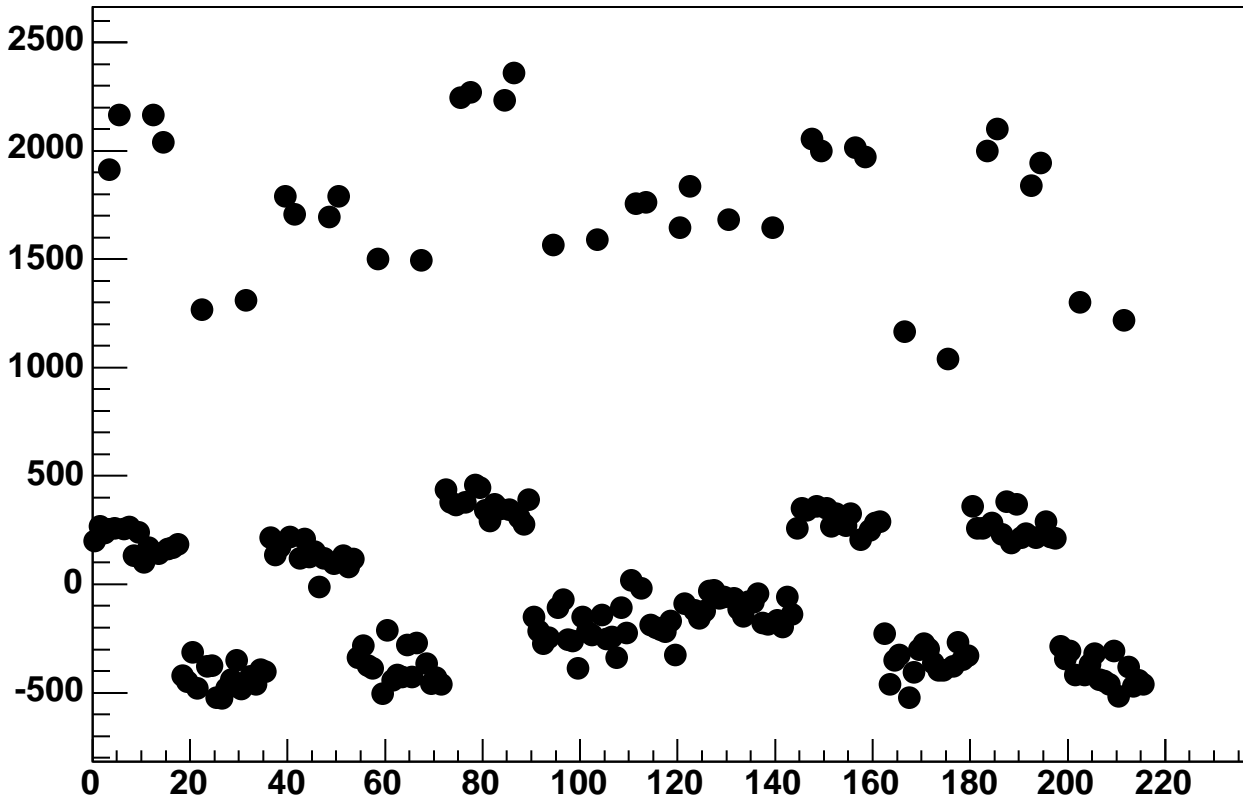
Enable 1, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



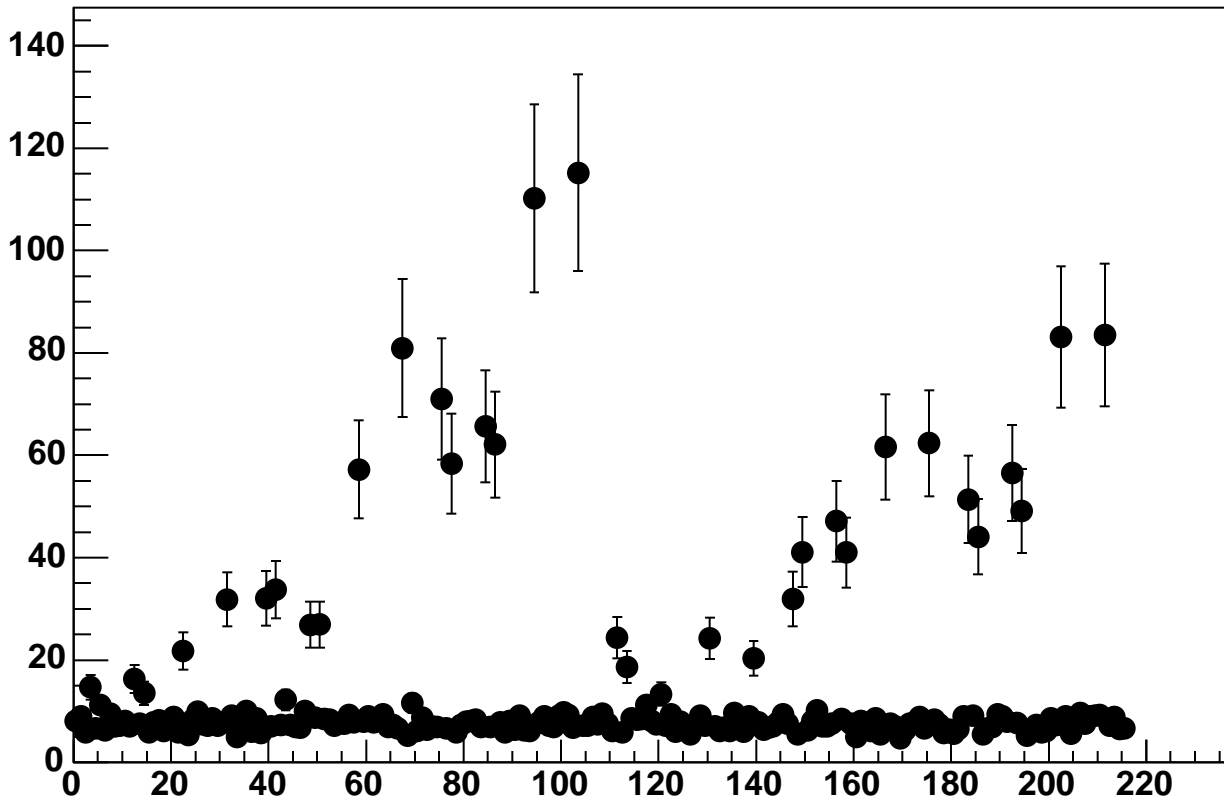
Enable 1, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



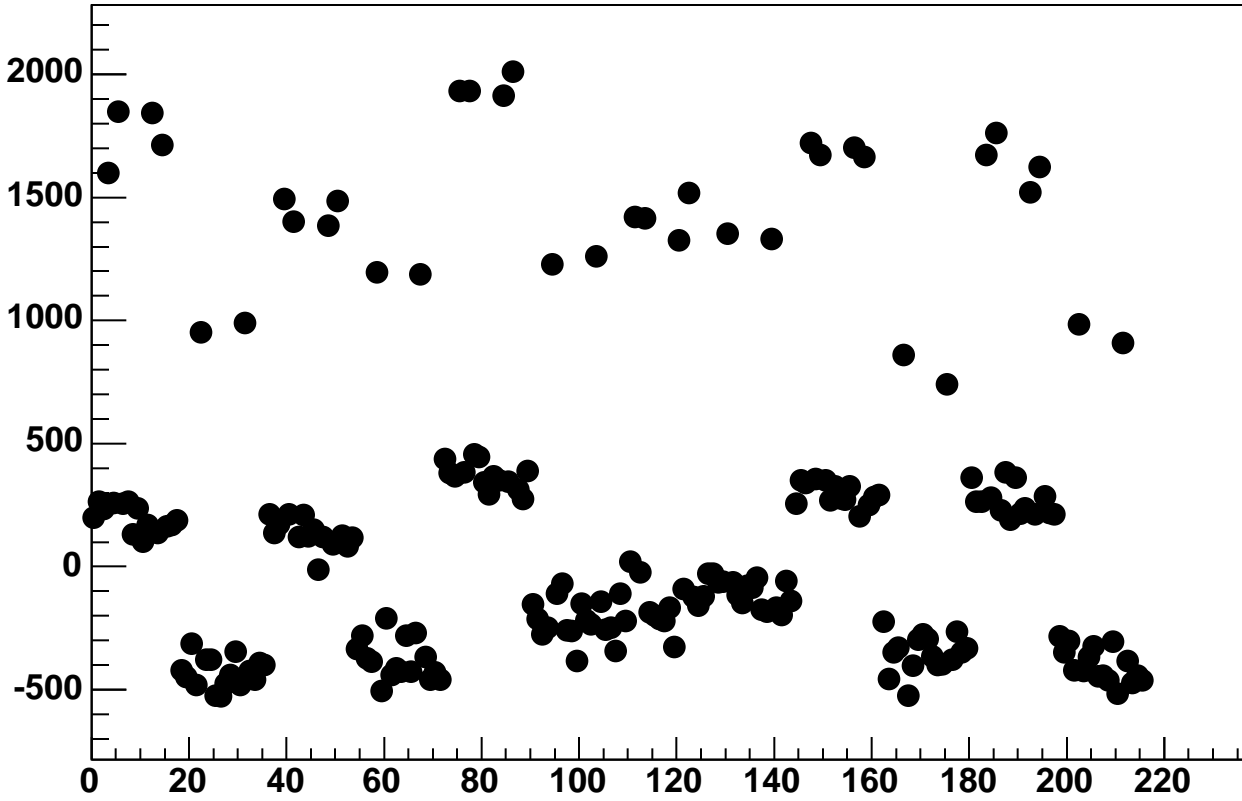
Enable 1, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



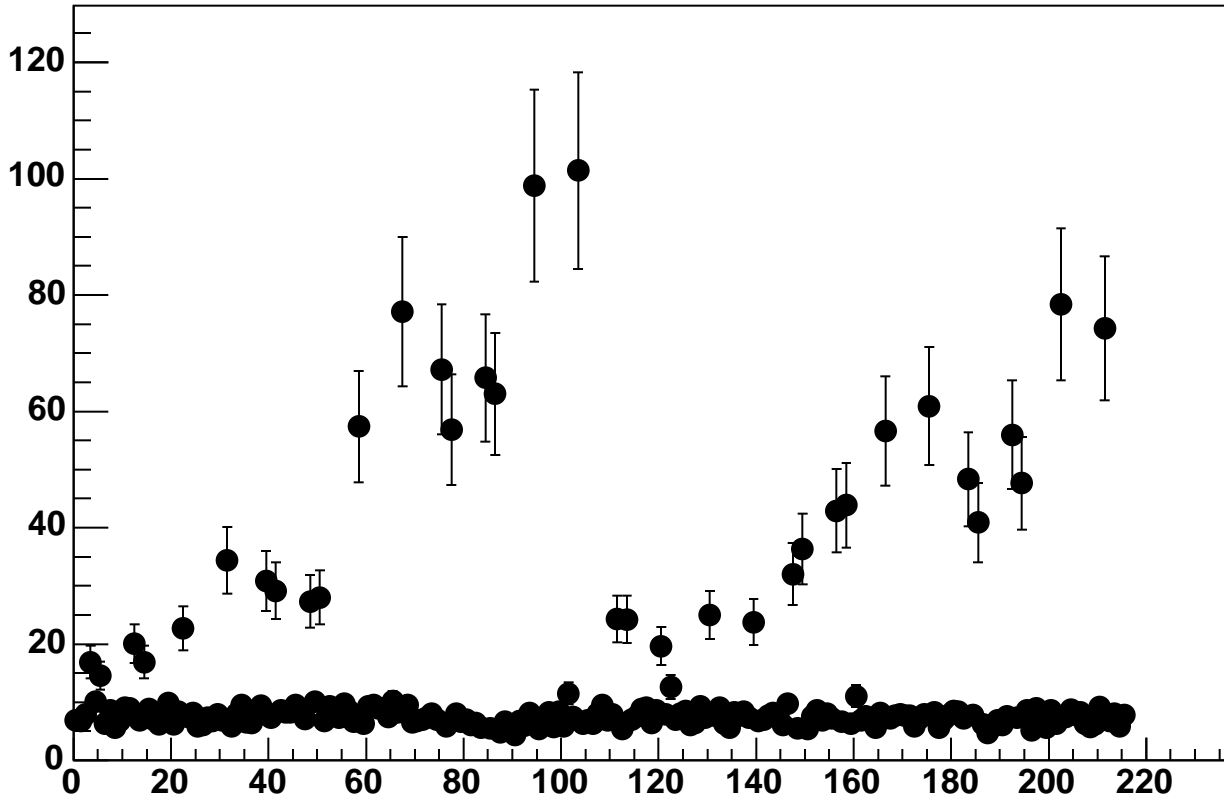
Enable 1, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



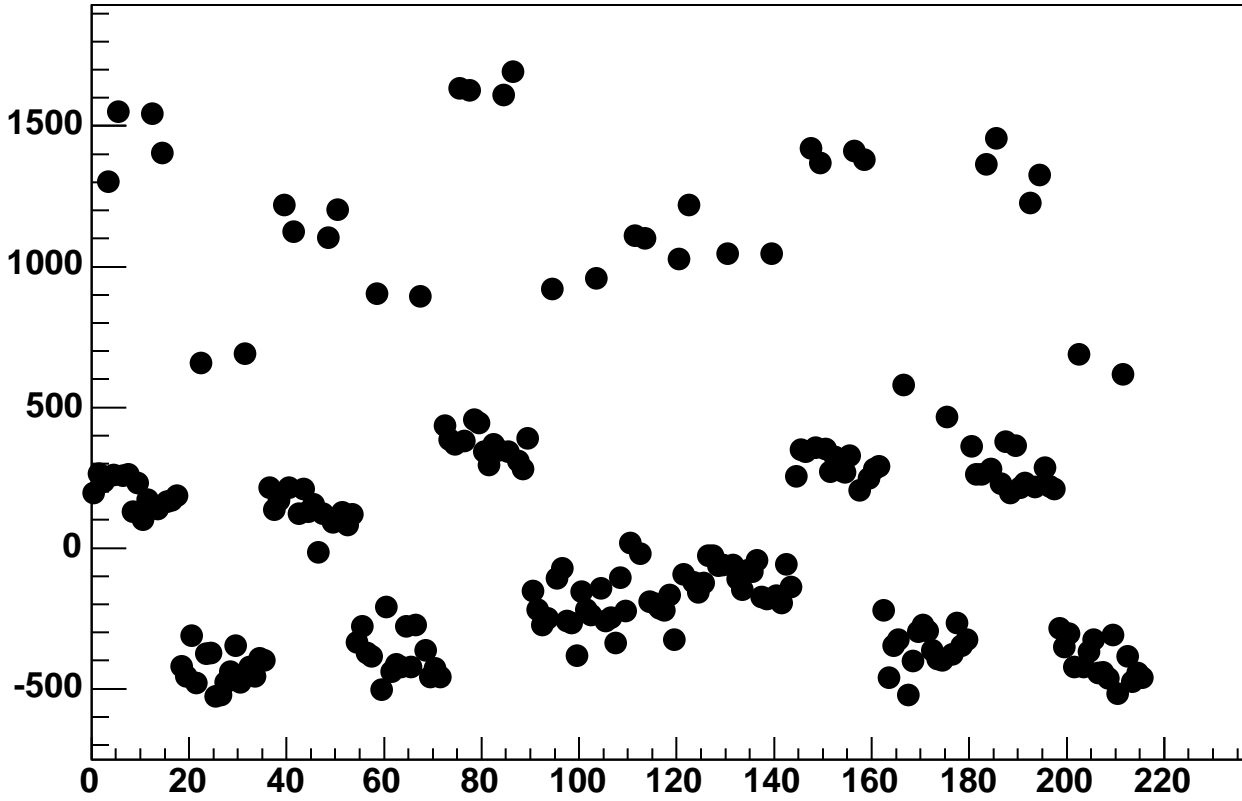
Enable 1, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



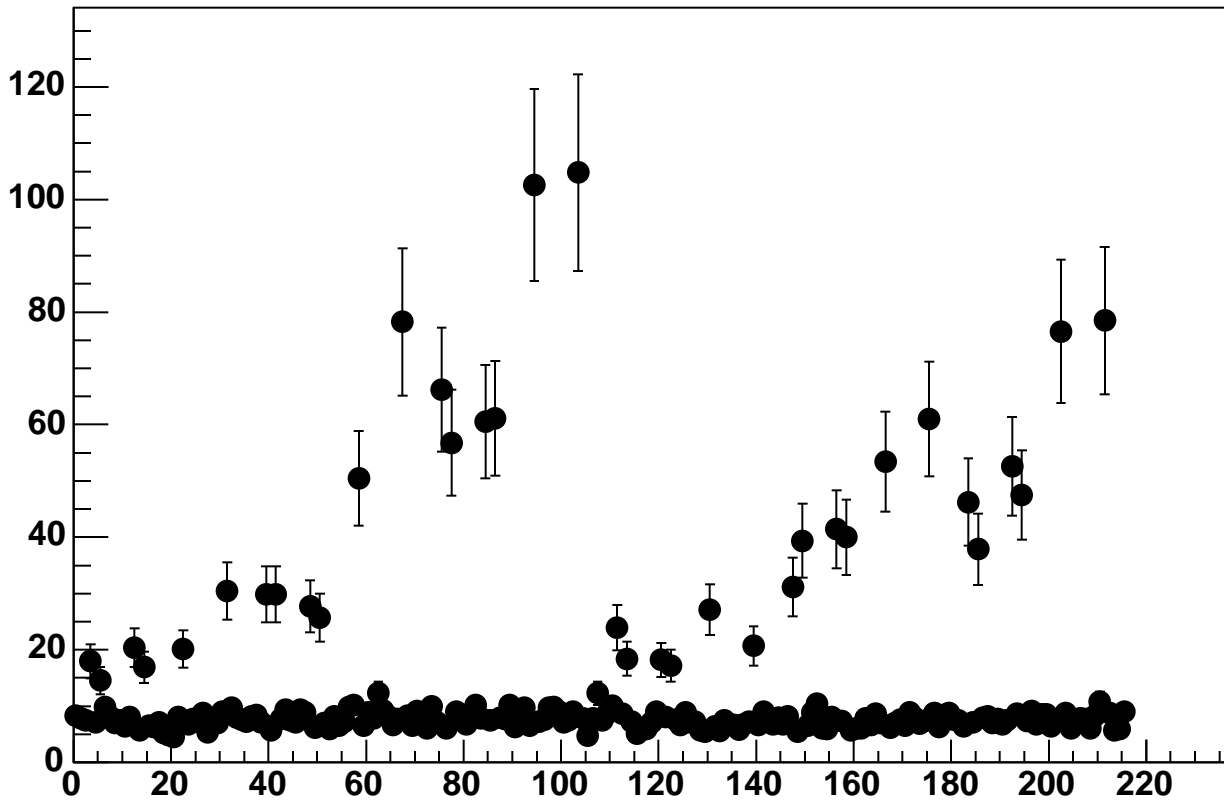
Enable 1, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



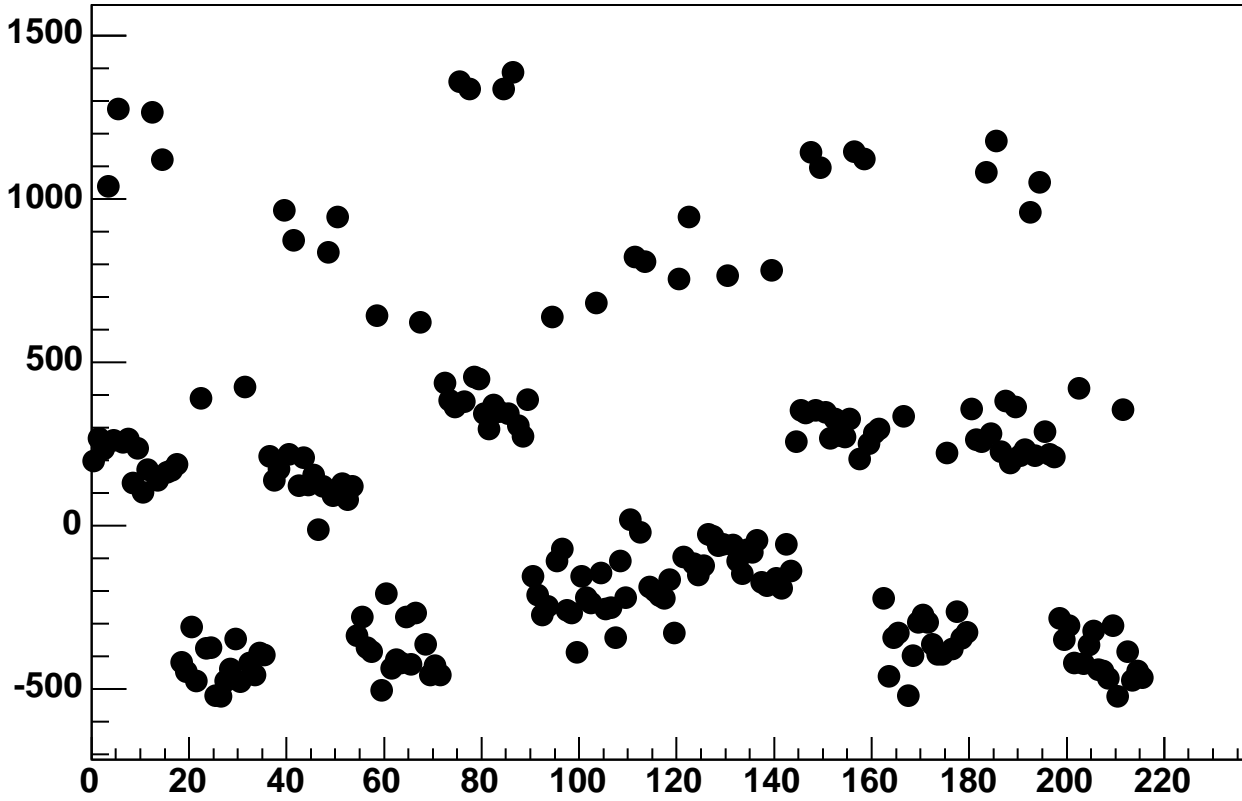
Enable 1, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



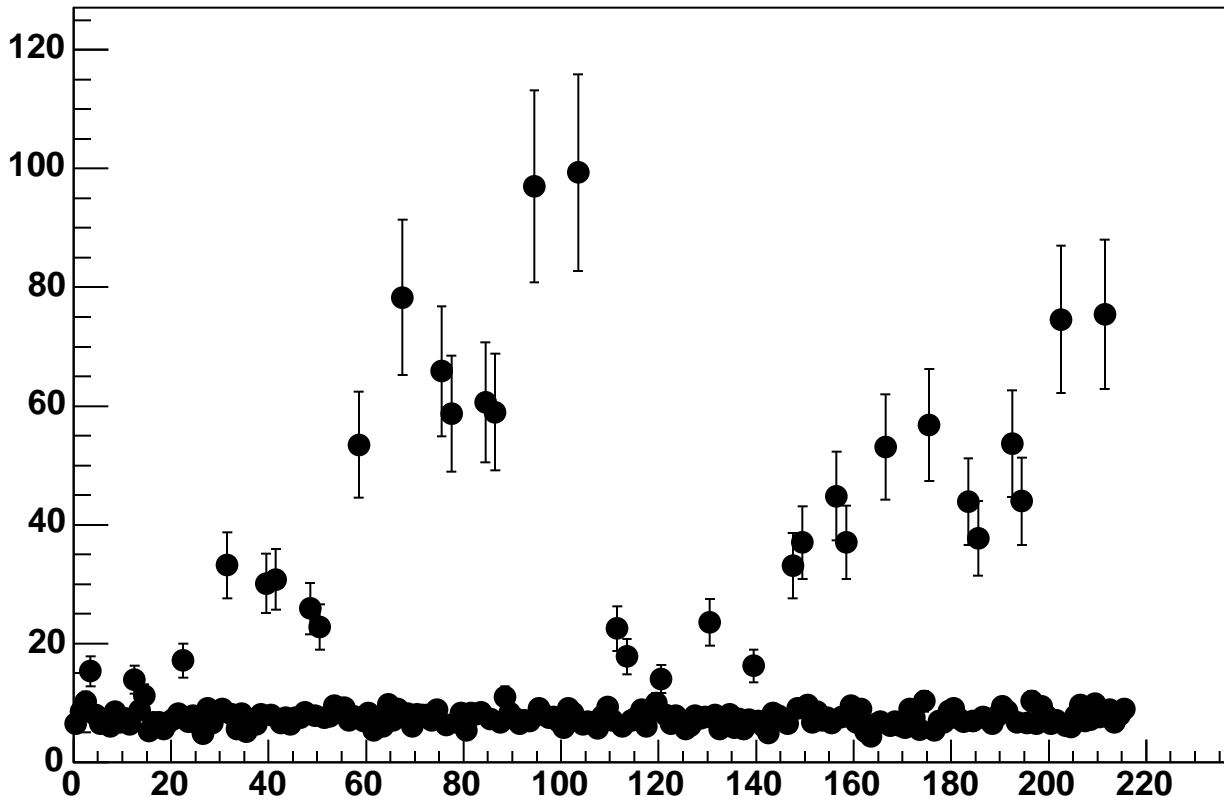
Enable 1, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



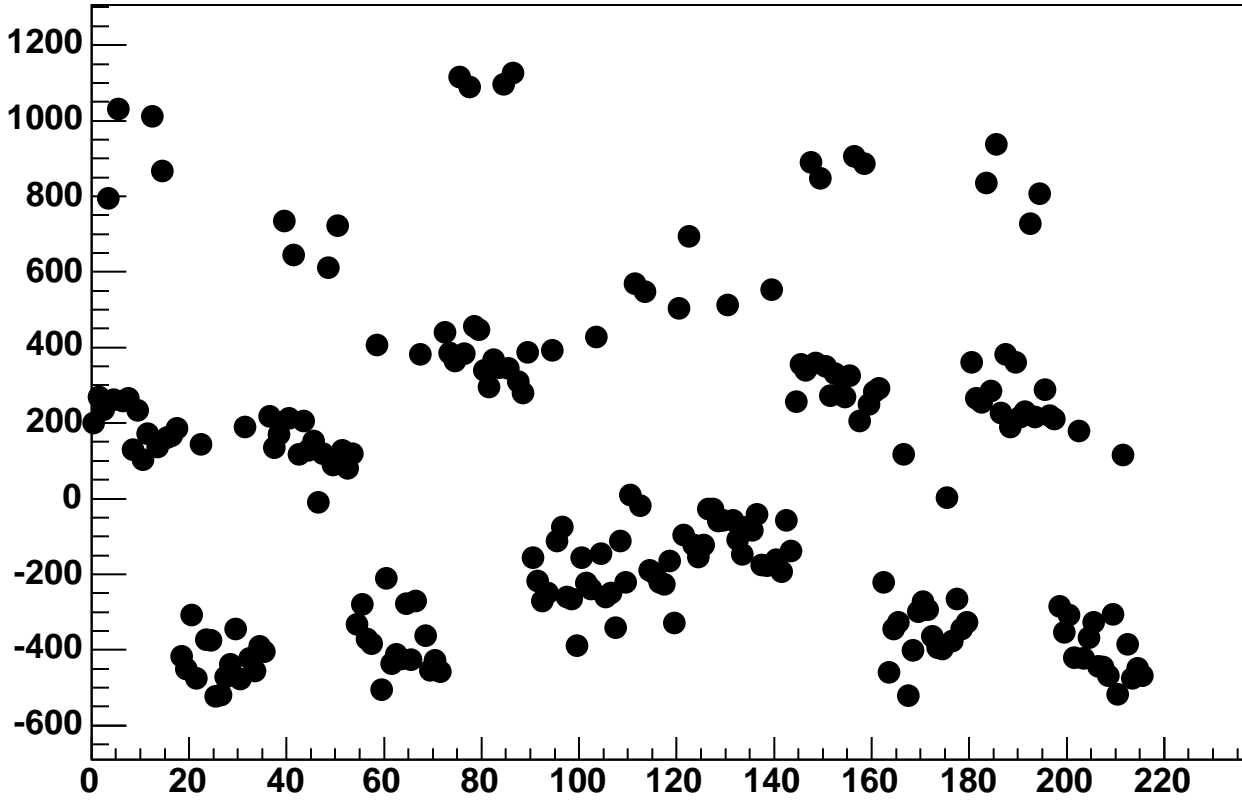
Enable 1, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



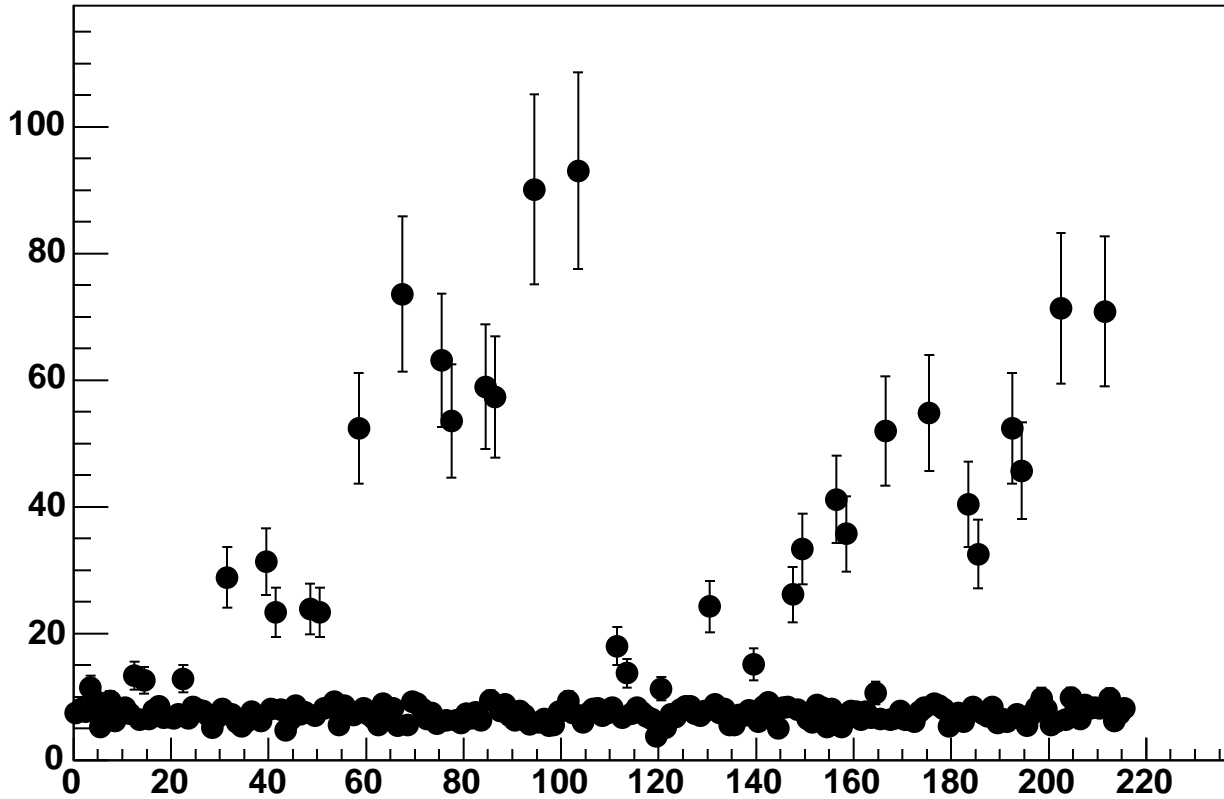
Enable 1, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



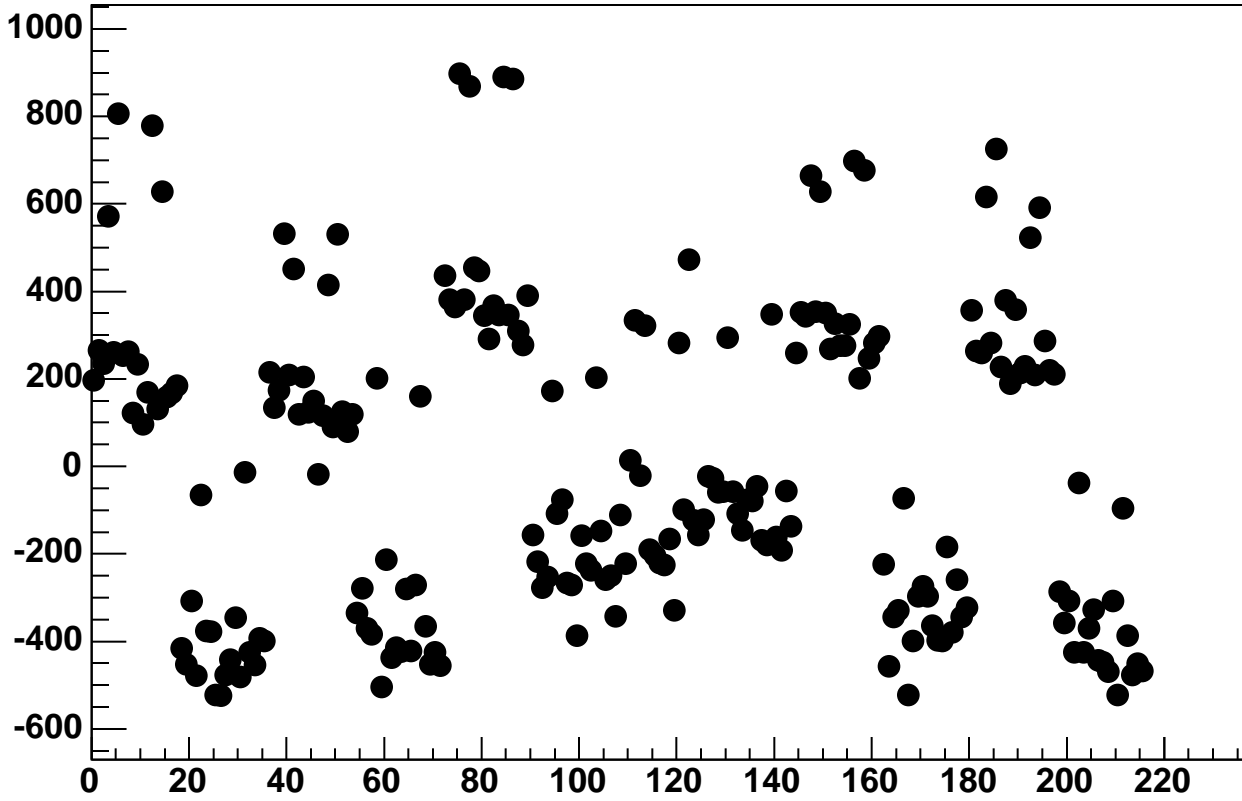
Enable 1, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



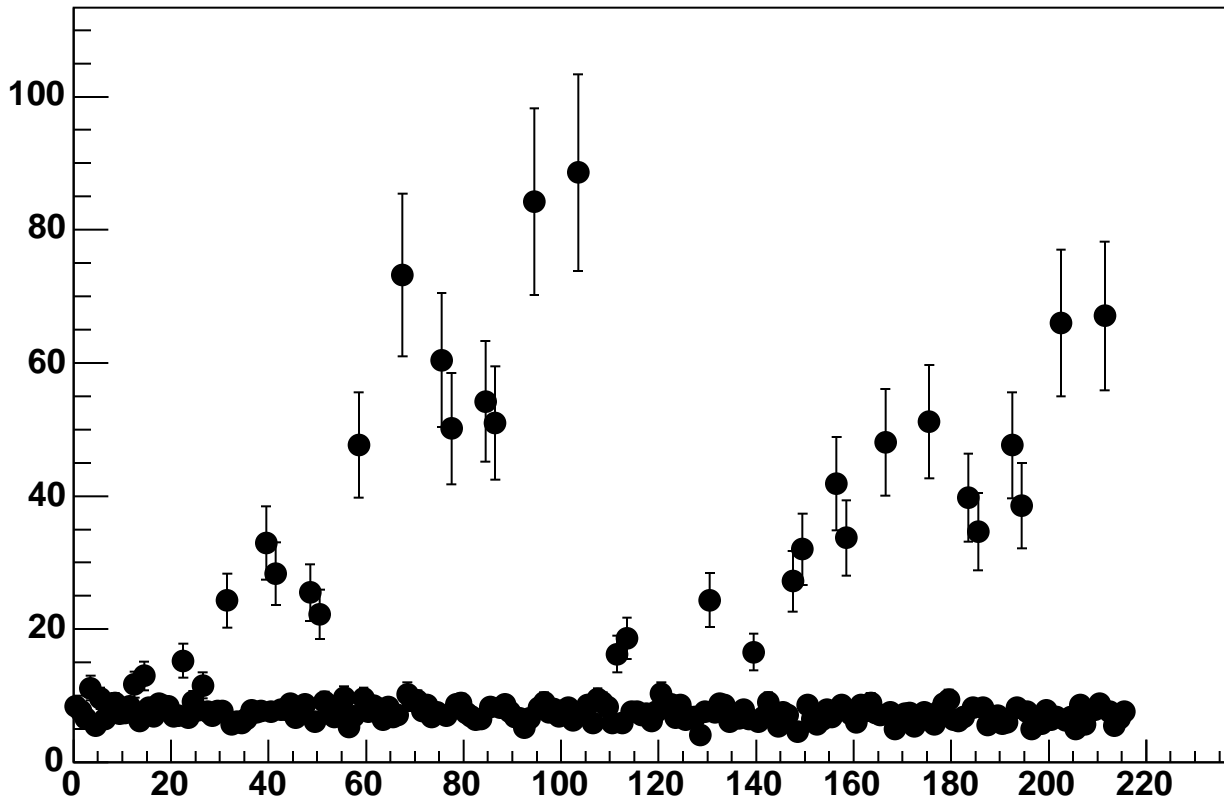
Enable 1, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



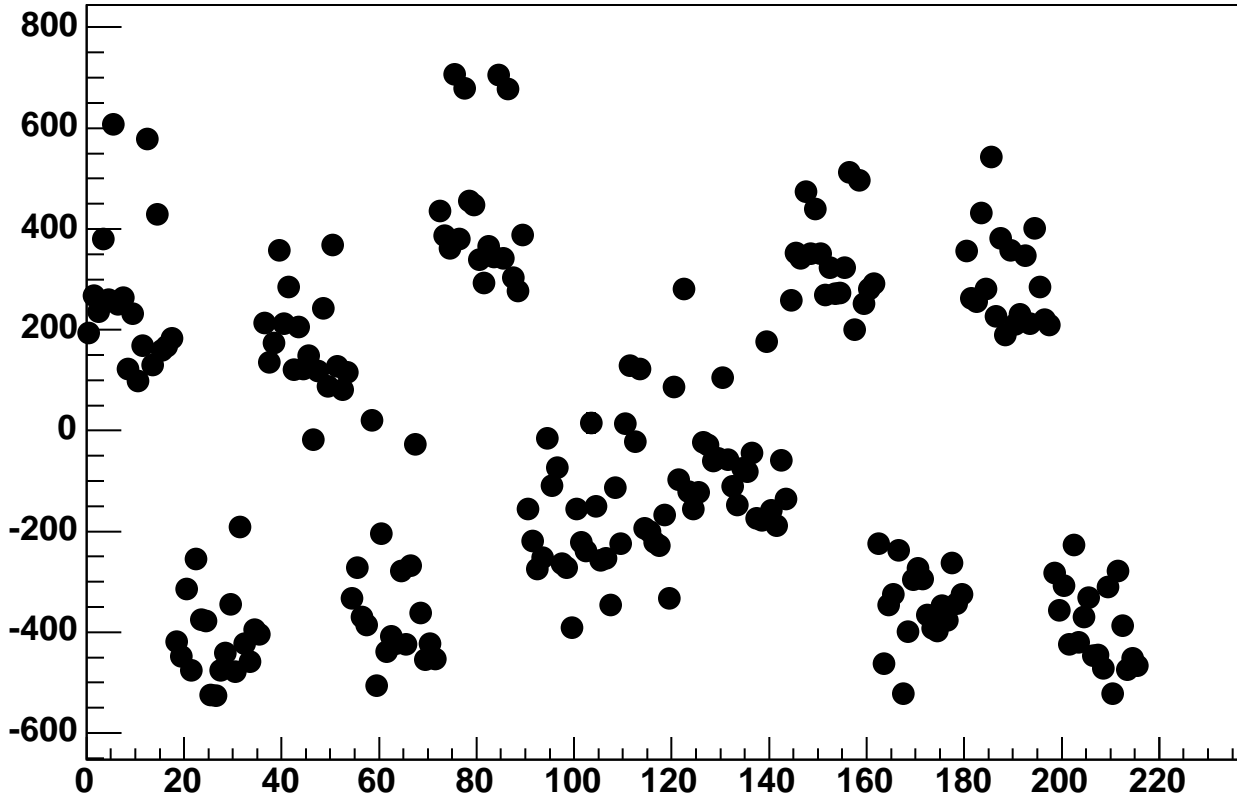
Enable 1, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



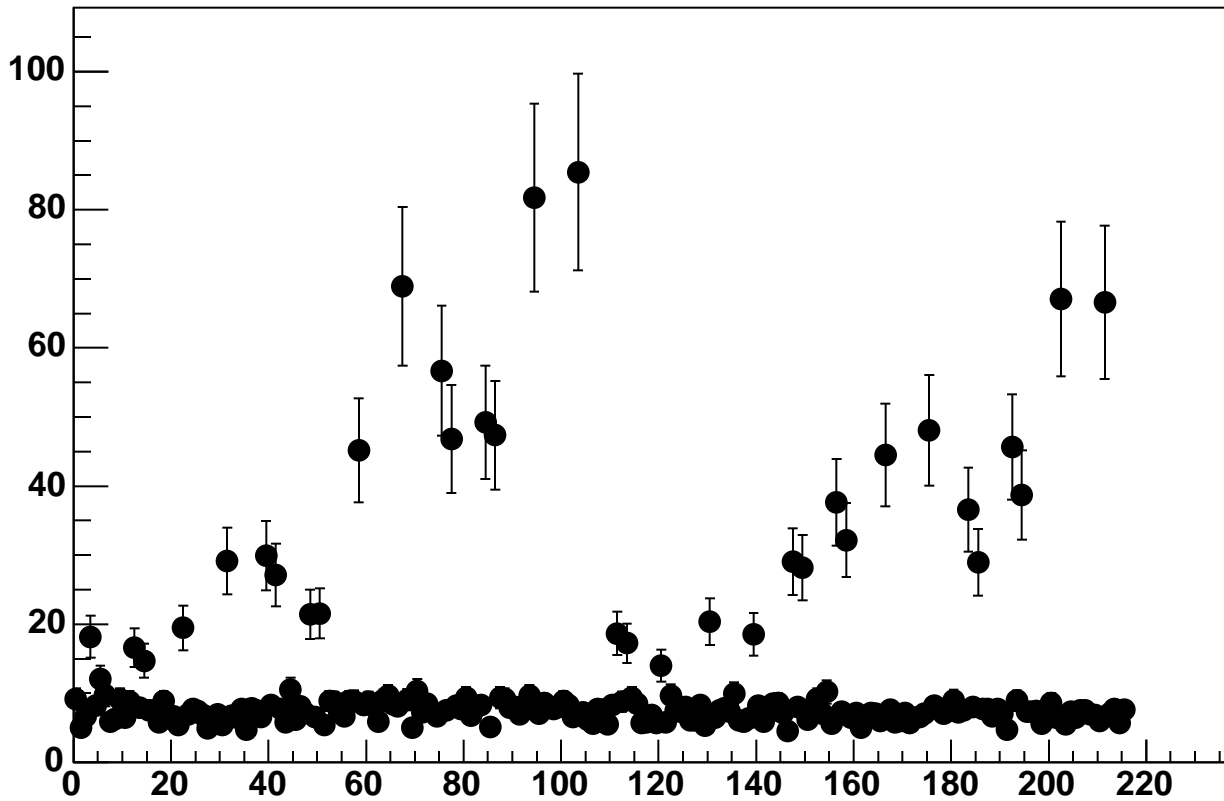
Enable 1, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



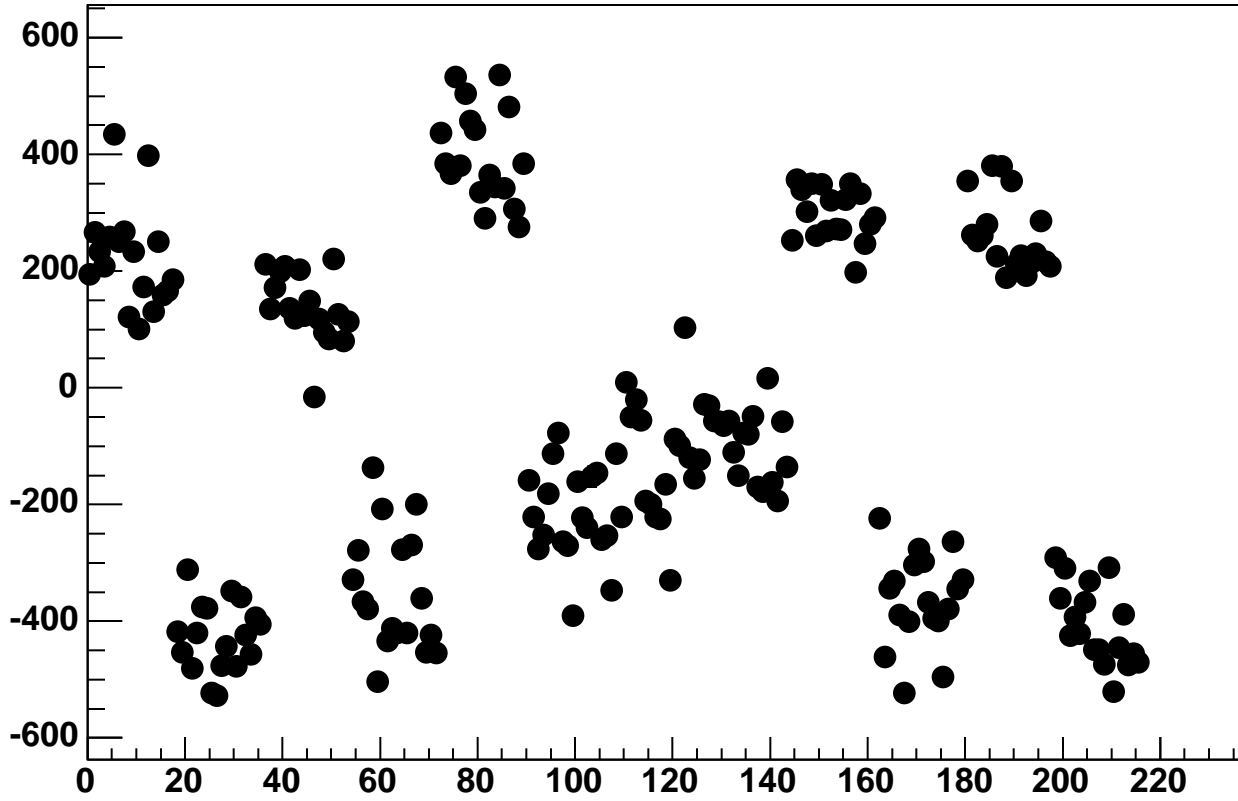
Enable 1, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



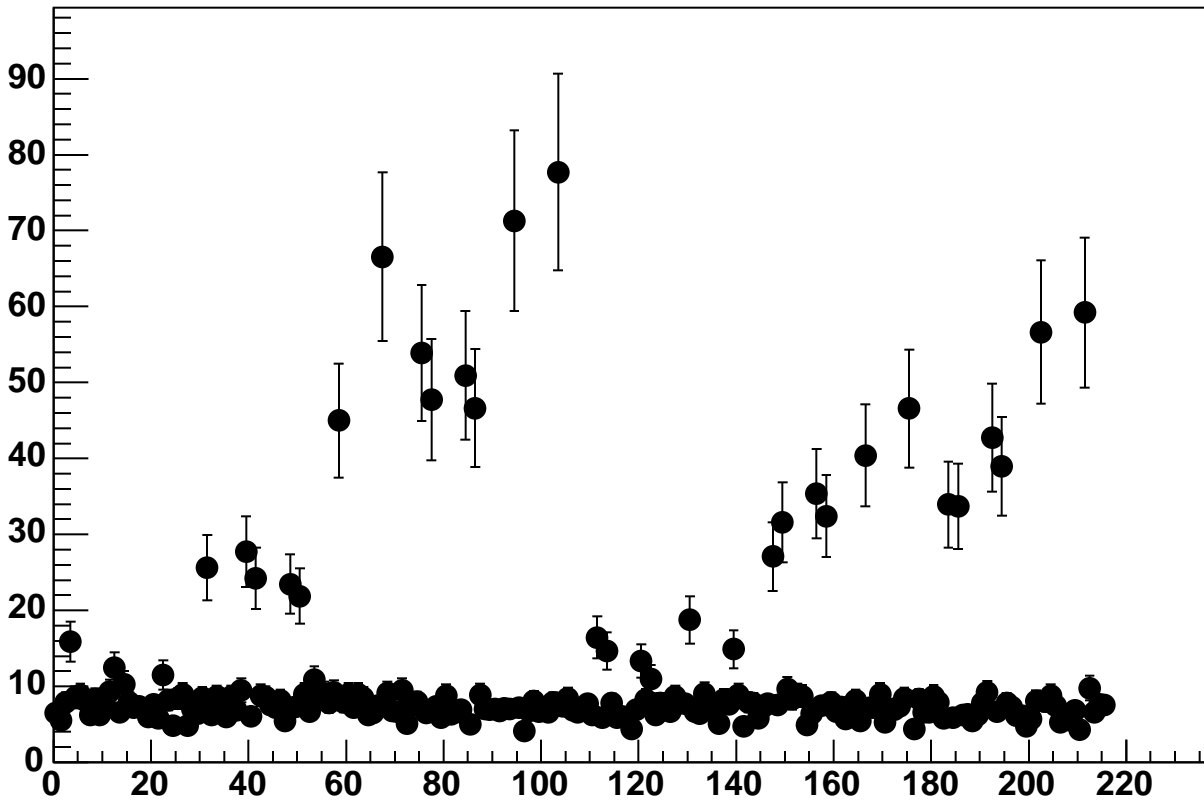
Enable 1, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



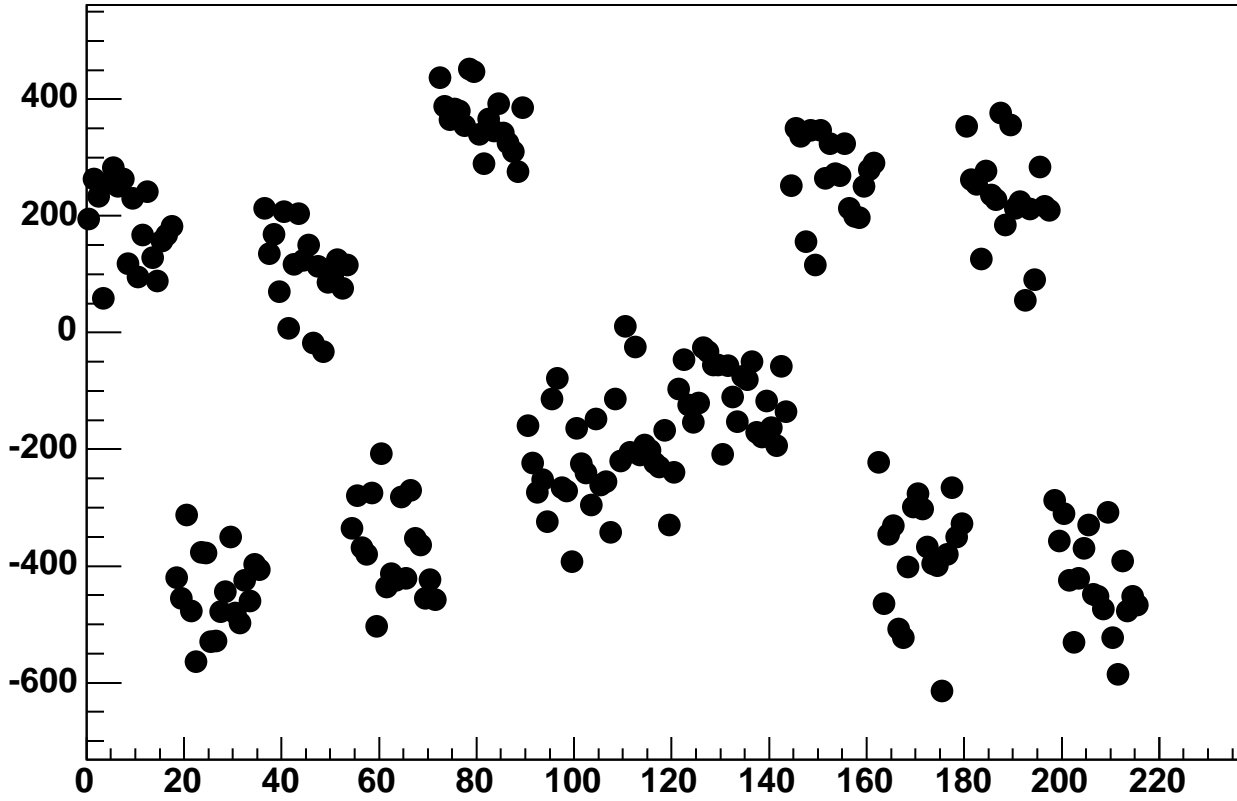
Enable 1, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



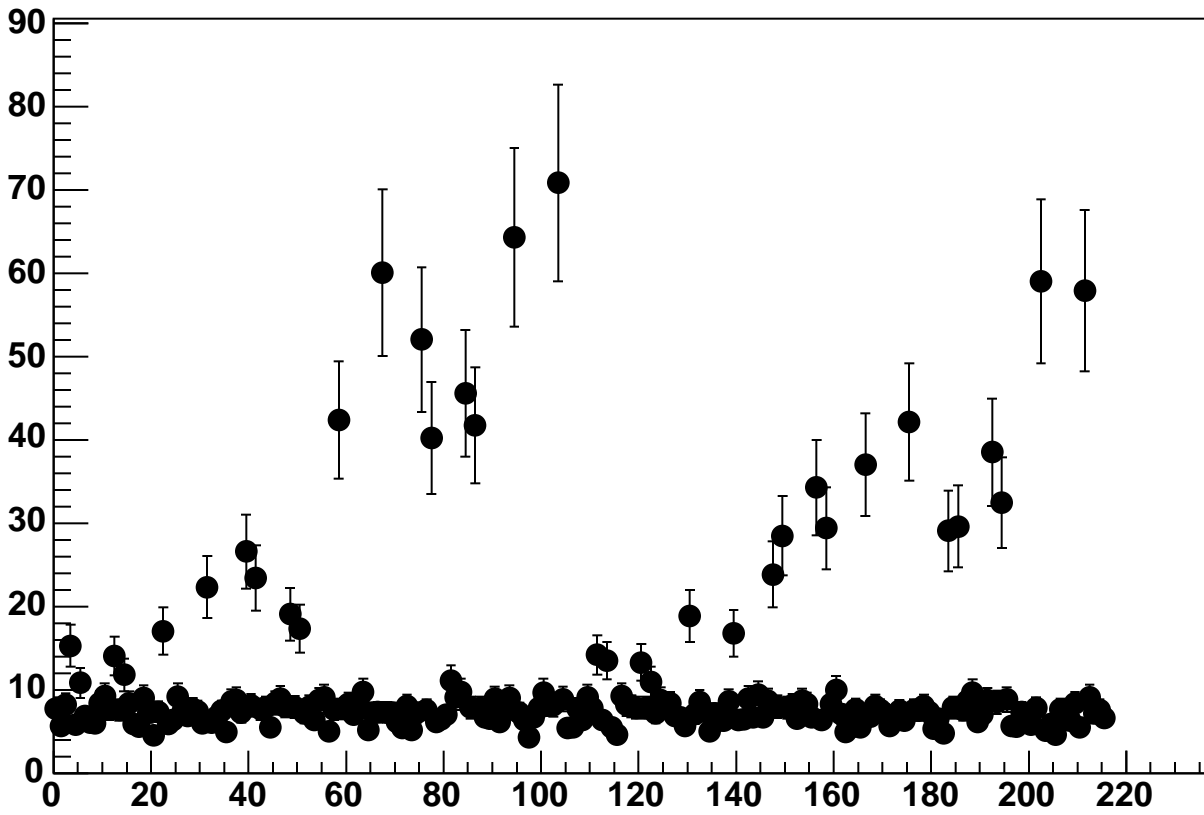
Enable 1, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



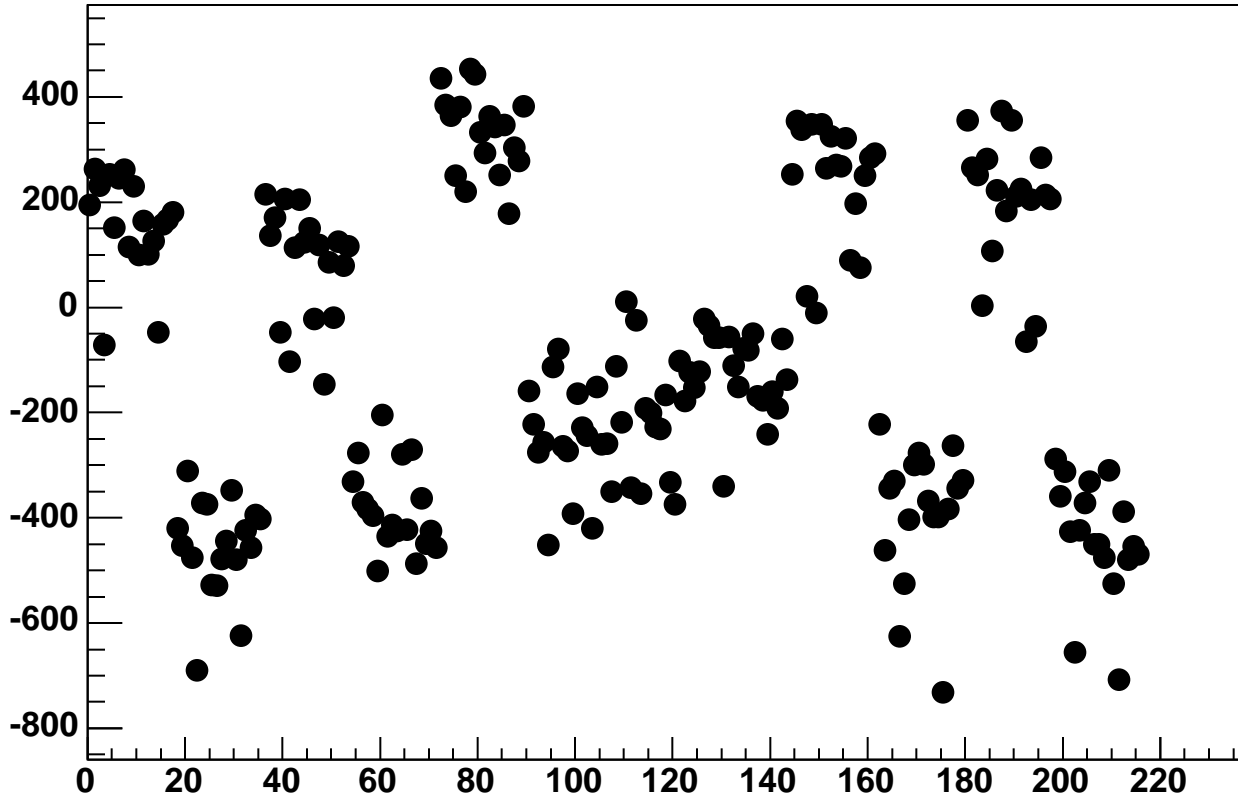
Enable 1, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



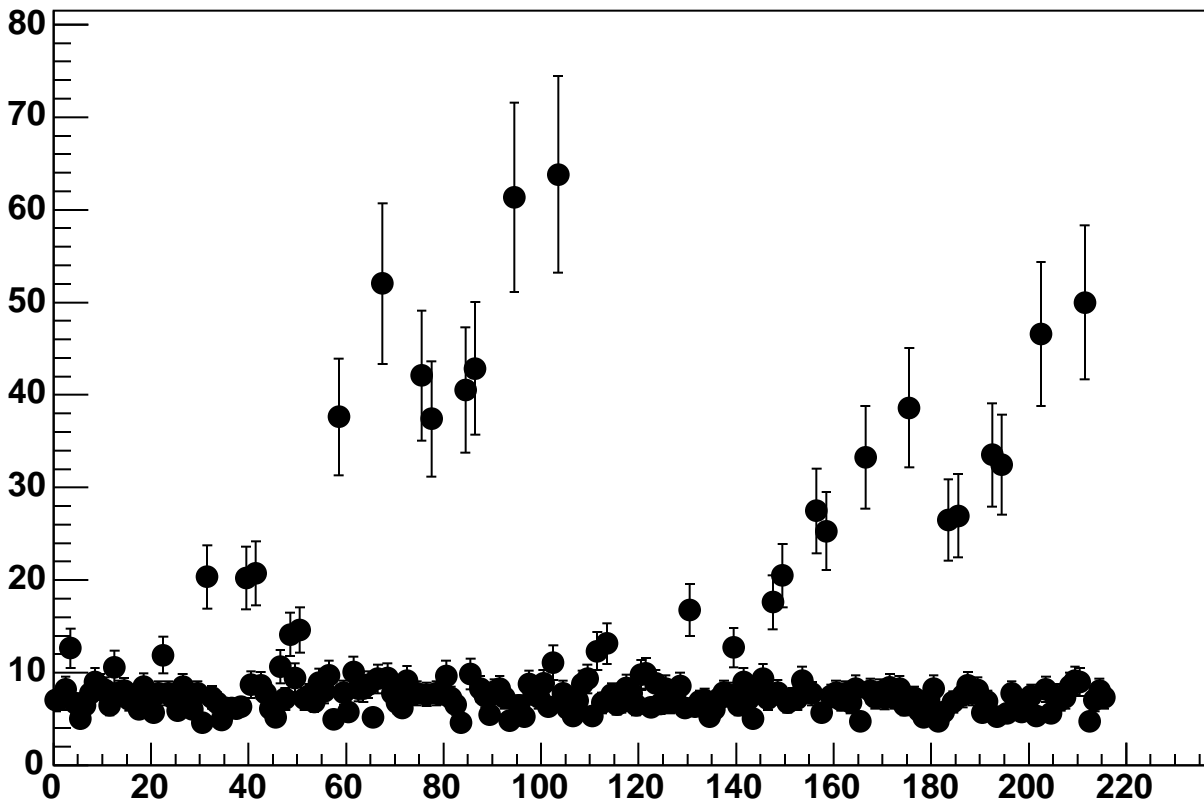
Enable 1, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



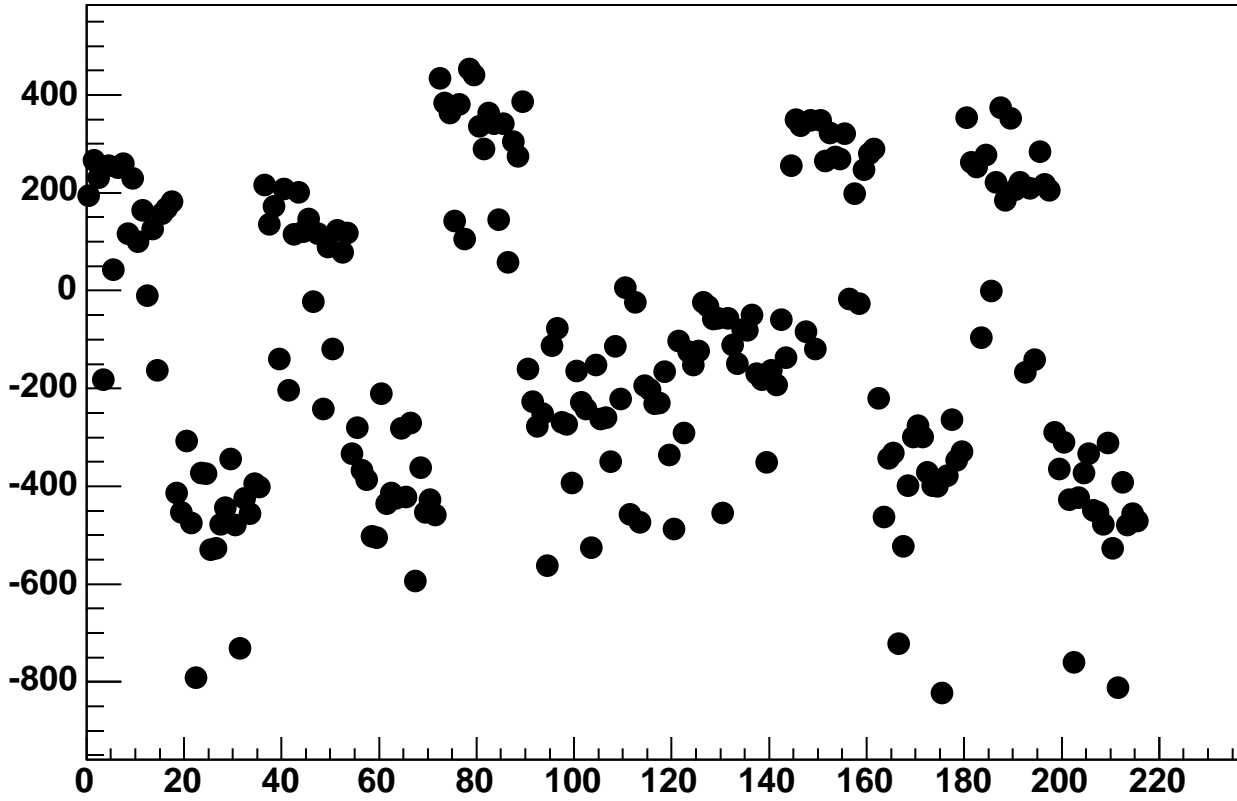
Enable 1, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



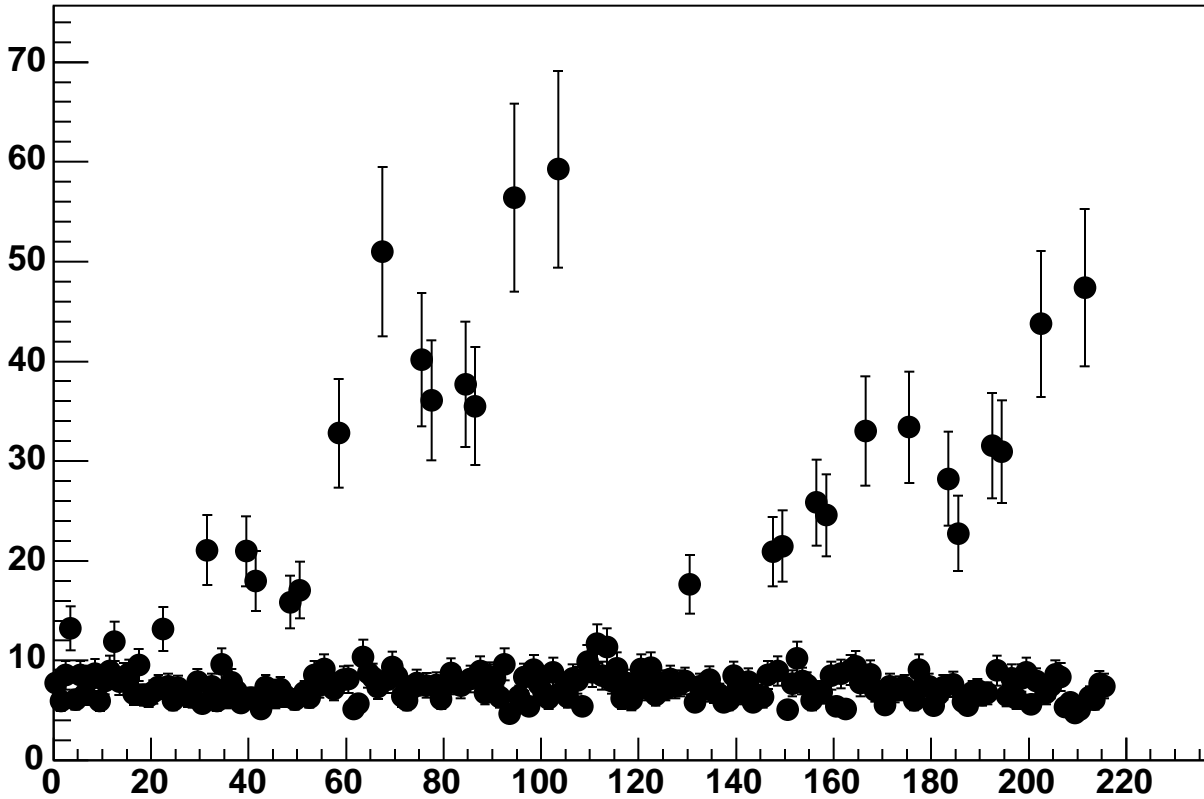
Enable 1, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



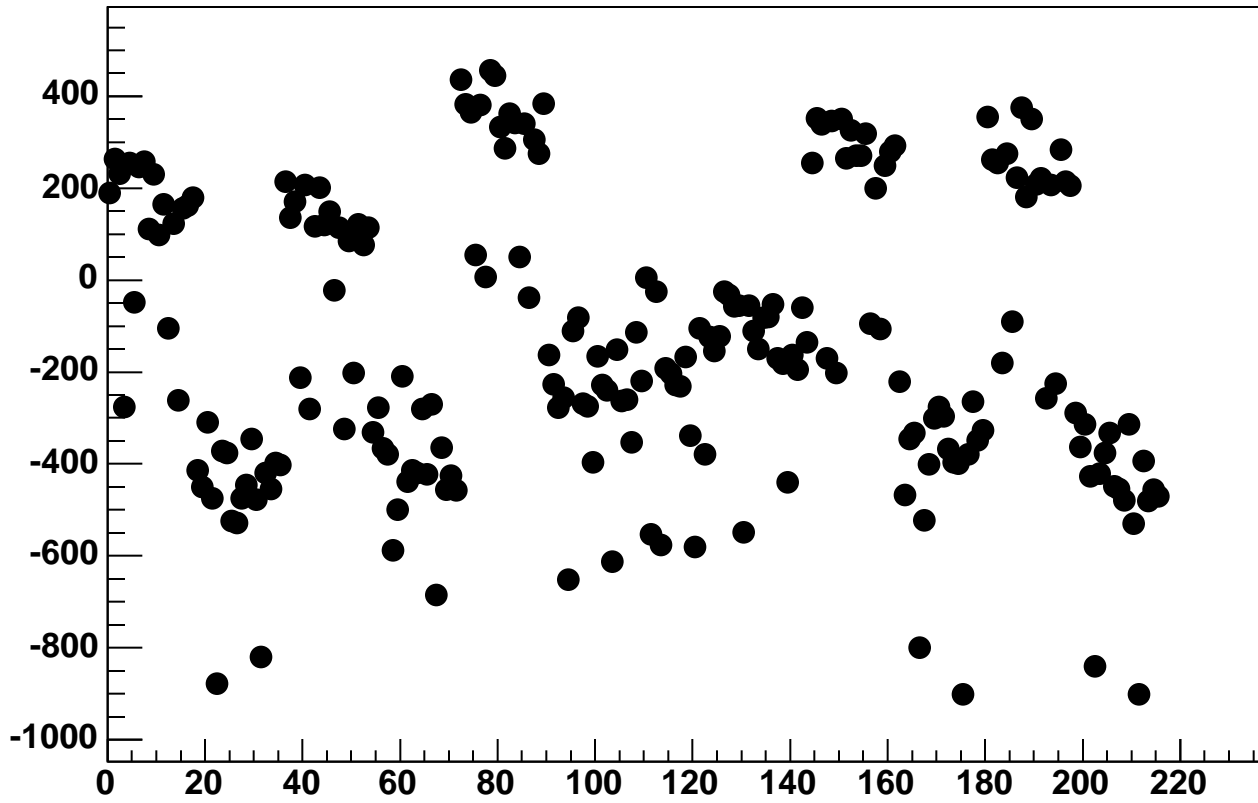
Enable 1, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



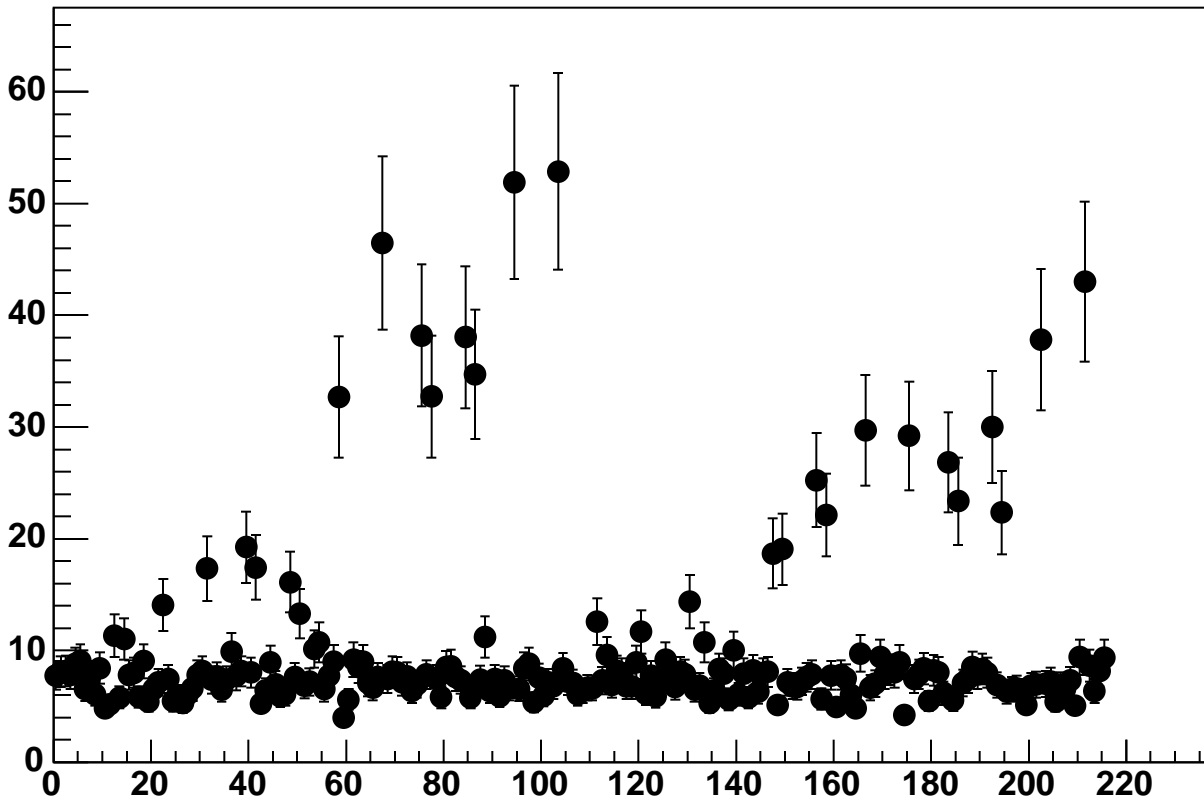
Enable 1, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



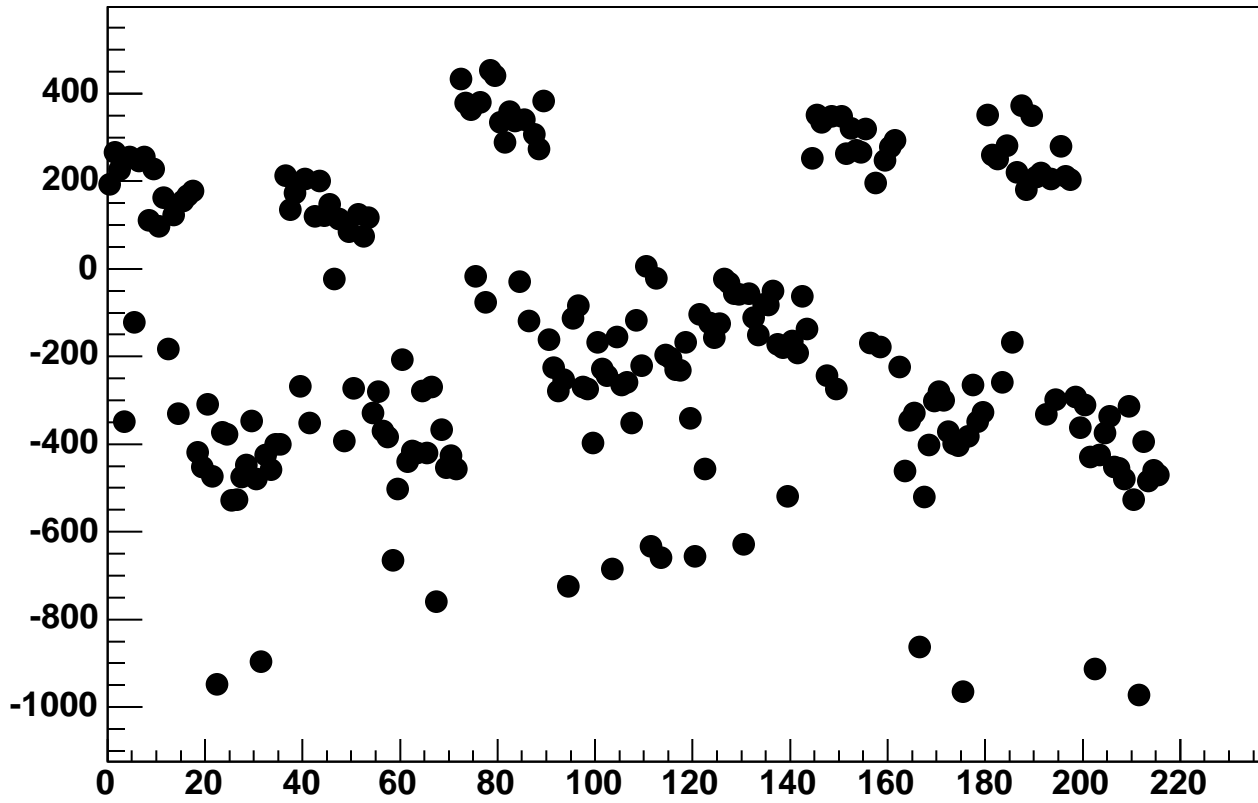
Enable 1, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



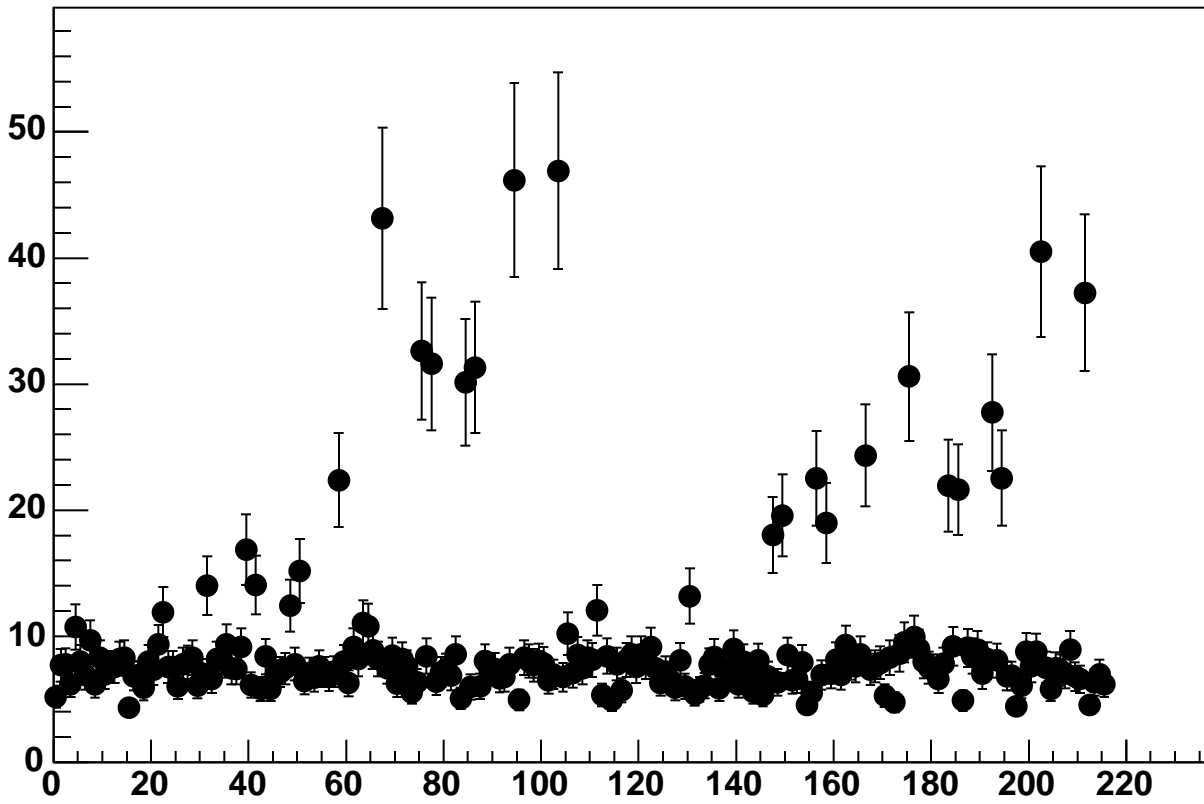
Enable 1, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



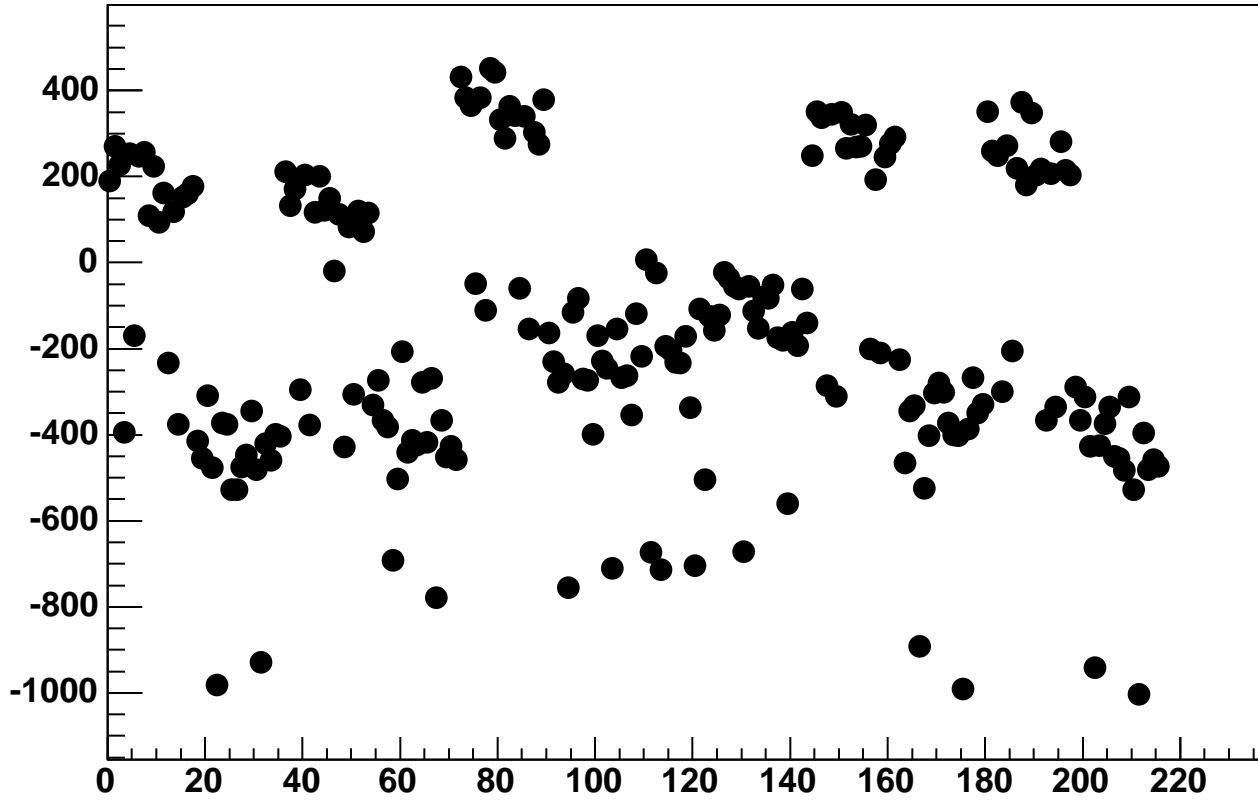
Enable 1, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



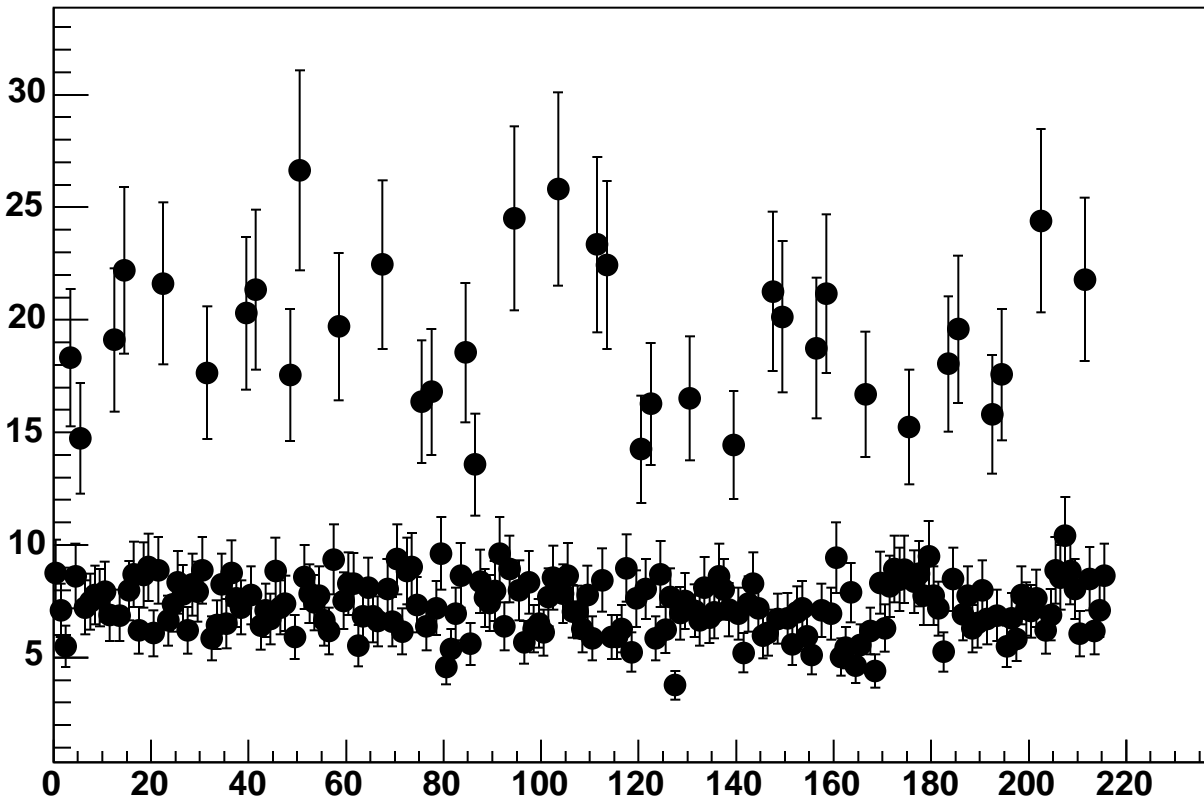
Enable 1, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



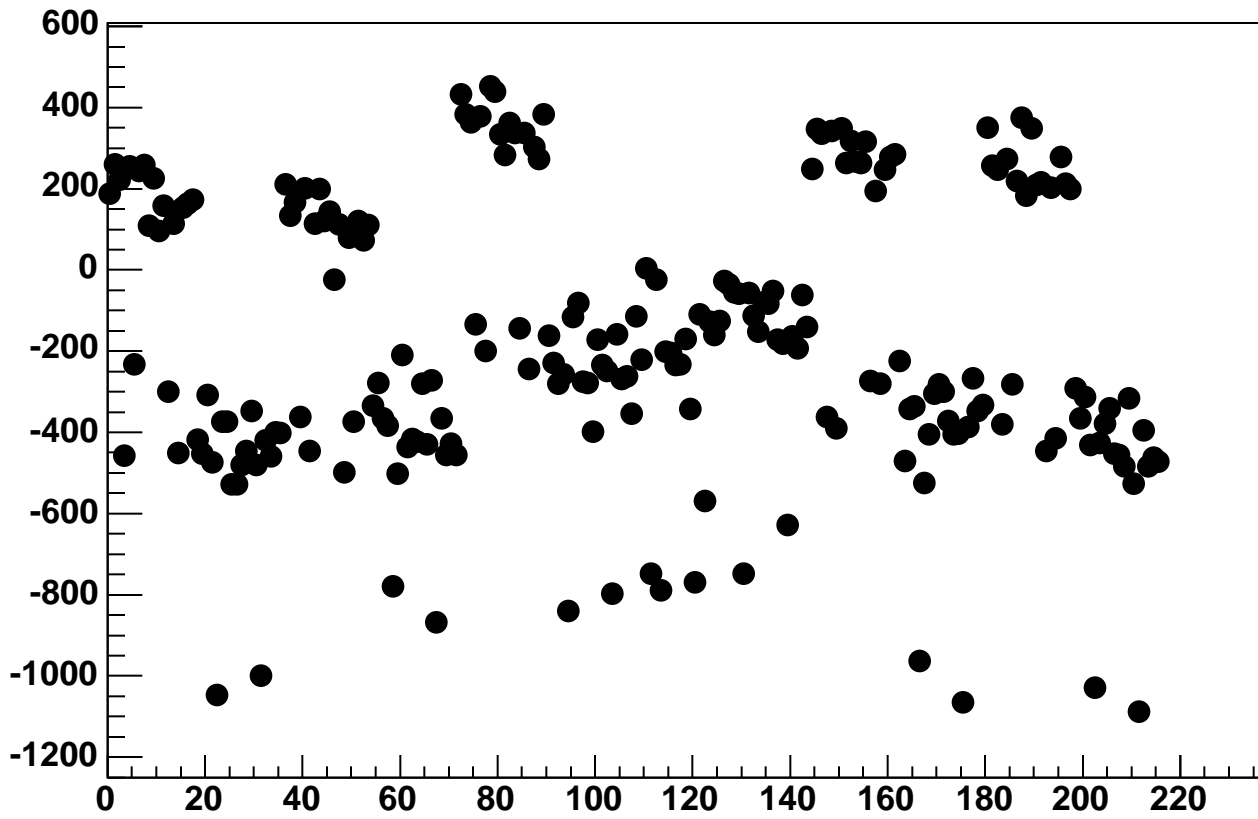
Enable 1, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



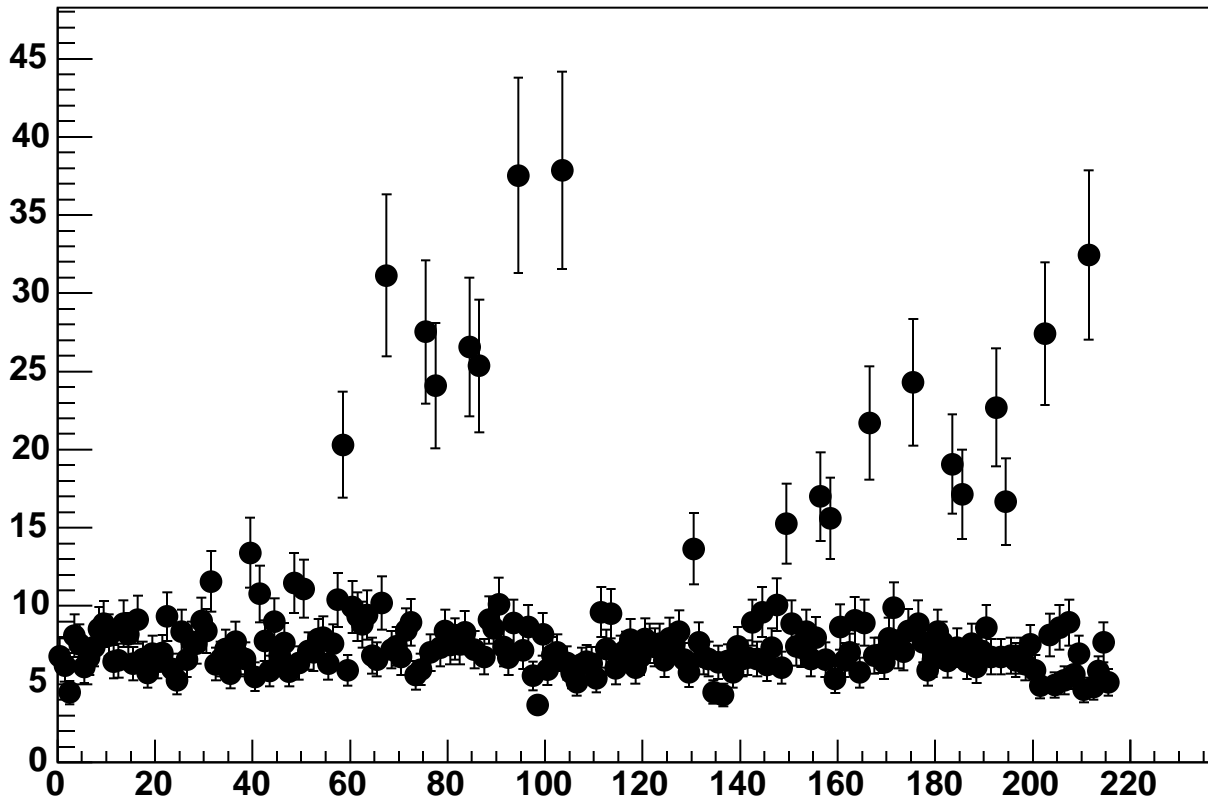
Enable 1, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



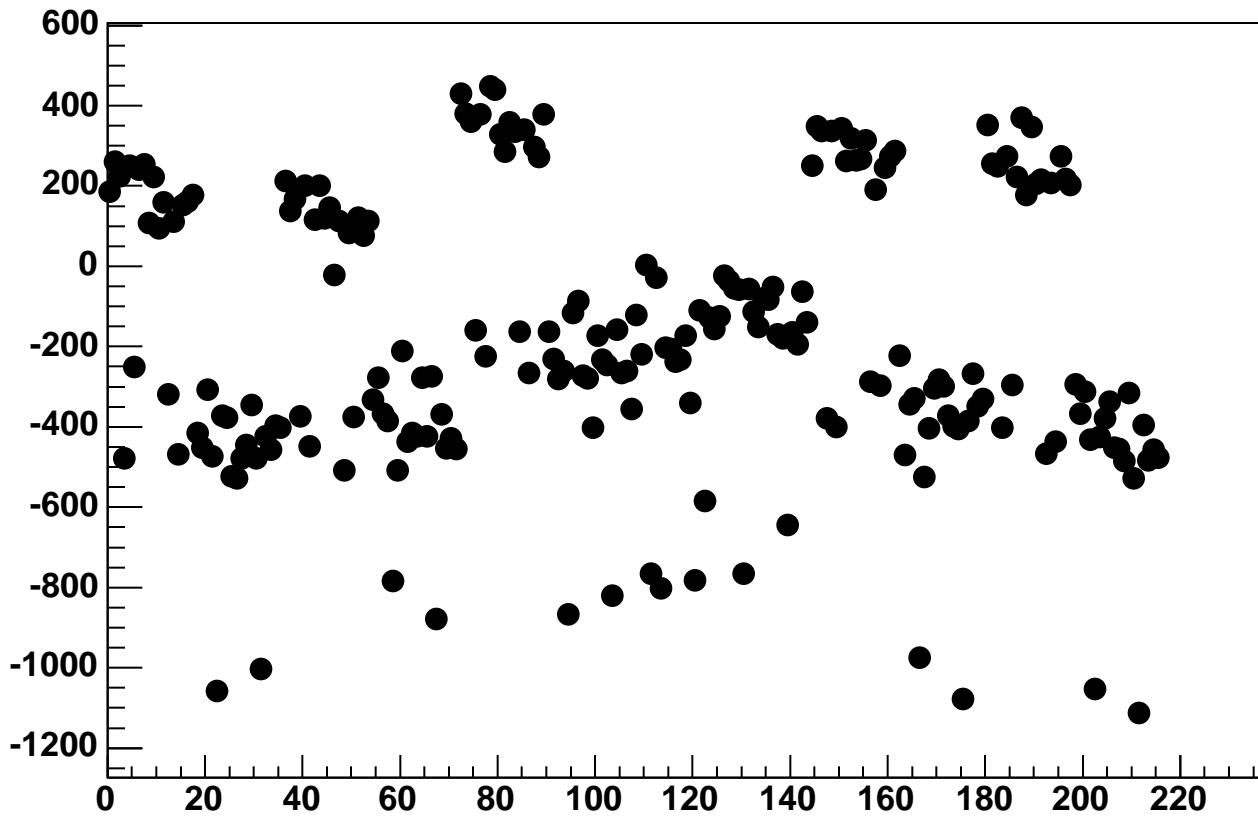
Enable 1, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



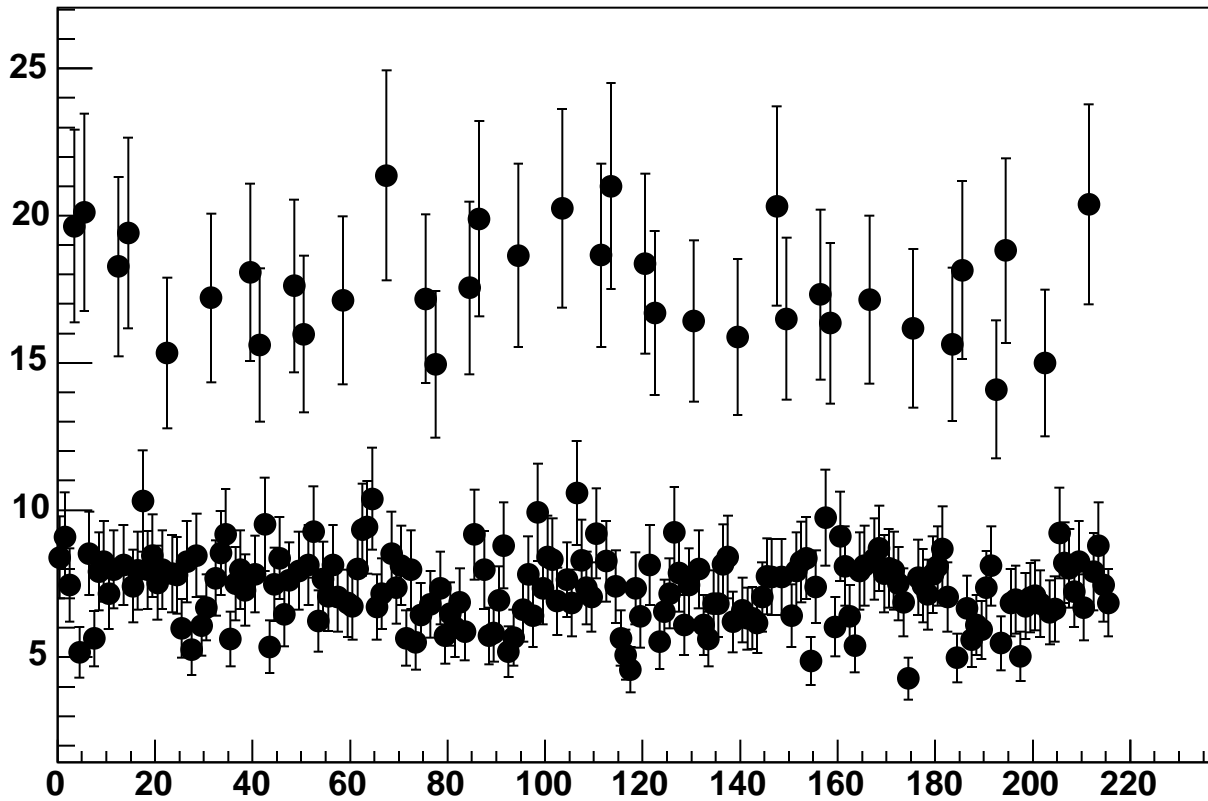
Enable 1, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



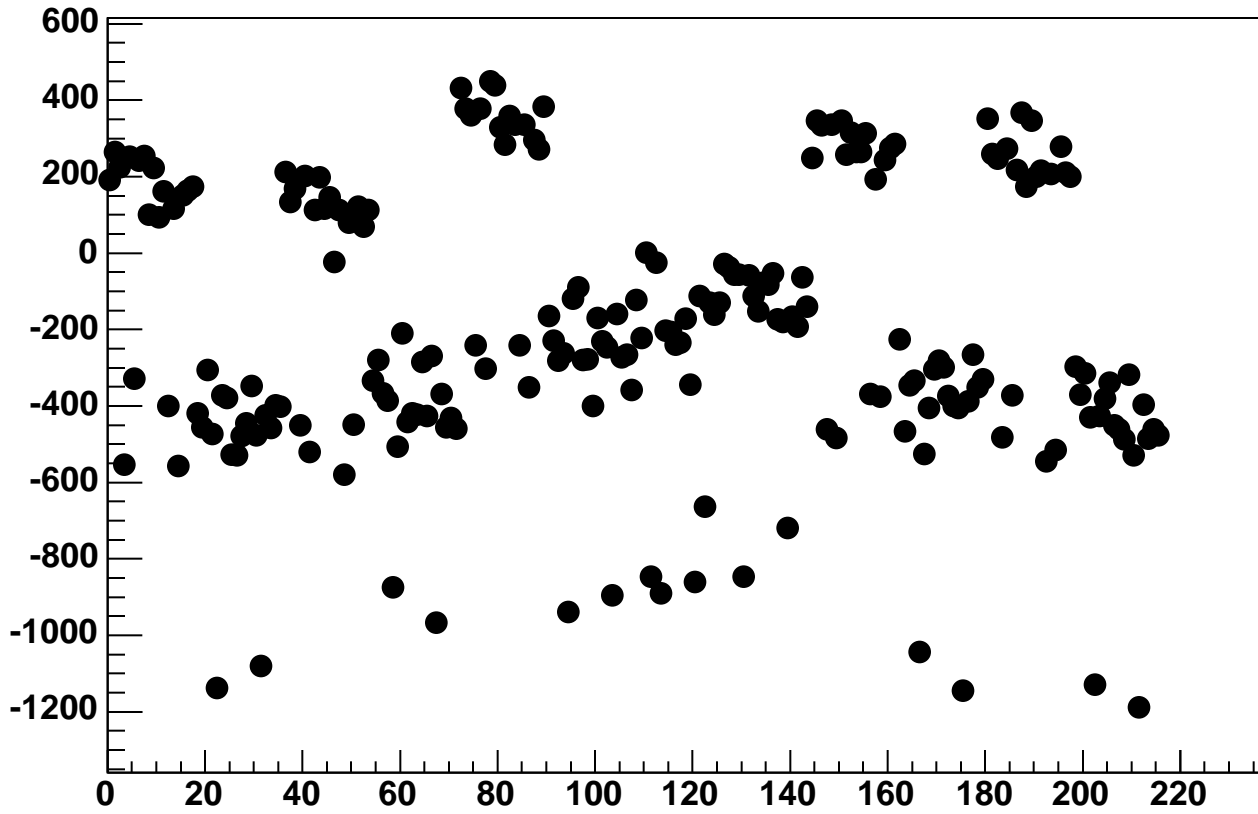
Enable 1, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



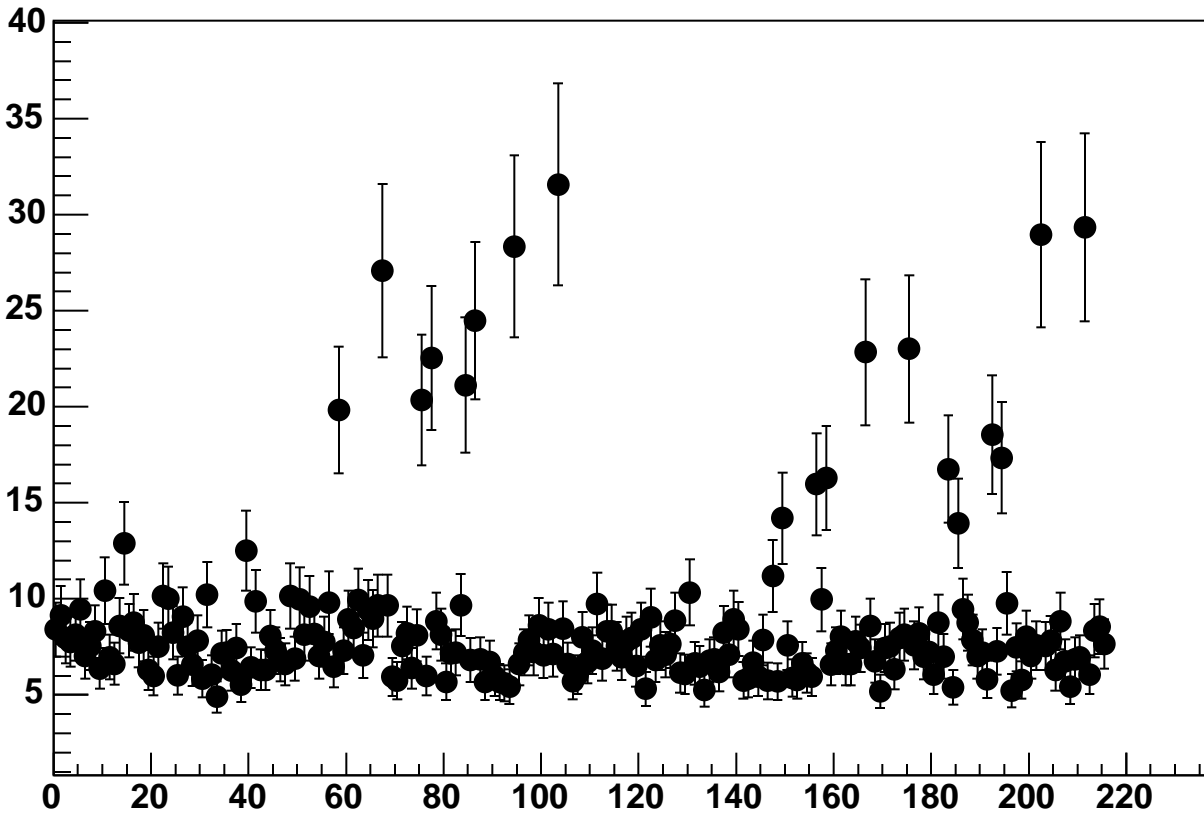
Enable 1, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



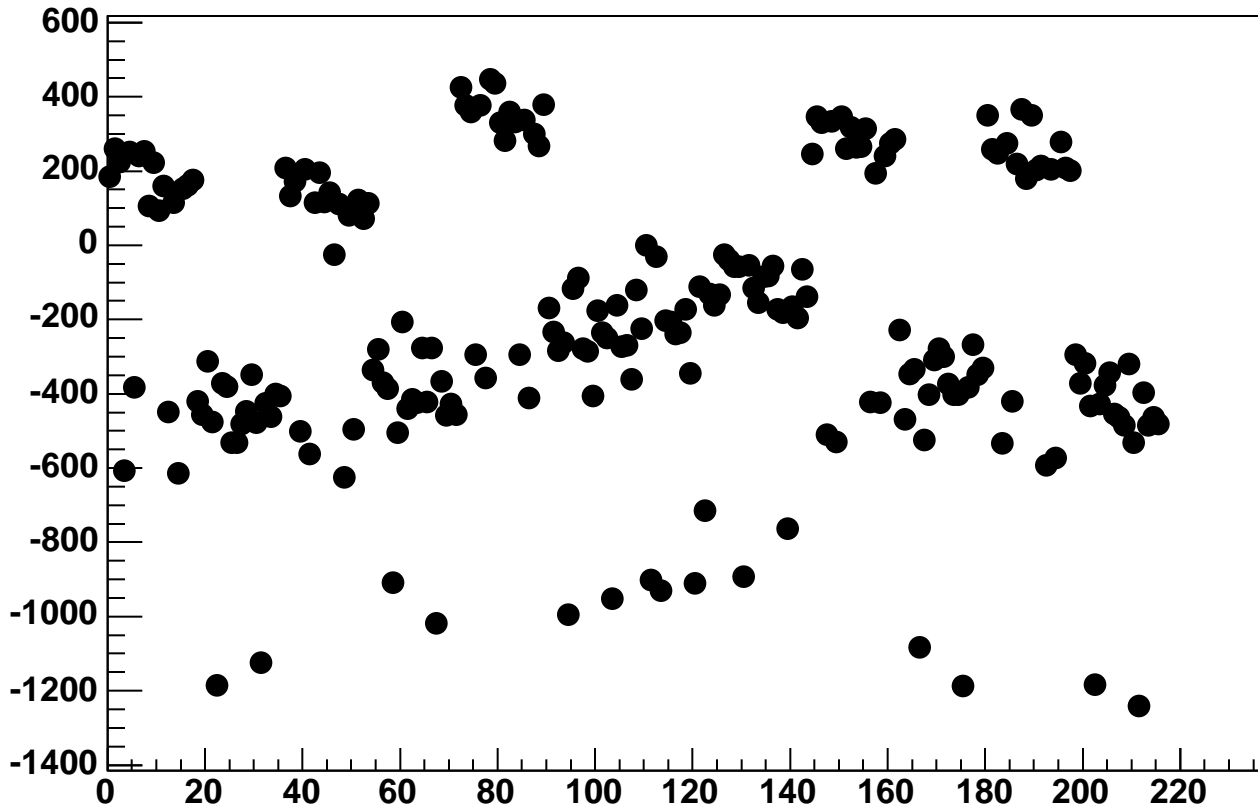
Enable 1, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



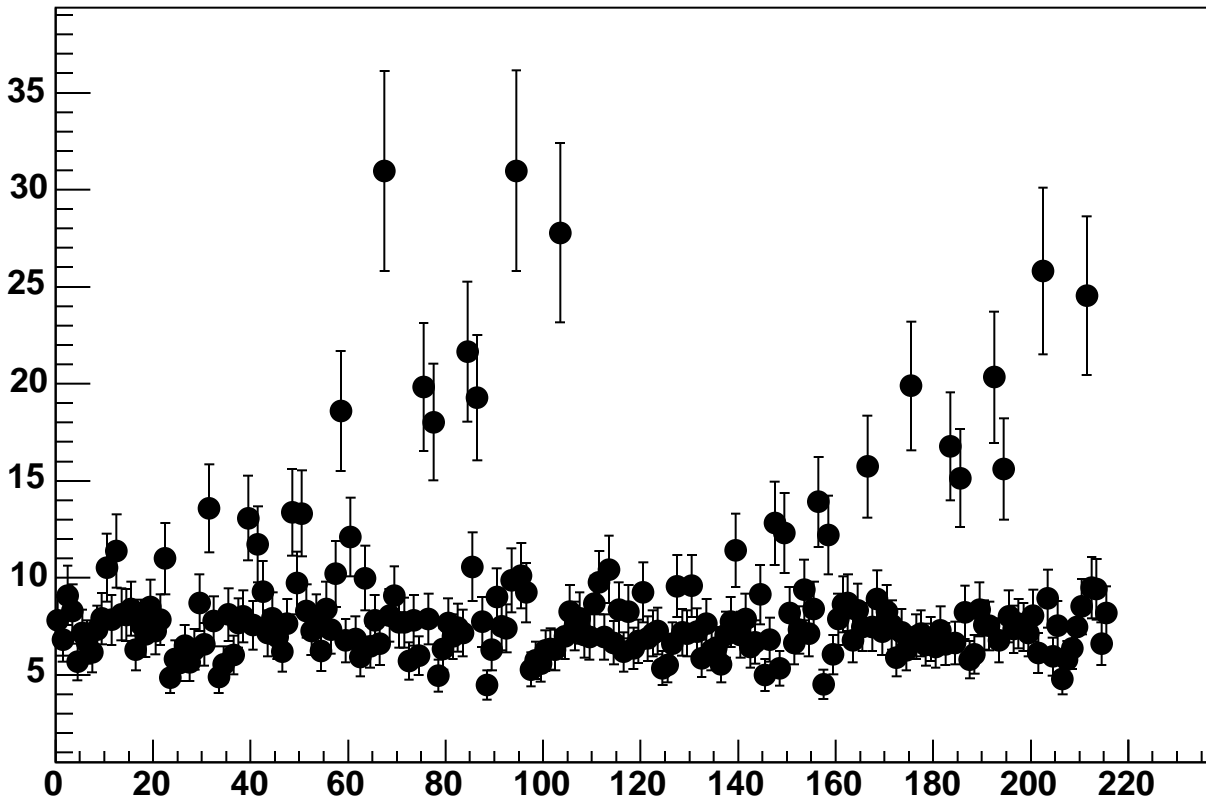
Enable 1, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



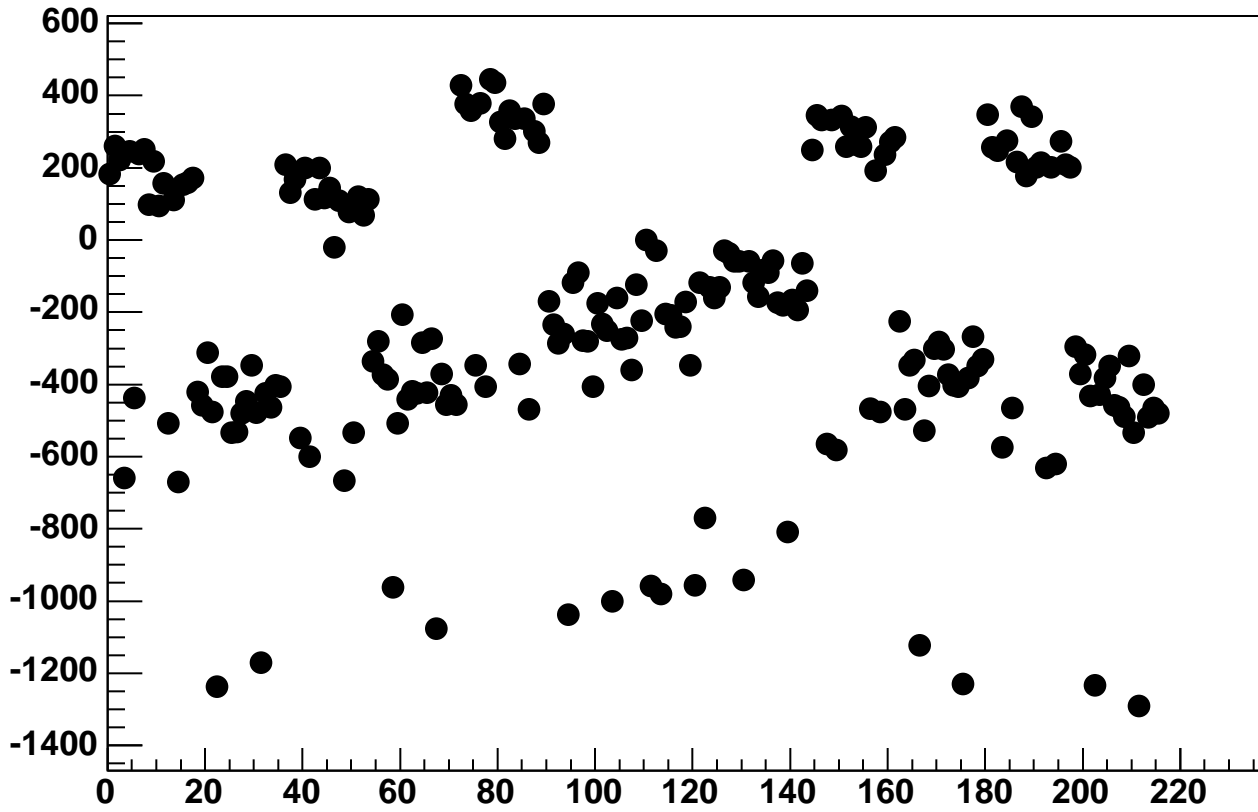
Enable 1, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



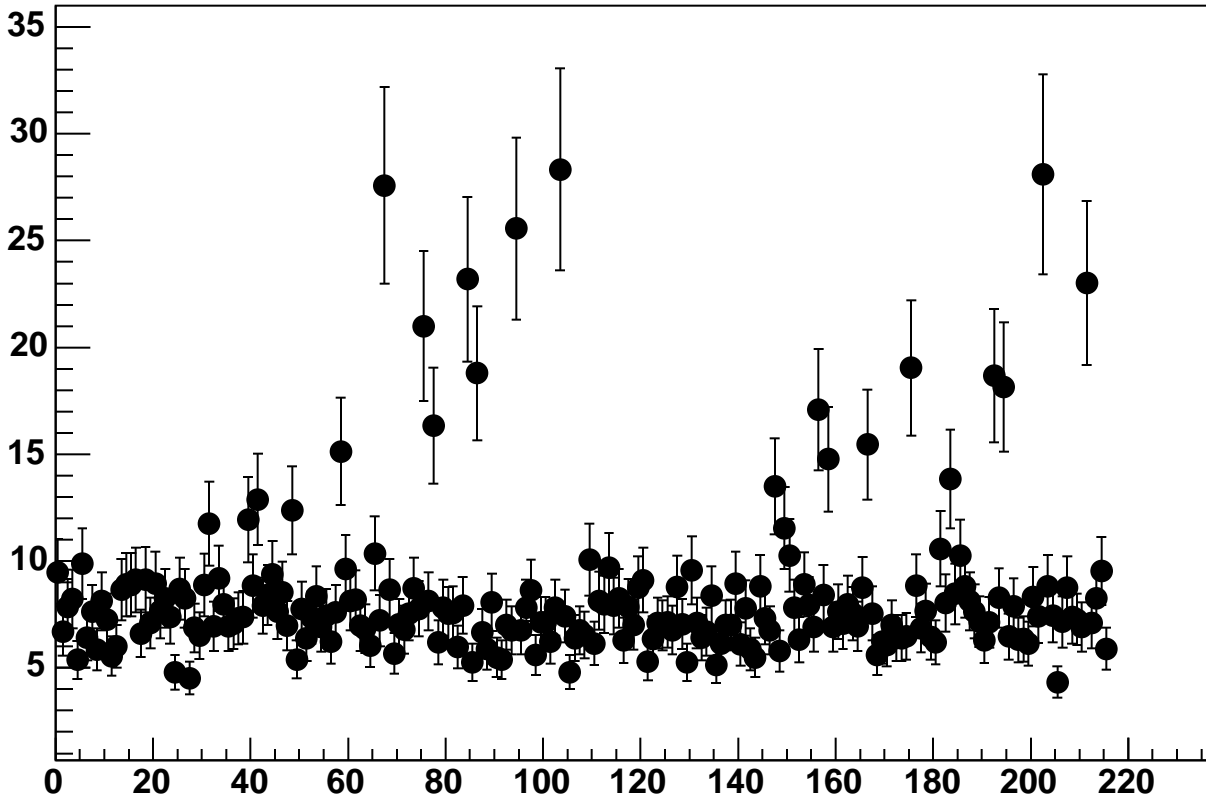
Enable 1, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



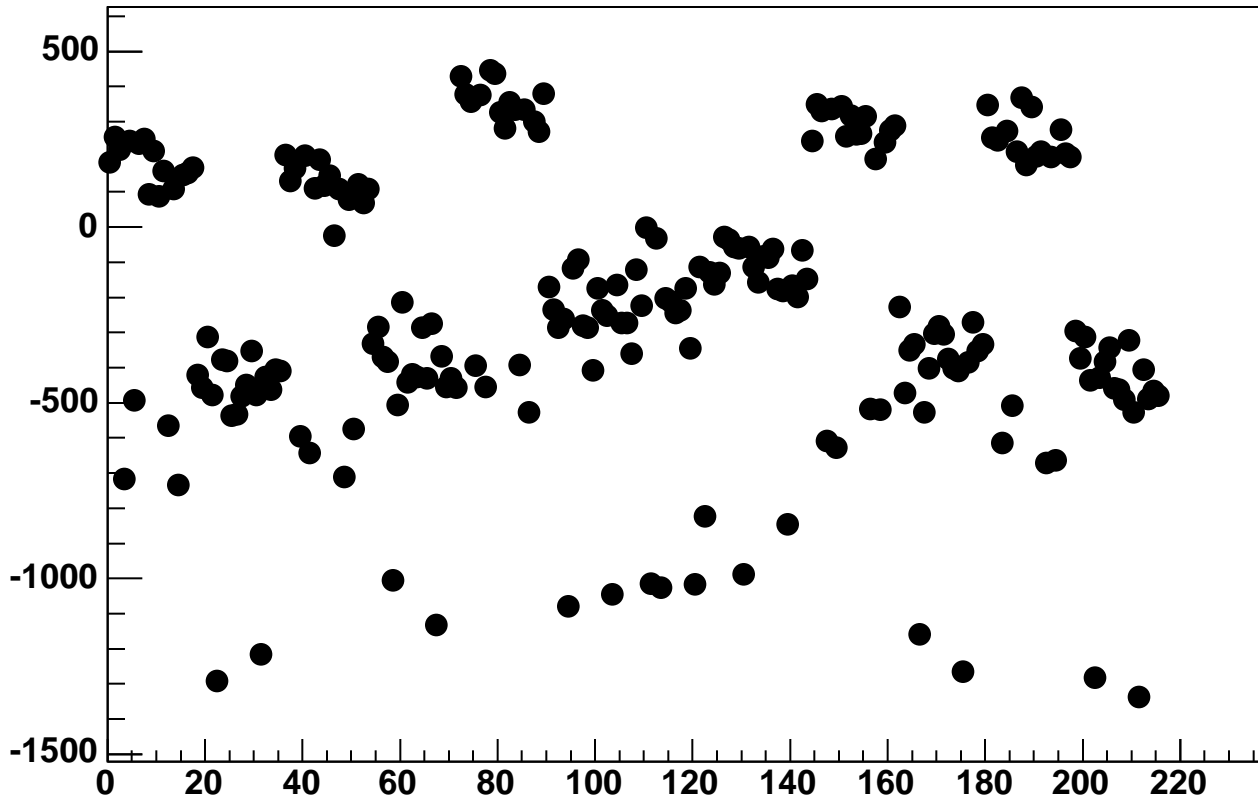
Enable 1, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



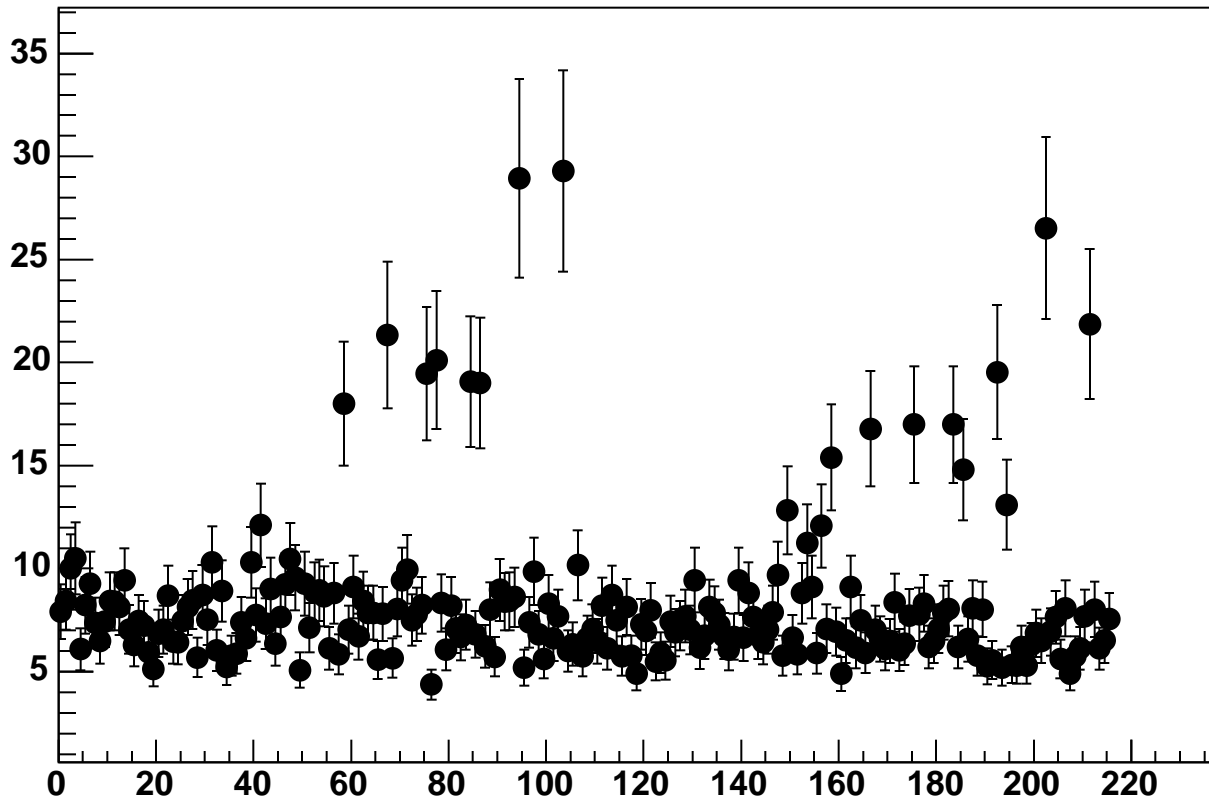
Enable 1, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



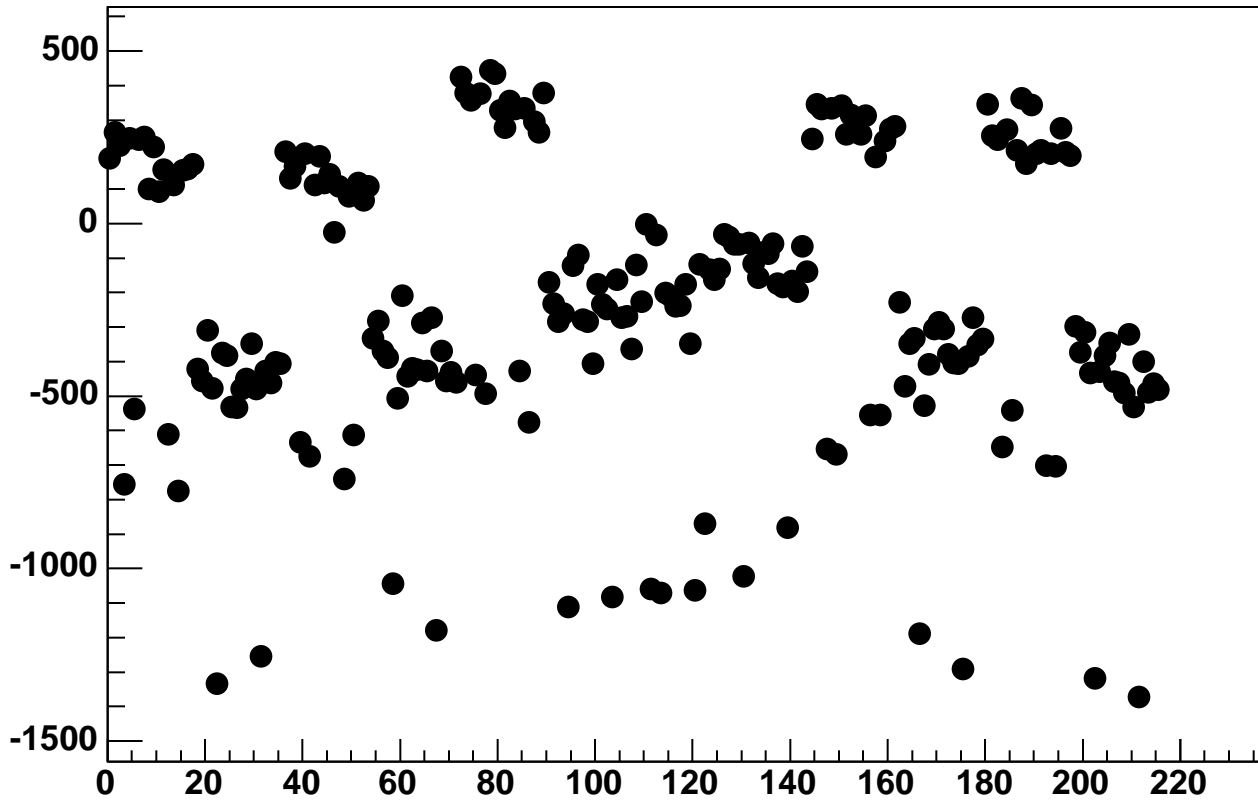
Enable 1, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



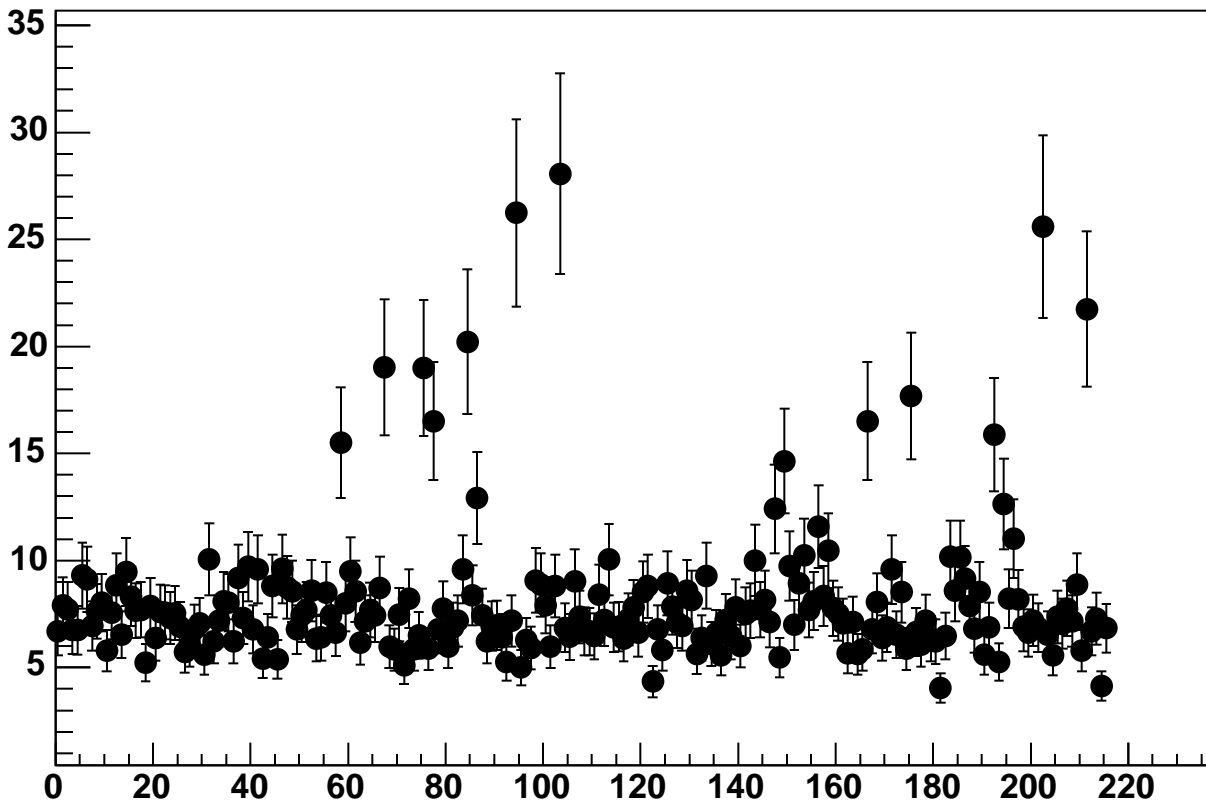
Enable 1, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



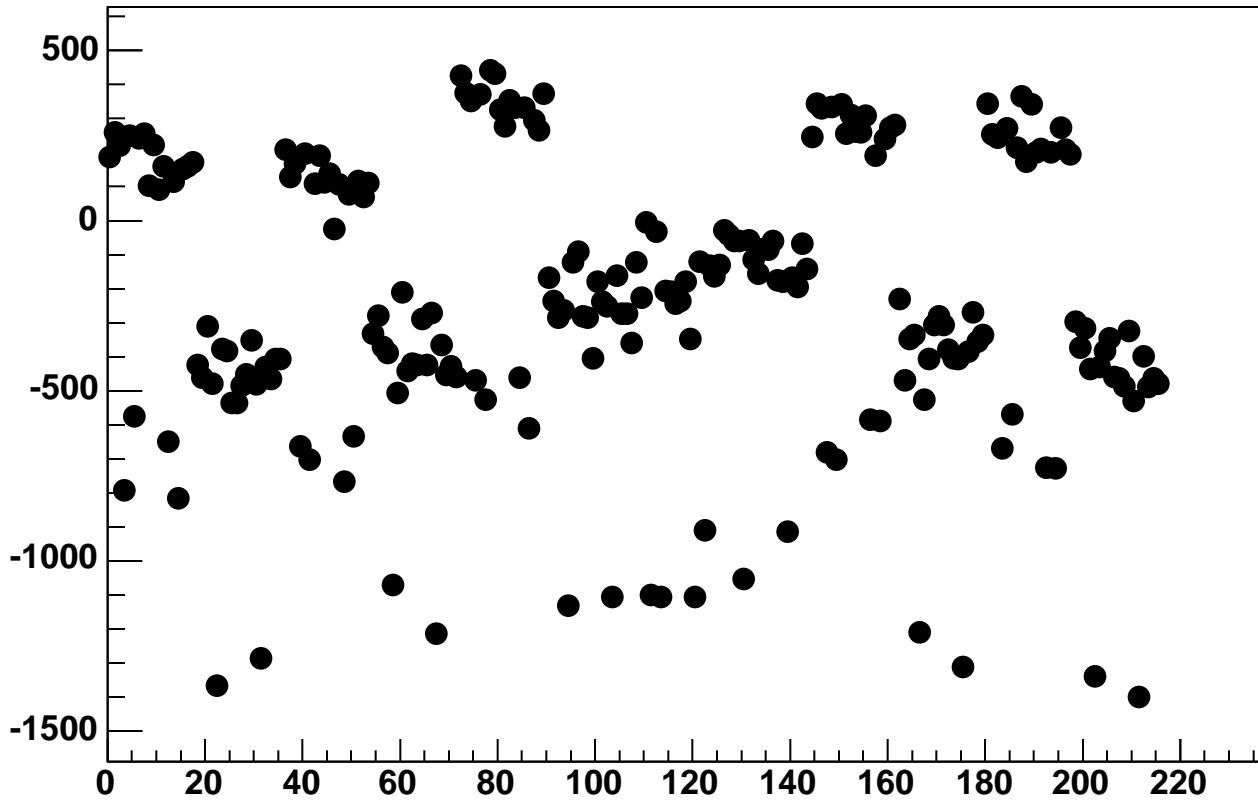
Enable 1, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



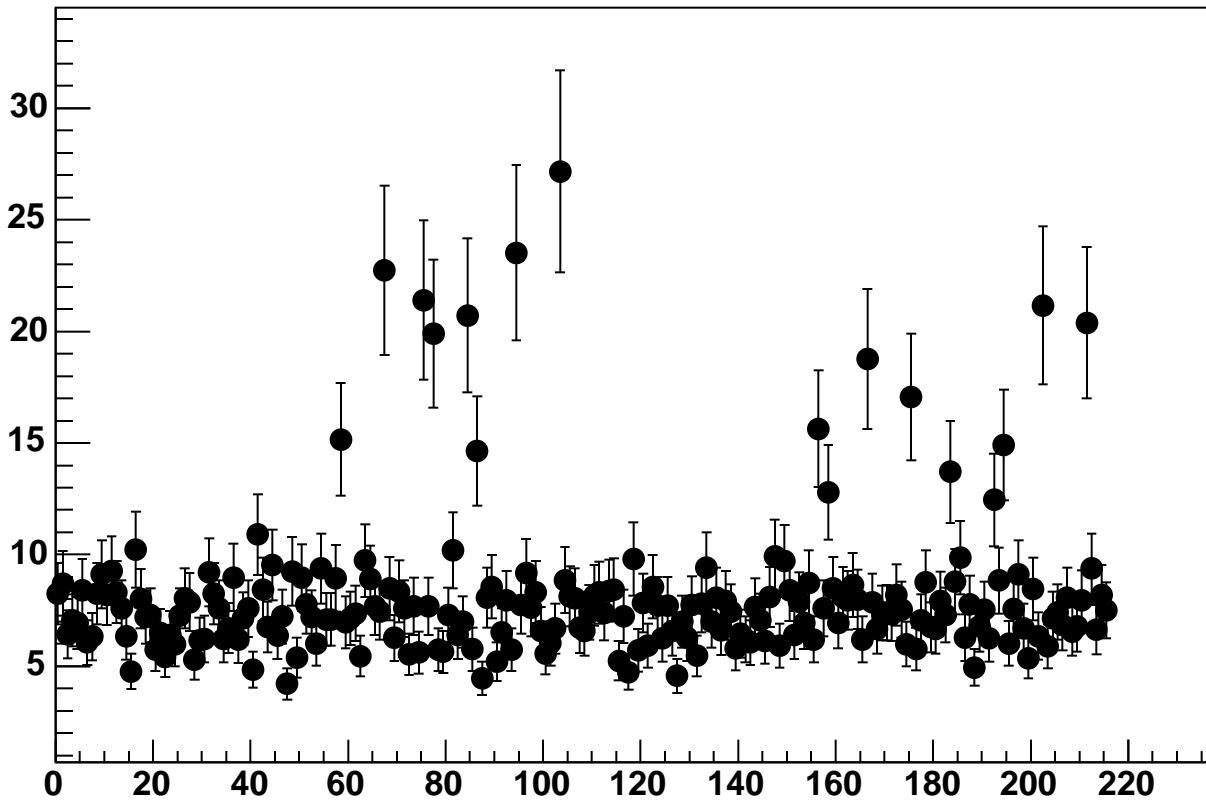
Enable 1, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



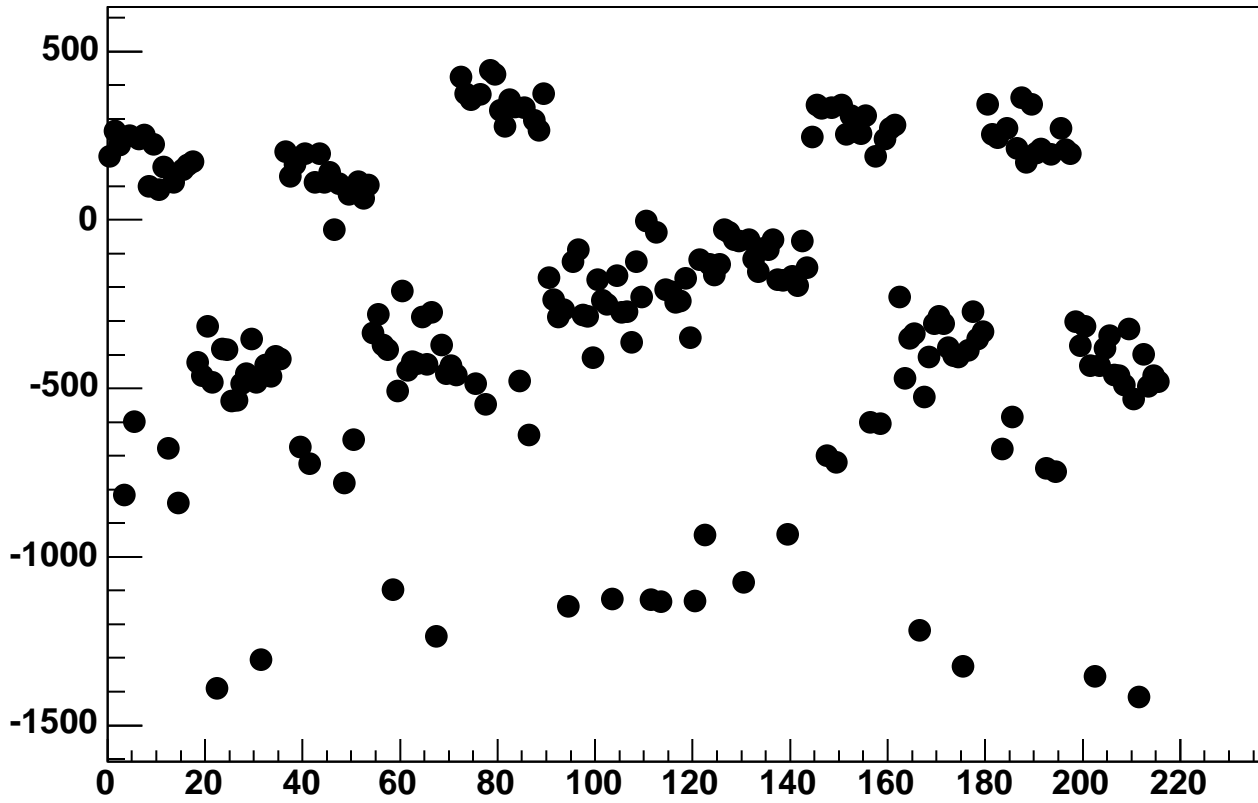
Enable 1, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



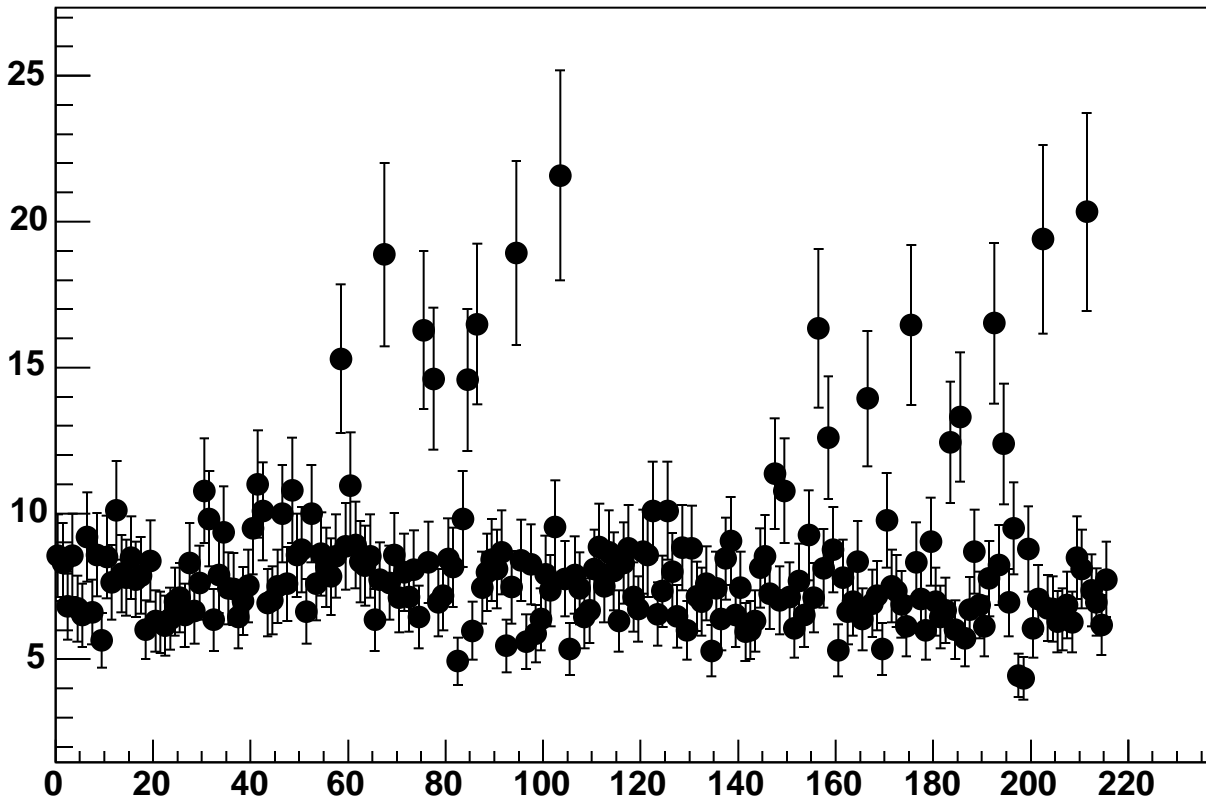
Enable 1, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



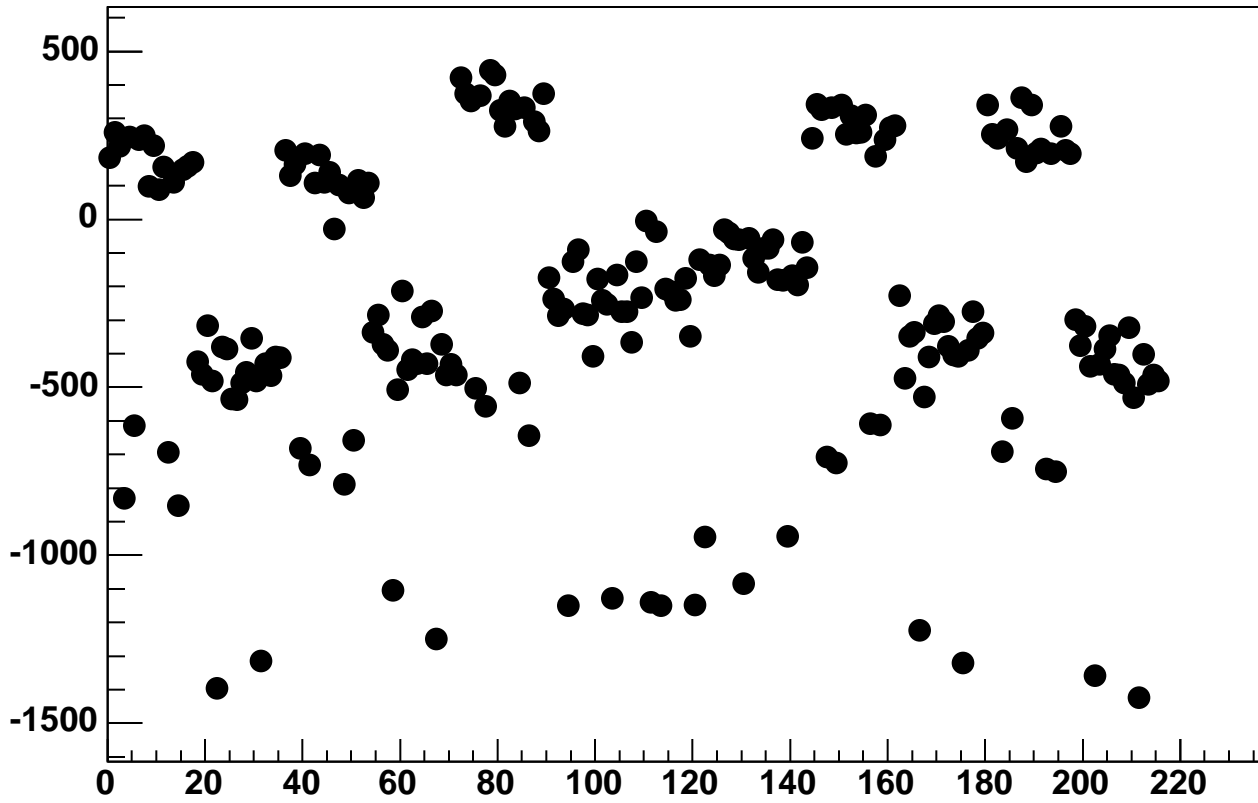
Enable 1, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



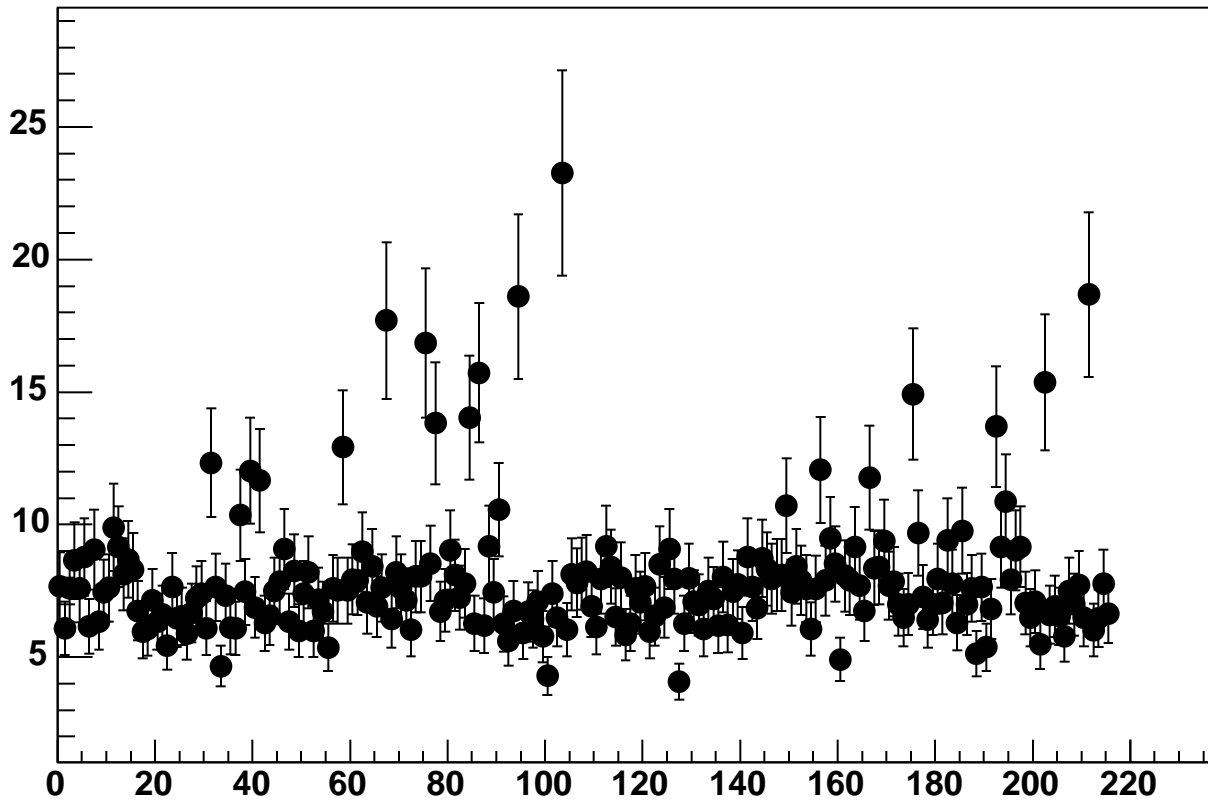
Enable 1, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



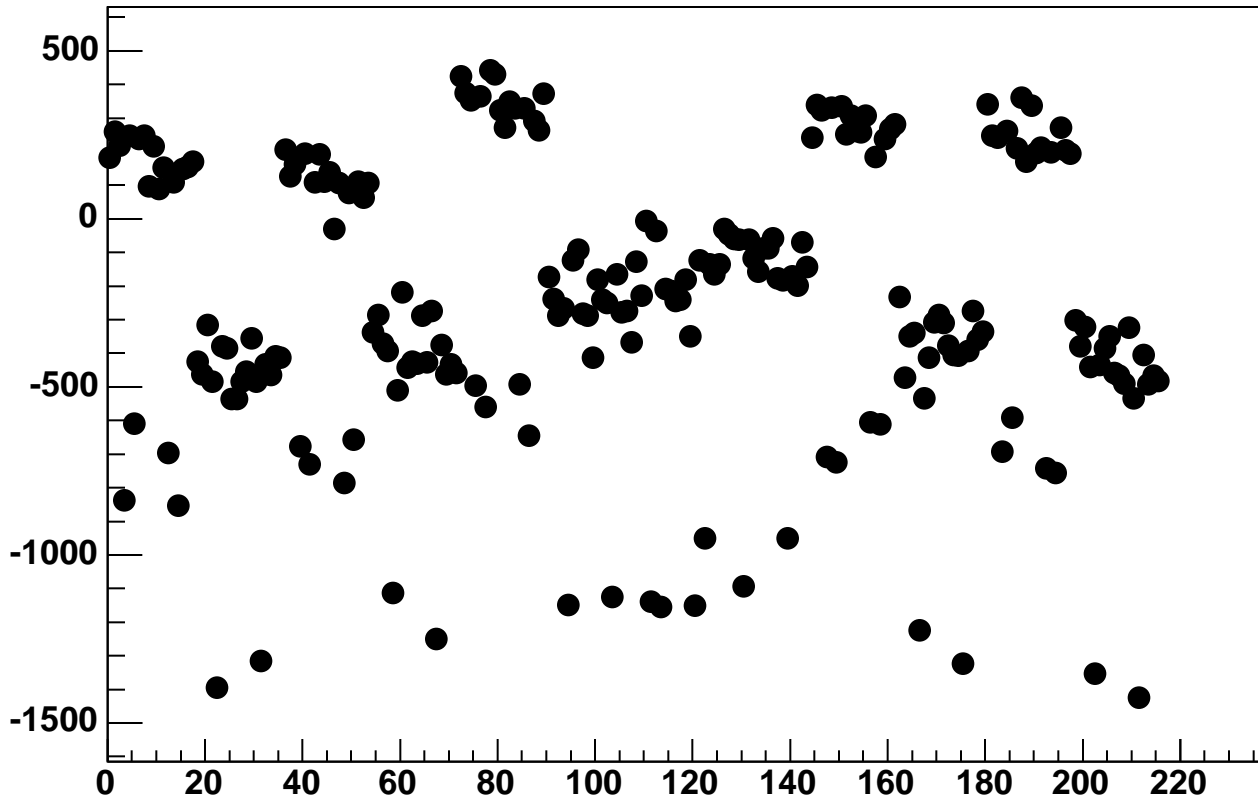
Enable 1, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



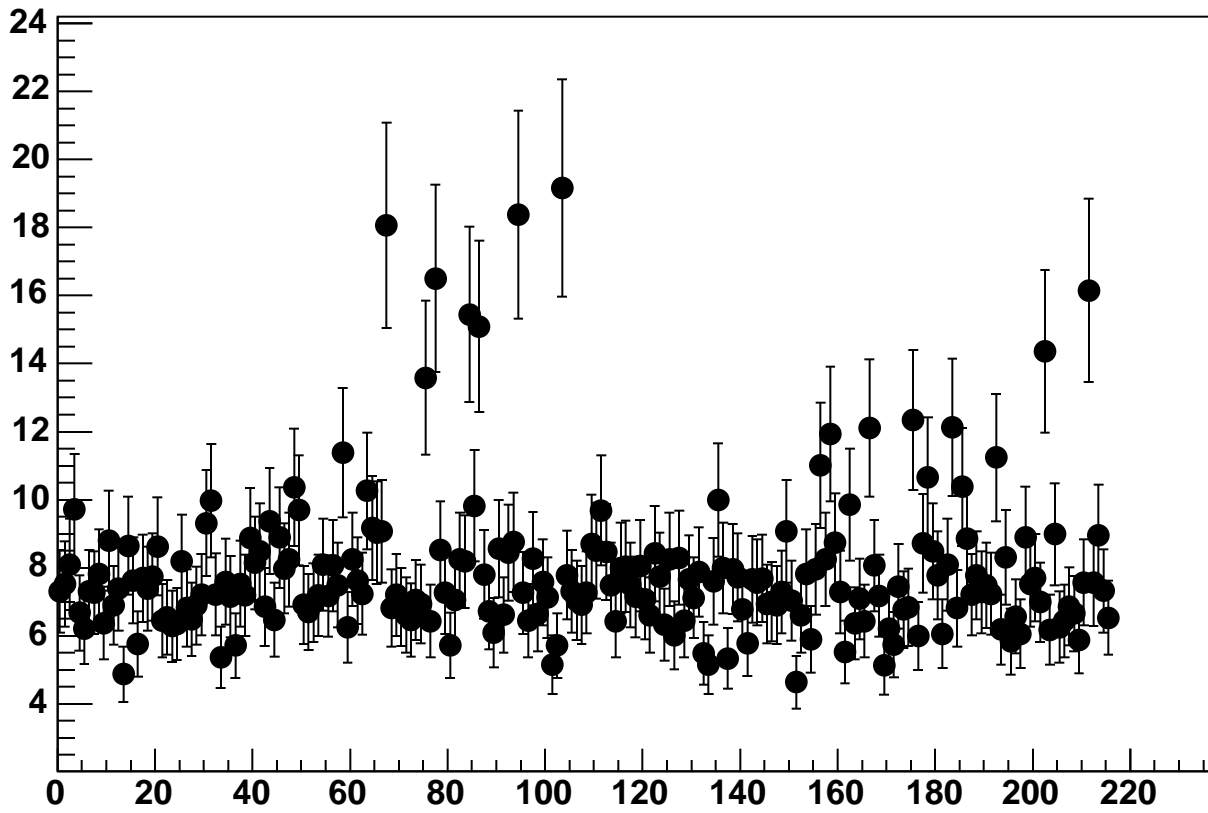
Enable 1, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



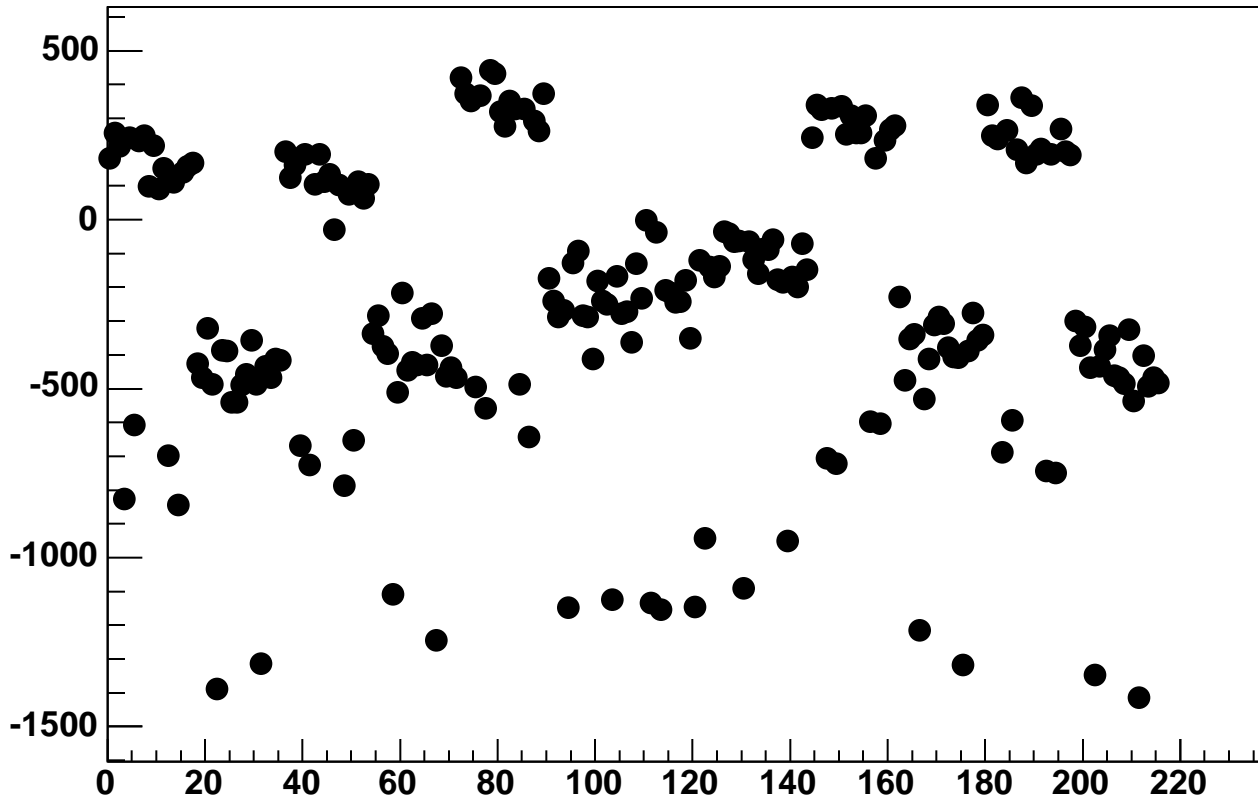
Enable 1, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



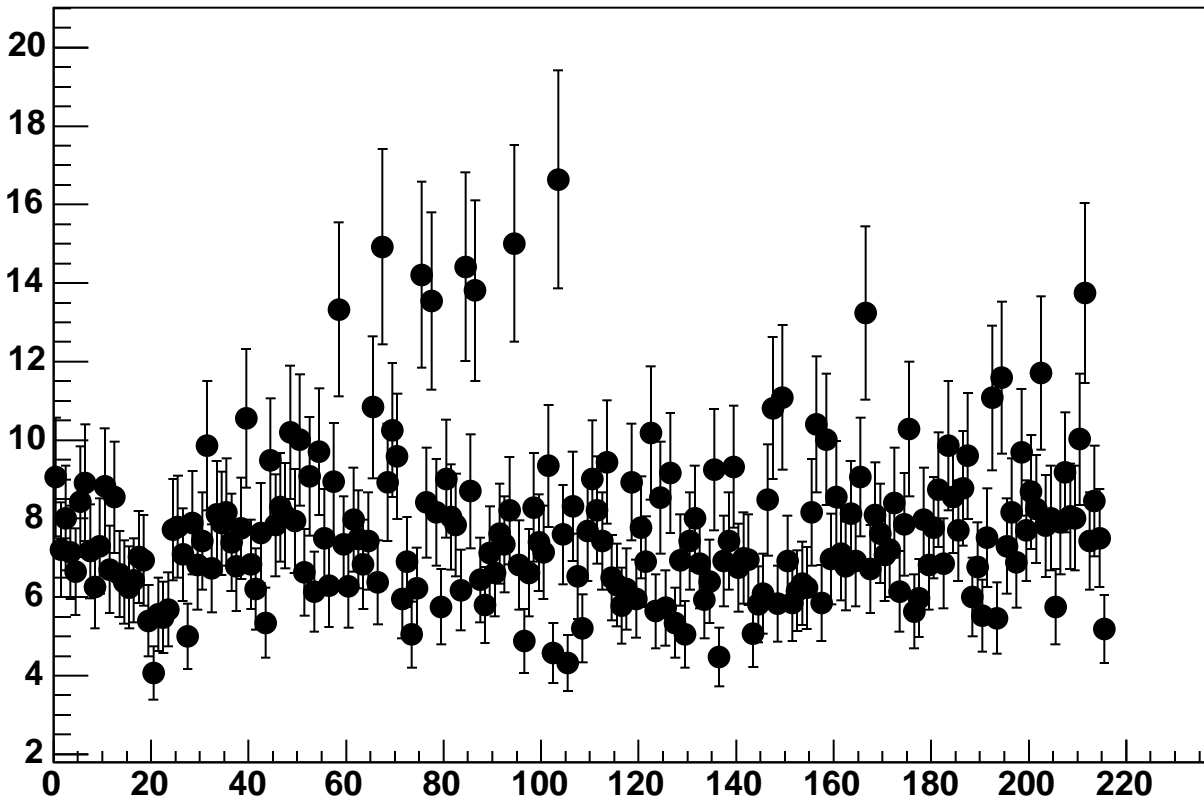
Enable 1, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



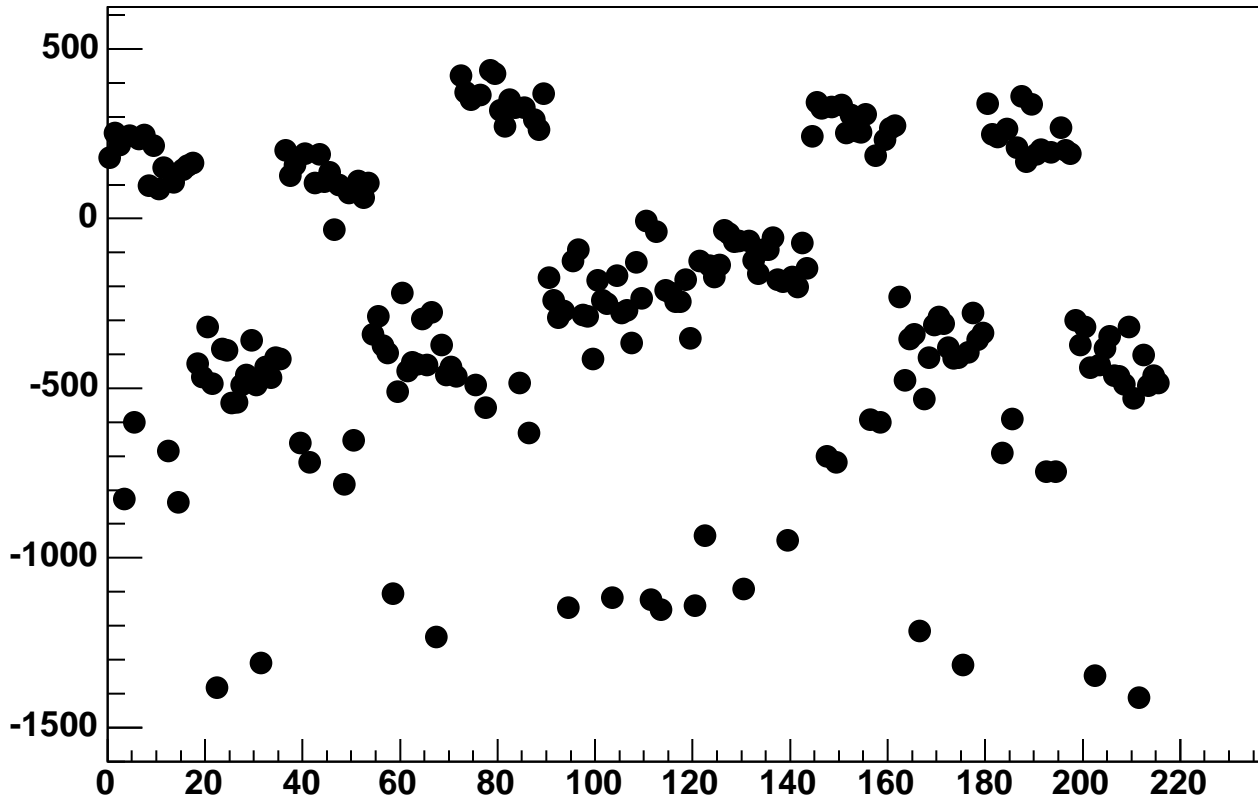
Enable 1, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



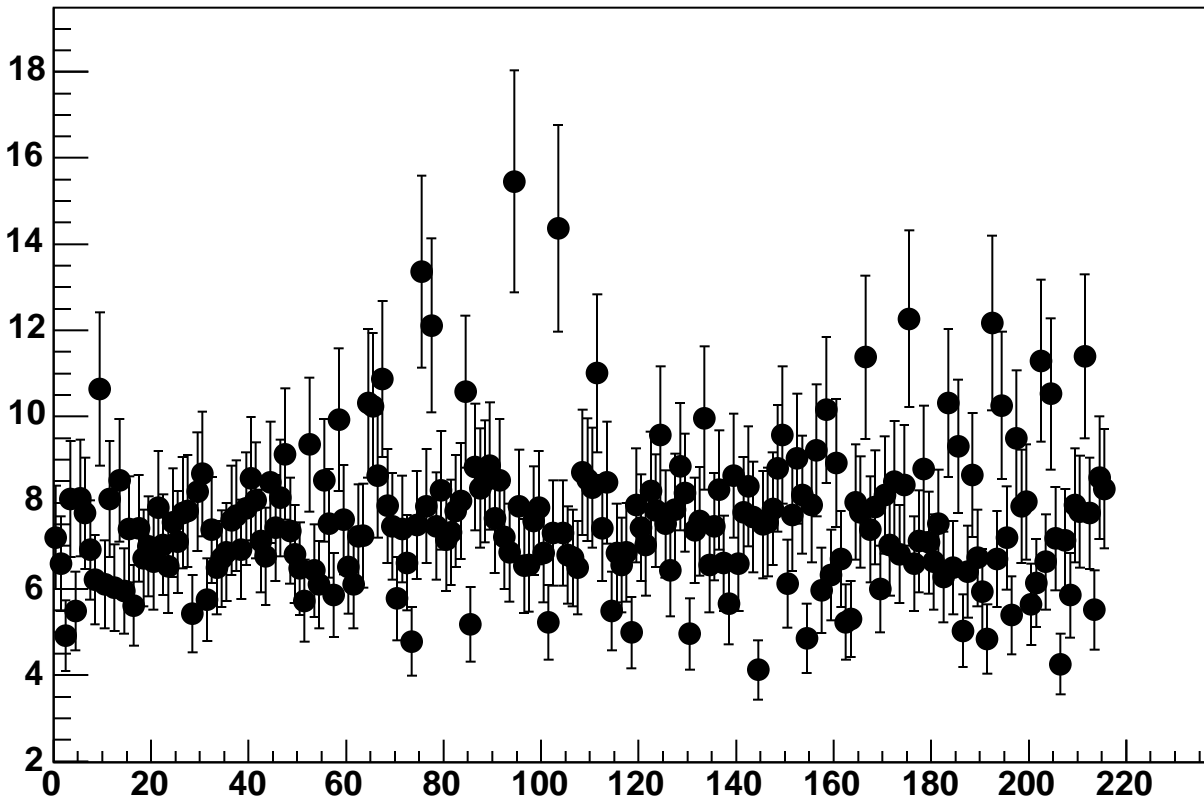
Enable 1, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



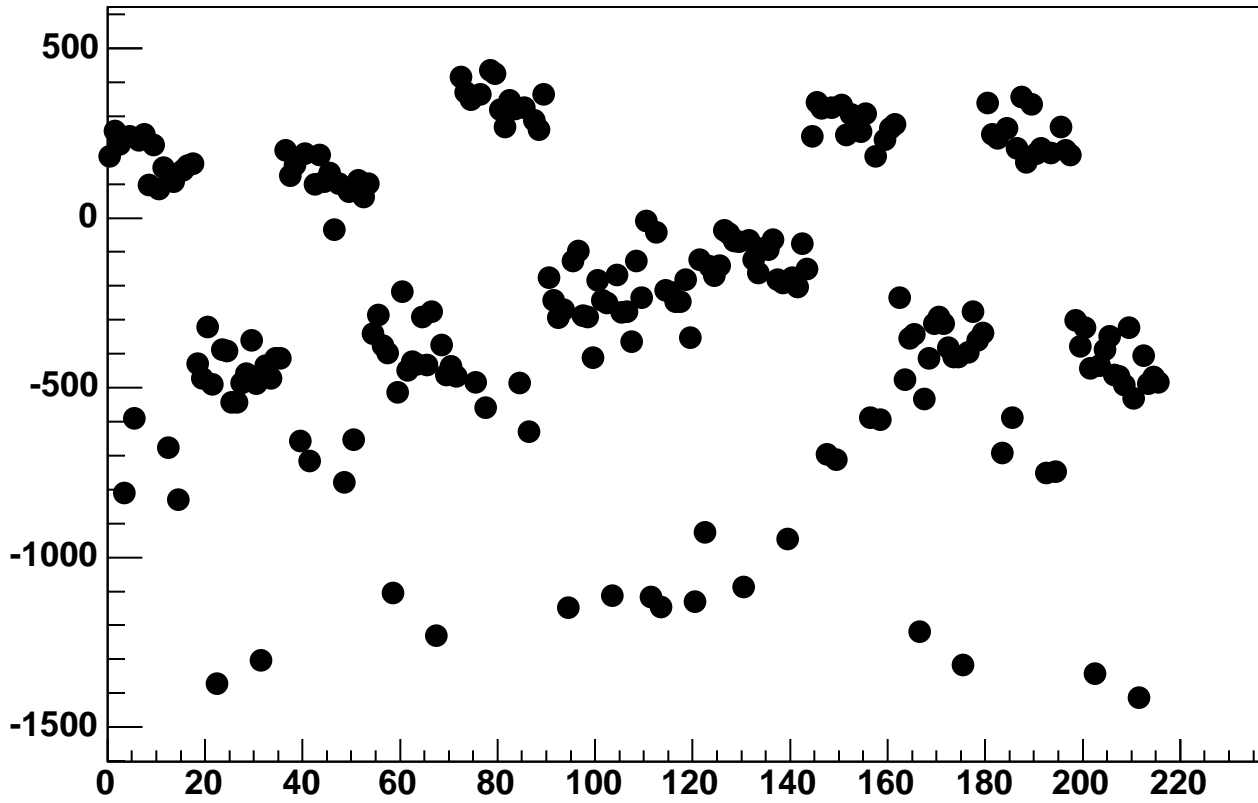
Enable 1, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



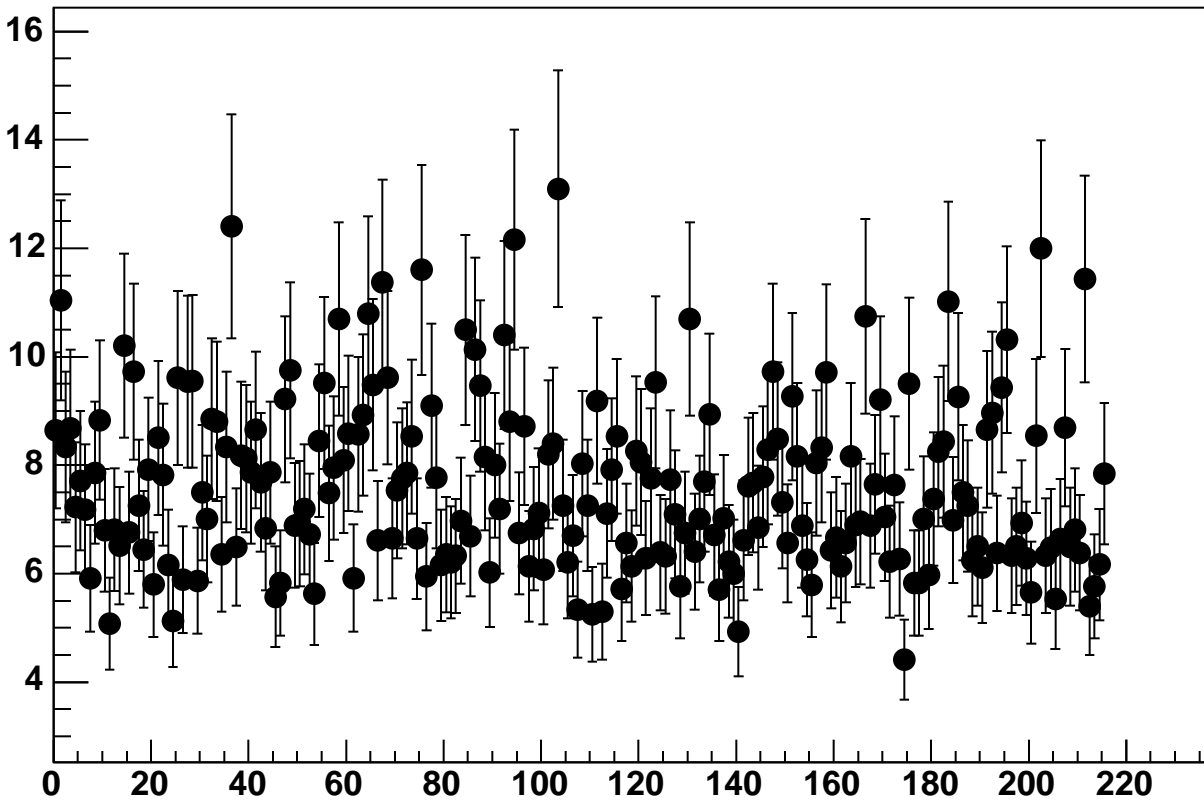
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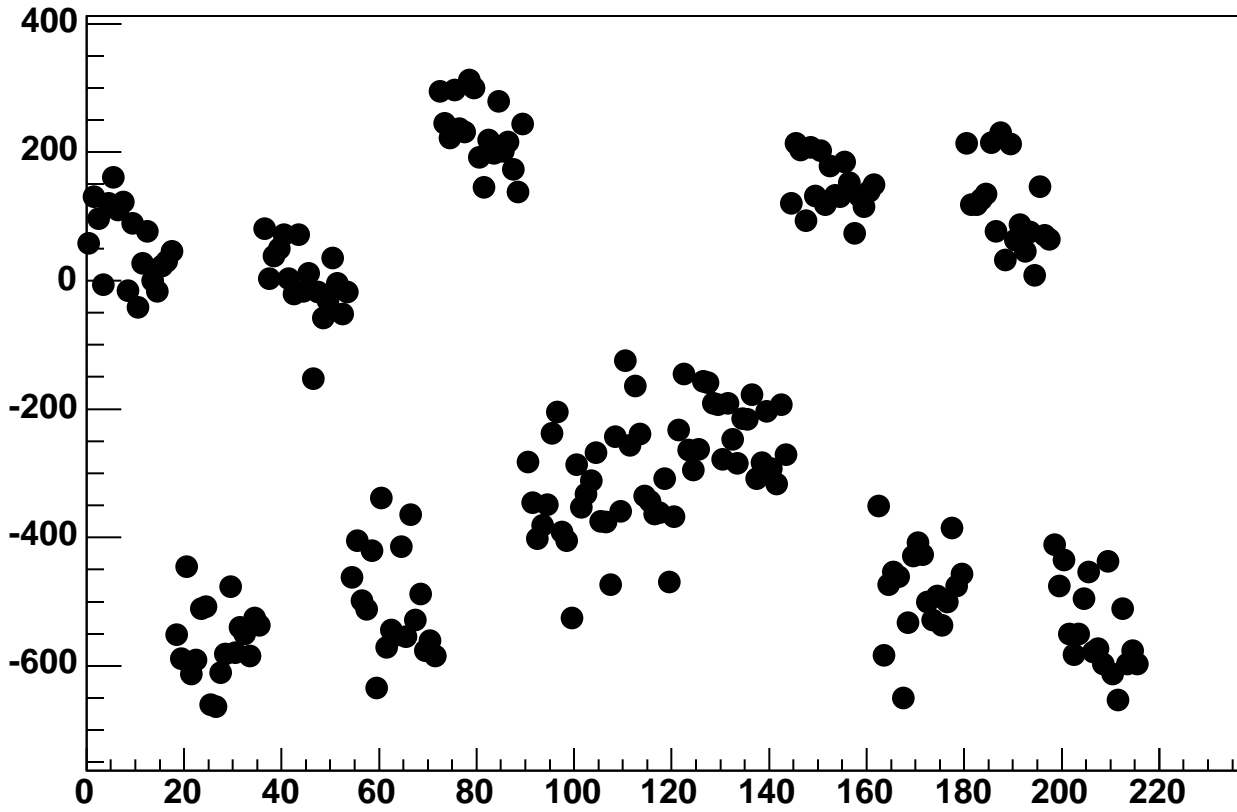
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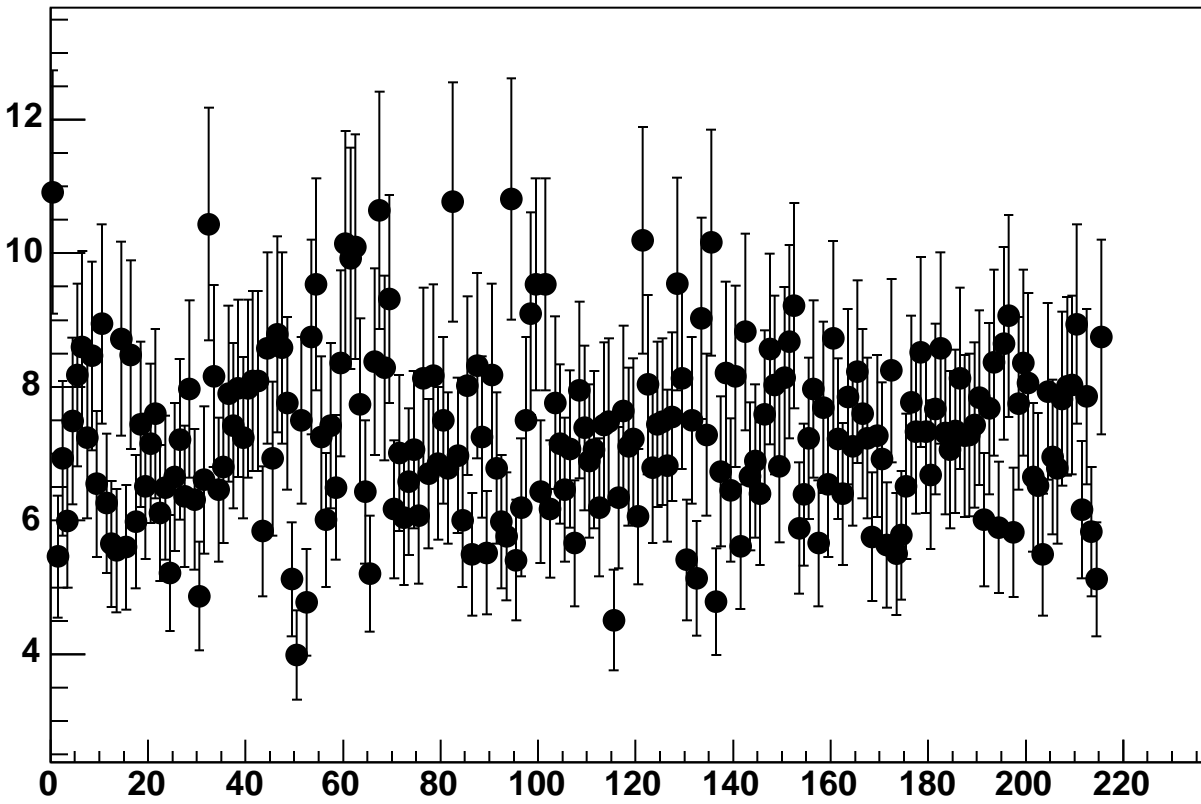
Enable 1, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



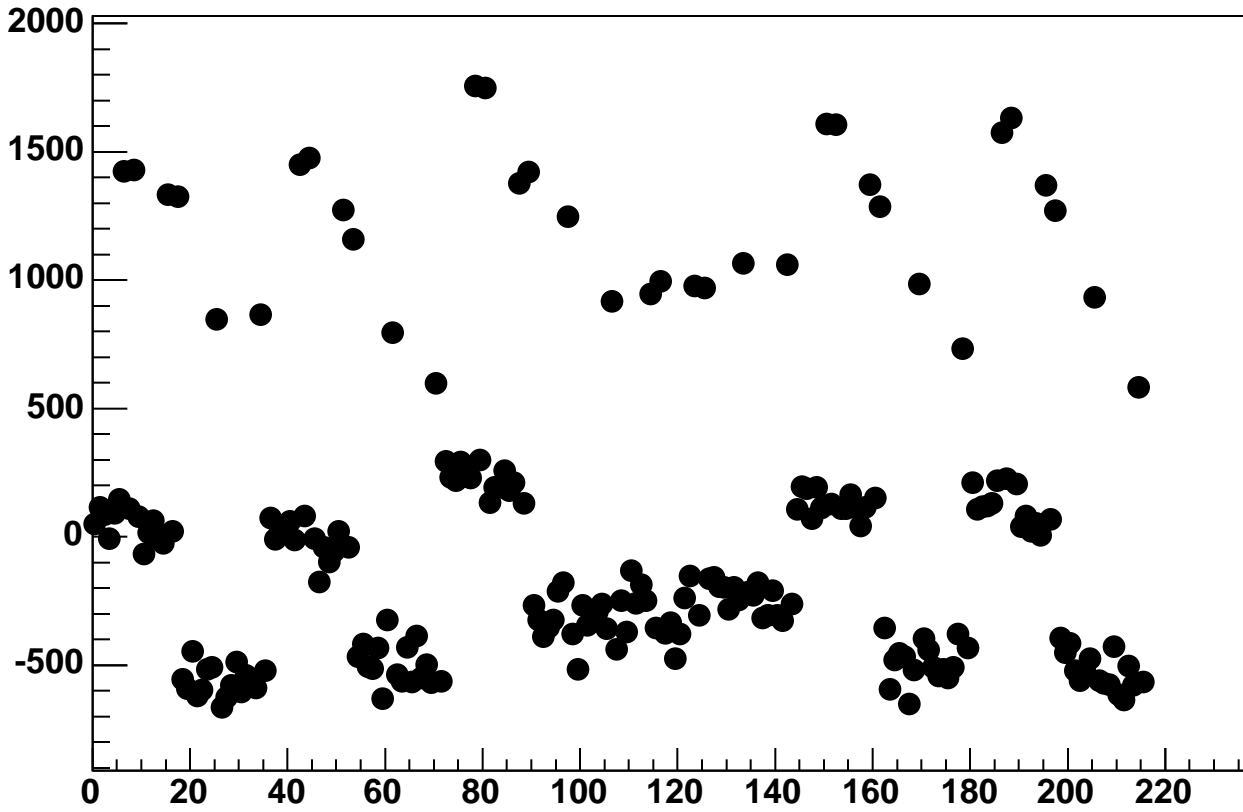
Enable 2, DAC=4000, Hold=0, ADC Mean vs 18*Chip+Chan



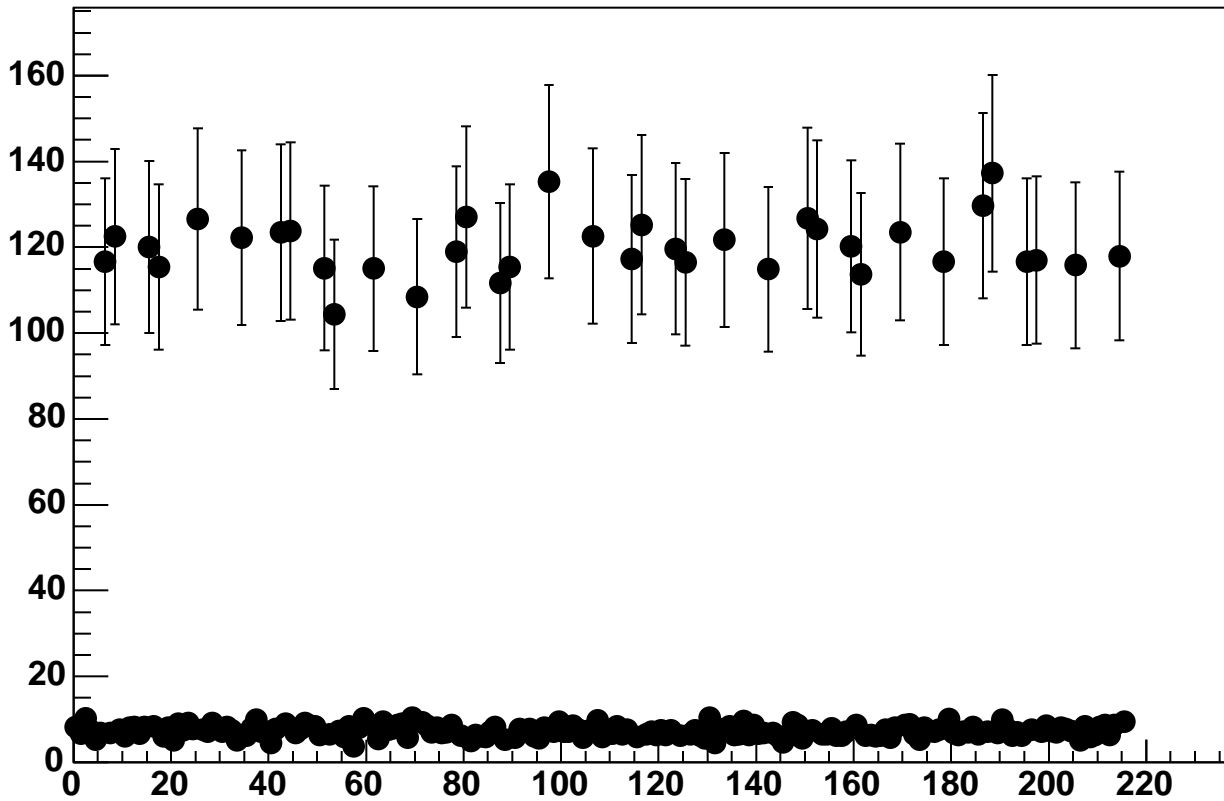
Enable 2, DAC=4000, Hold=0, ADC Noise vs 18*Chip+Chan



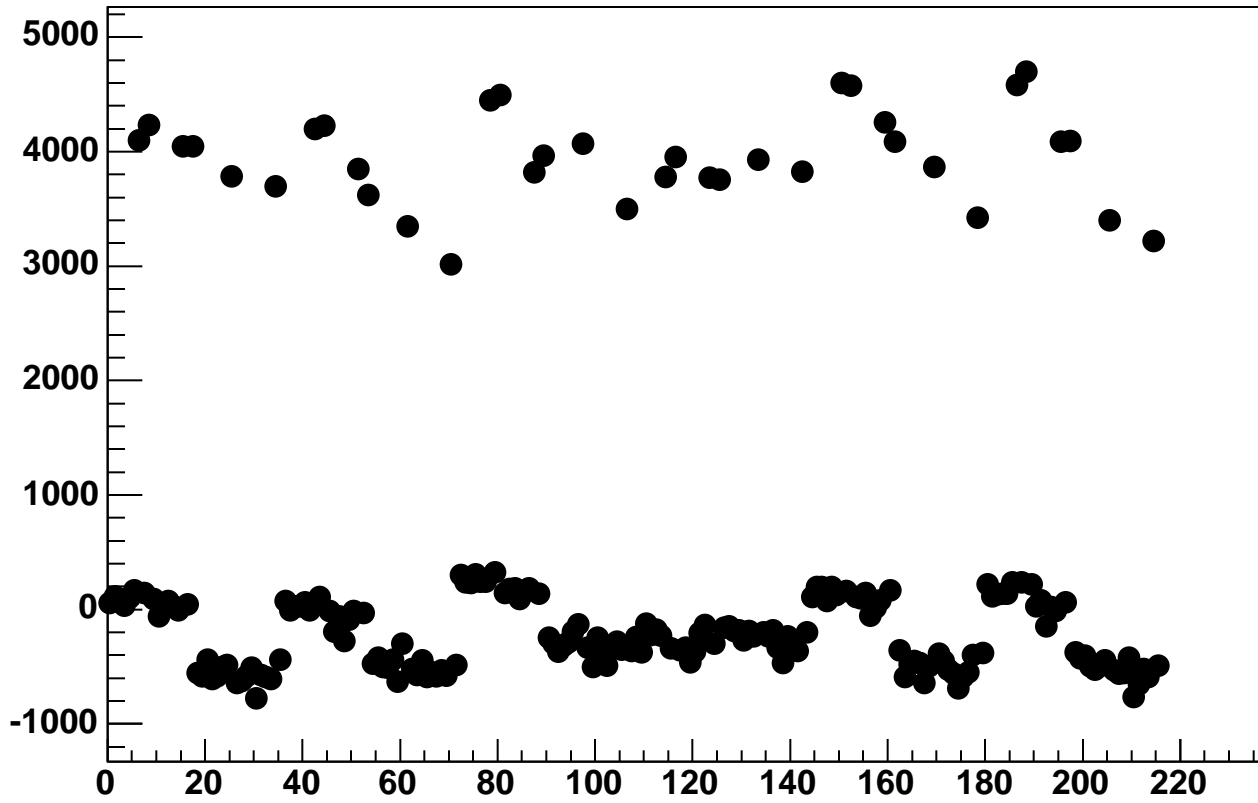
Enable 2, DAC=4000, Hold=5, ADC Mean vs 18*Chip+Chan



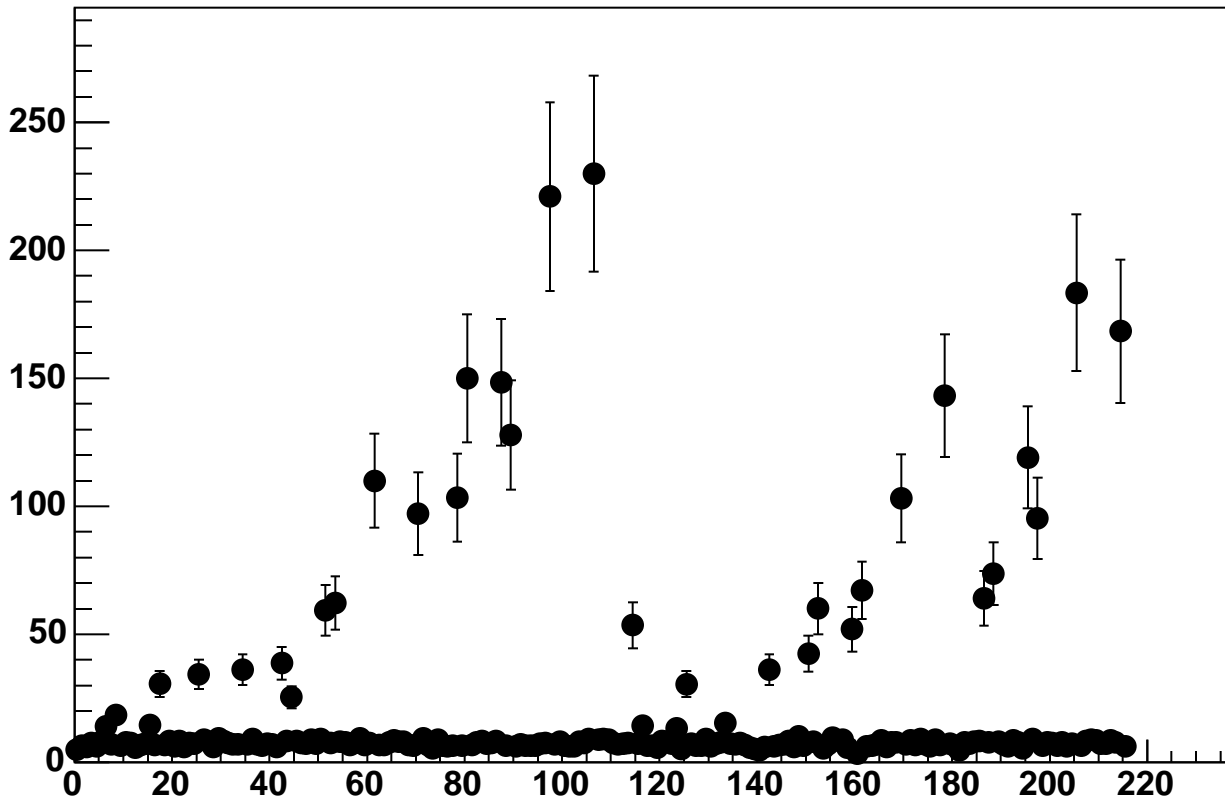
Enable 2, DAC=4000, Hold=5, ADC Noise vs 18*Chip+Chan



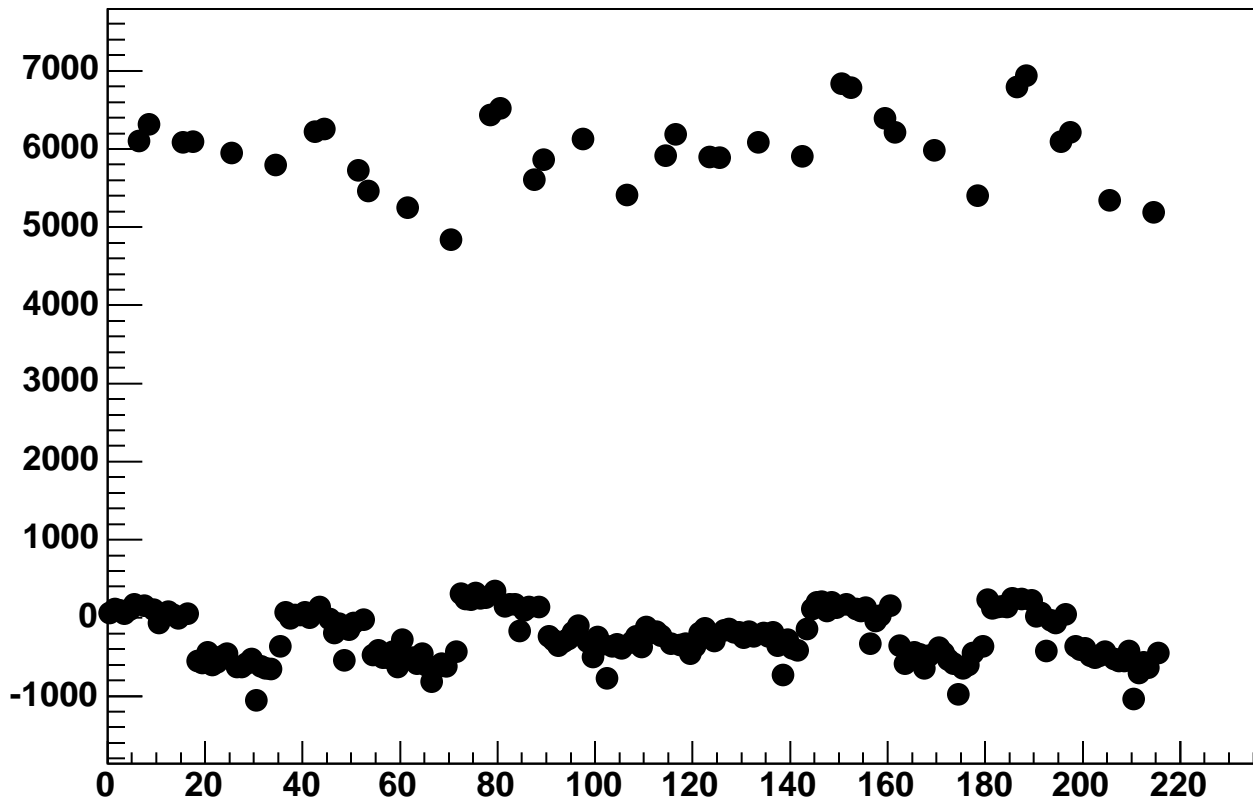
Enable 2, DAC=4000, Hold=10, ADC Mean vs 18*Chip+Chan



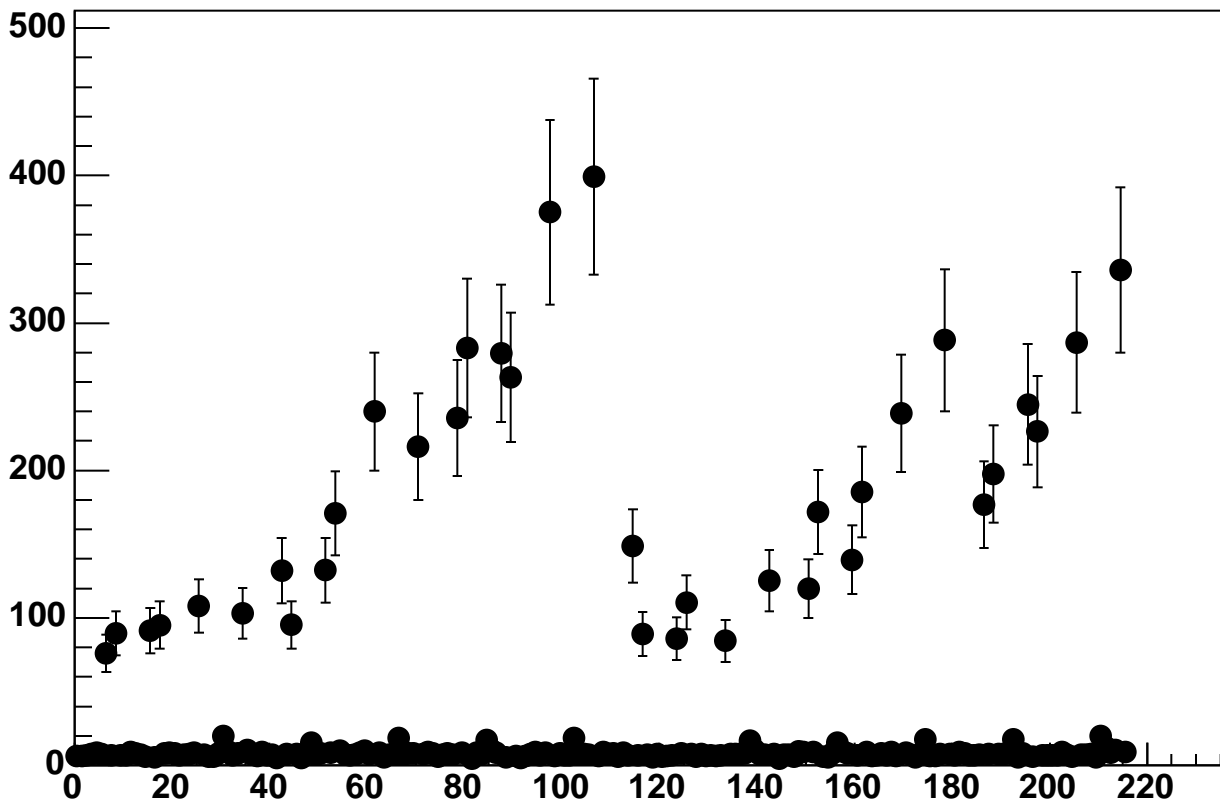
Enable 2, DAC=4000, Hold=10, ADC Noise vs 18*Chip+Chan



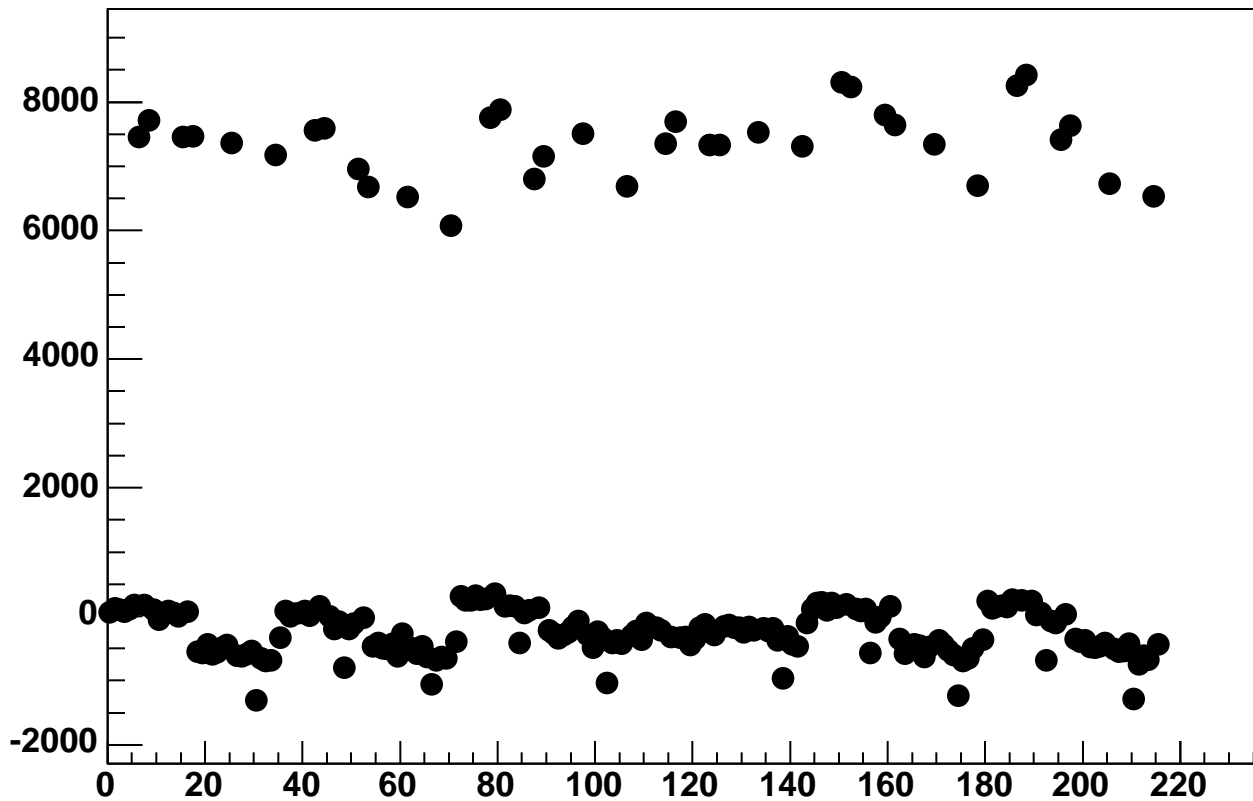
Enable 2, DAC=4000, Hold=15, ADC Mean vs 18*Chip+Chan



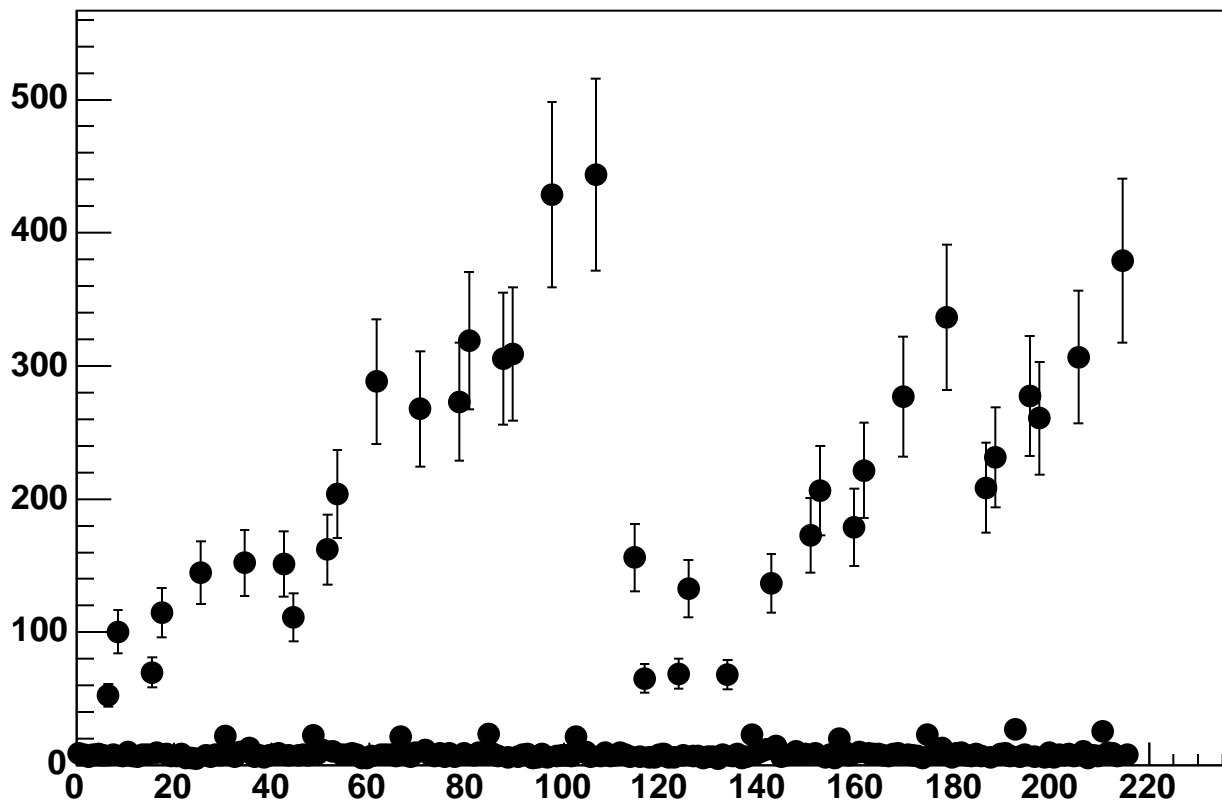
Enable 2, DAC=4000, Hold=15, ADC Noise vs 18*Chip+Chan



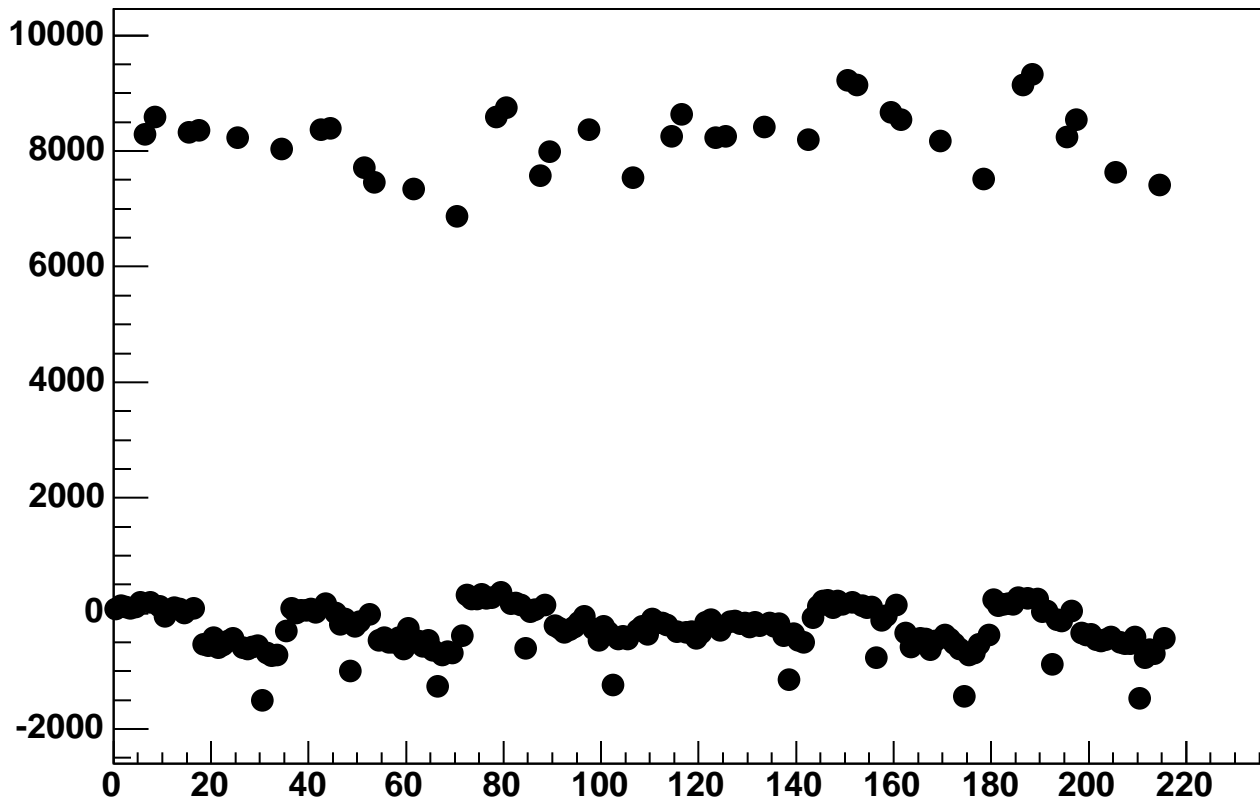
Enable 2, DAC=4000, Hold=20, ADC Mean vs 18*Chip+Chan



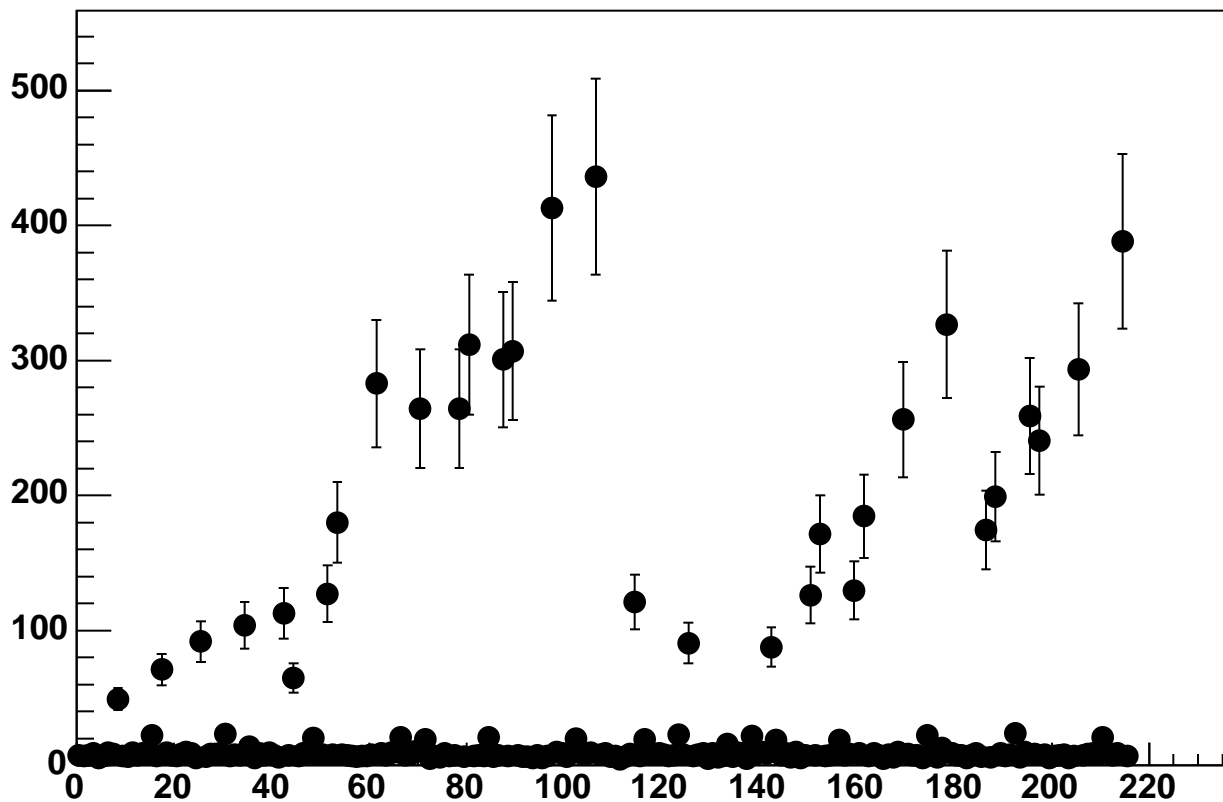
Enable 2, DAC=4000, Hold=20, ADC Noise vs 18*Chip+Chan



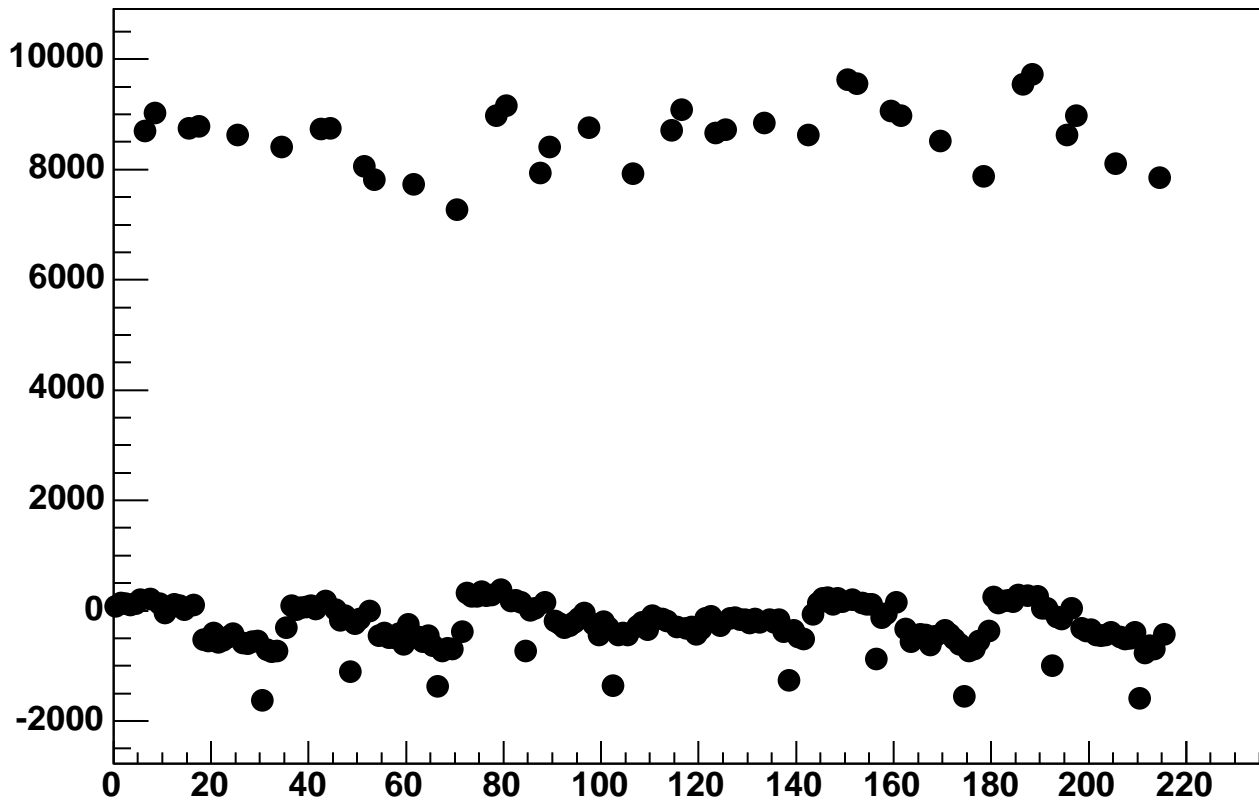
Enable 2, DAC=4000, Hold=25, ADC Mean vs 18*Chip+Chan



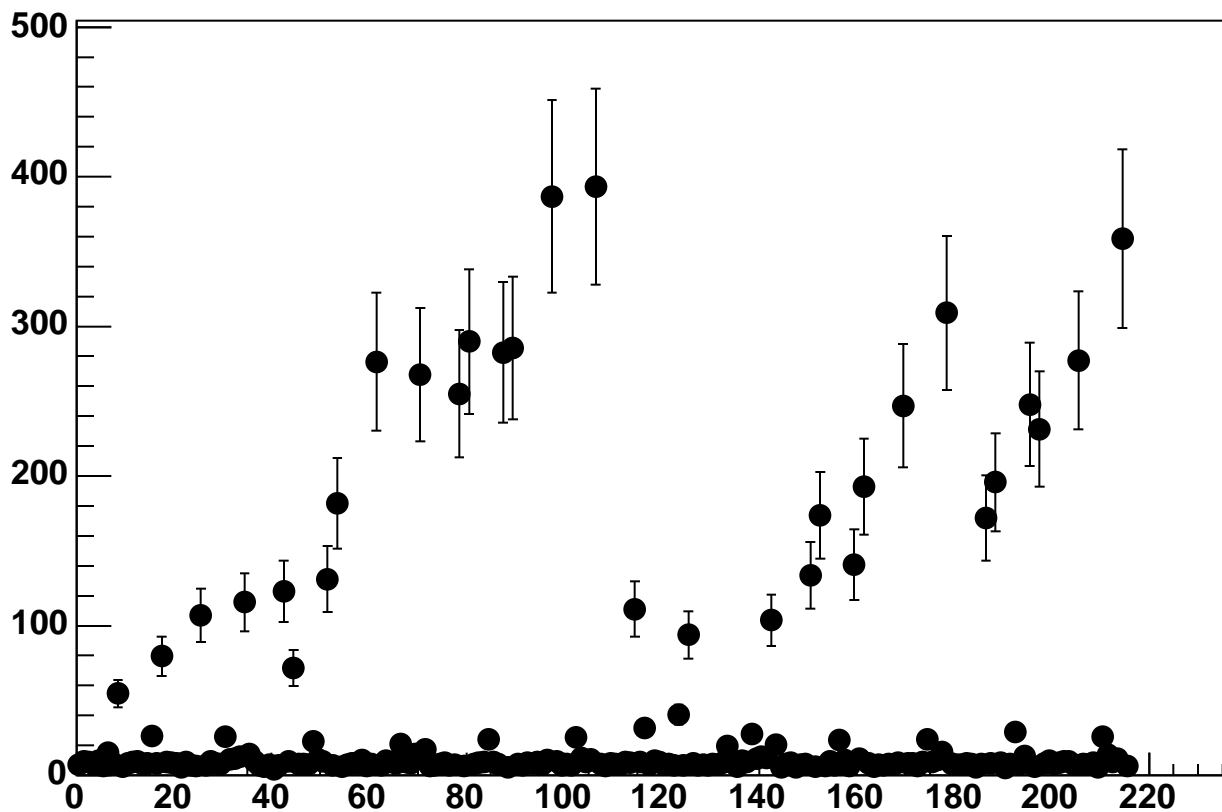
Enable 2, DAC=4000, Hold=25, ADC Noise vs 18*Chip+Chan



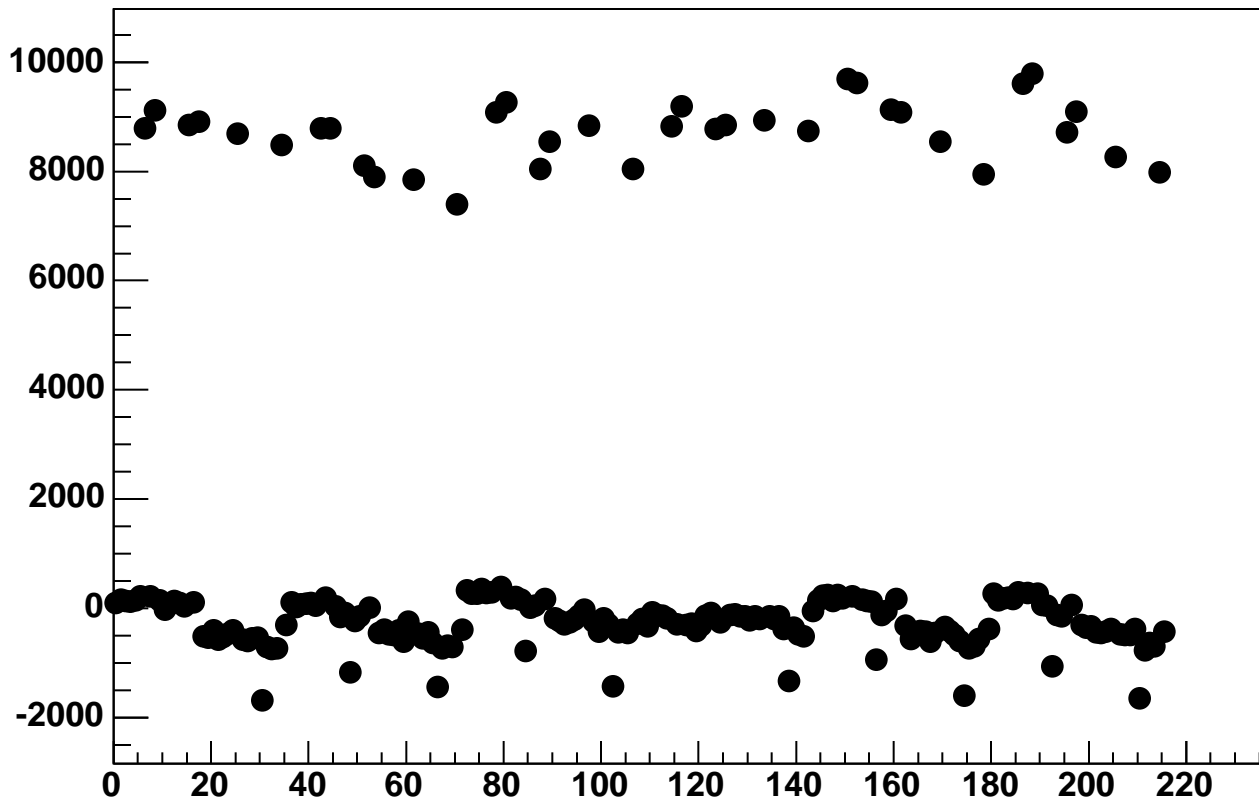
Enable 2, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



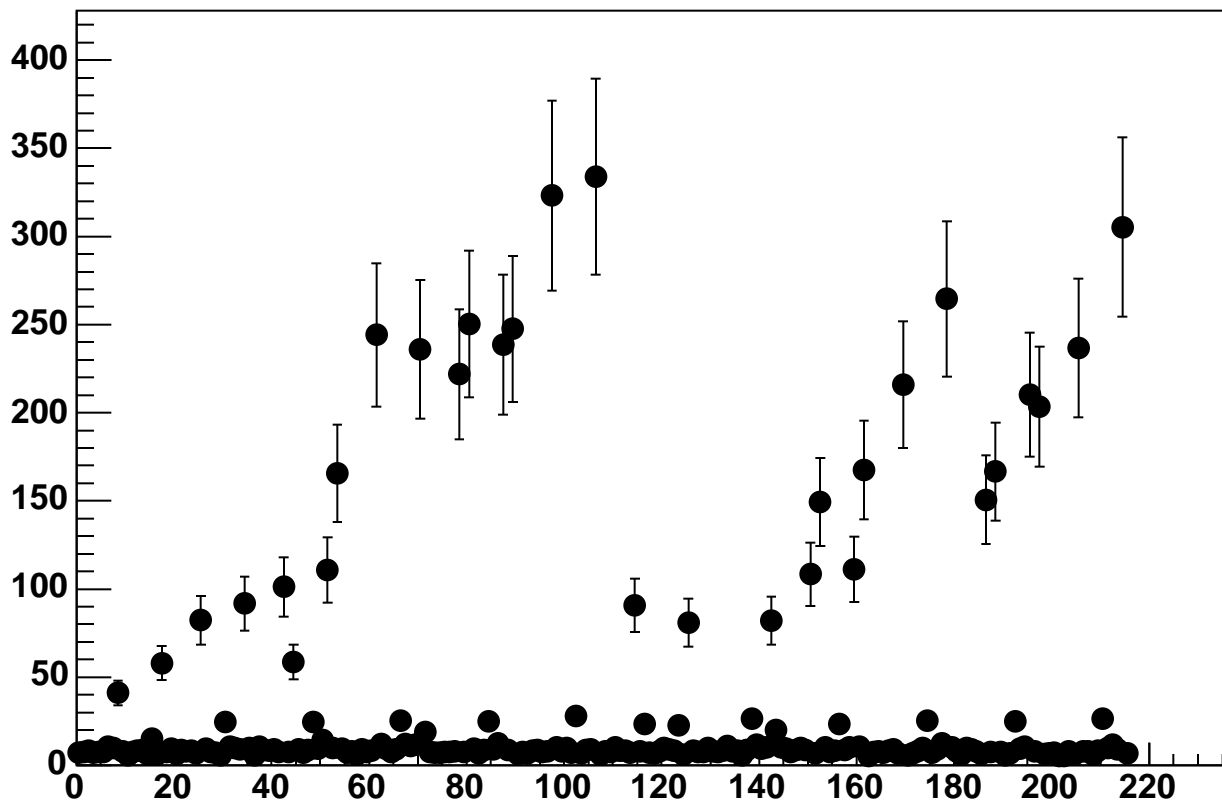
Enable 2, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



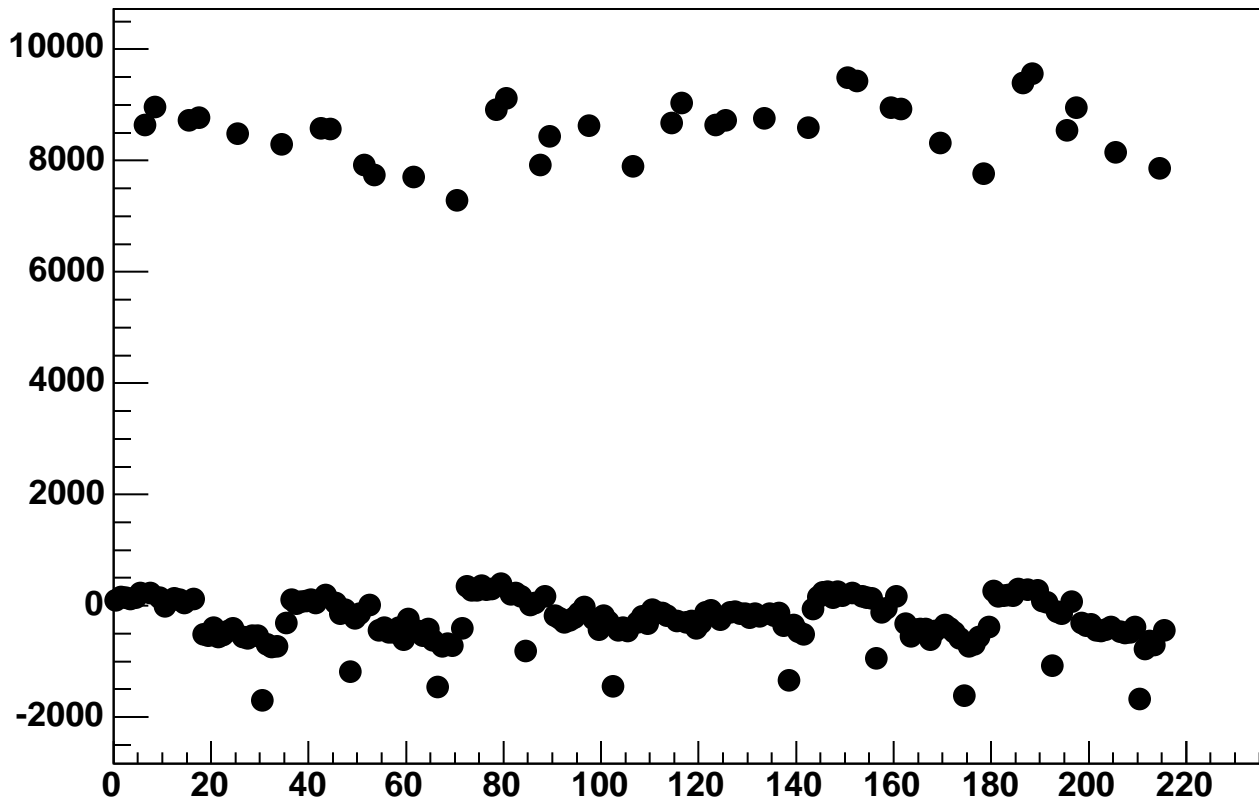
Enable 2, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



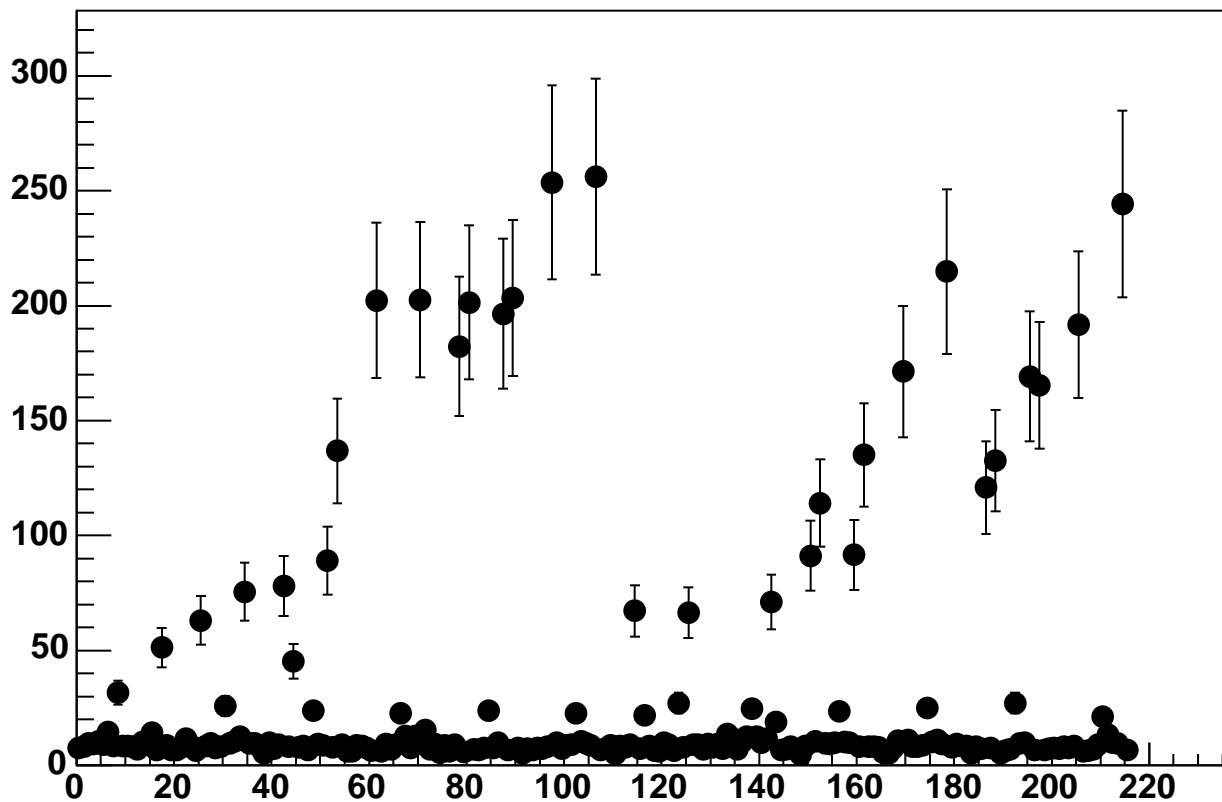
Enable 2, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



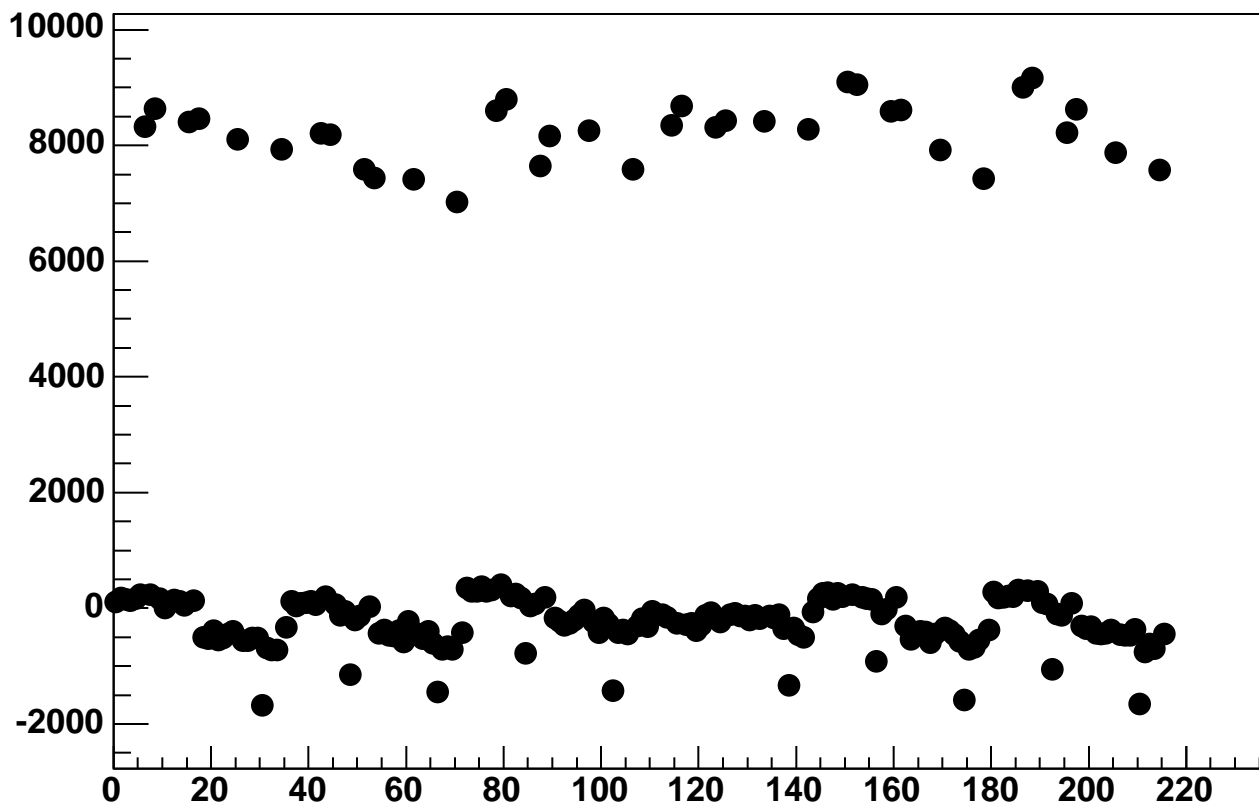
Enable 2, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



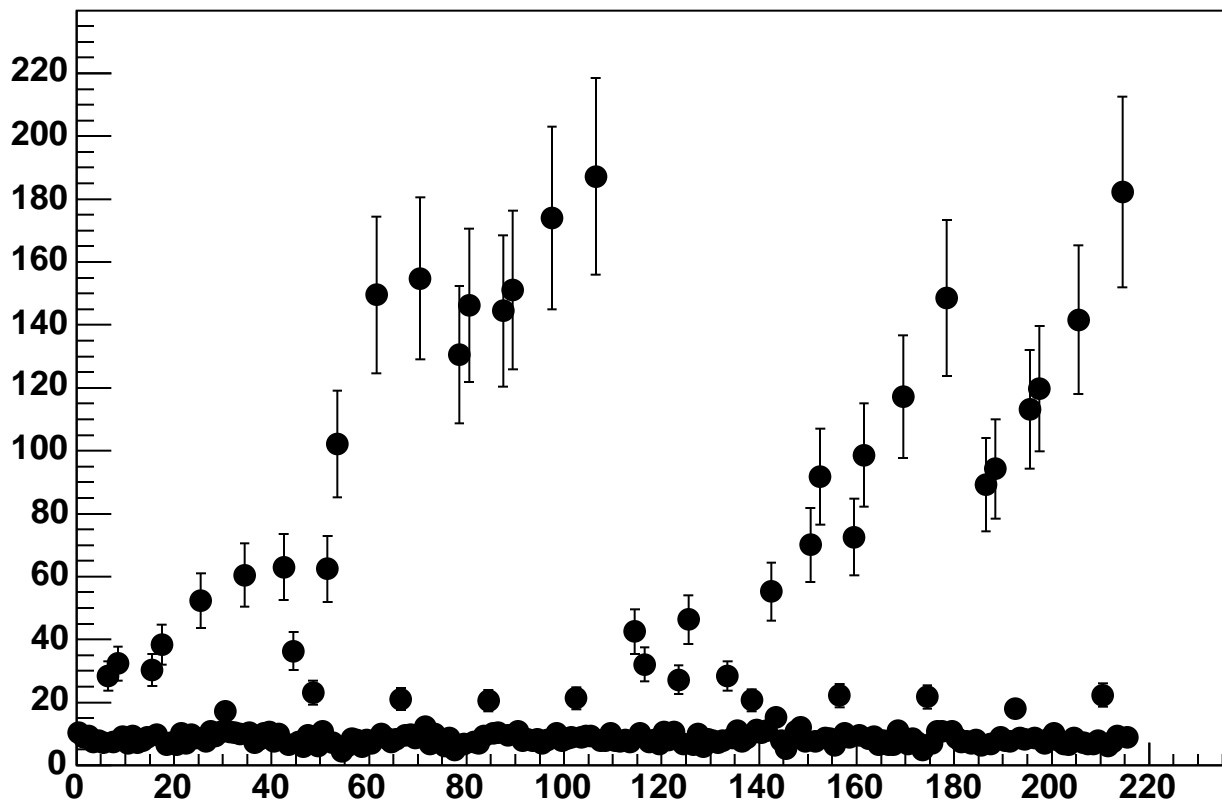
Enable 2, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



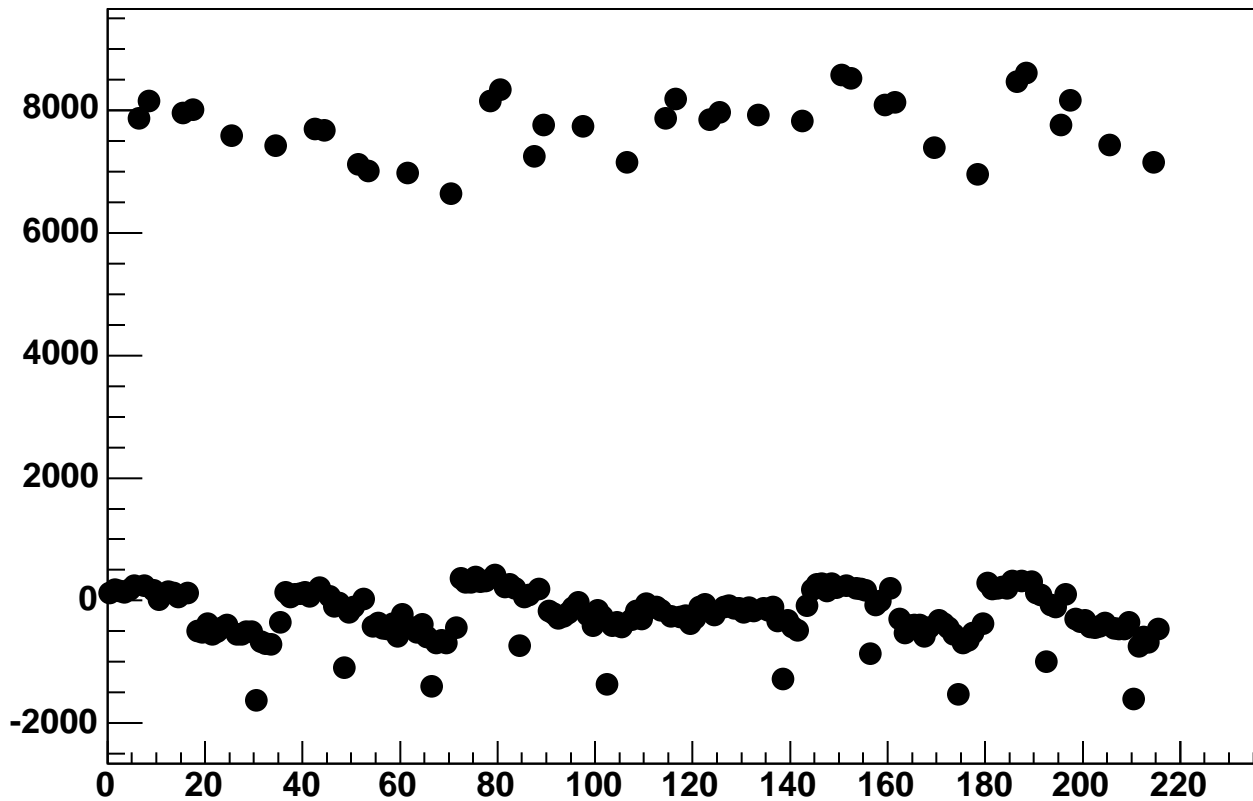
Enable 2, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



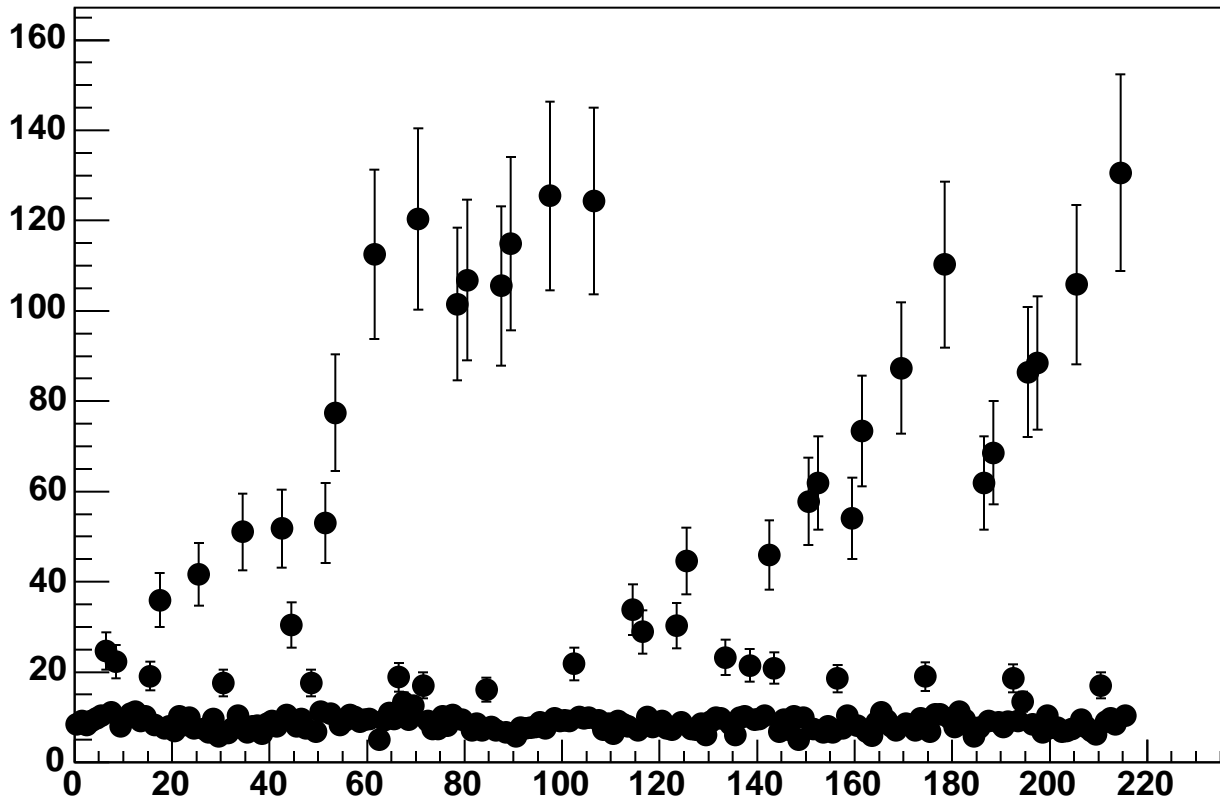
Enable 2, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



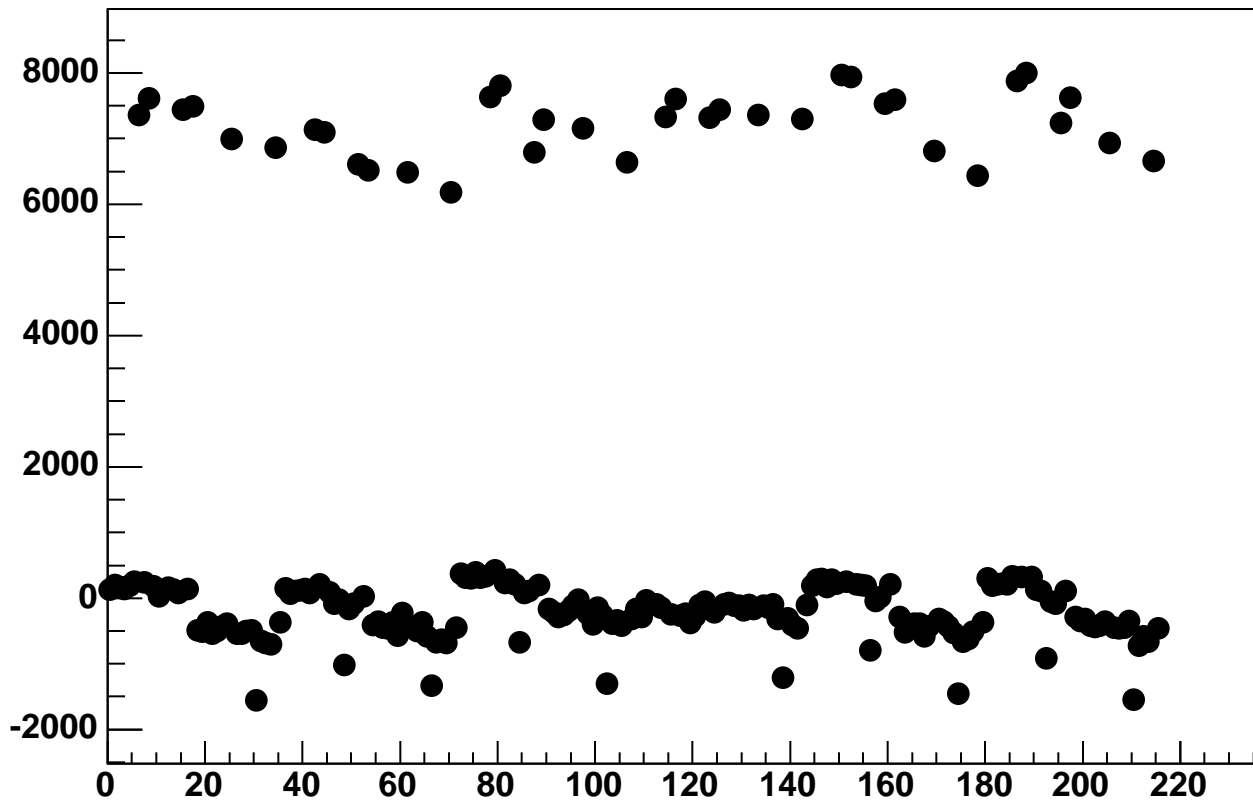
Enable 2, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



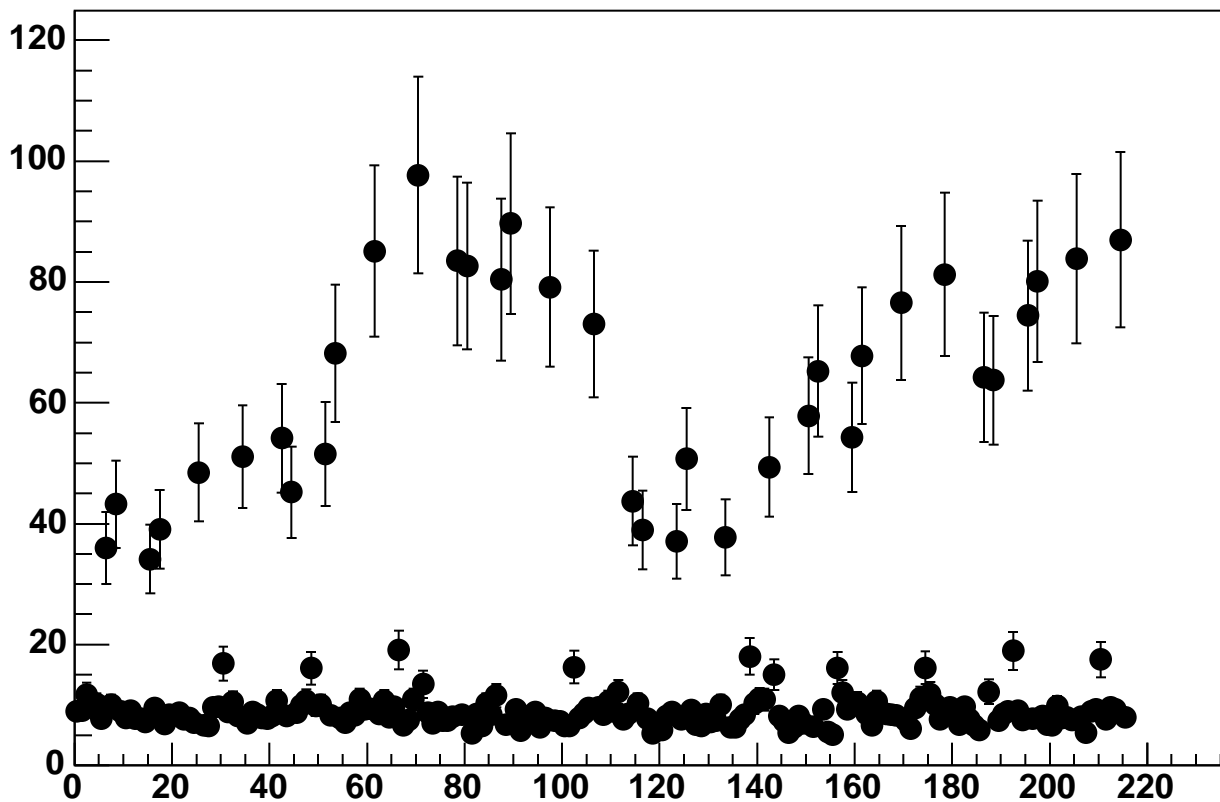
Enable 2, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



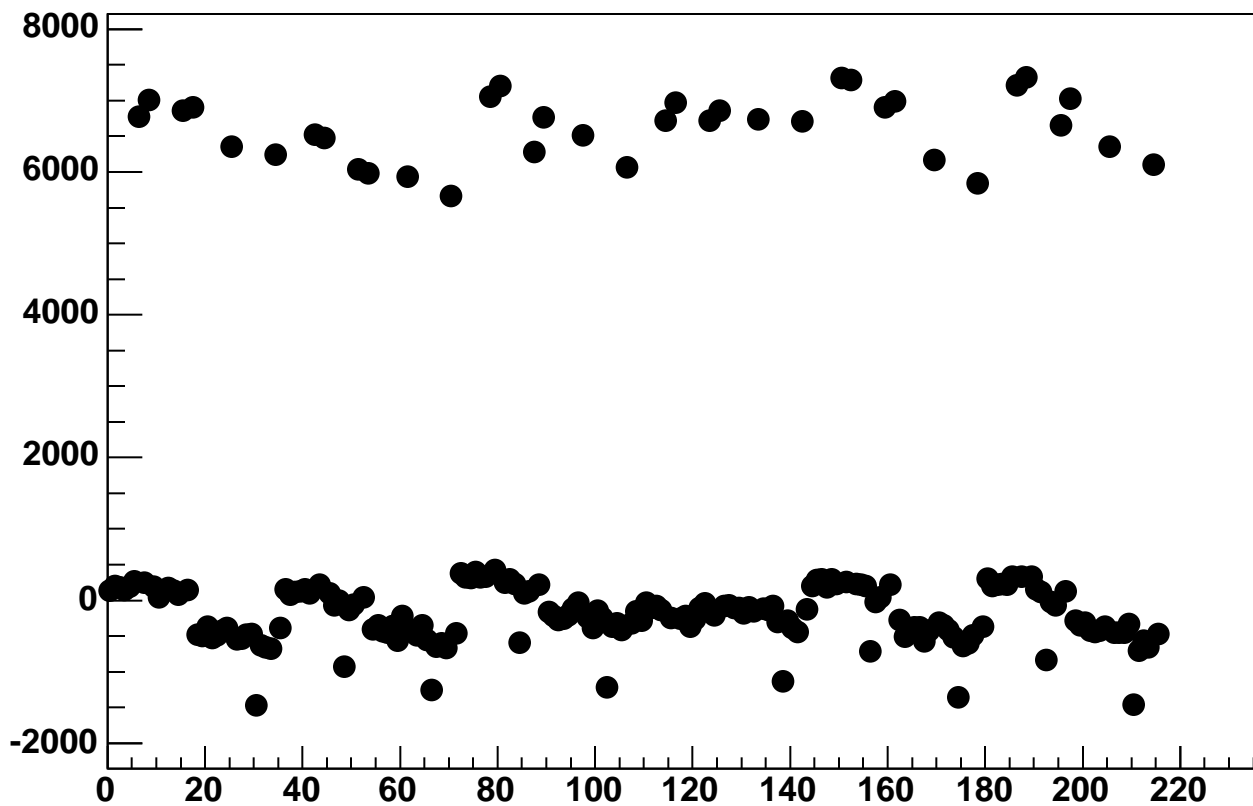
Enable 2, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



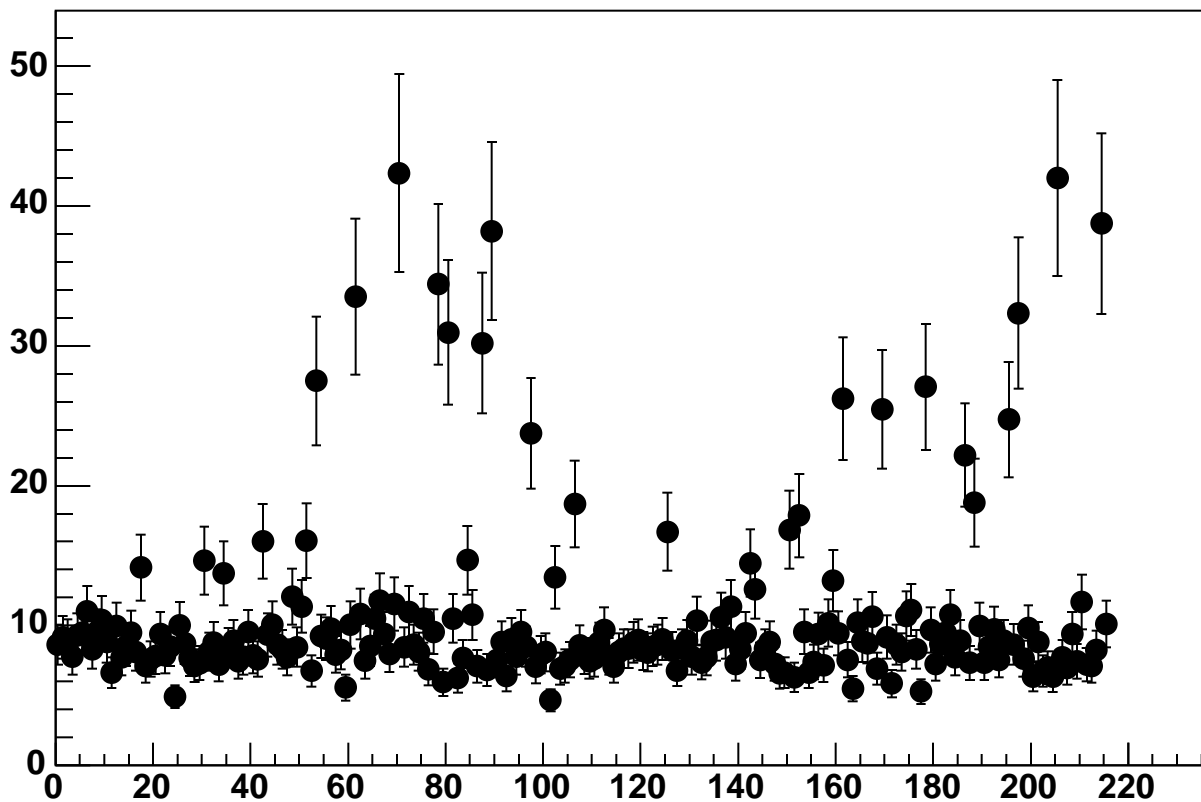
Enable 2, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



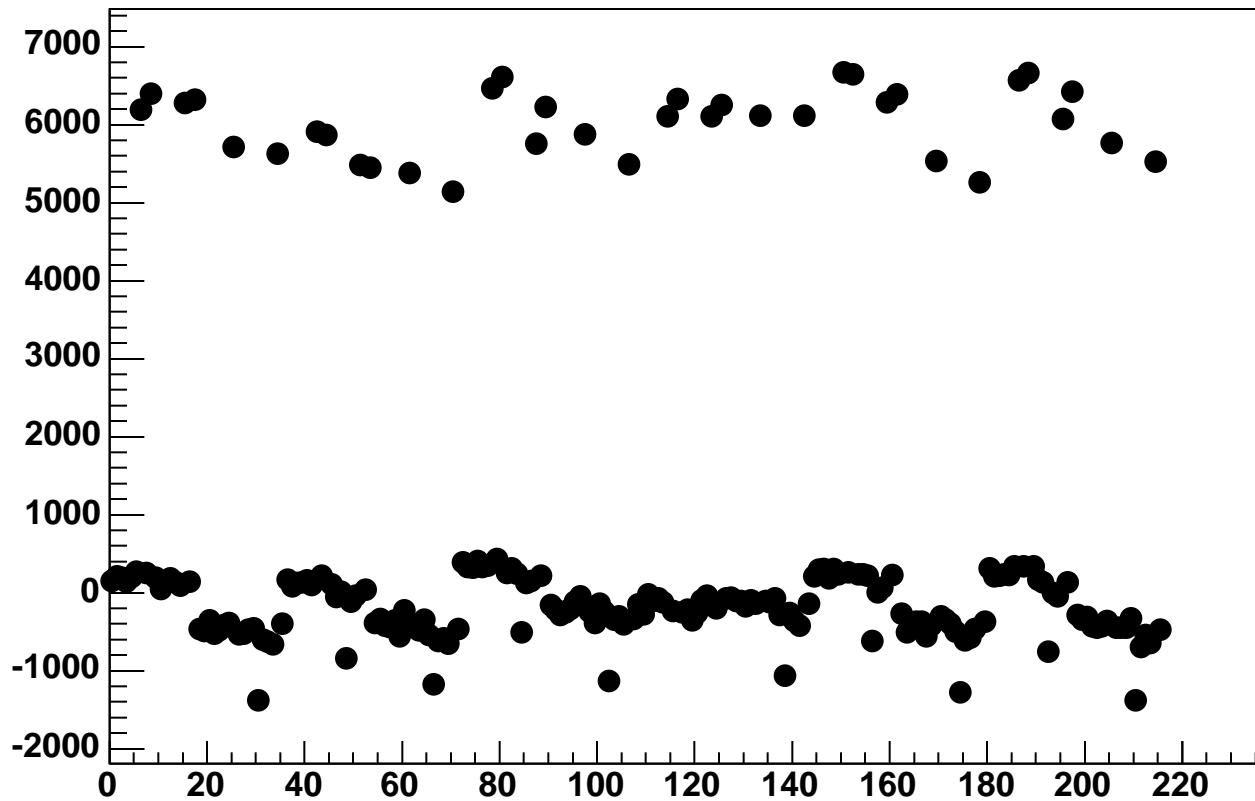
Enable 2, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



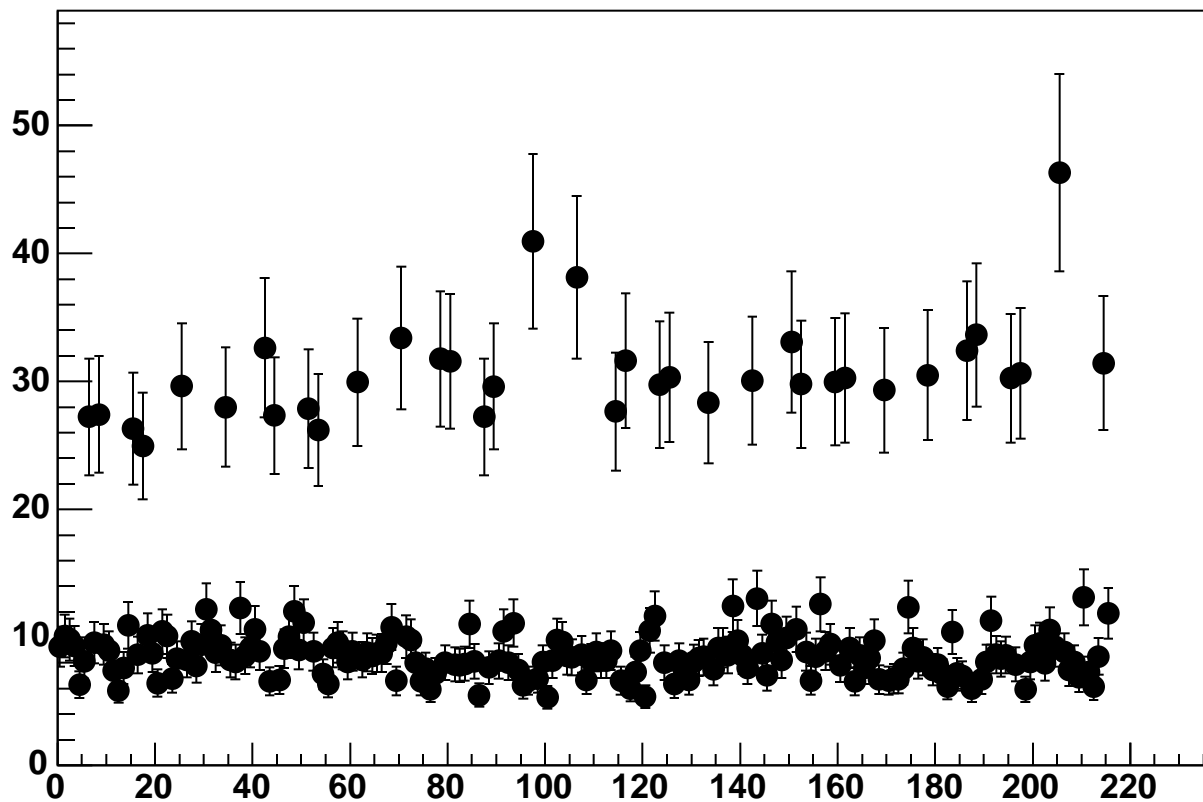
Enable 2, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



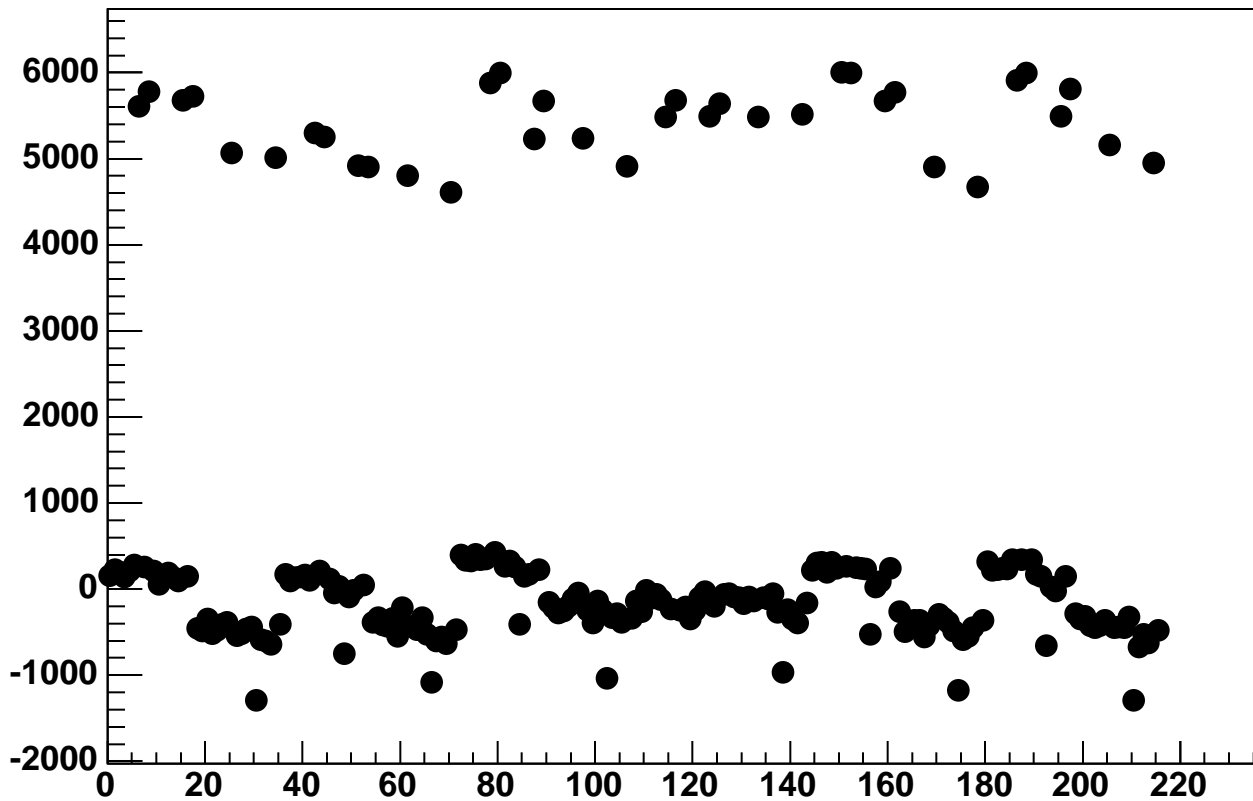
Enable 2, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



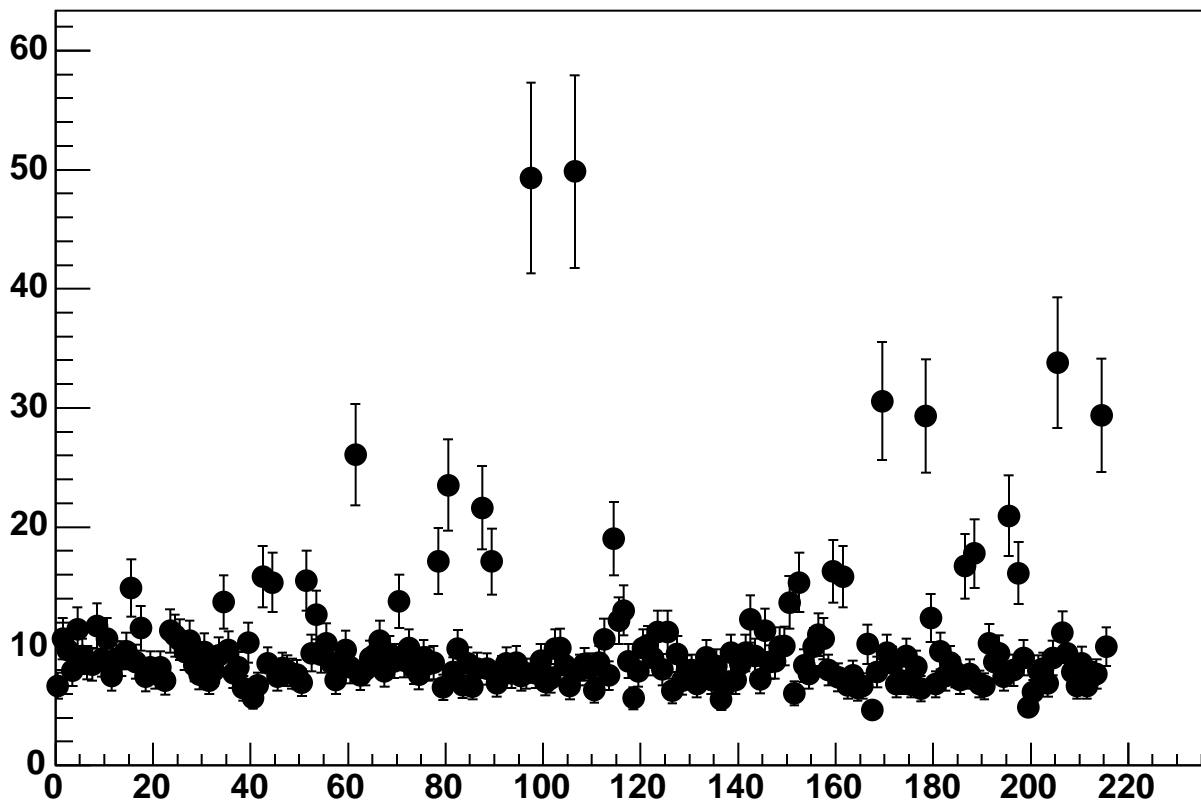
Enable 2, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



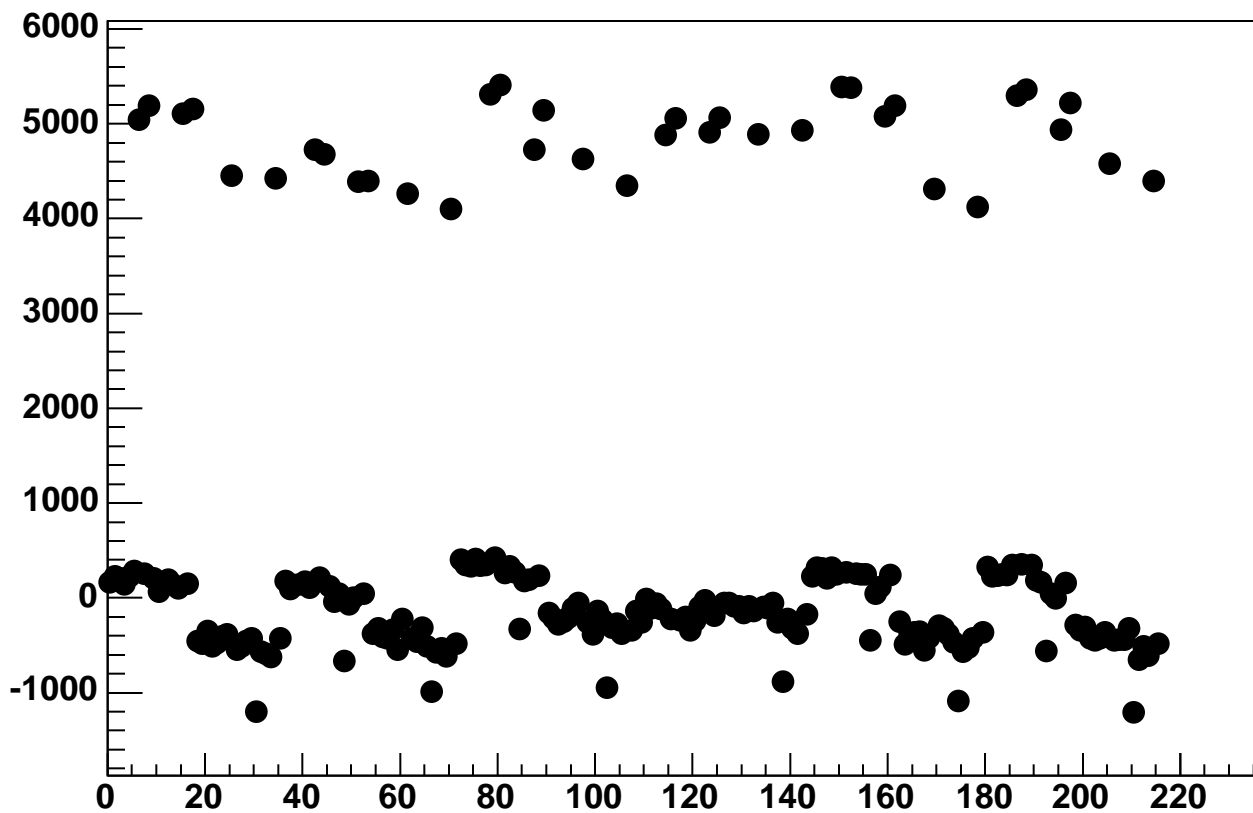
Enable 2, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



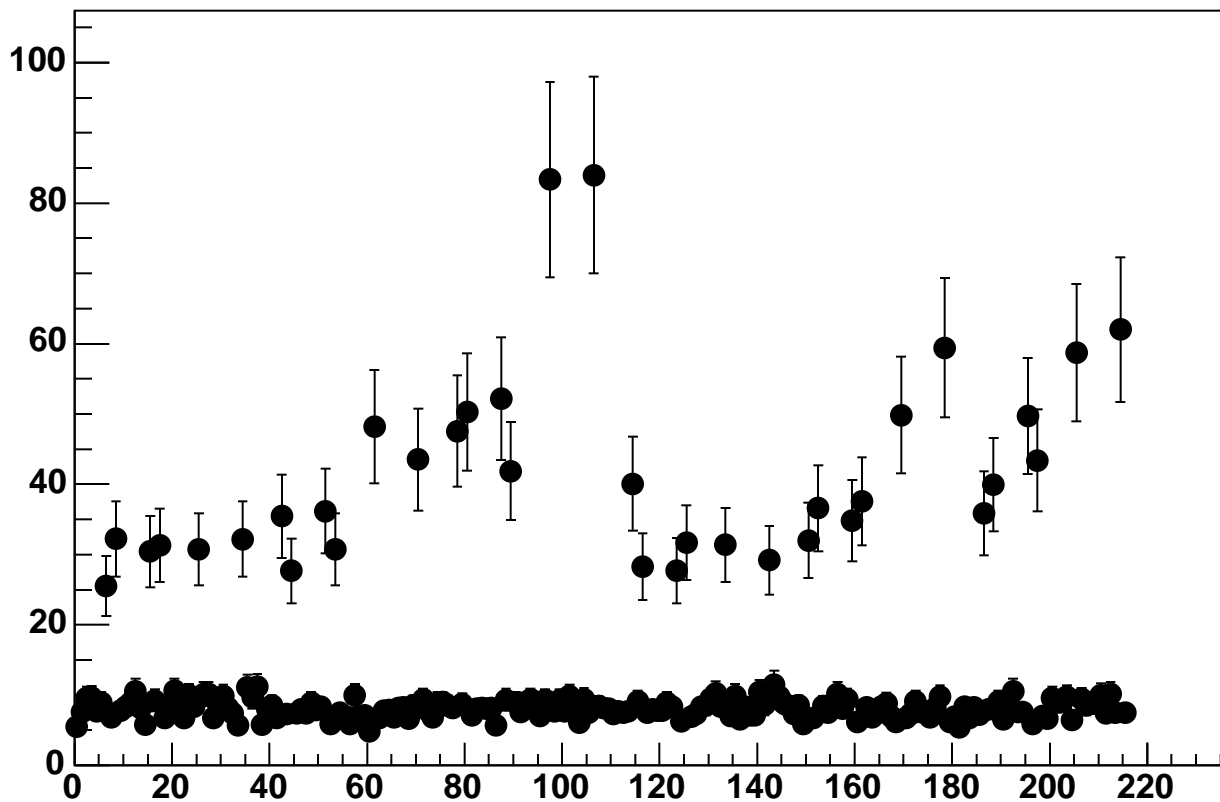
Enable 2, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



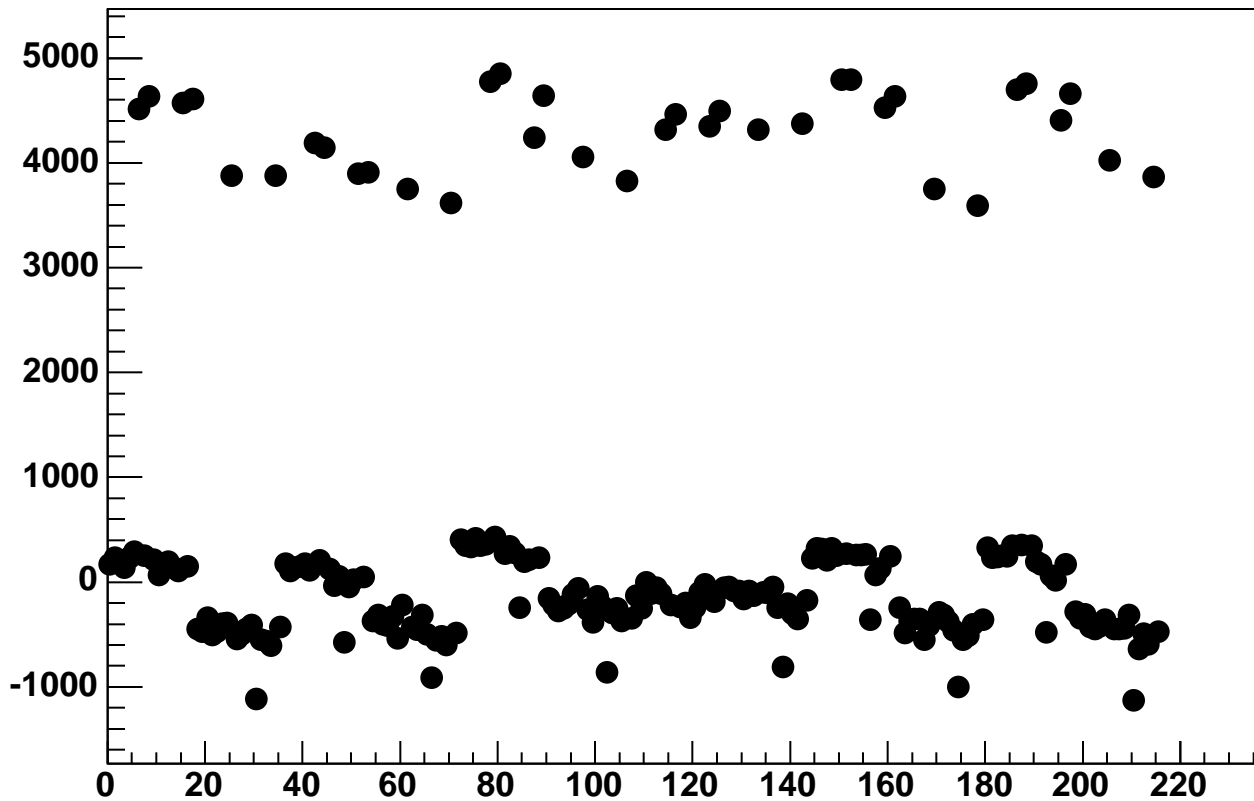
Enable 2, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



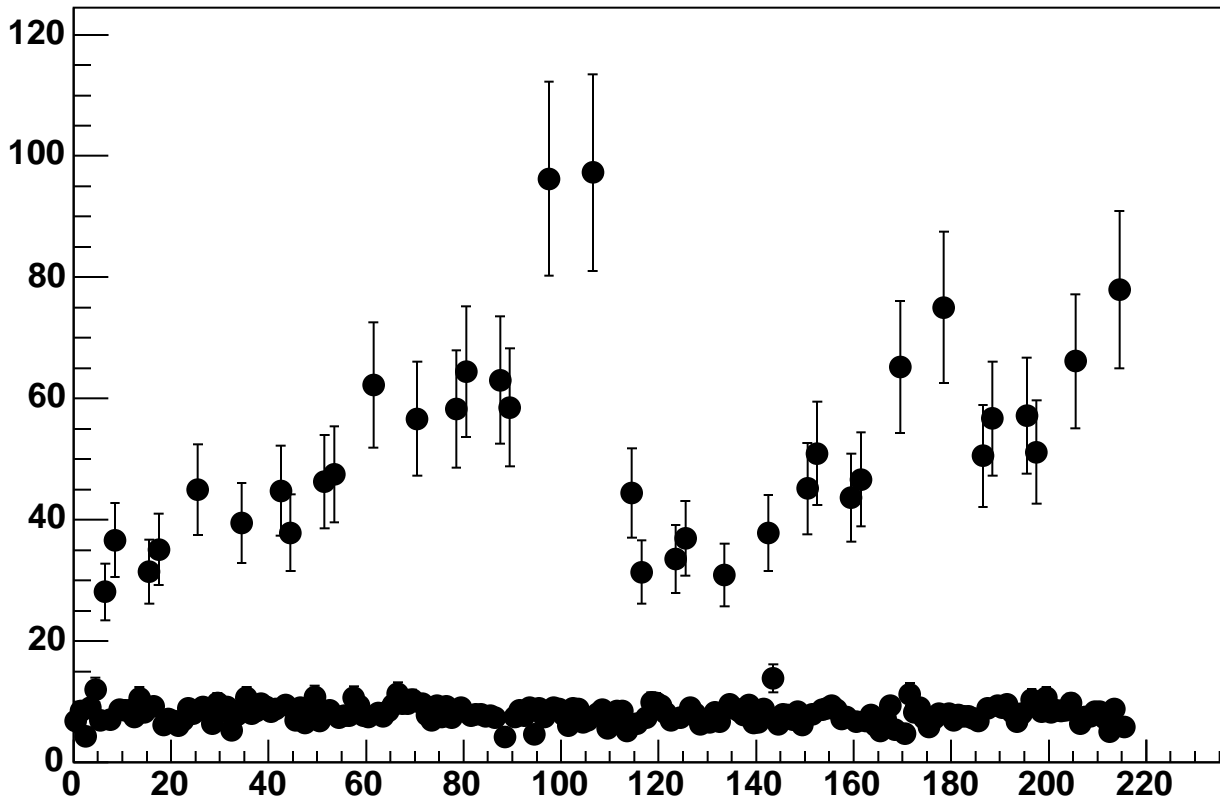
Enable 2, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



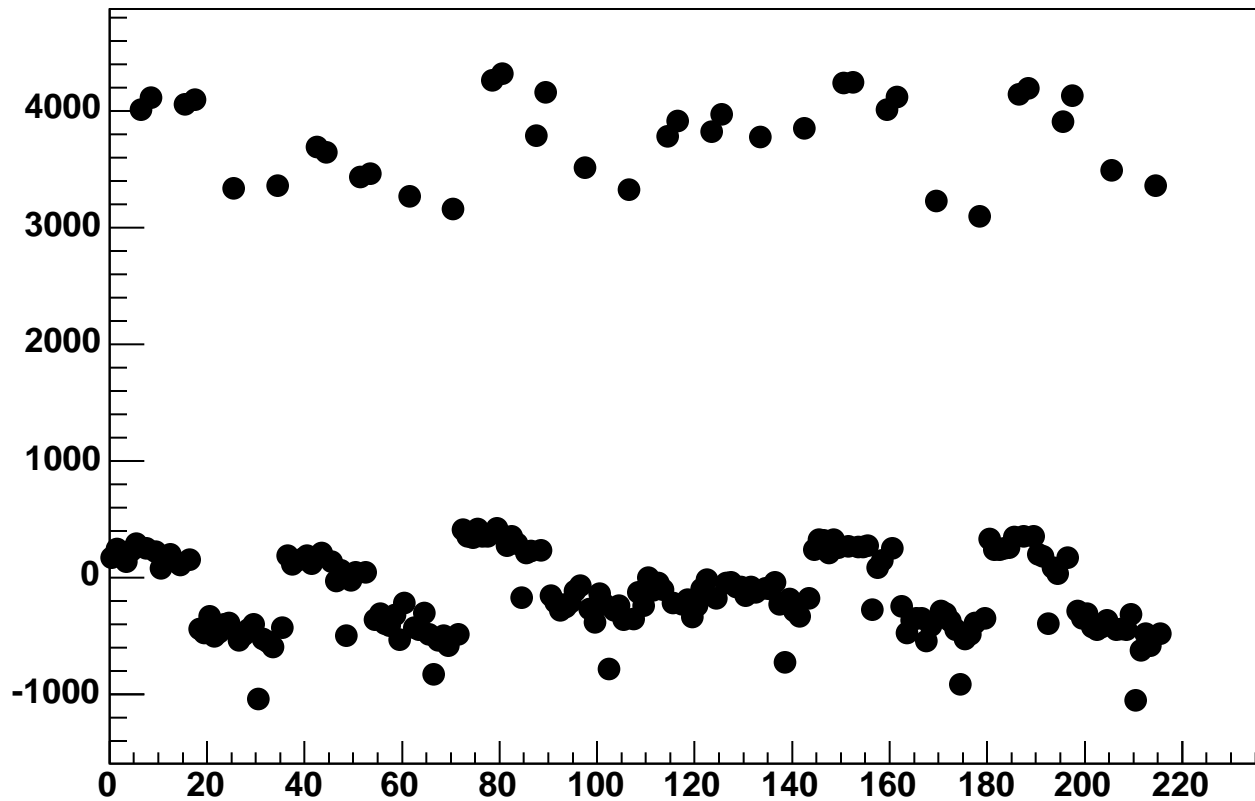
Enable 2, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



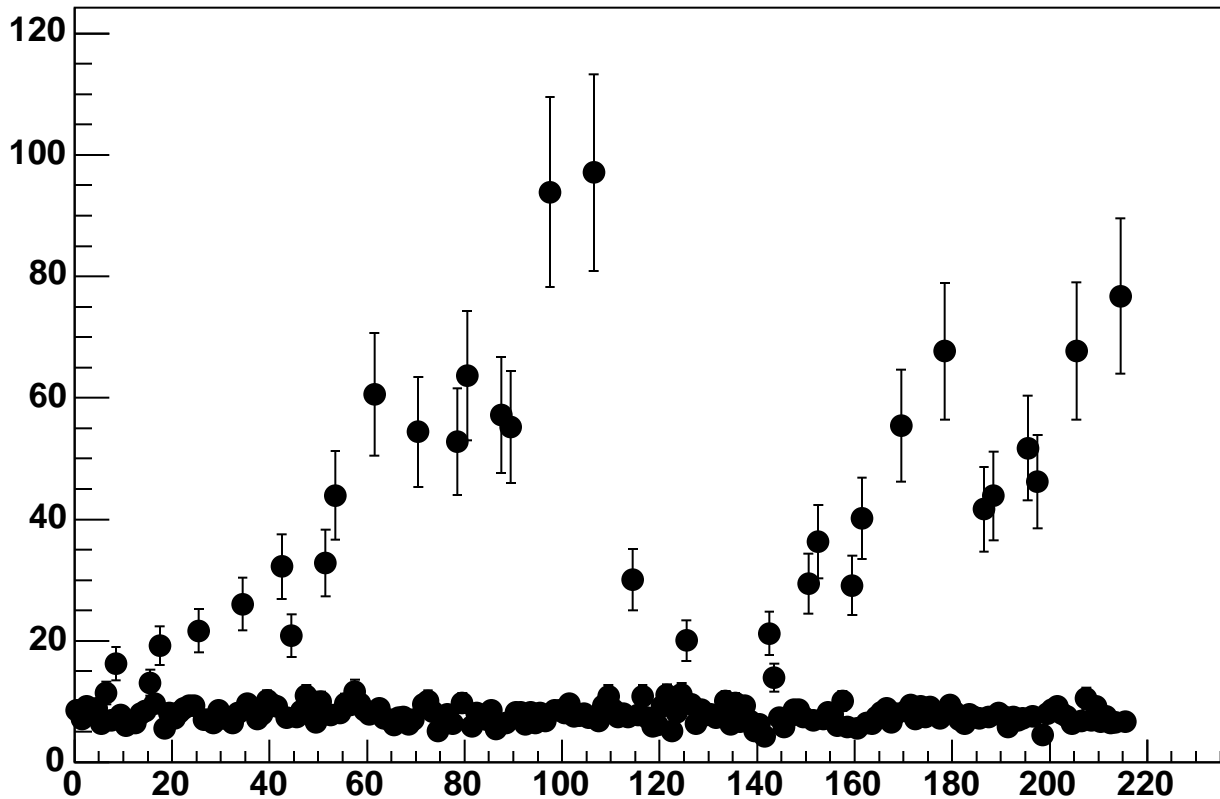
Enable 2, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



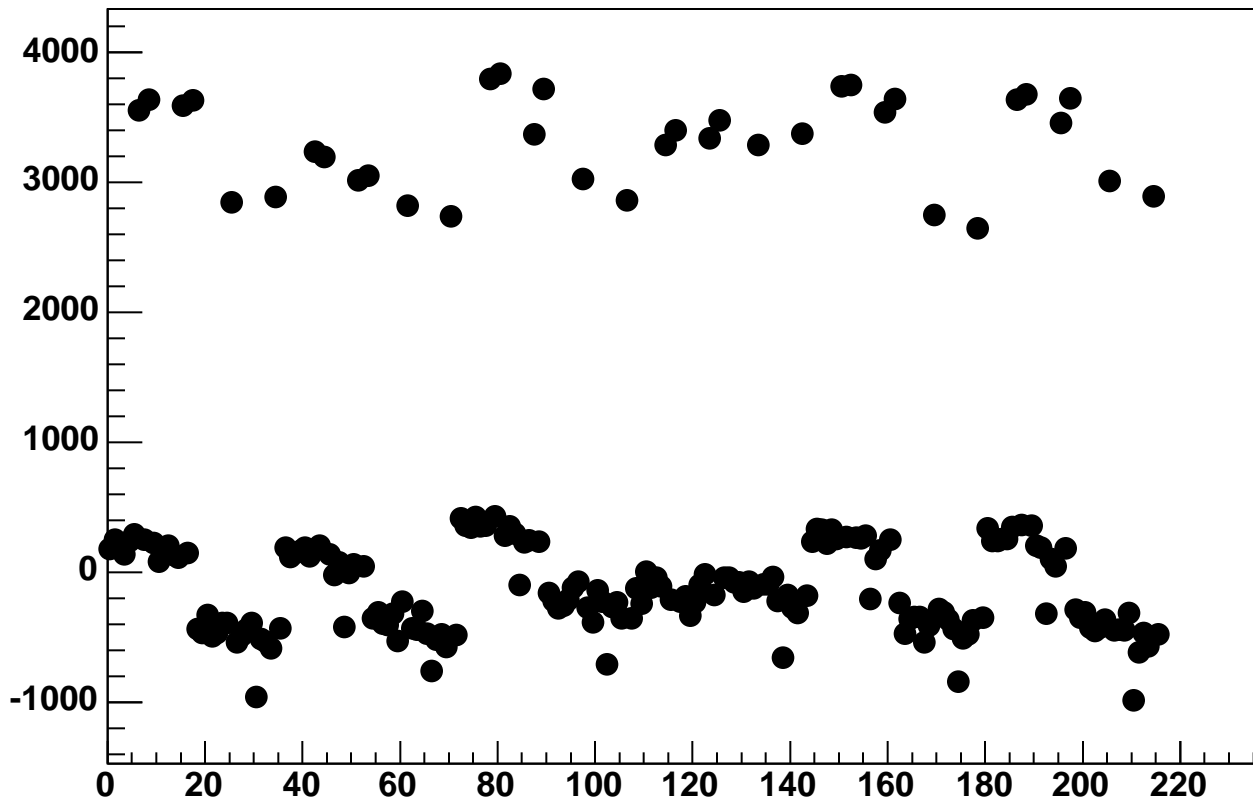
Enable 2, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



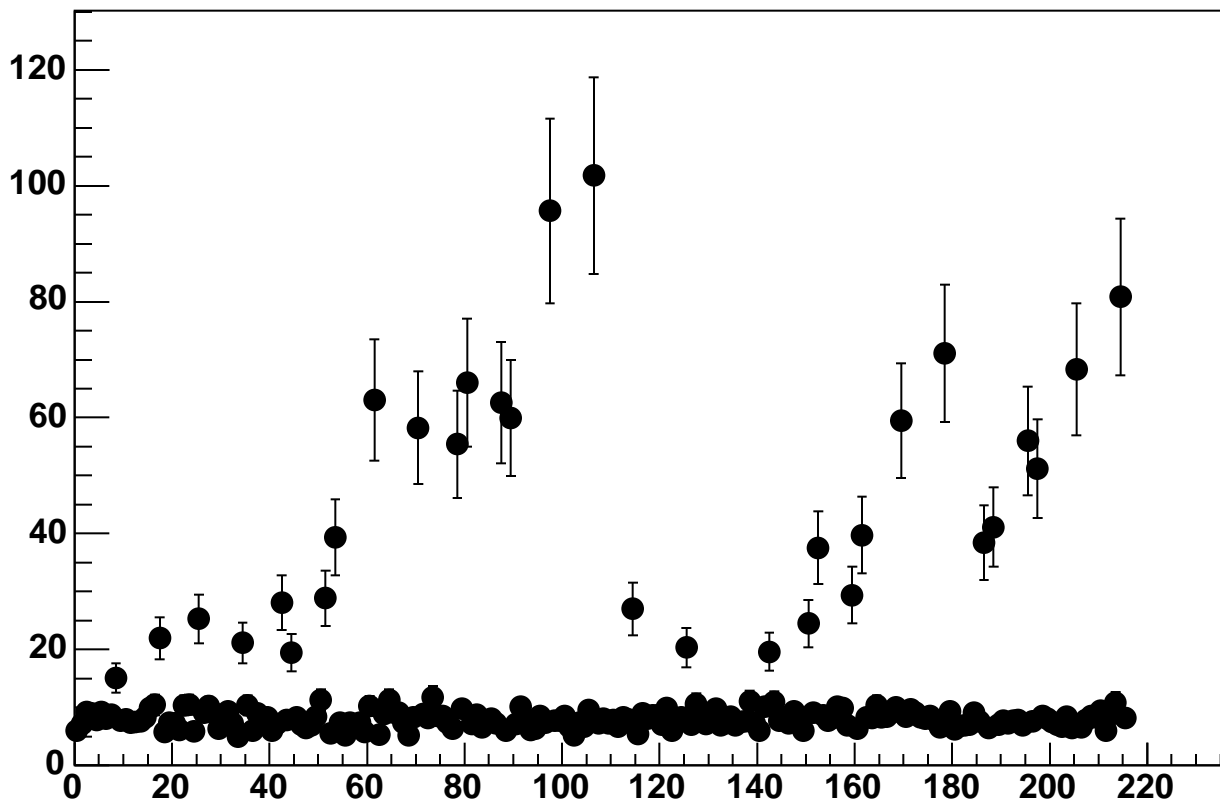
Enable 2, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



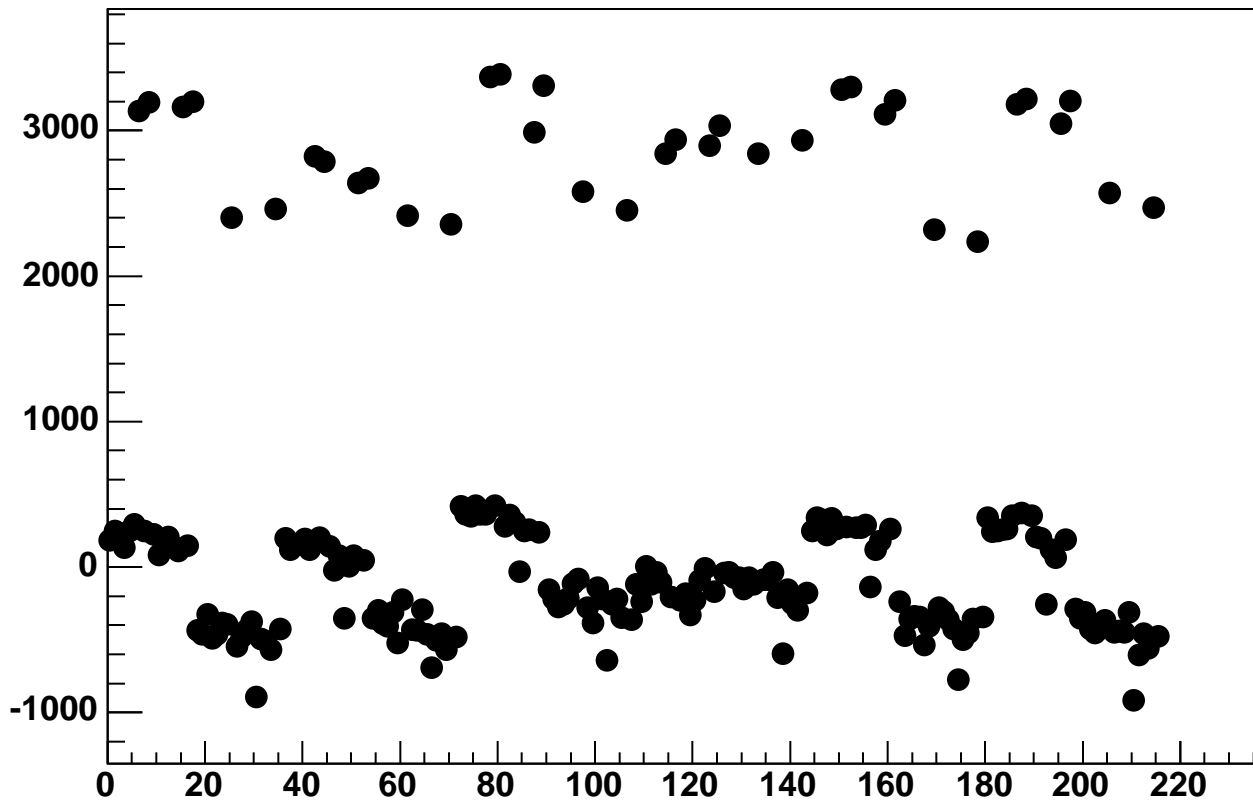
Enable 2, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



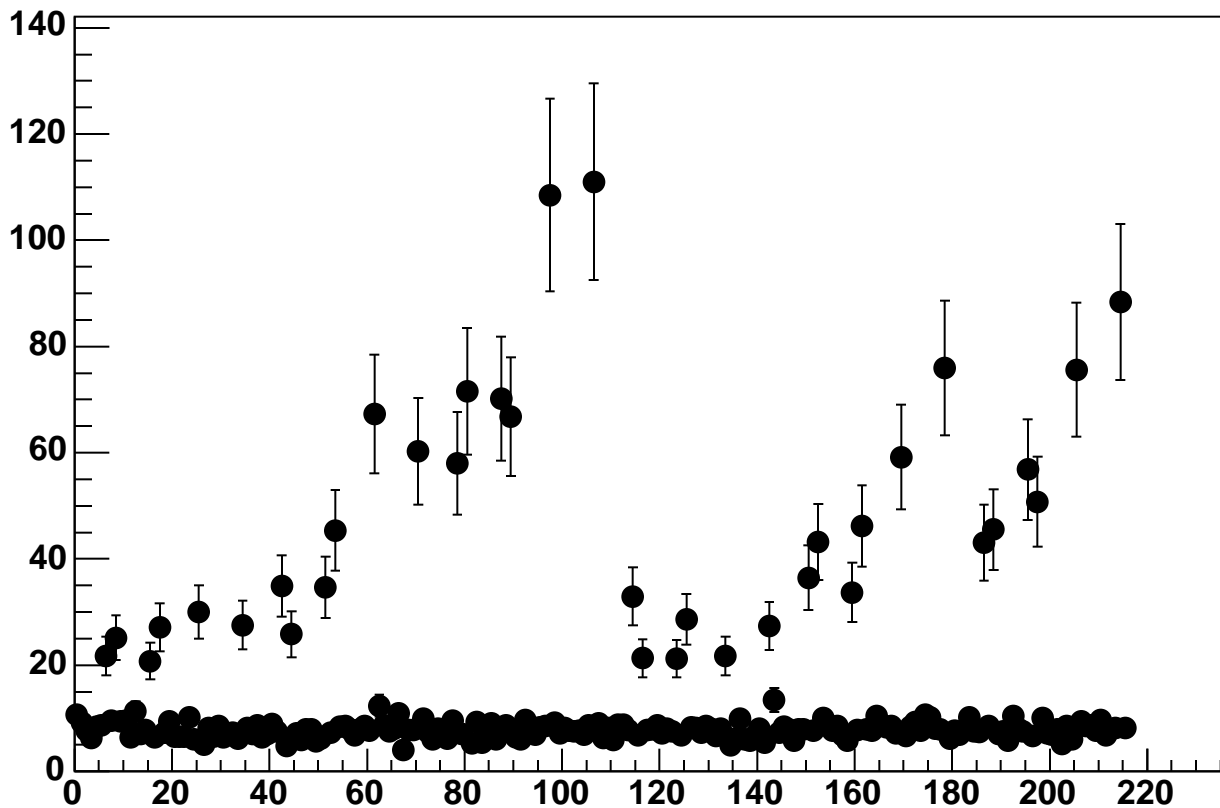
Enable 2, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



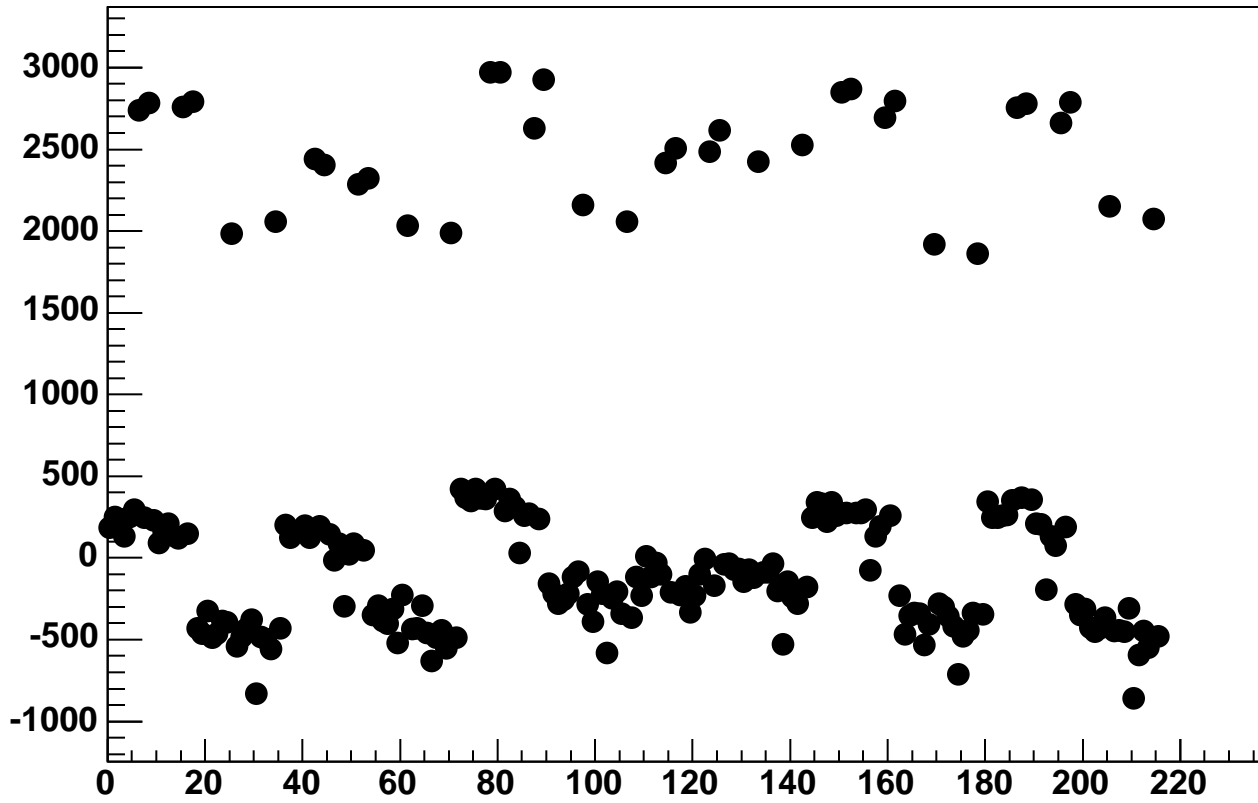
Enable 2, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



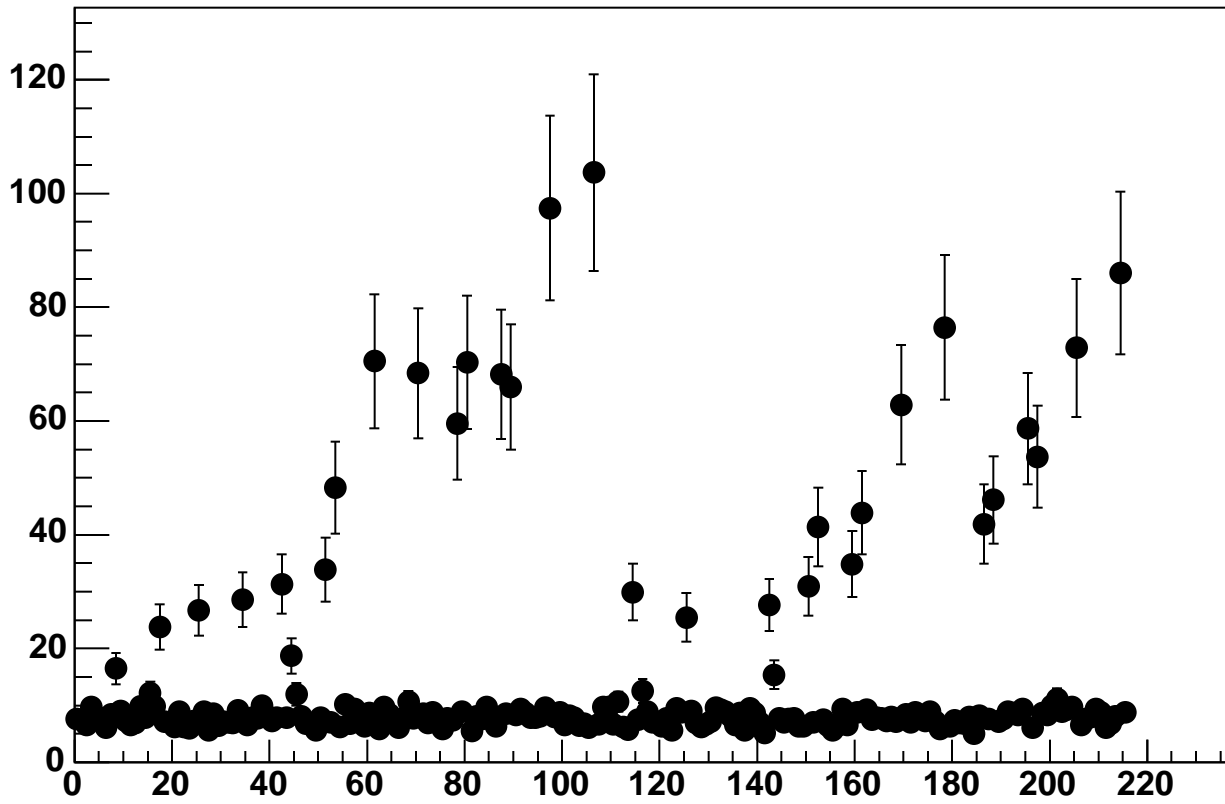
Enable 2, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



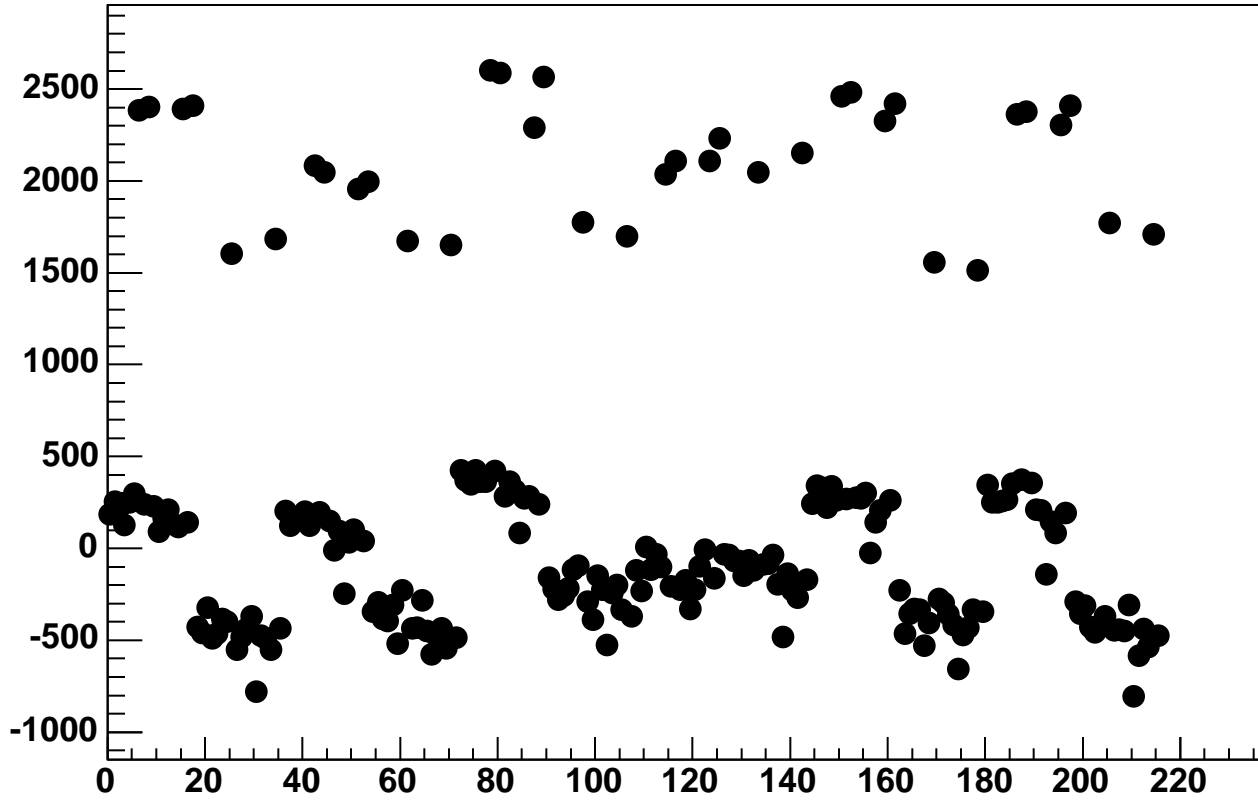
Enable 2, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



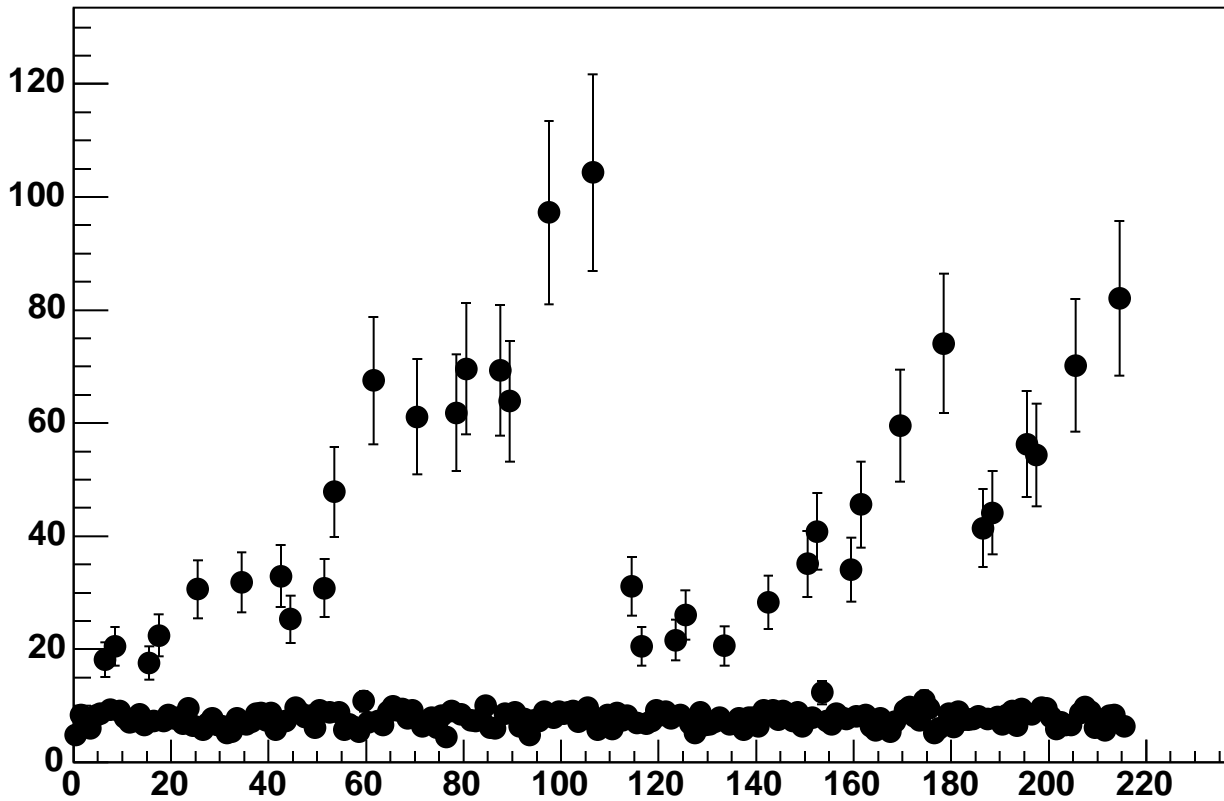
Enable 2, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



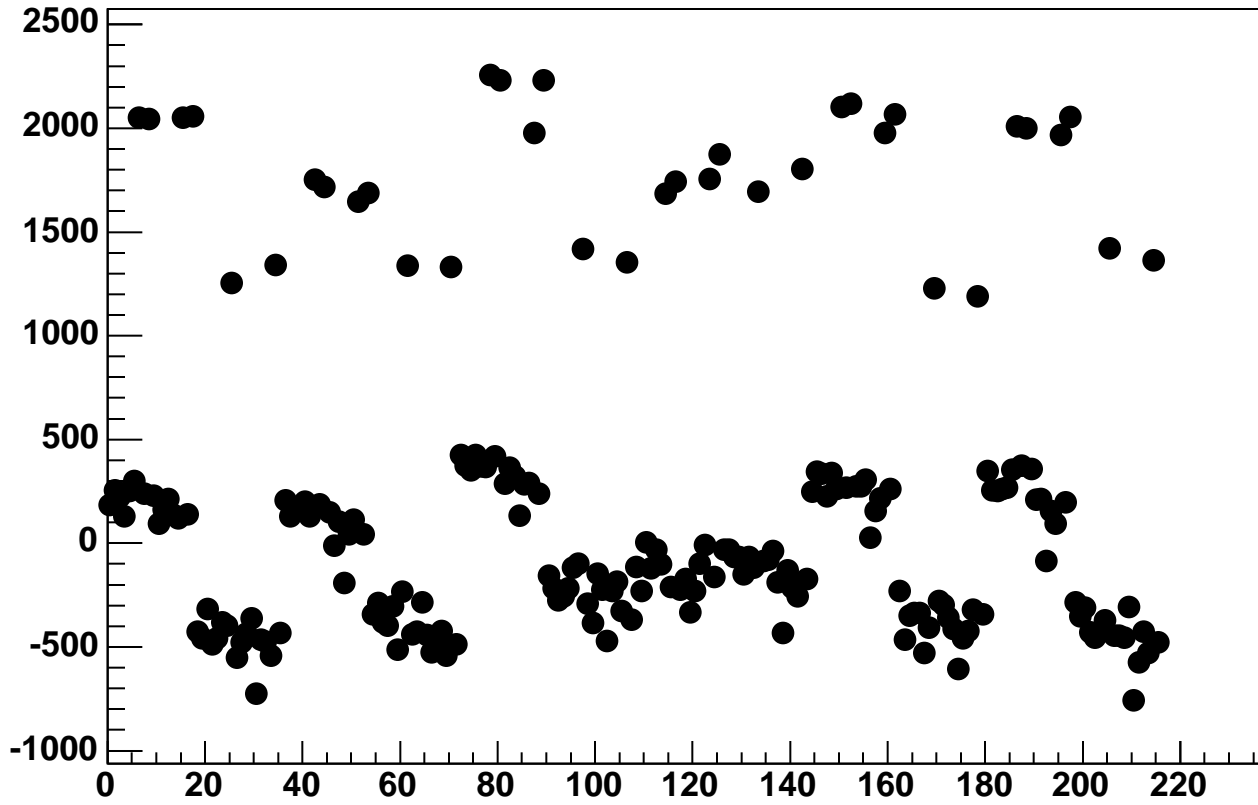
Enable 2, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



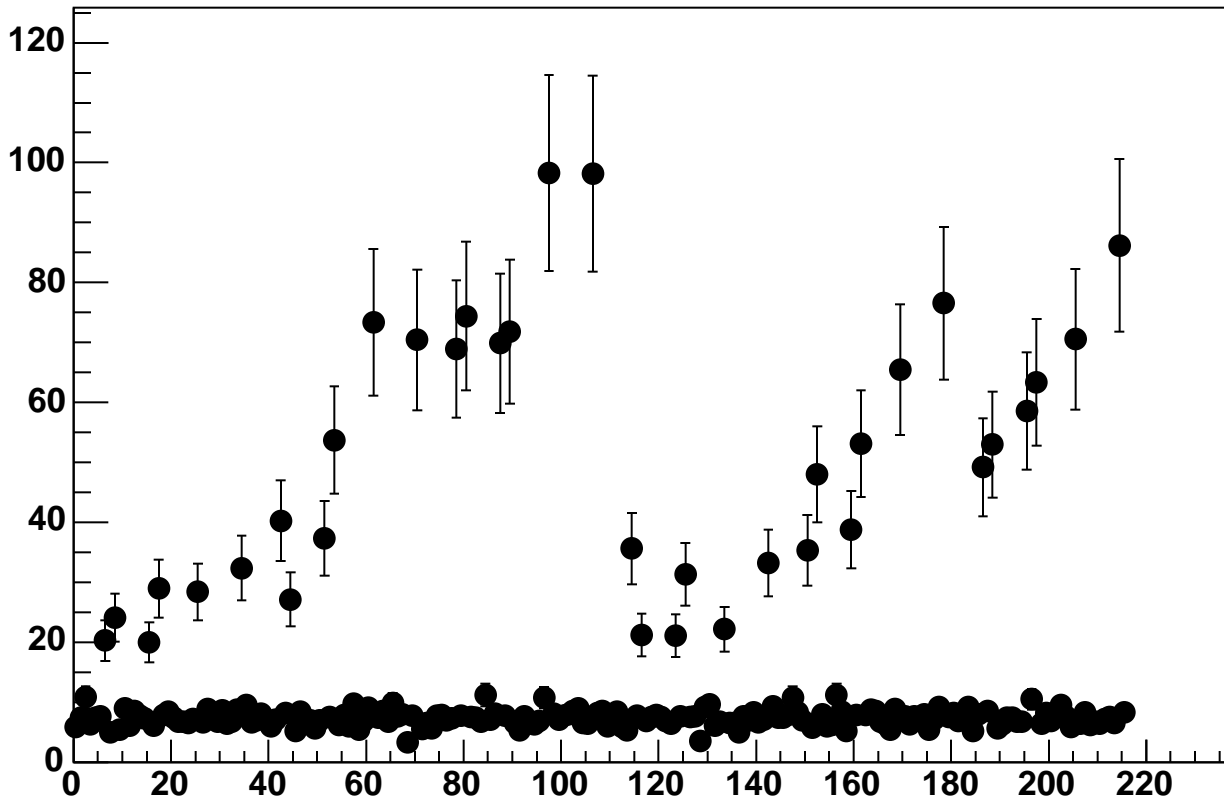
Enable 2, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



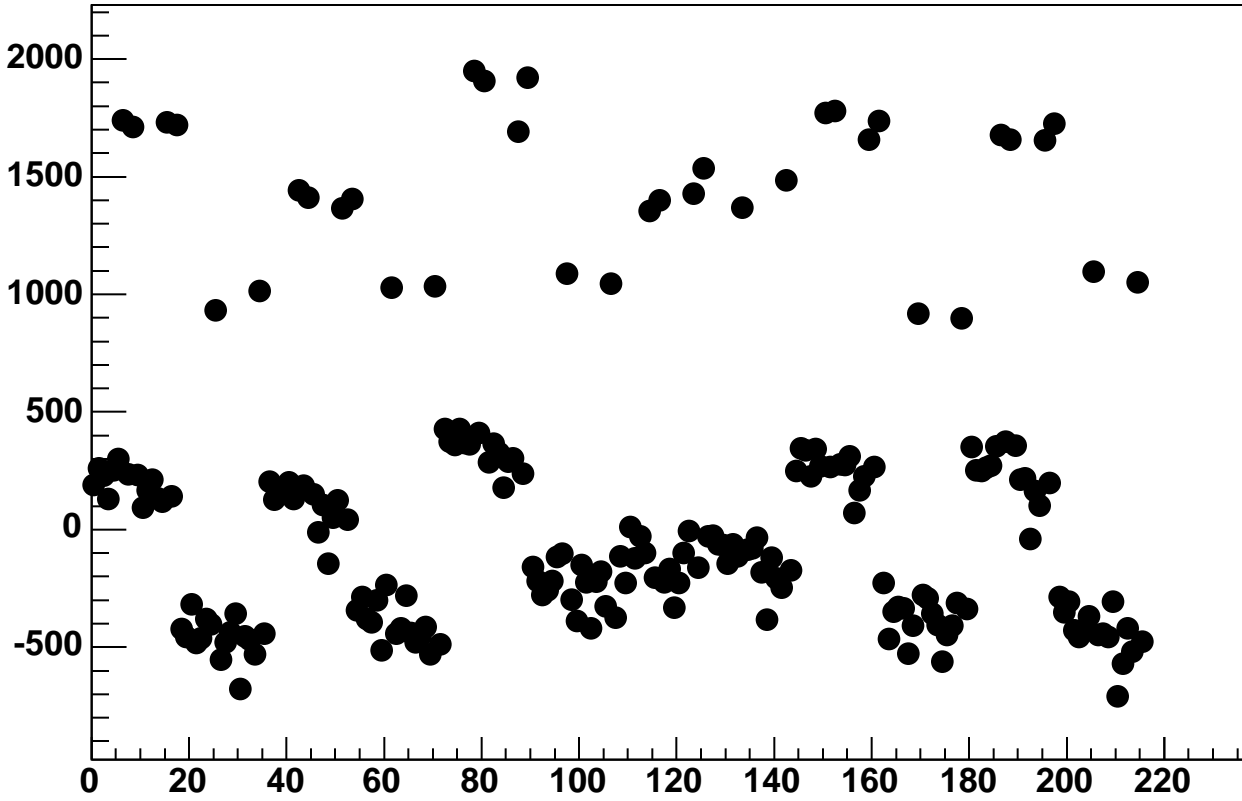
Enable 2, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



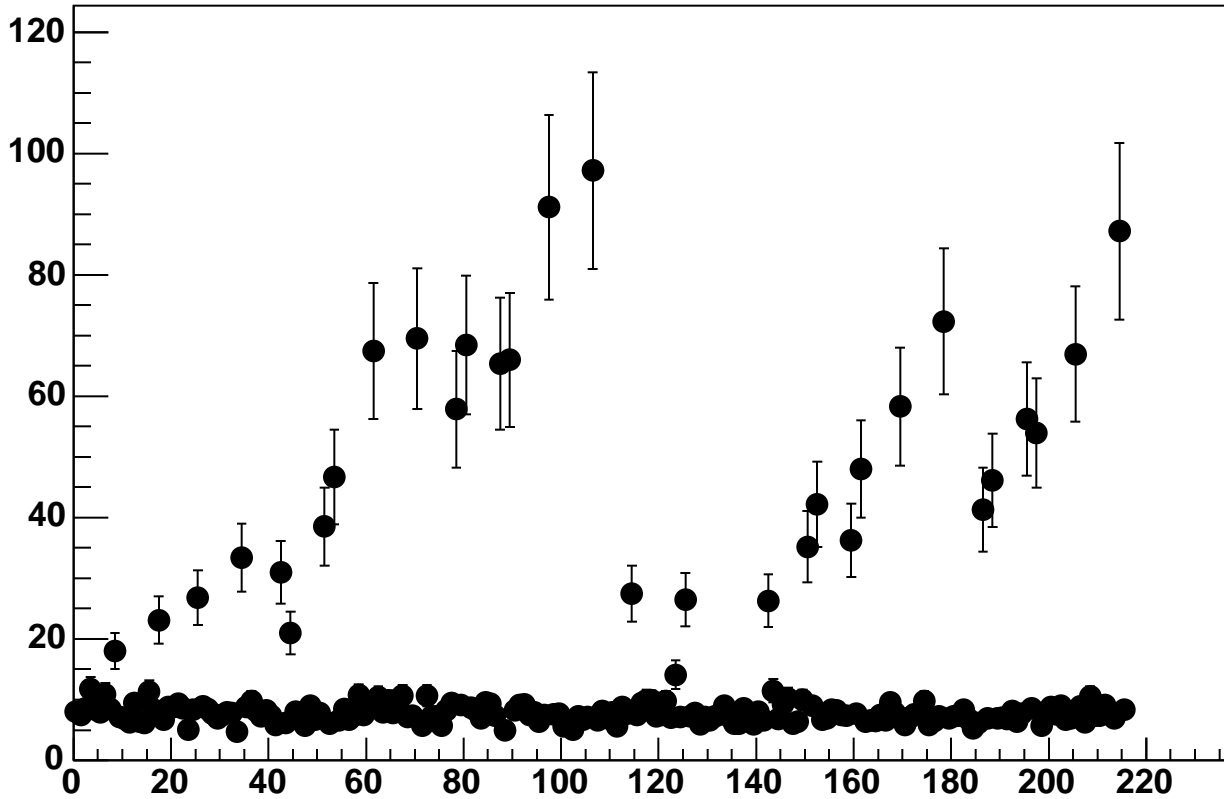
Enable 2, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



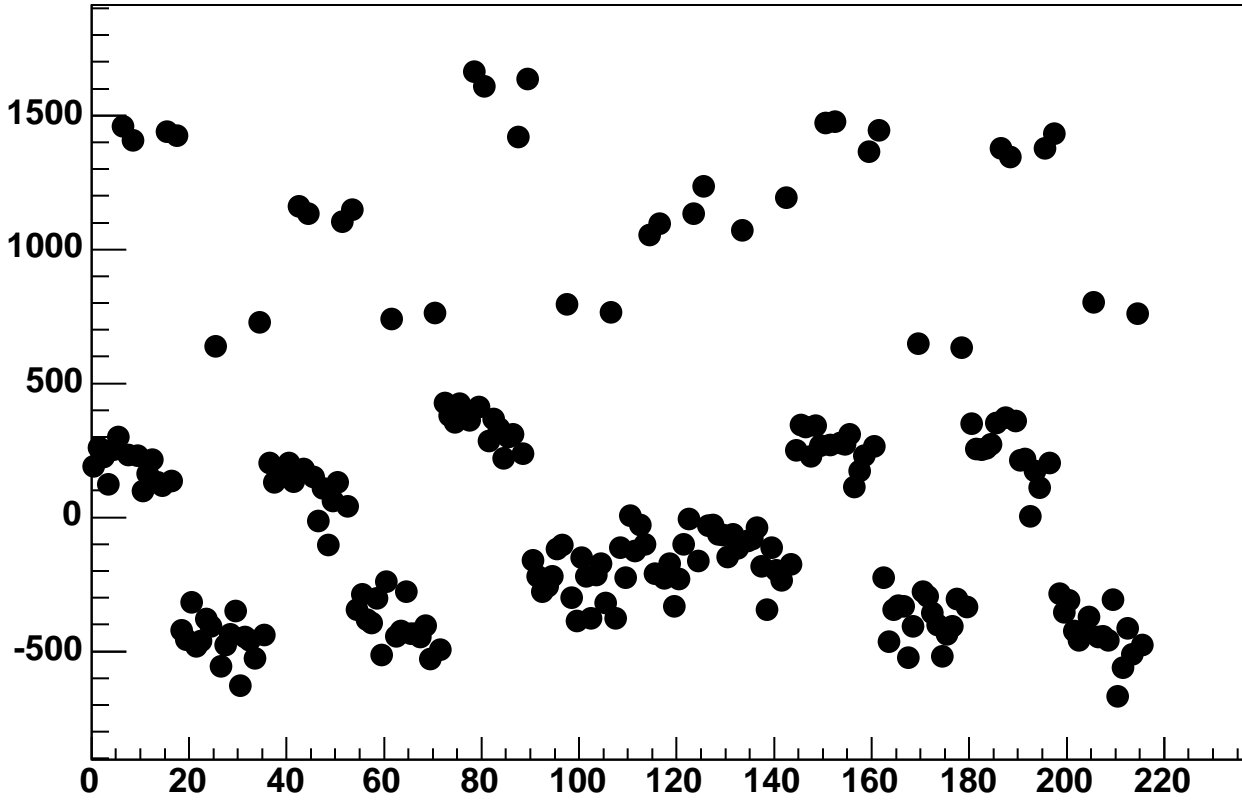
Enable 2, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



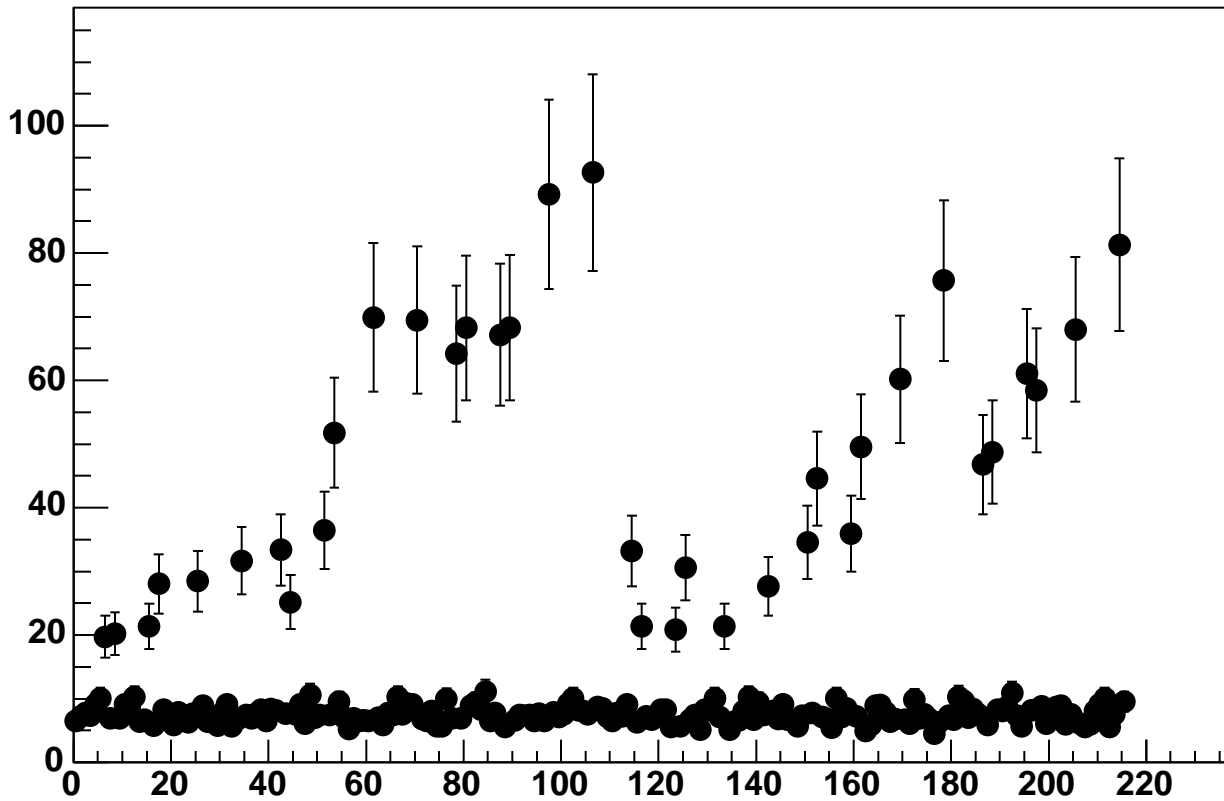
Enable 2, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



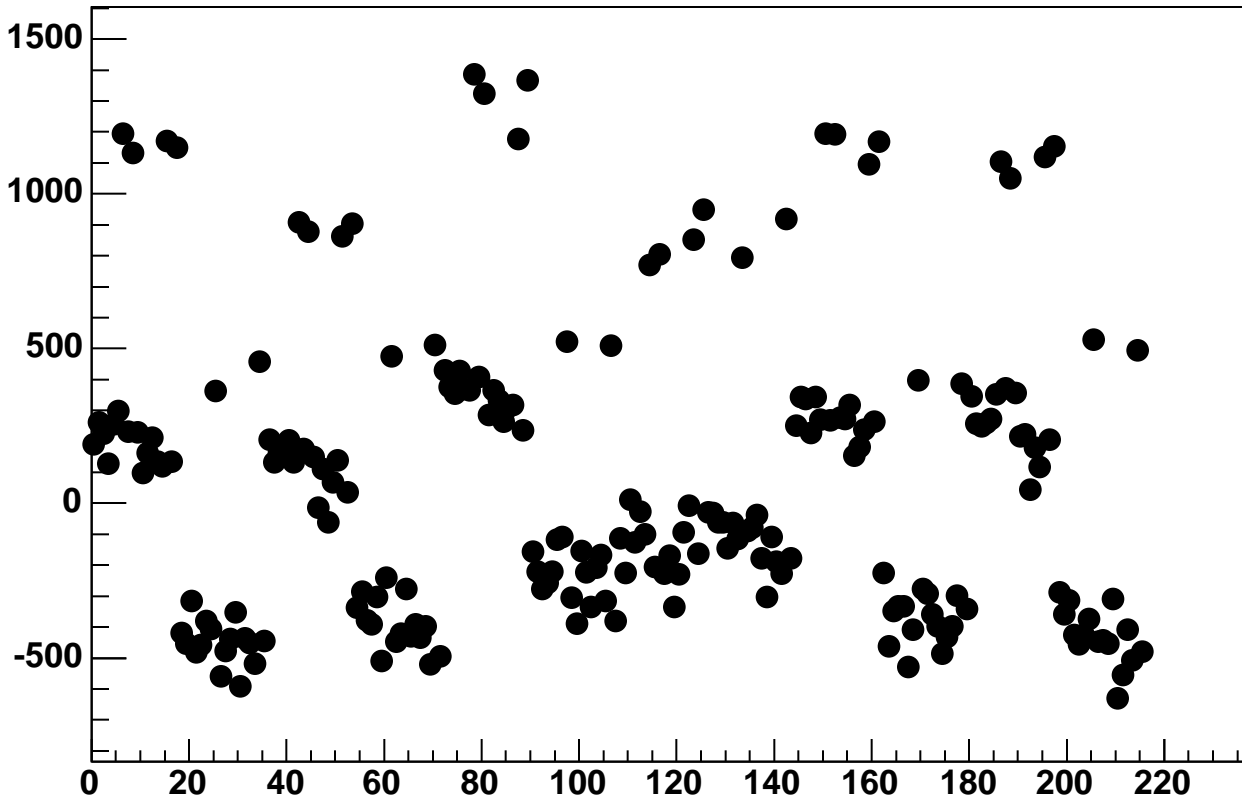
Enable 2, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



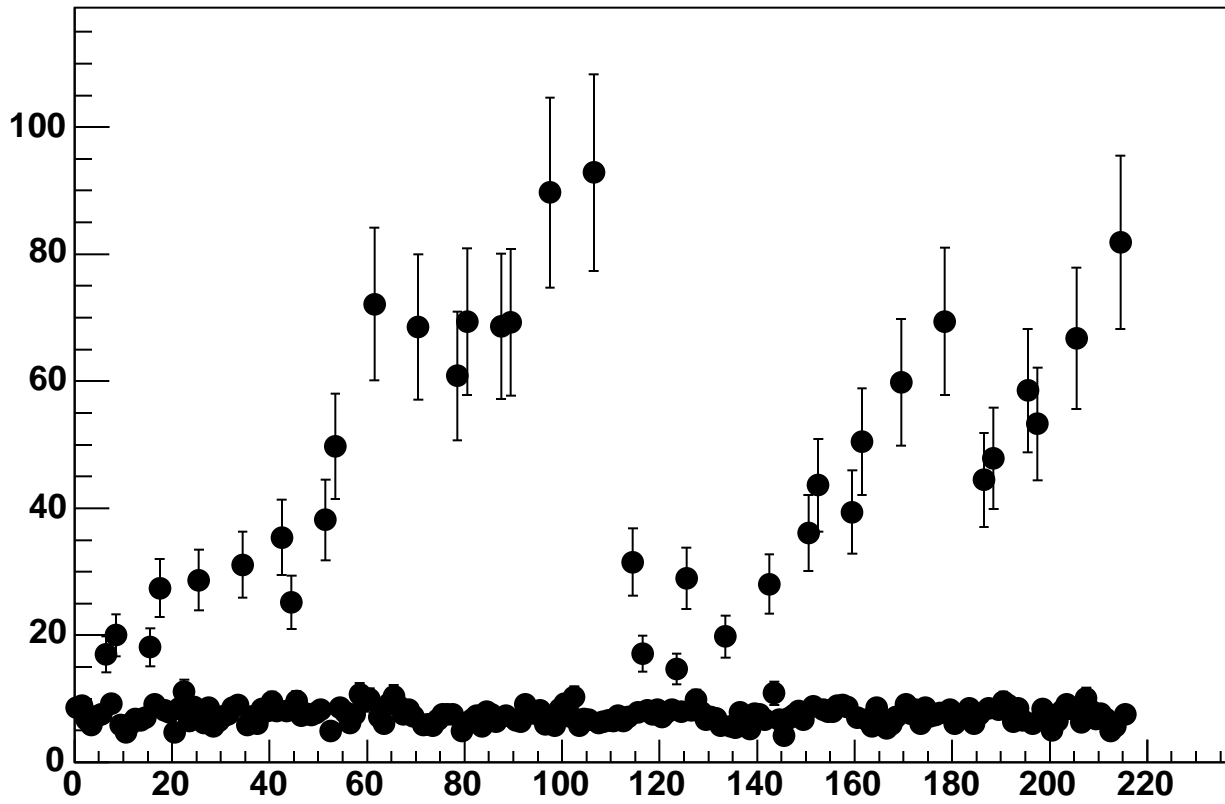
Enable 2, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



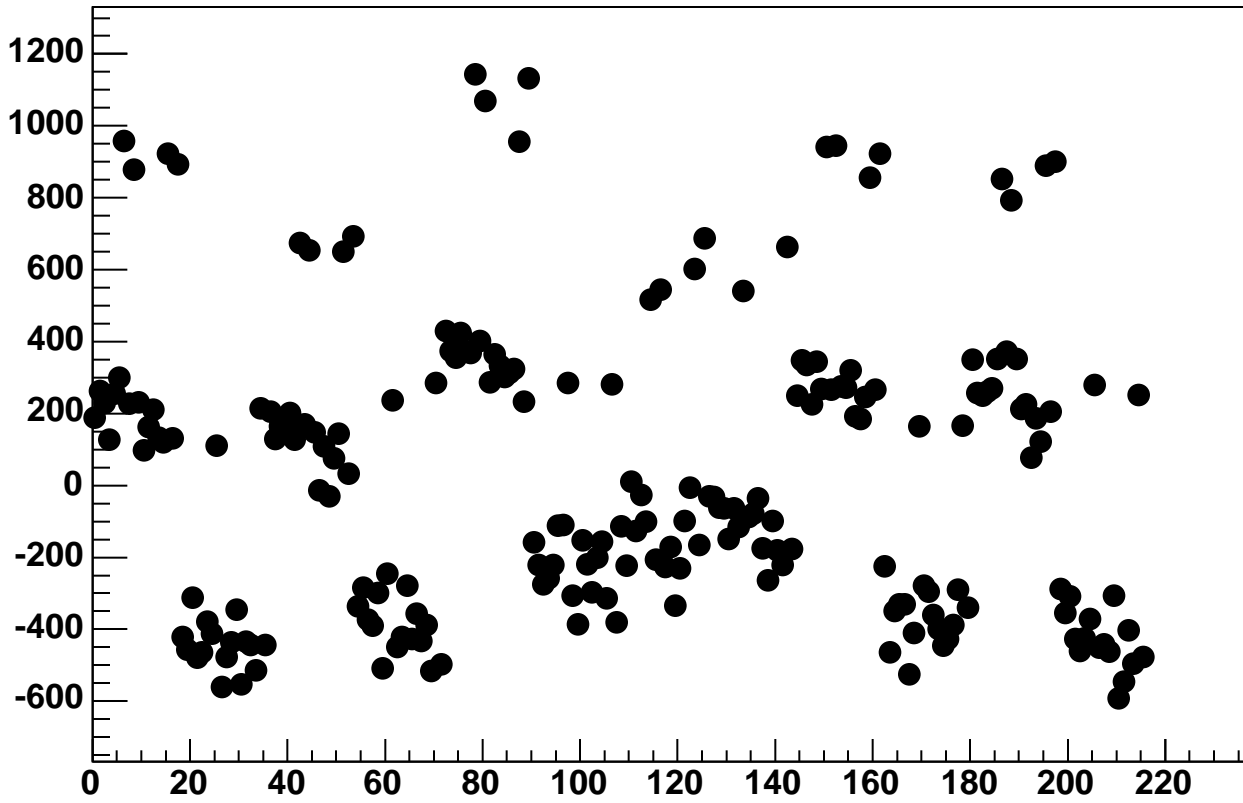
Enable 2, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



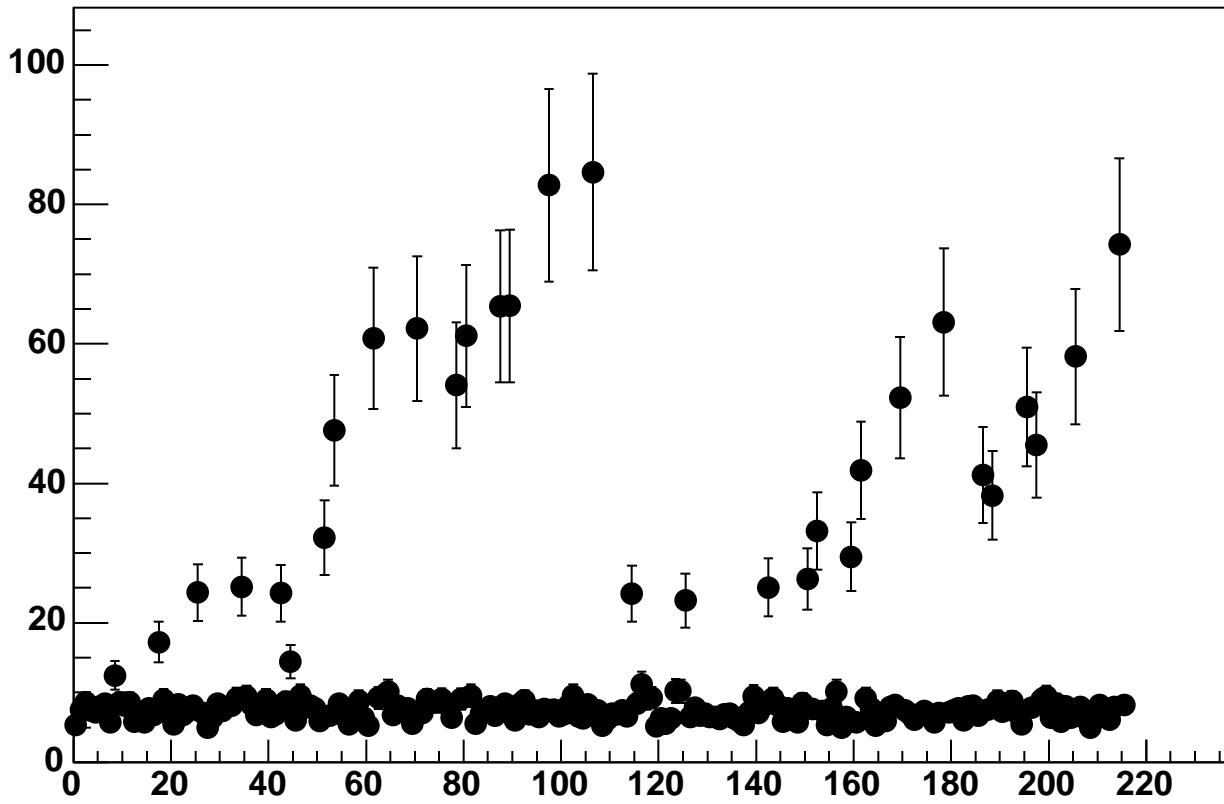
Enable 2, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



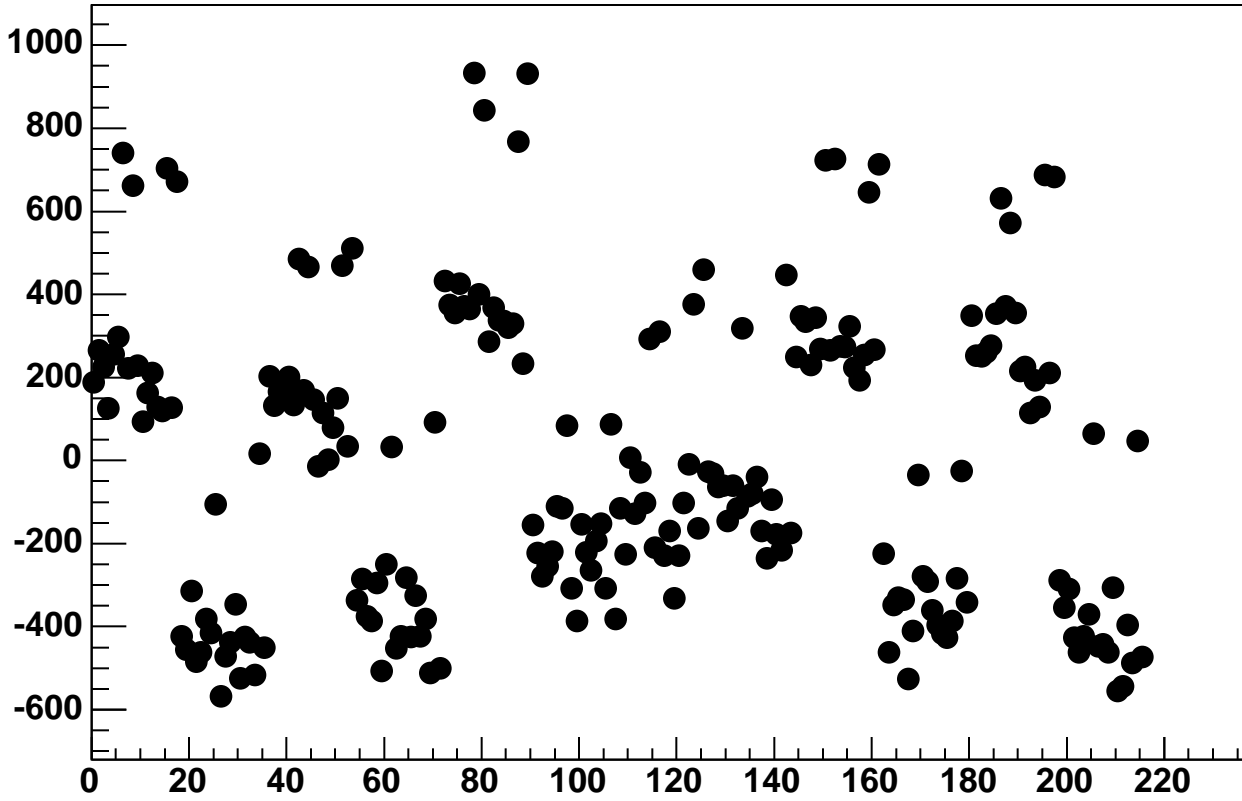
Enable 2, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



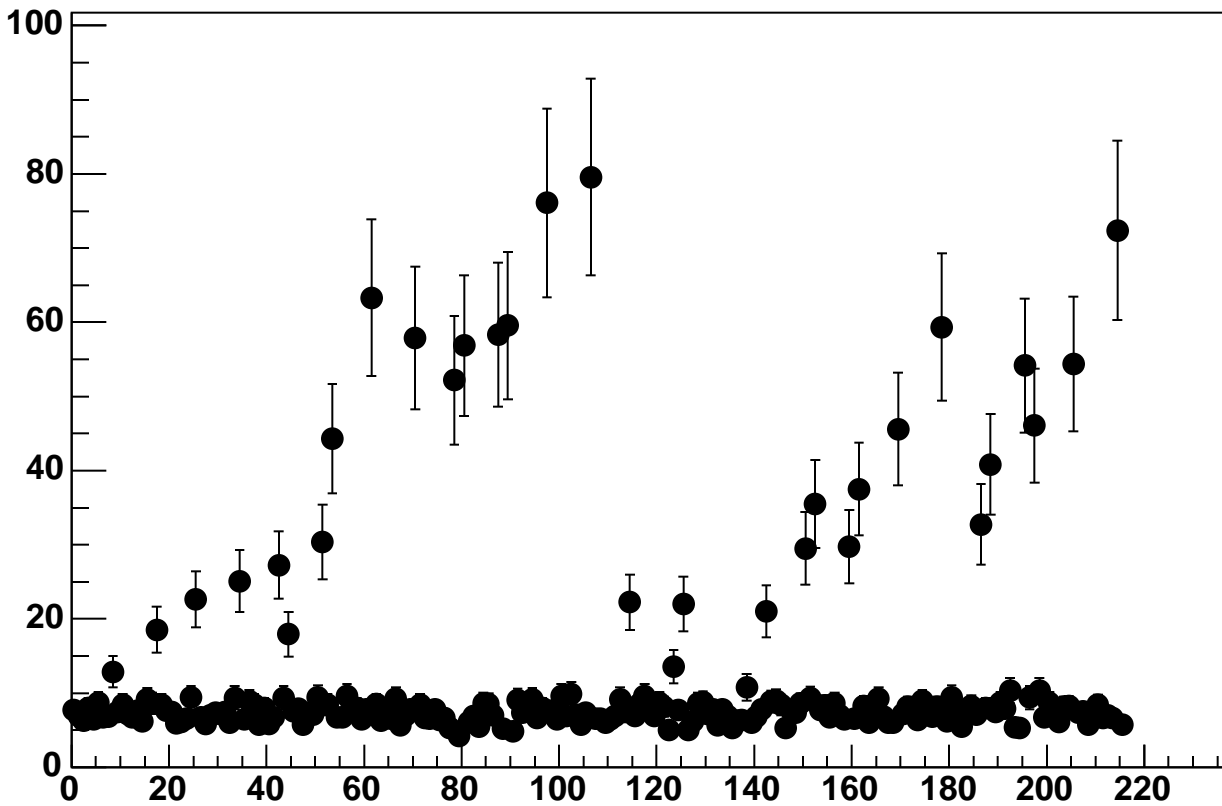
Enable 2, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



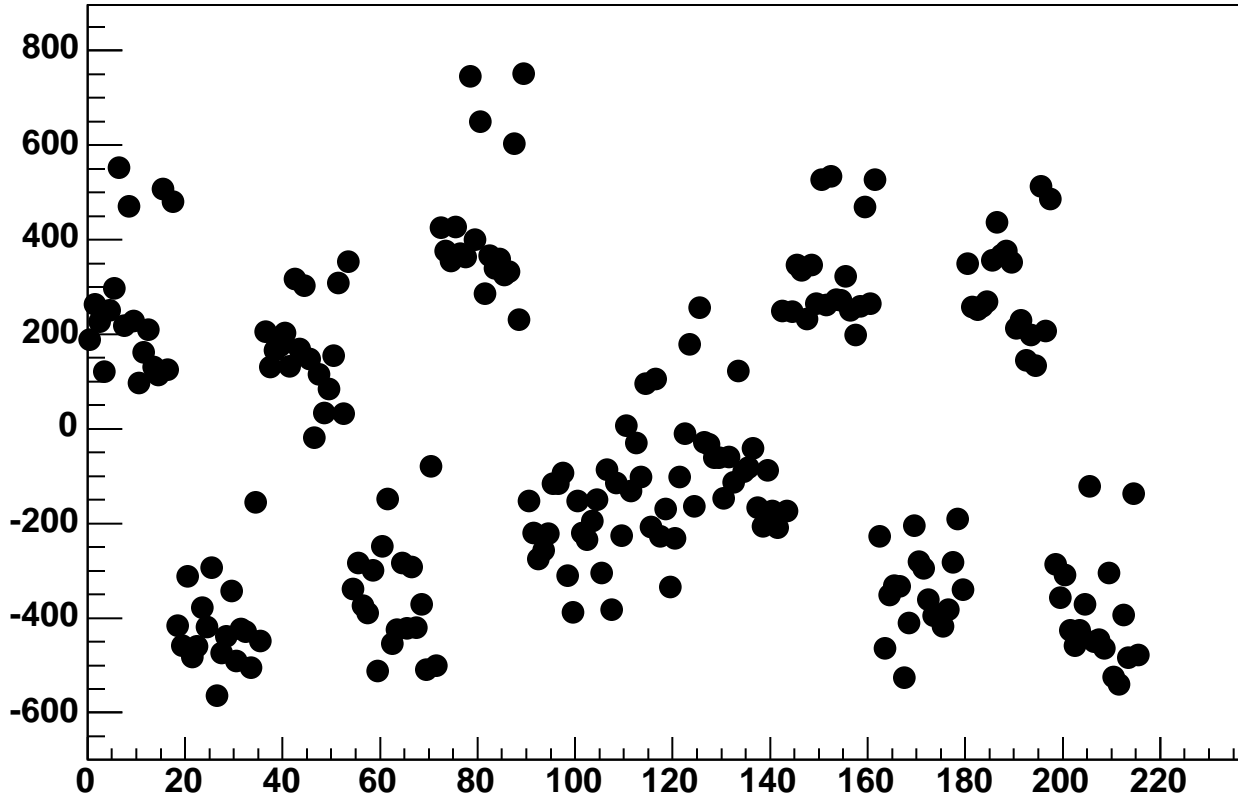
Enable 2, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



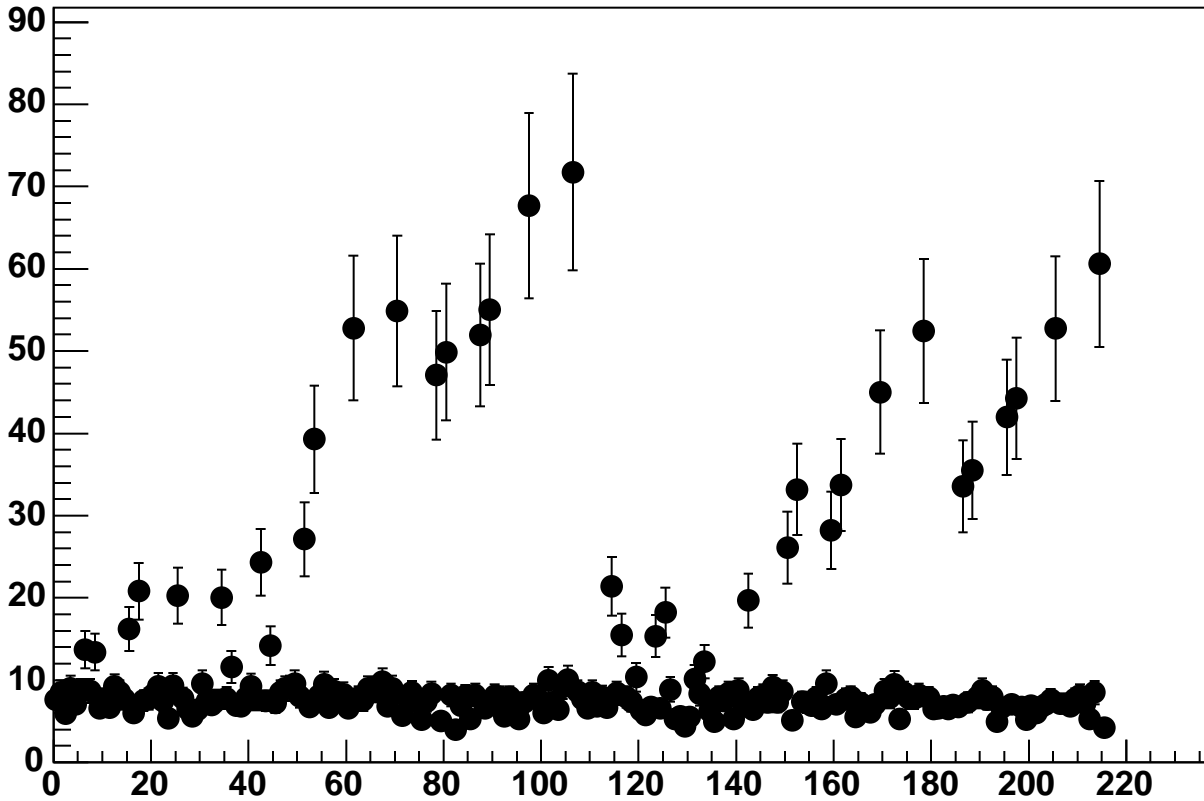
Enable 2, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



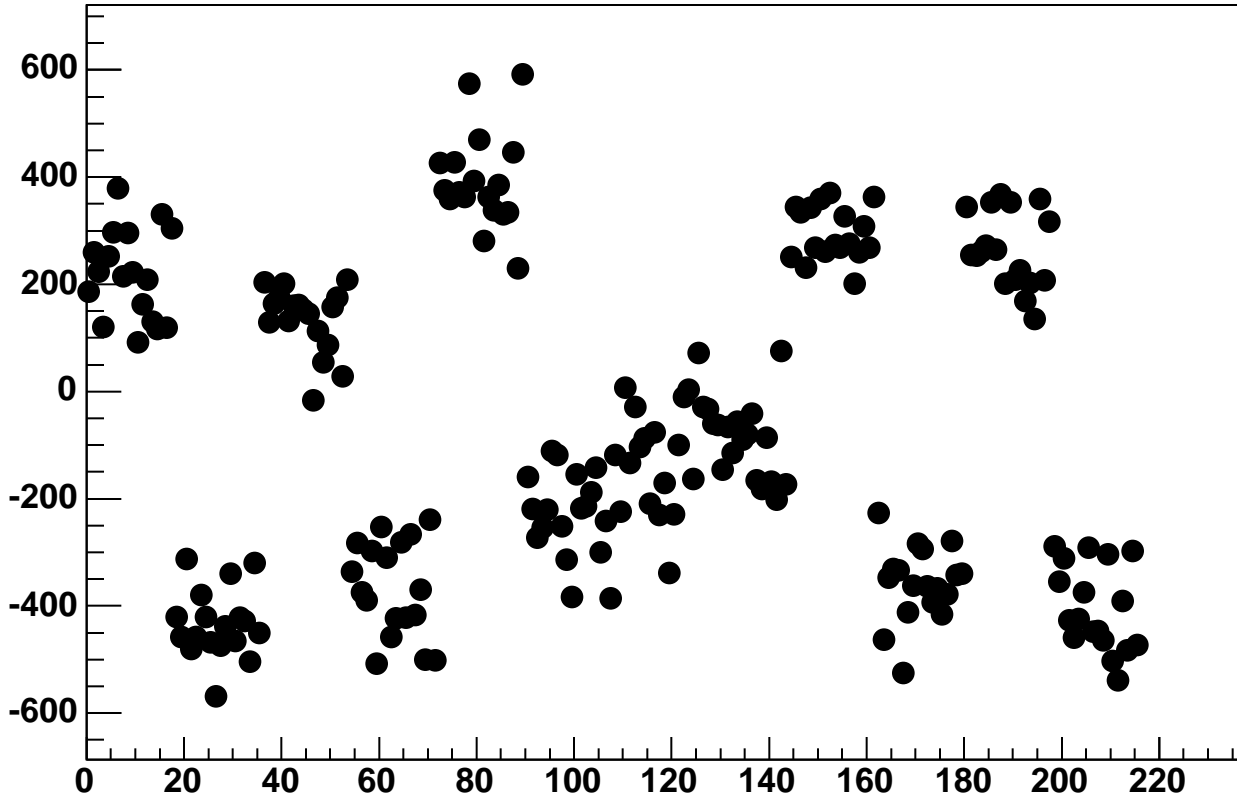
Enable 2, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



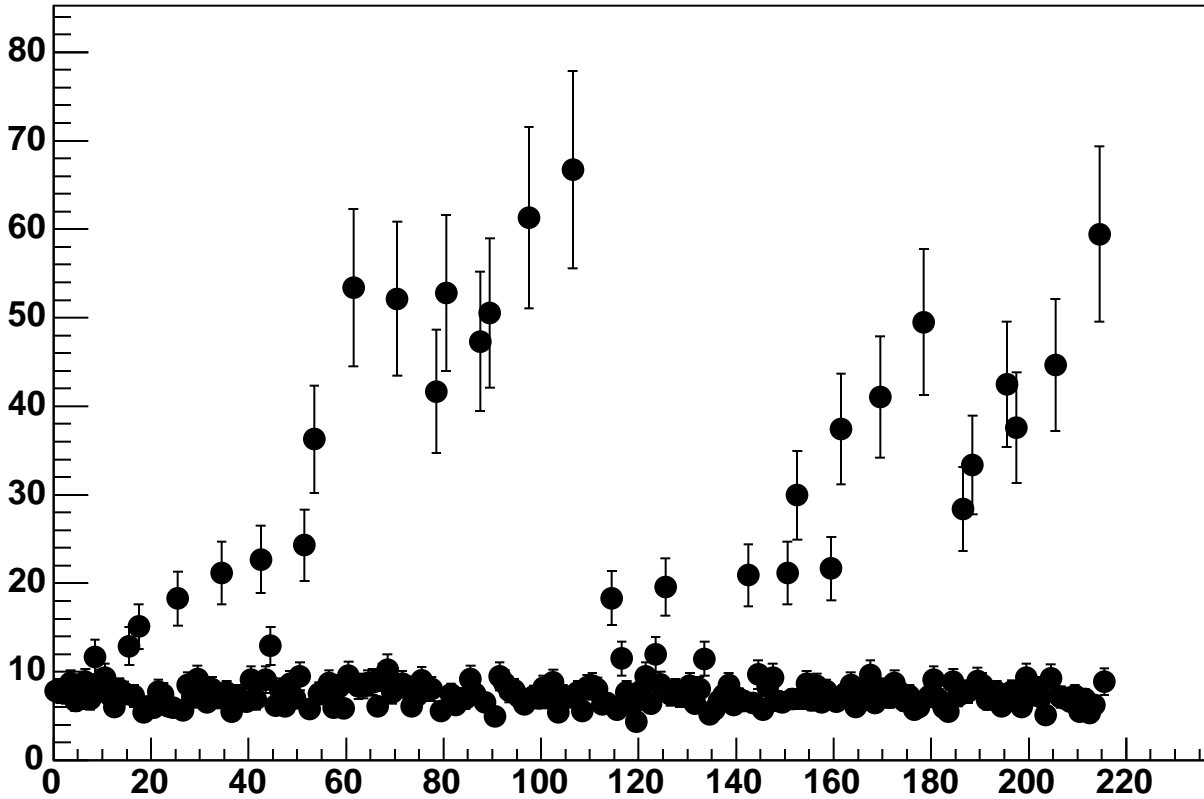
Enable 2, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



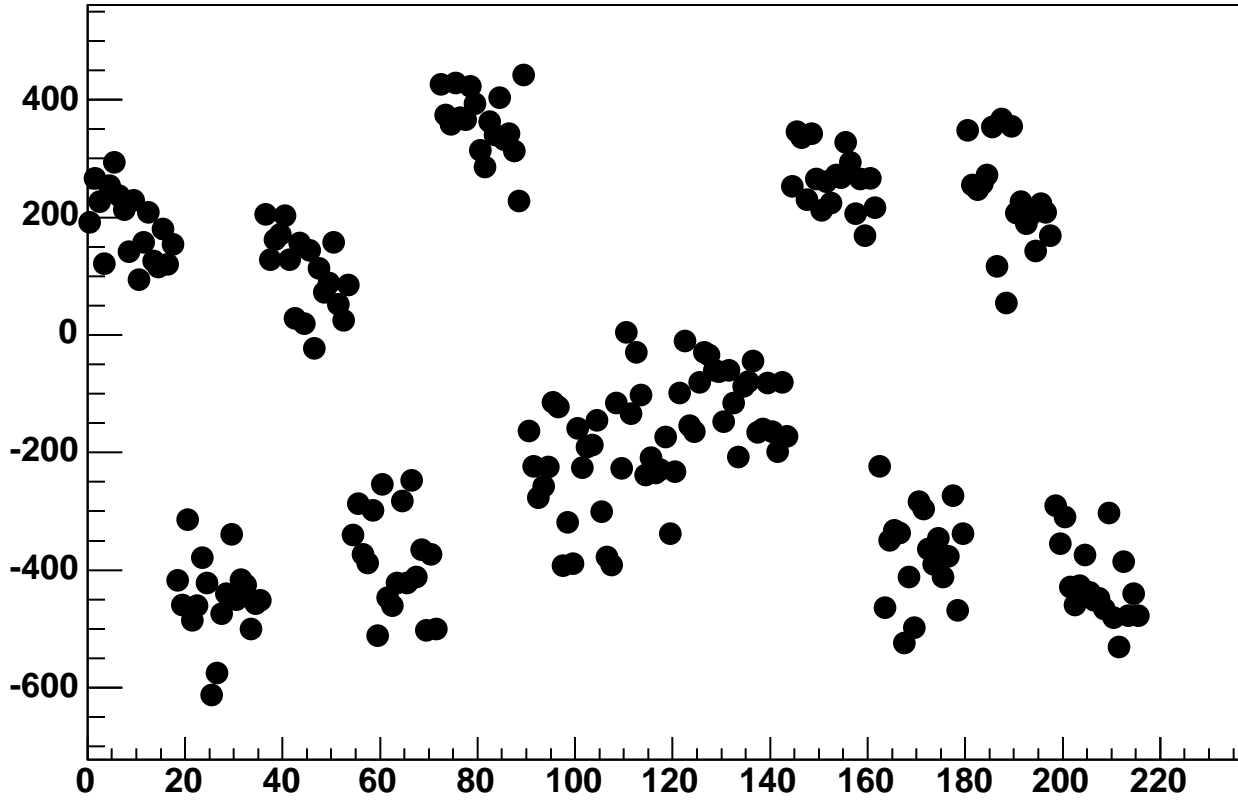
Enable 2, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



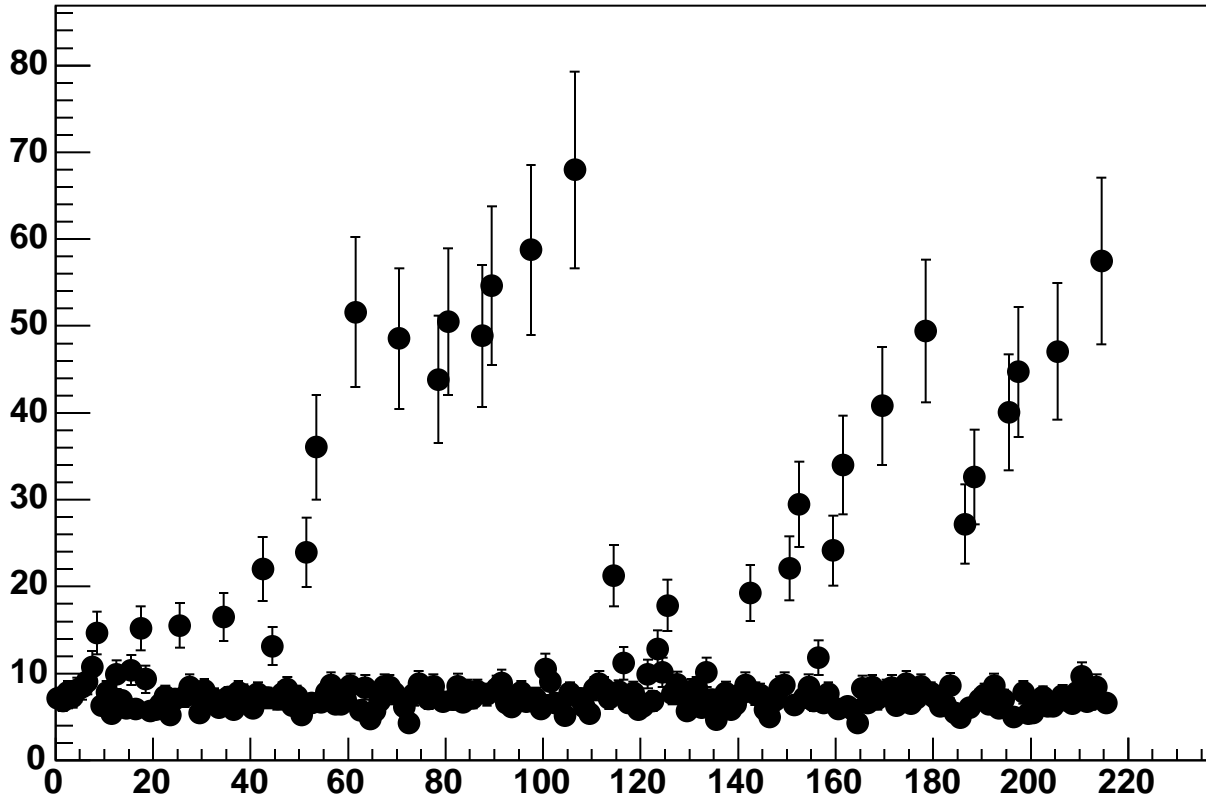
Enable 2, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



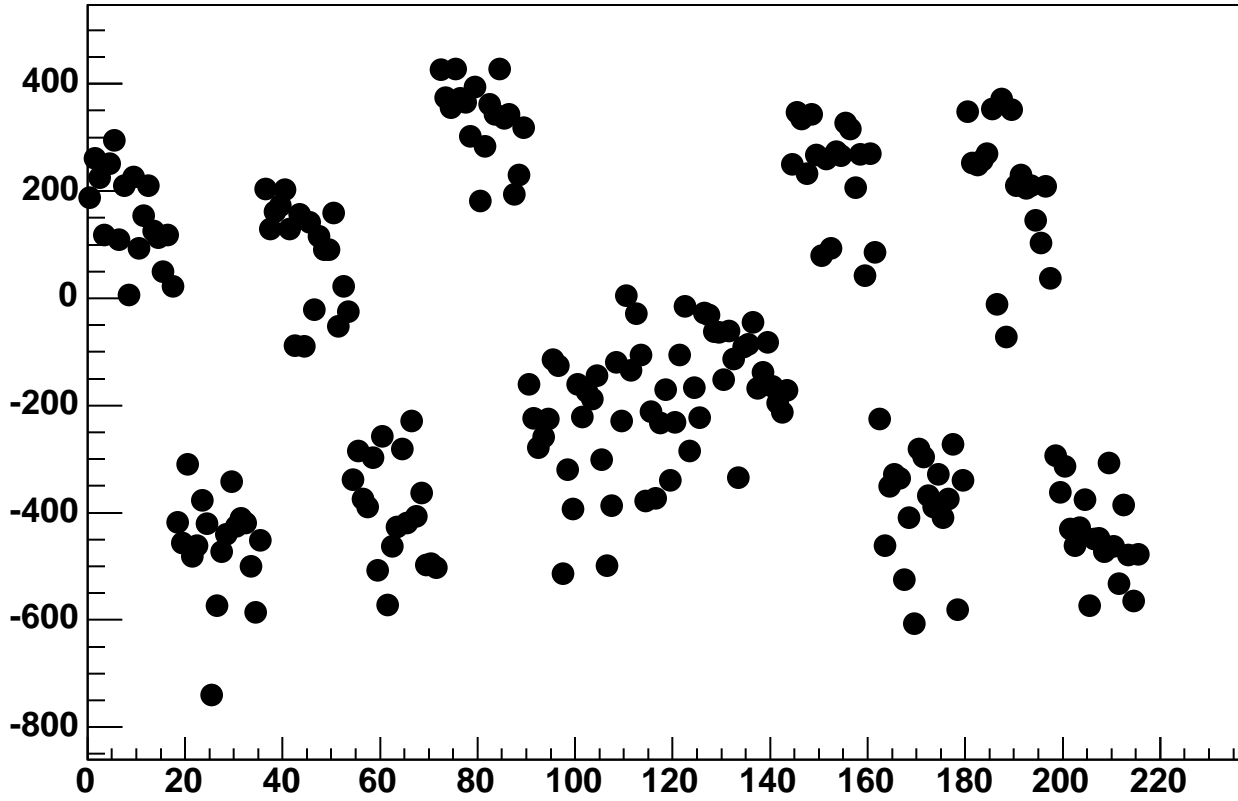
Enable 2, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



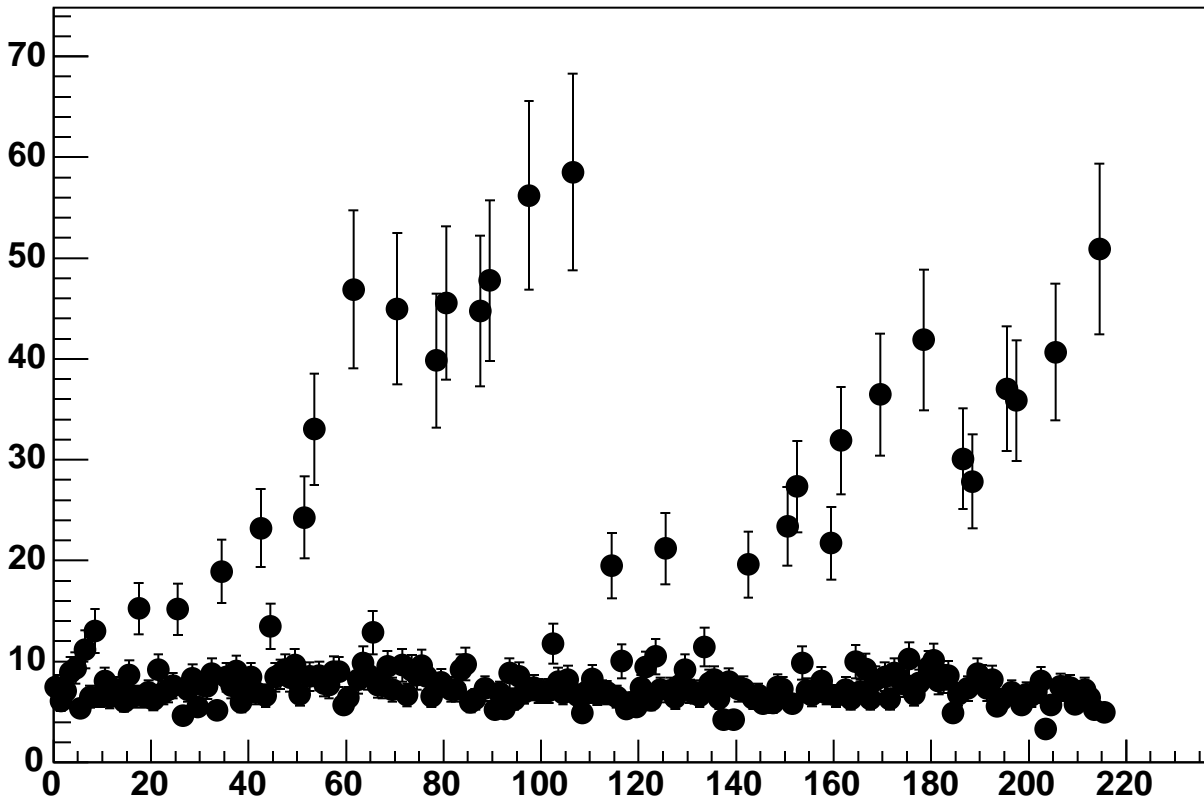
Enable 2, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



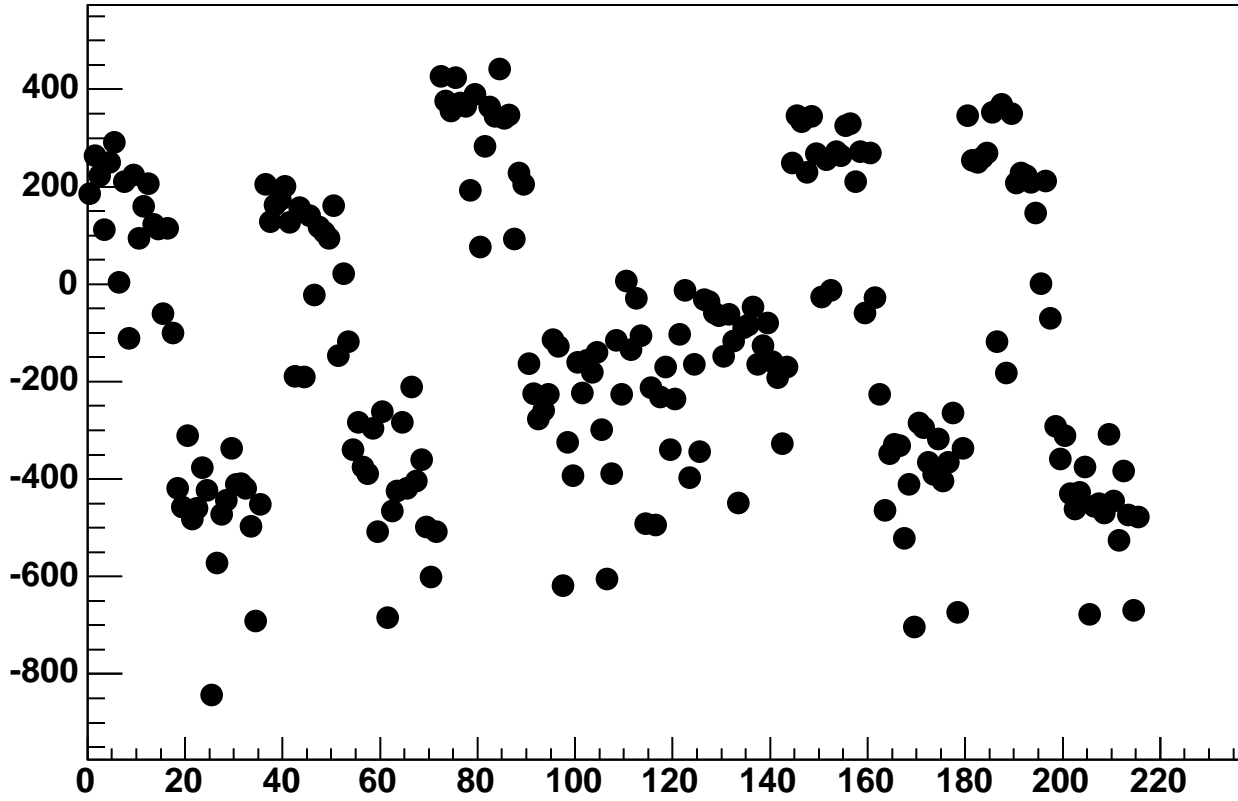
Enable 2, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



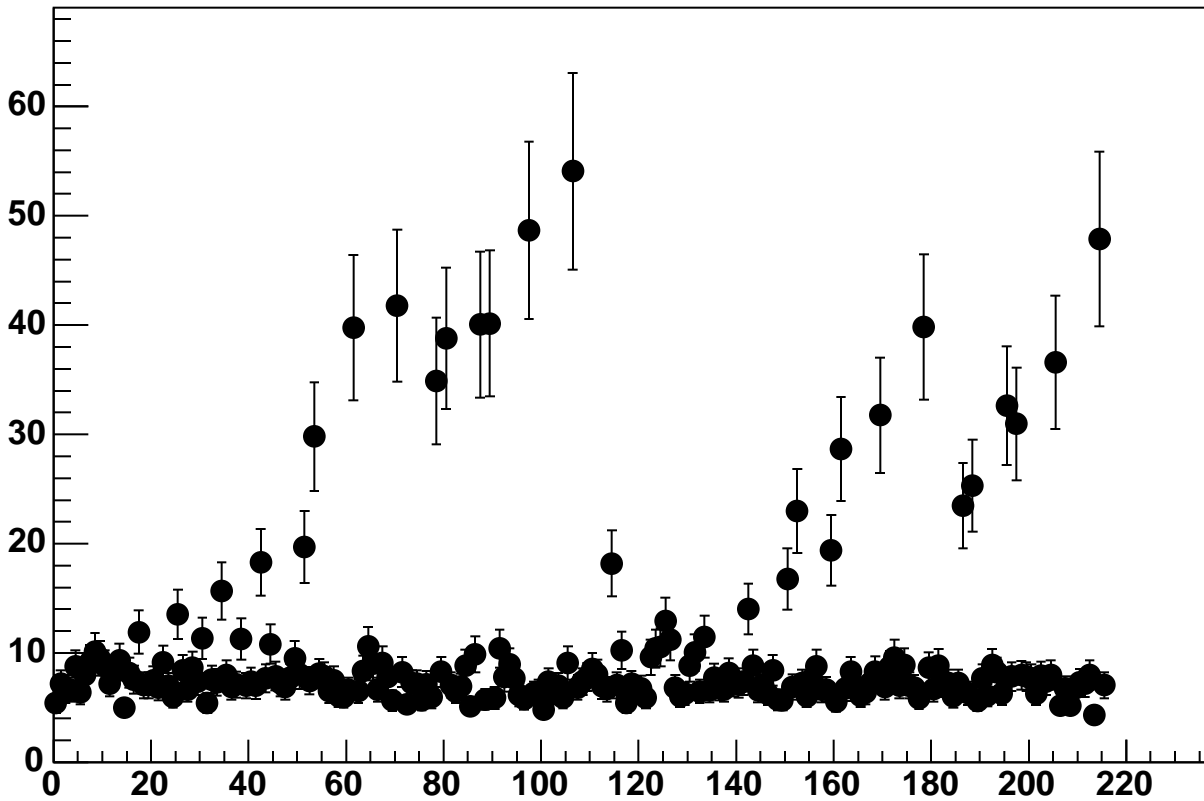
Enable 2, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



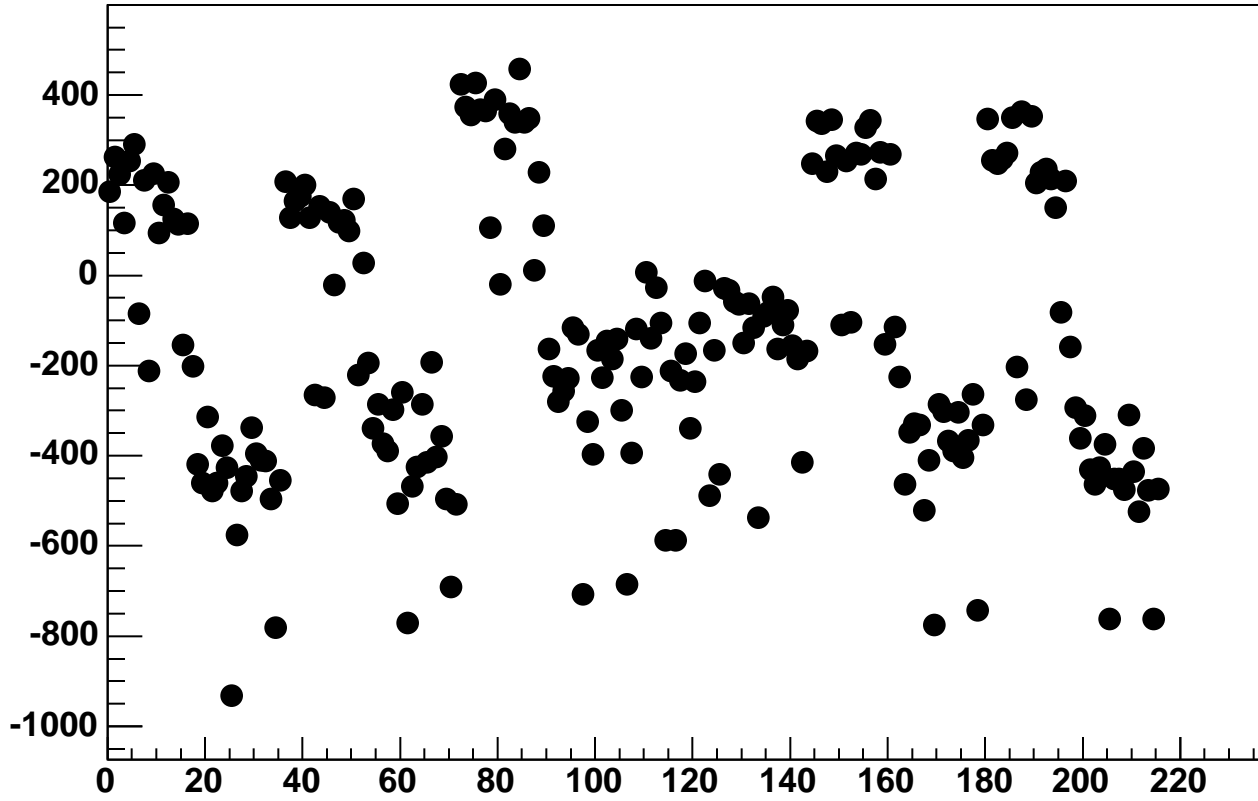
Enable 2, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



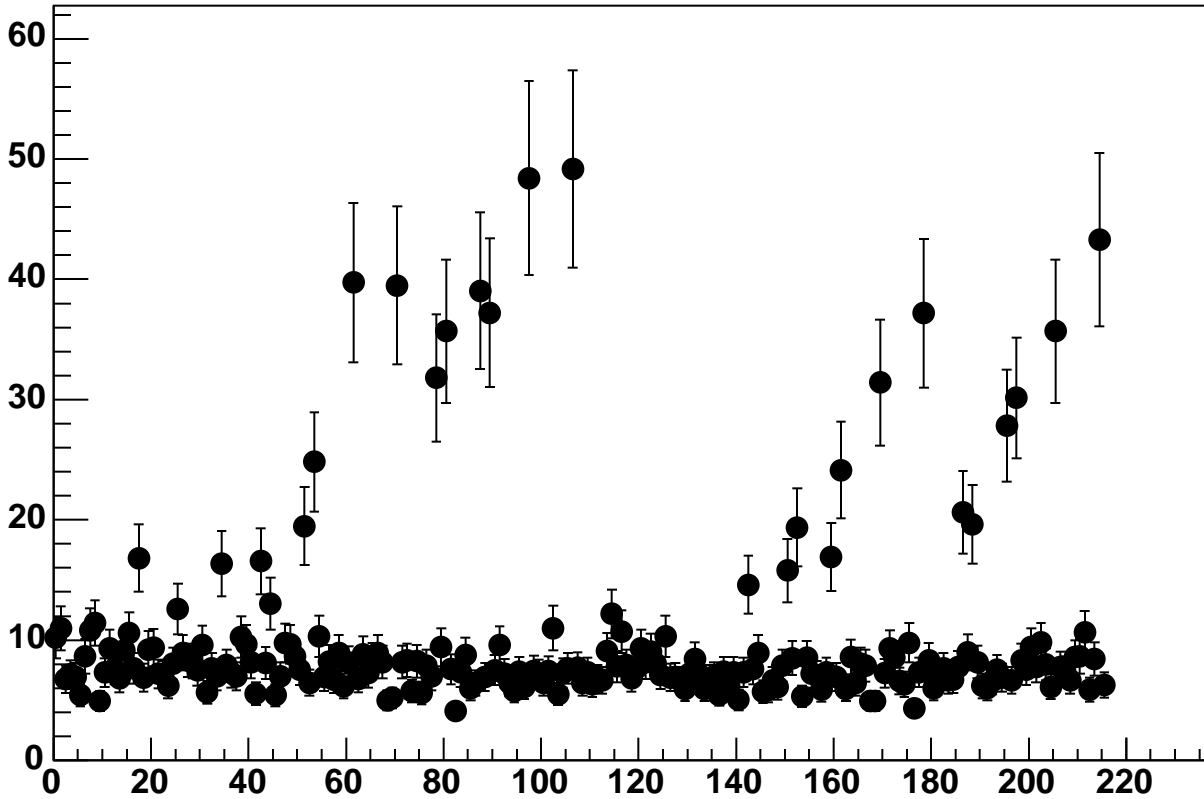
Enable 2, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



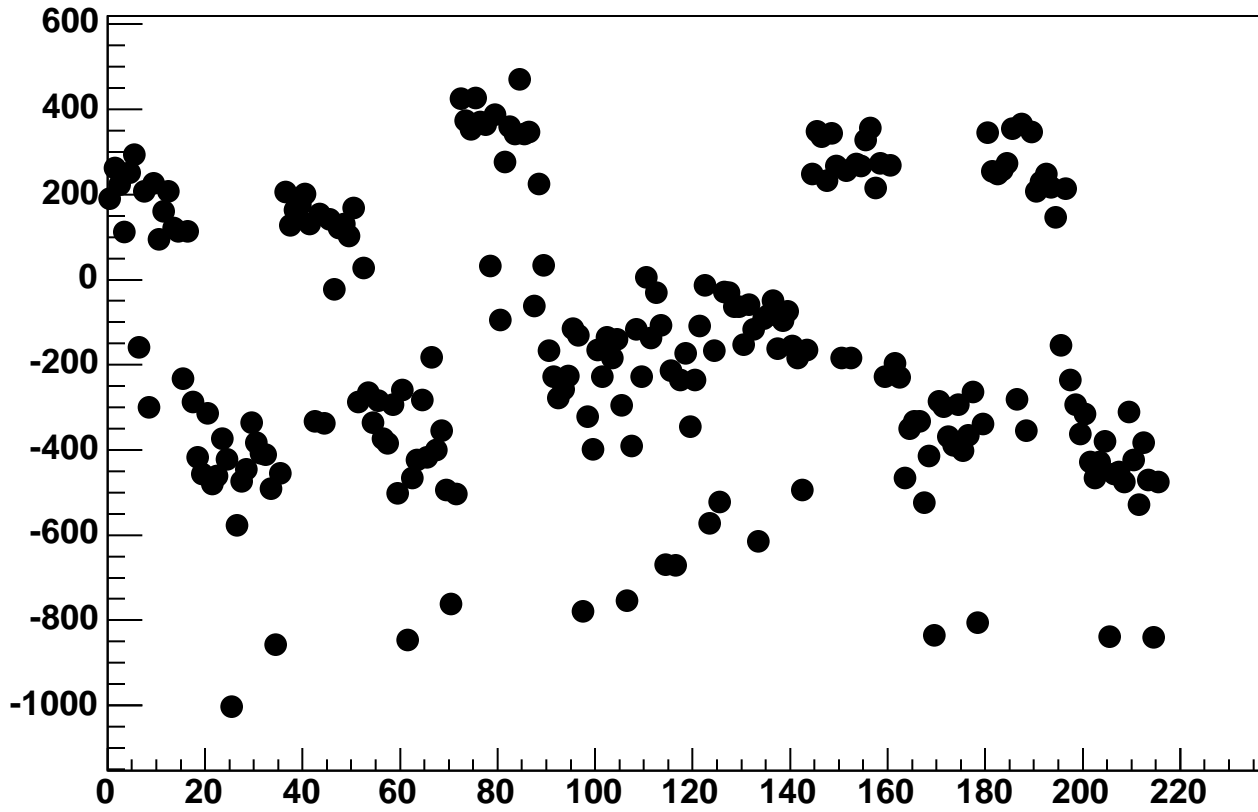
Enable 2, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



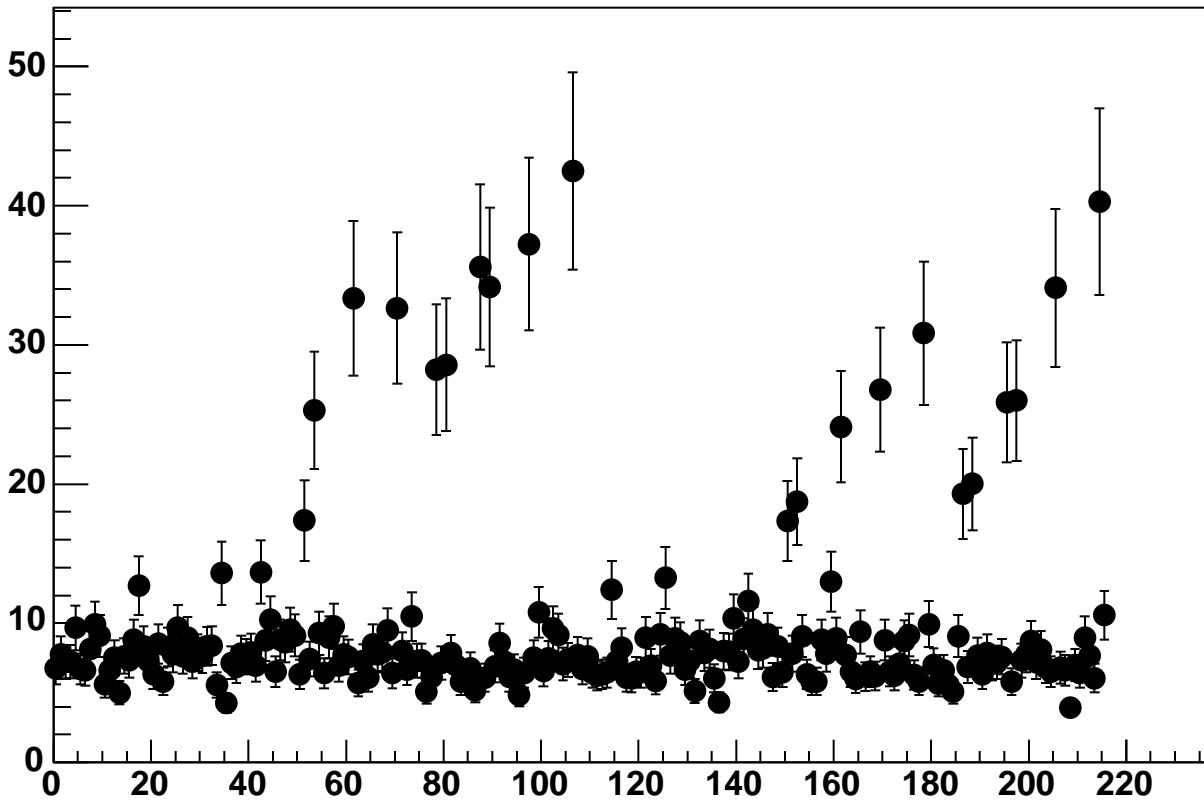
Enable 2, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



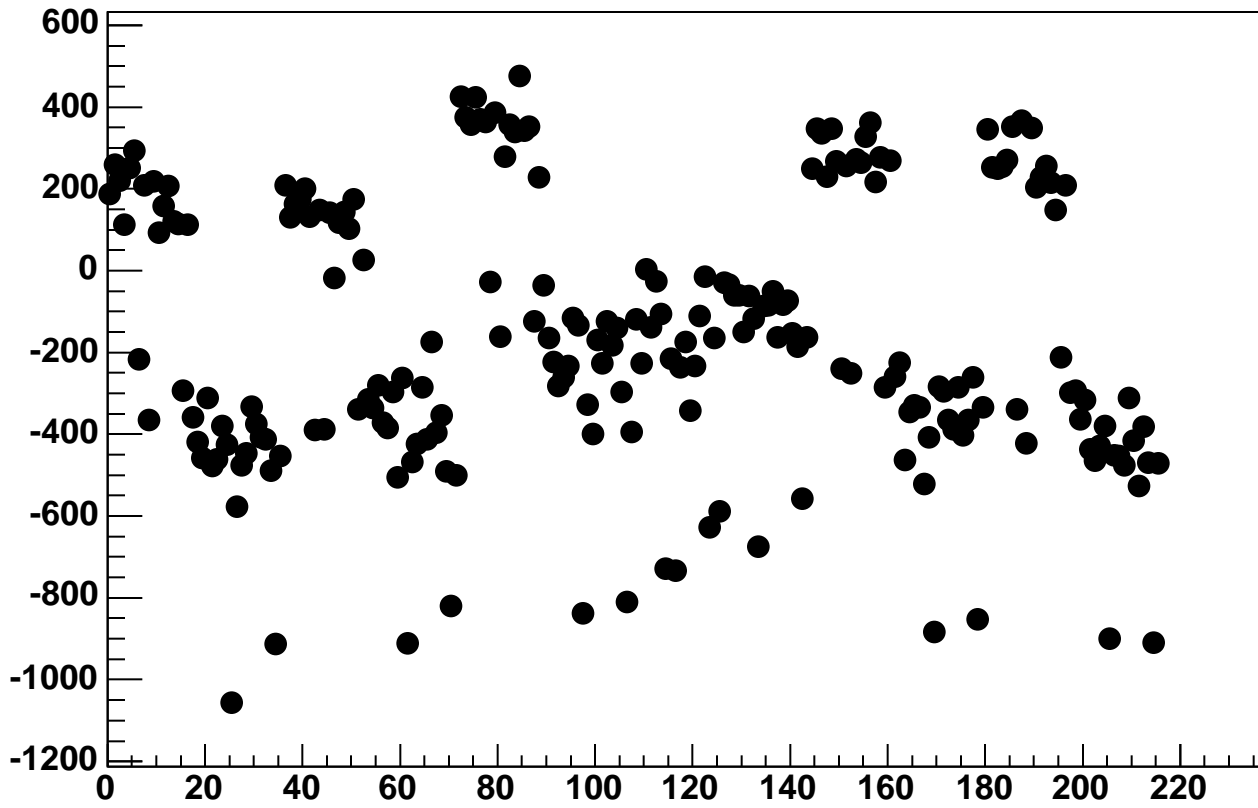
Enable 2, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



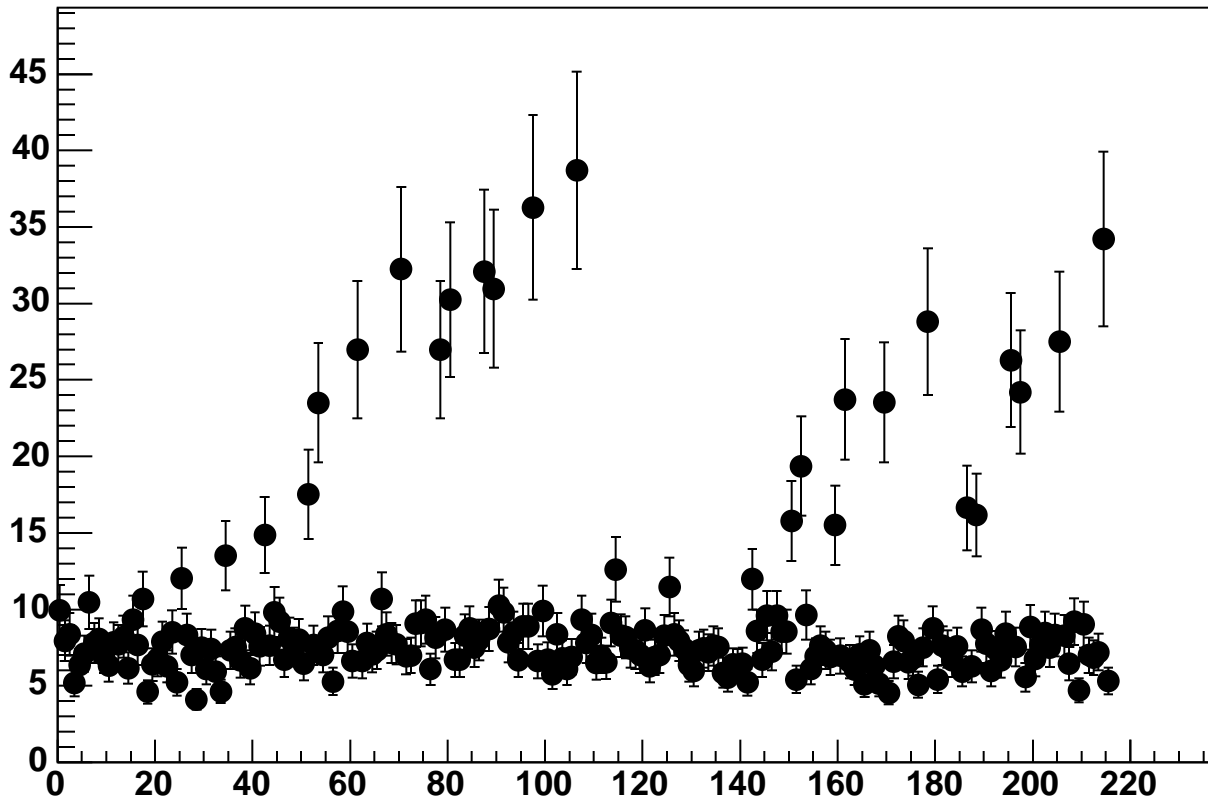
Enable 2, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



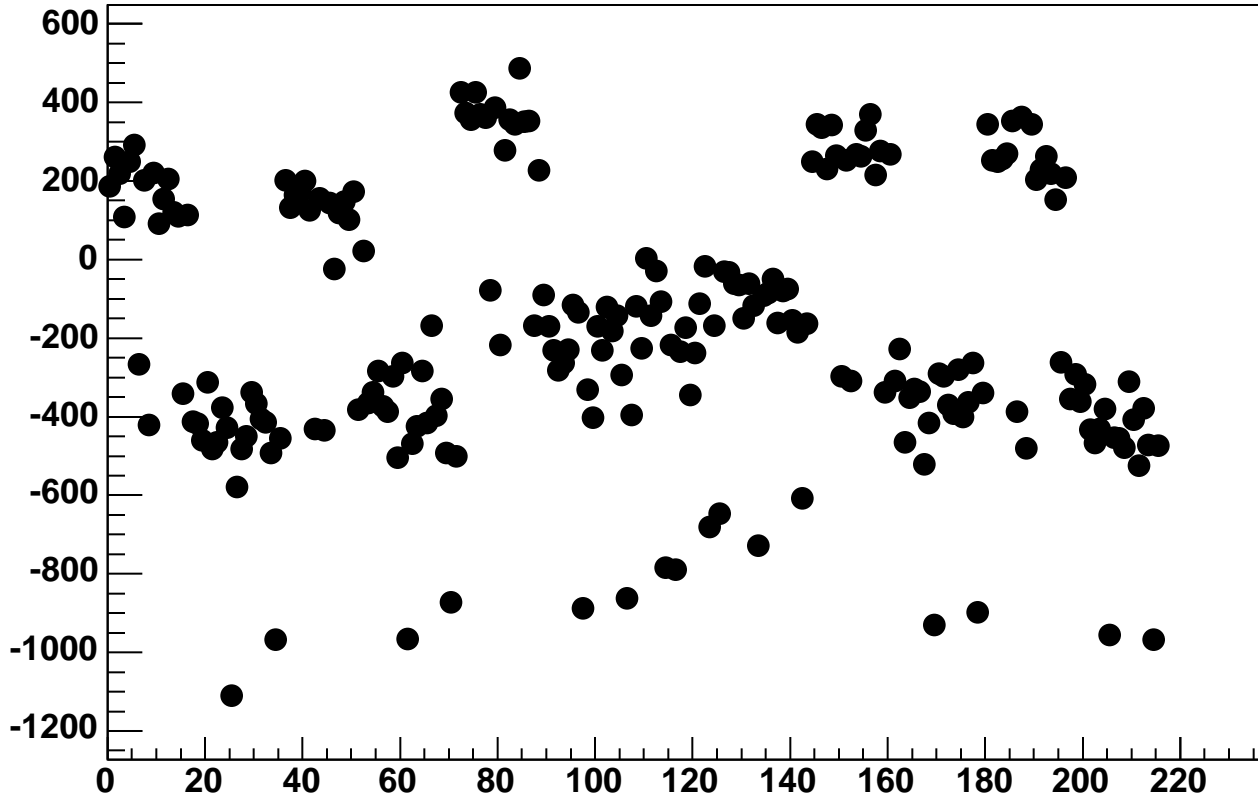
Enable 2, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



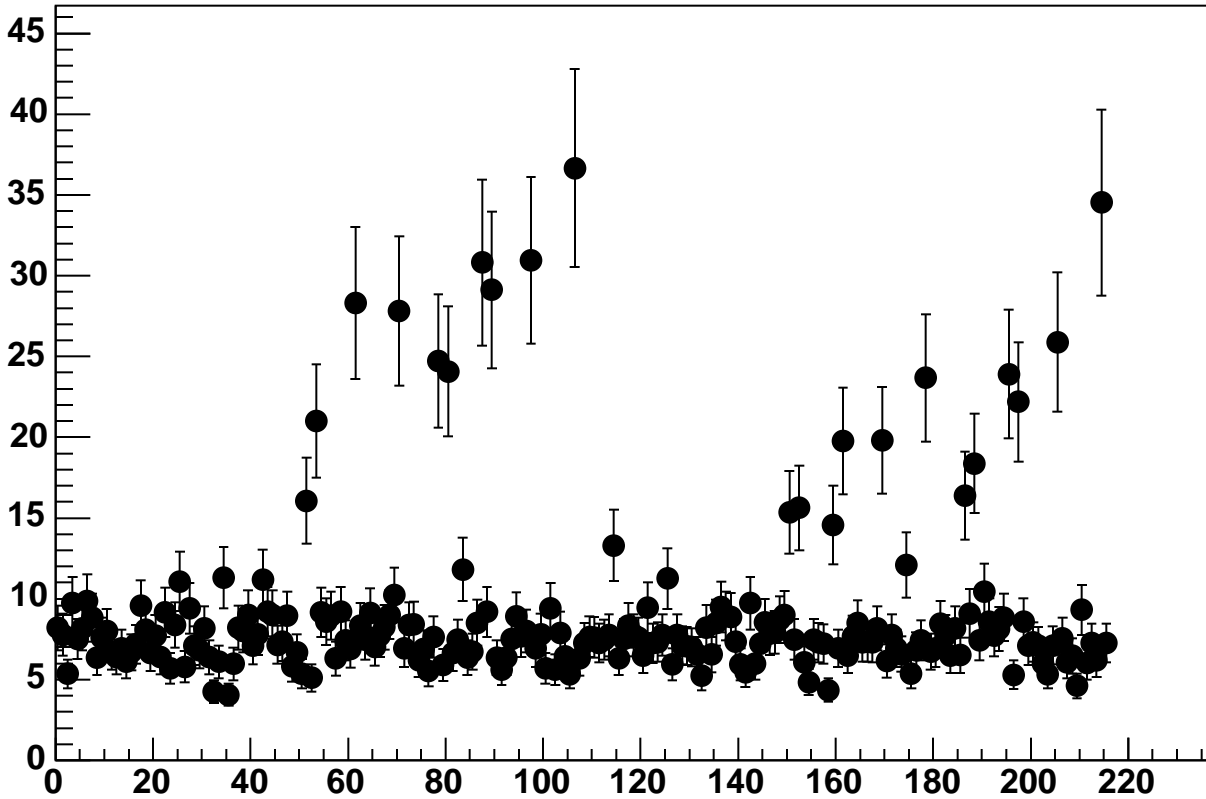
Enable 2, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



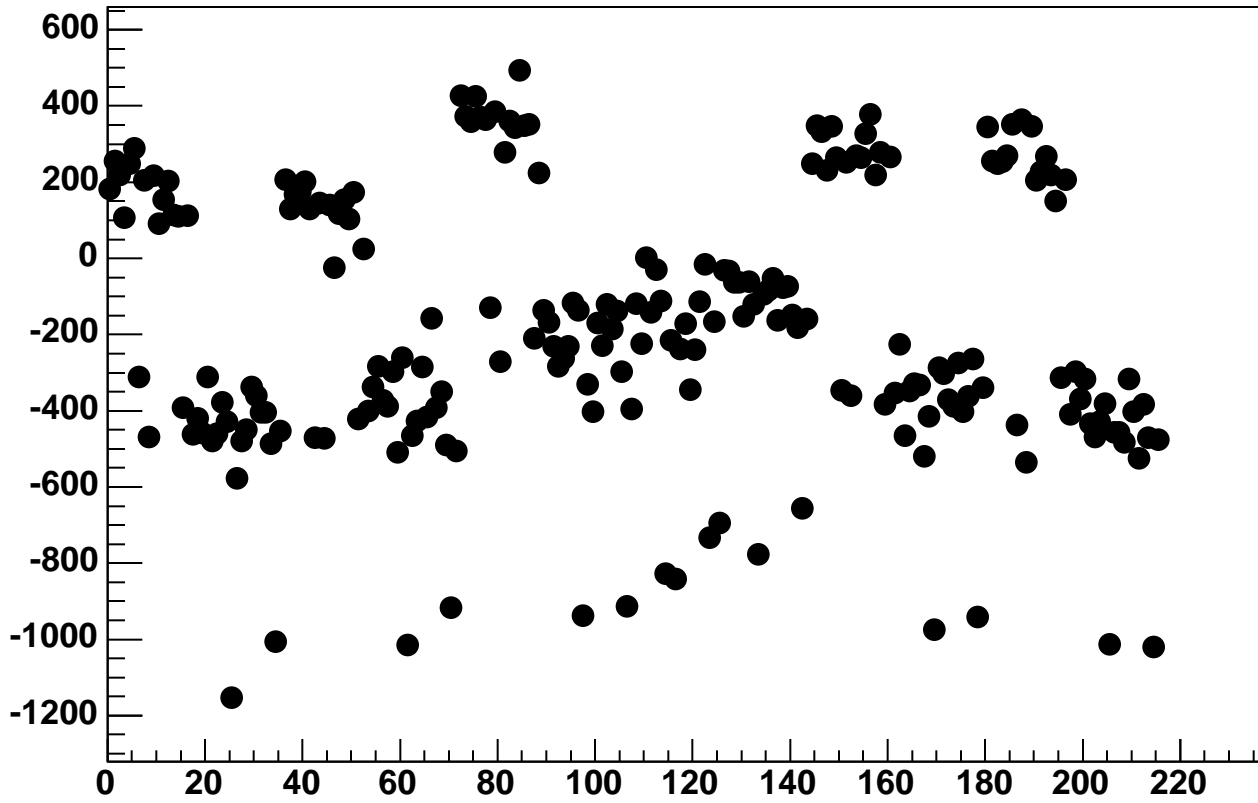
Enable 2, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



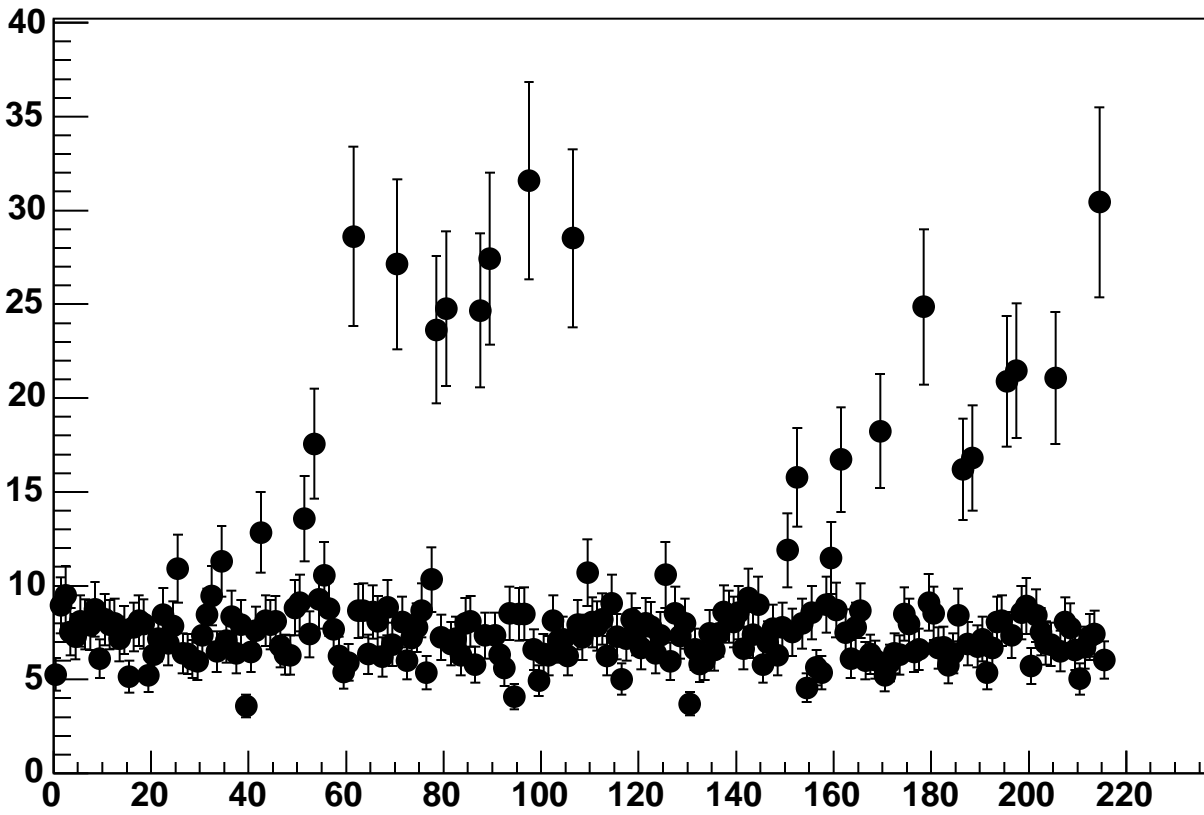
Enable 2, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



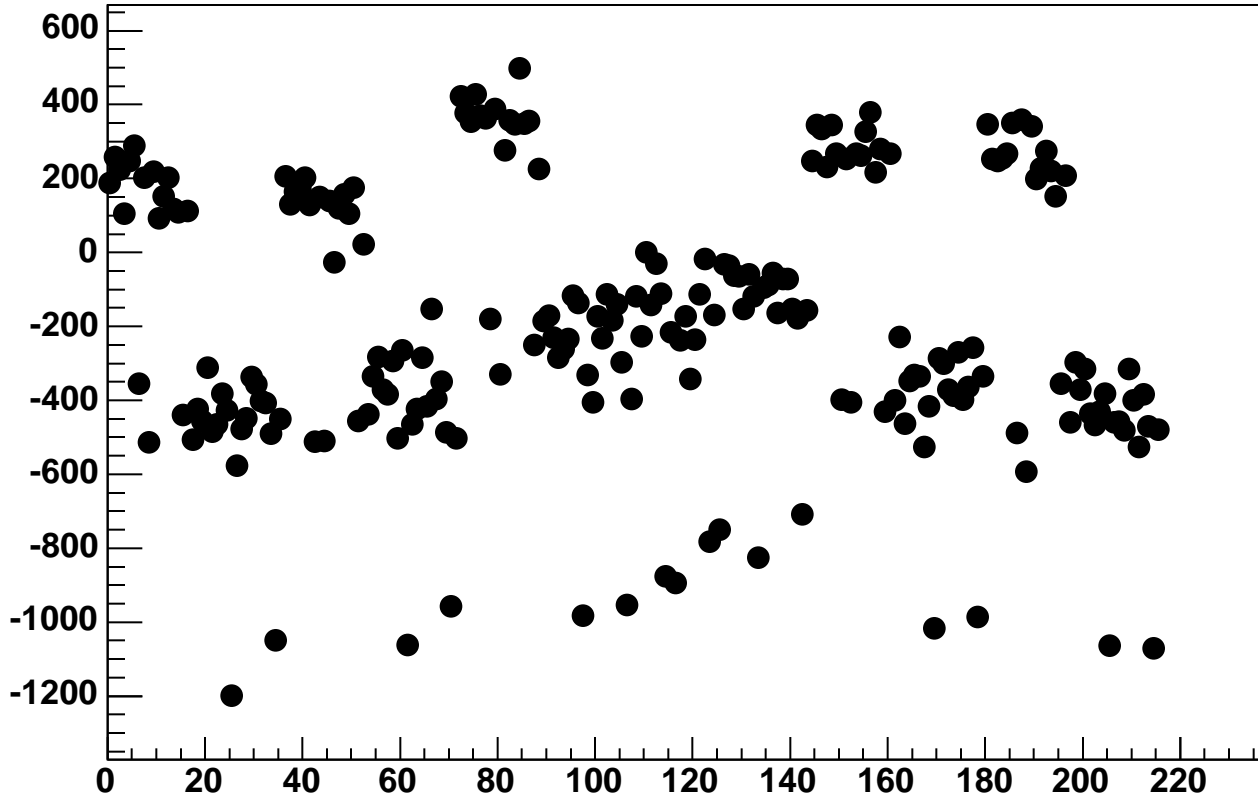
Enable 2, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



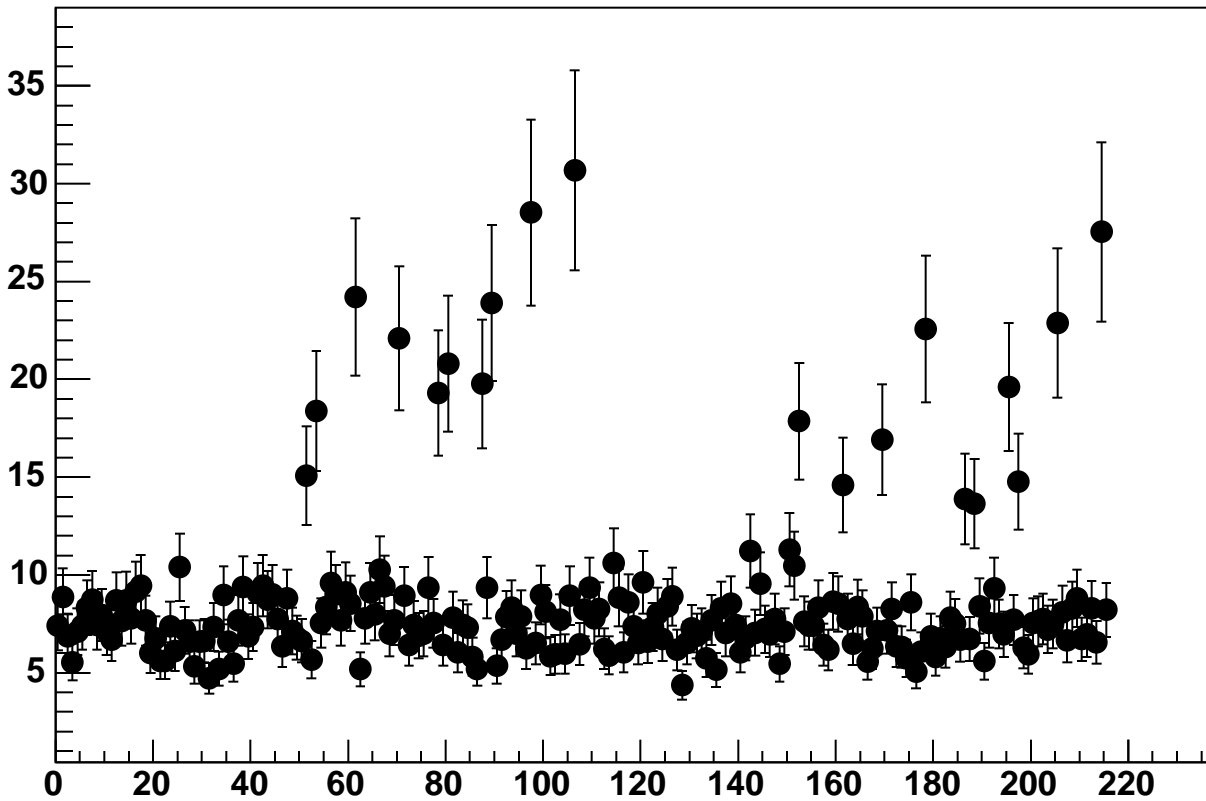
Enable 2, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



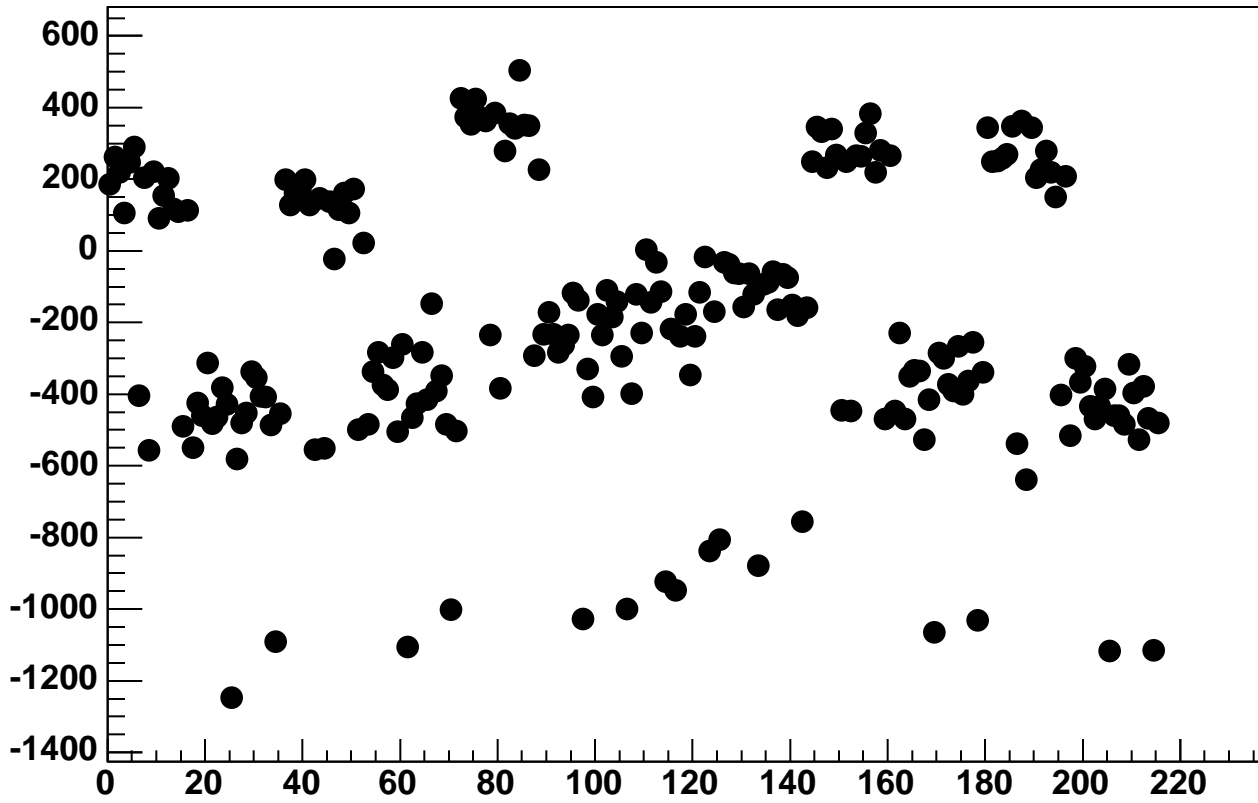
Enable 2, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



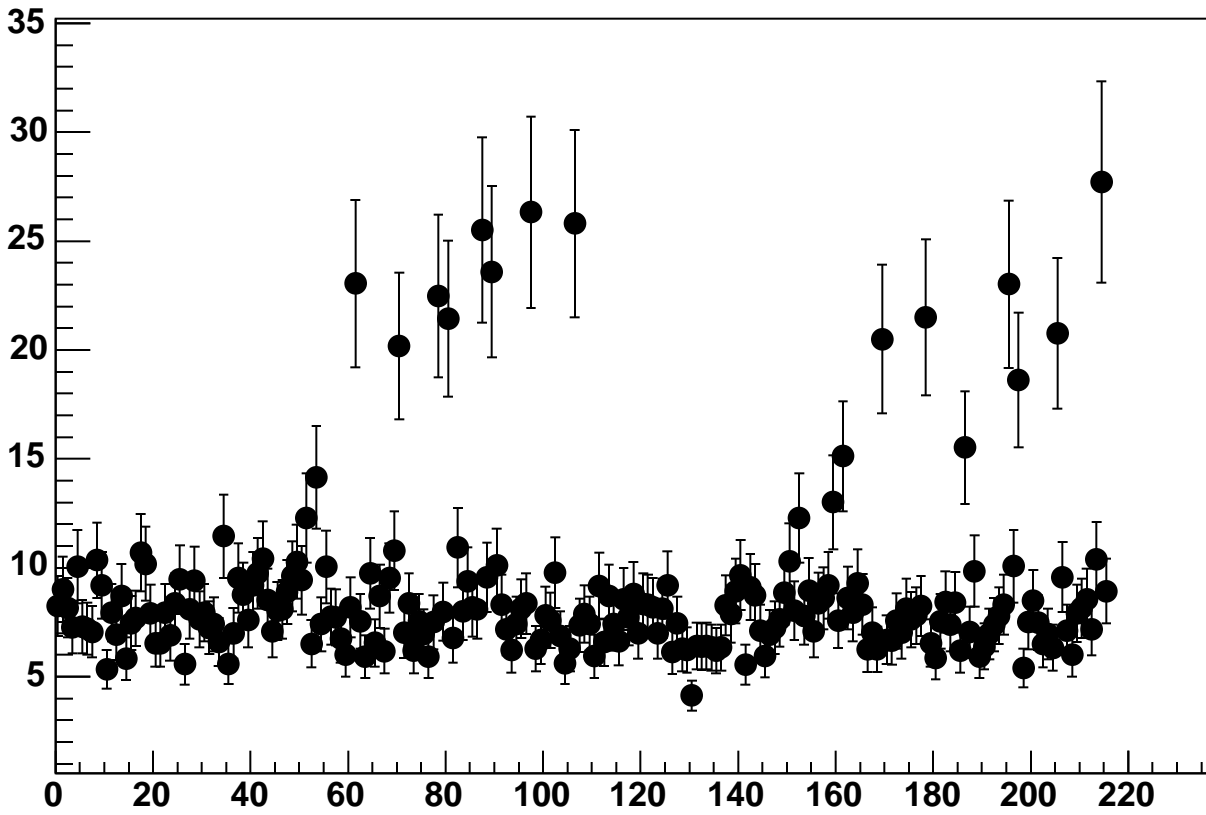
Enable 2, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



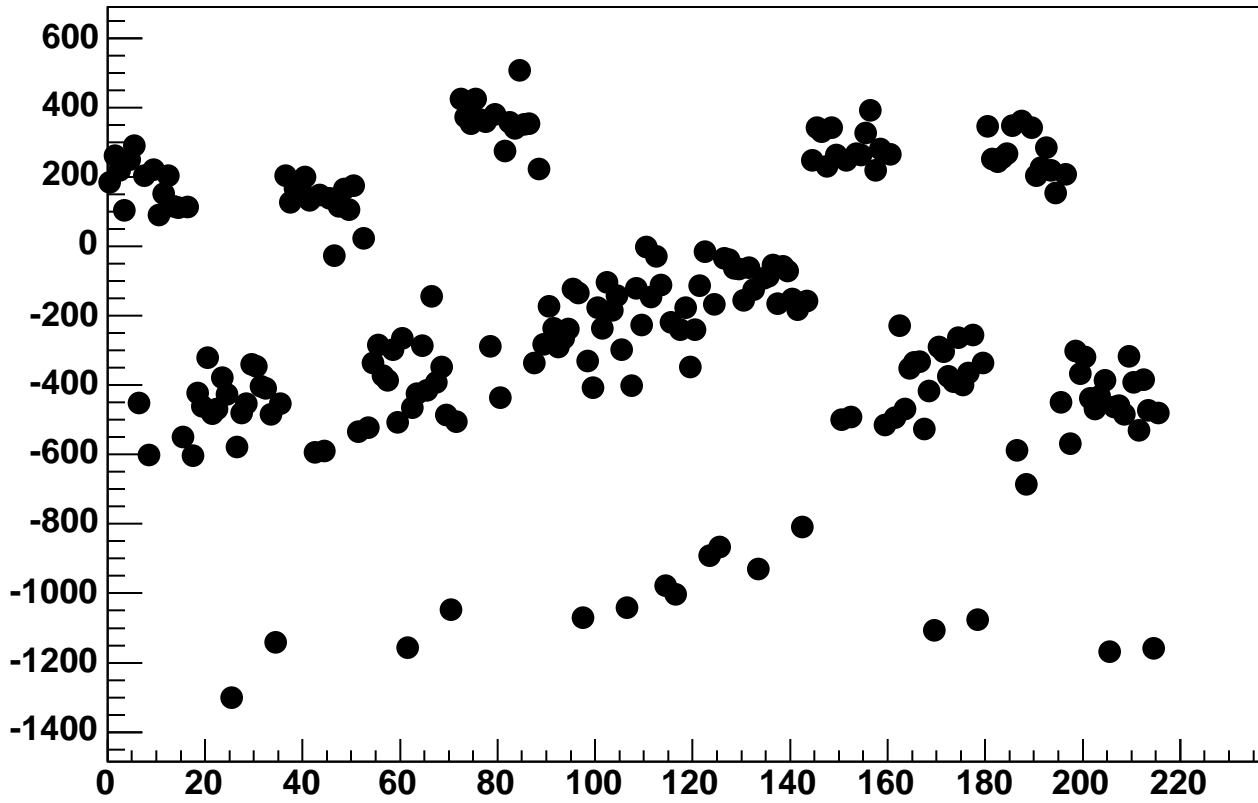
Enable 2, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



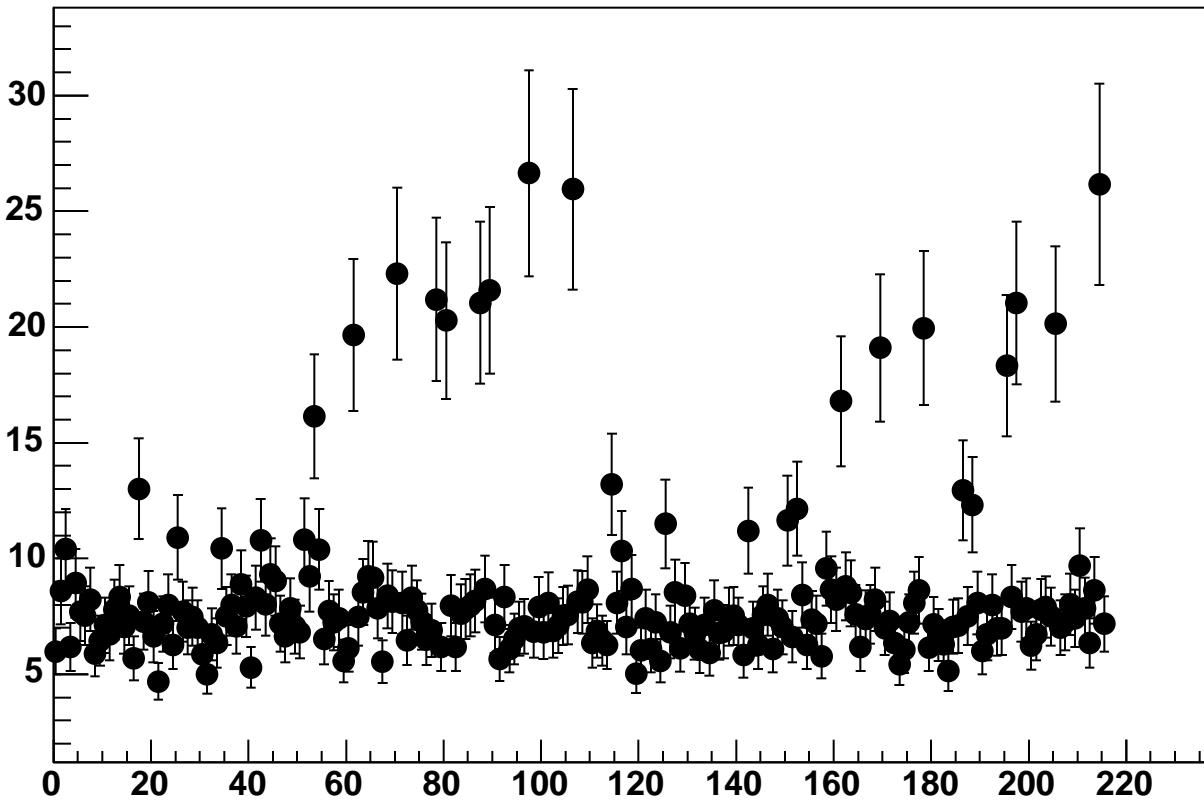
Enable 2, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



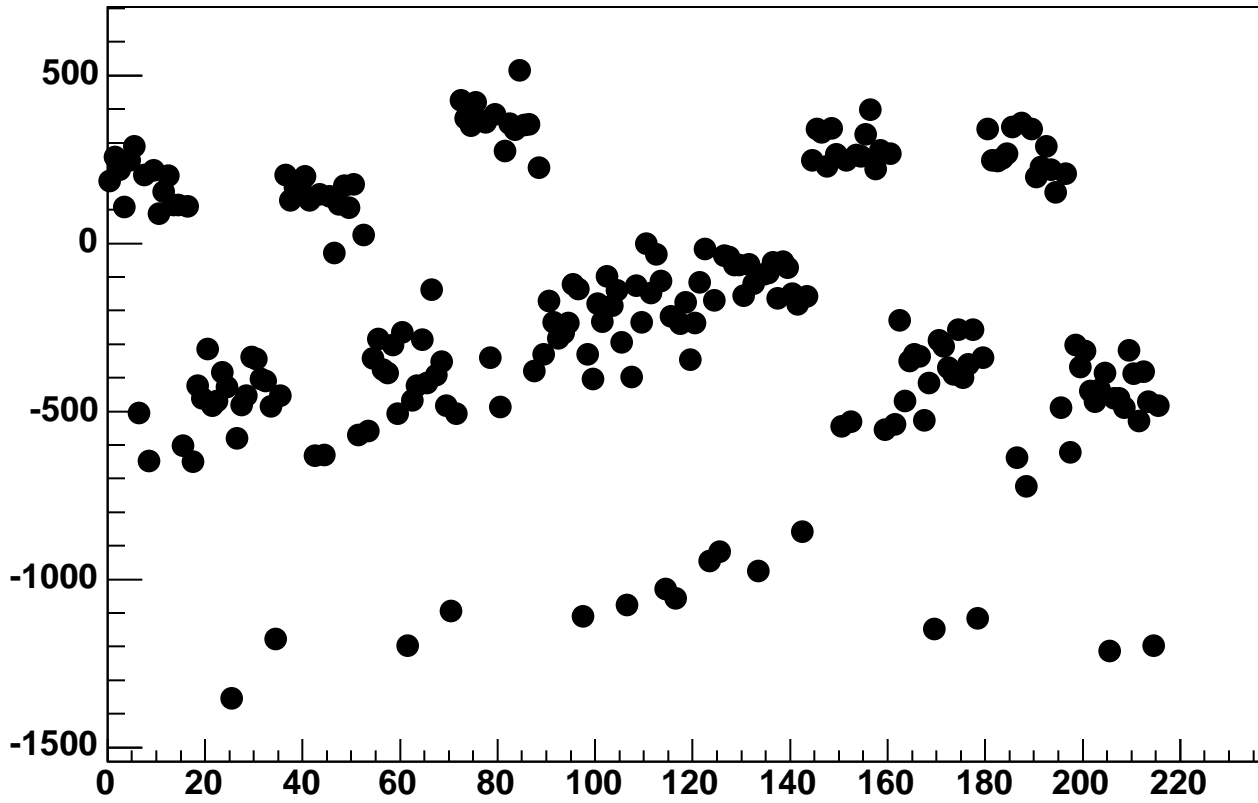
Enable 2, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



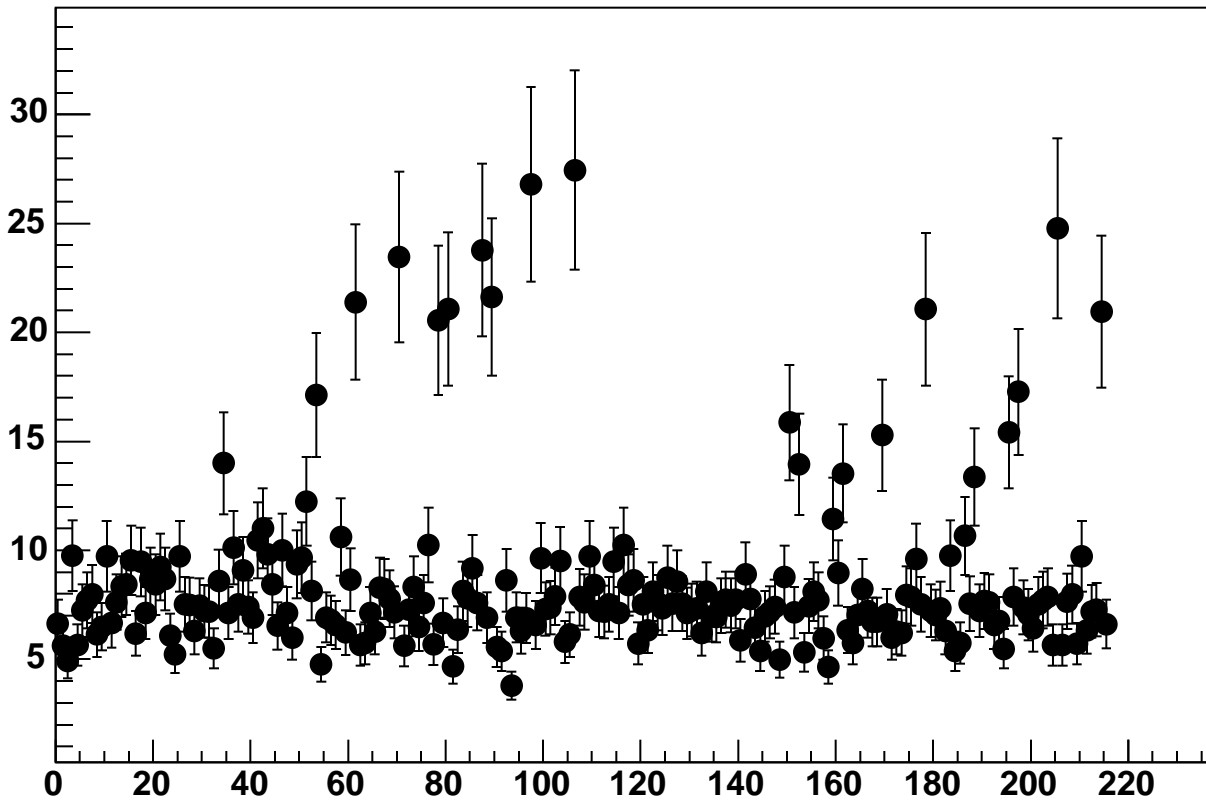
Enable 2, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



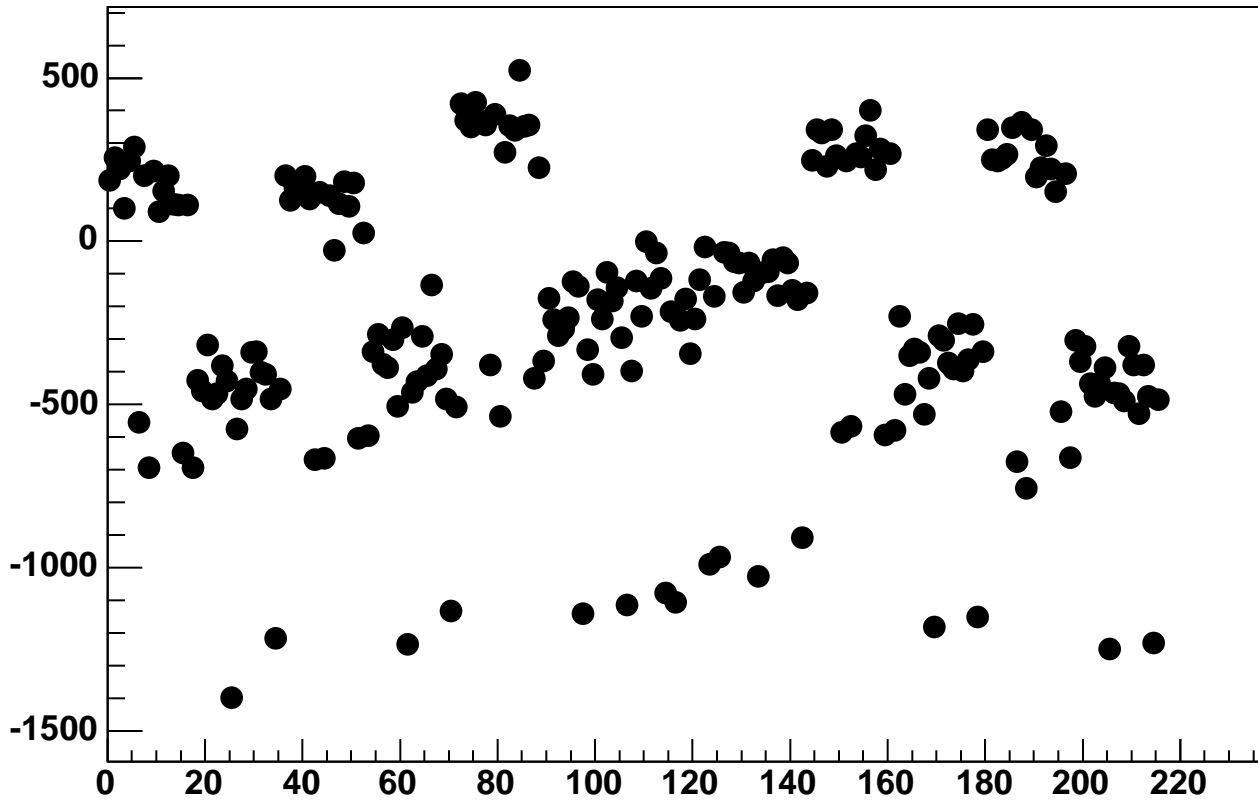
Enable 2, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



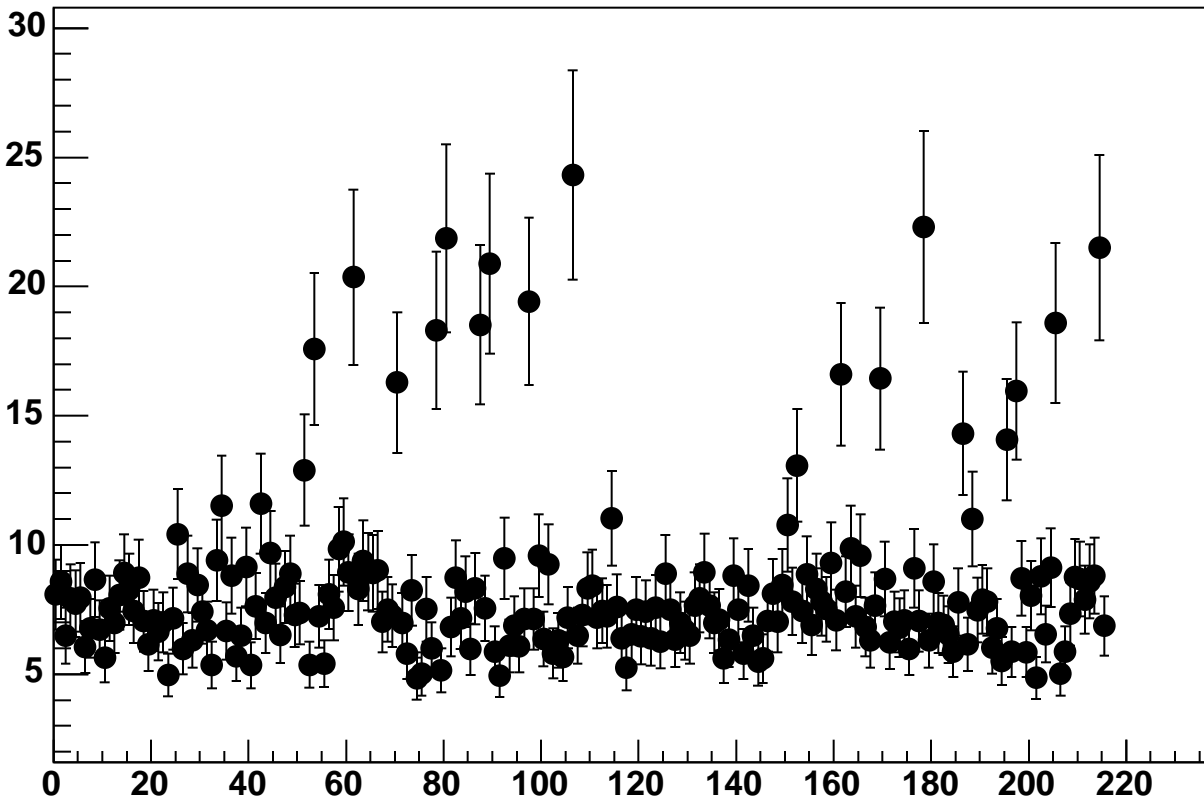
Enable 2, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



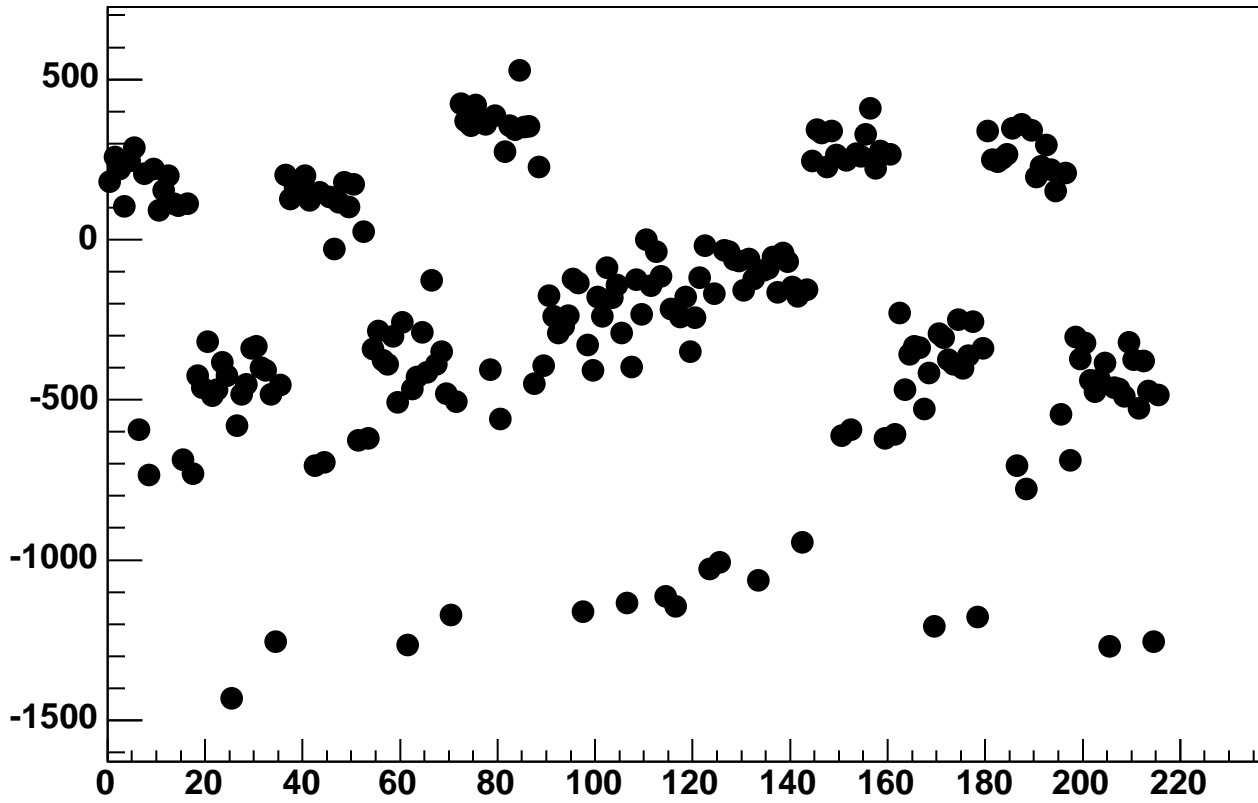
Enable 2, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



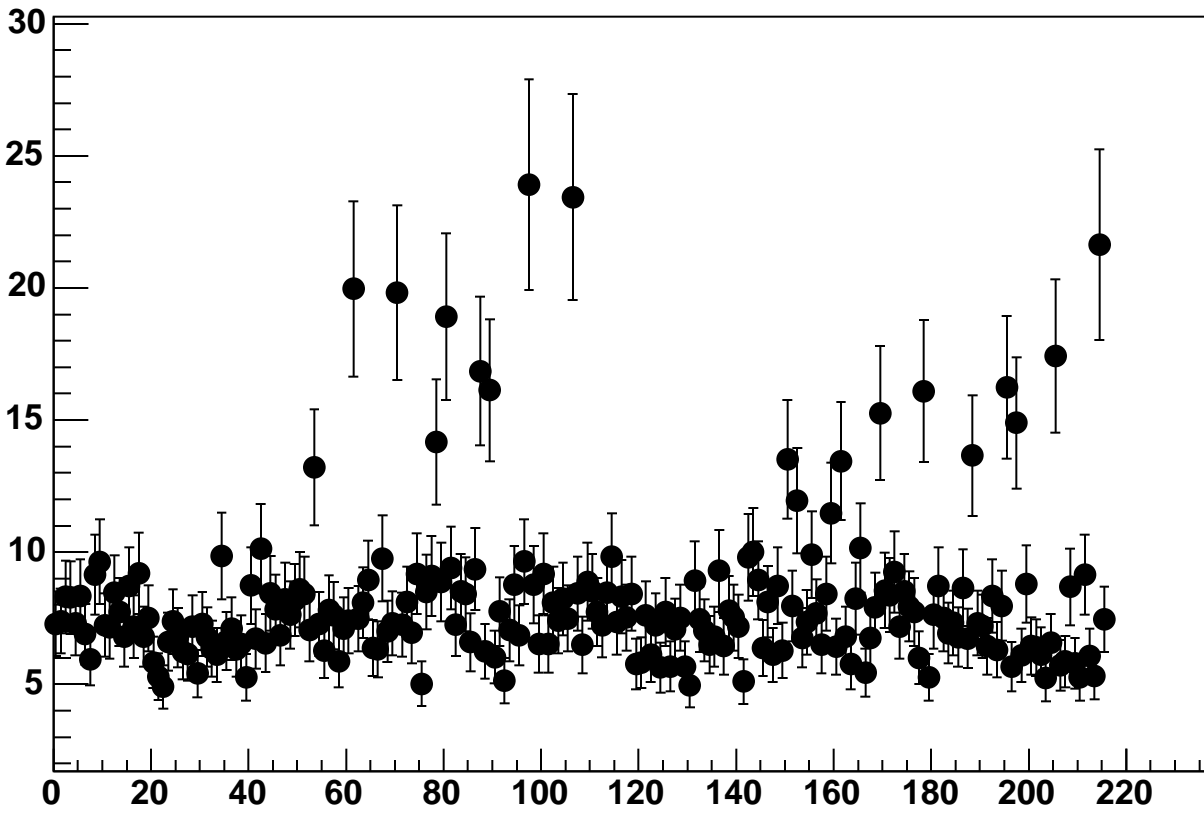
Enable 2, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



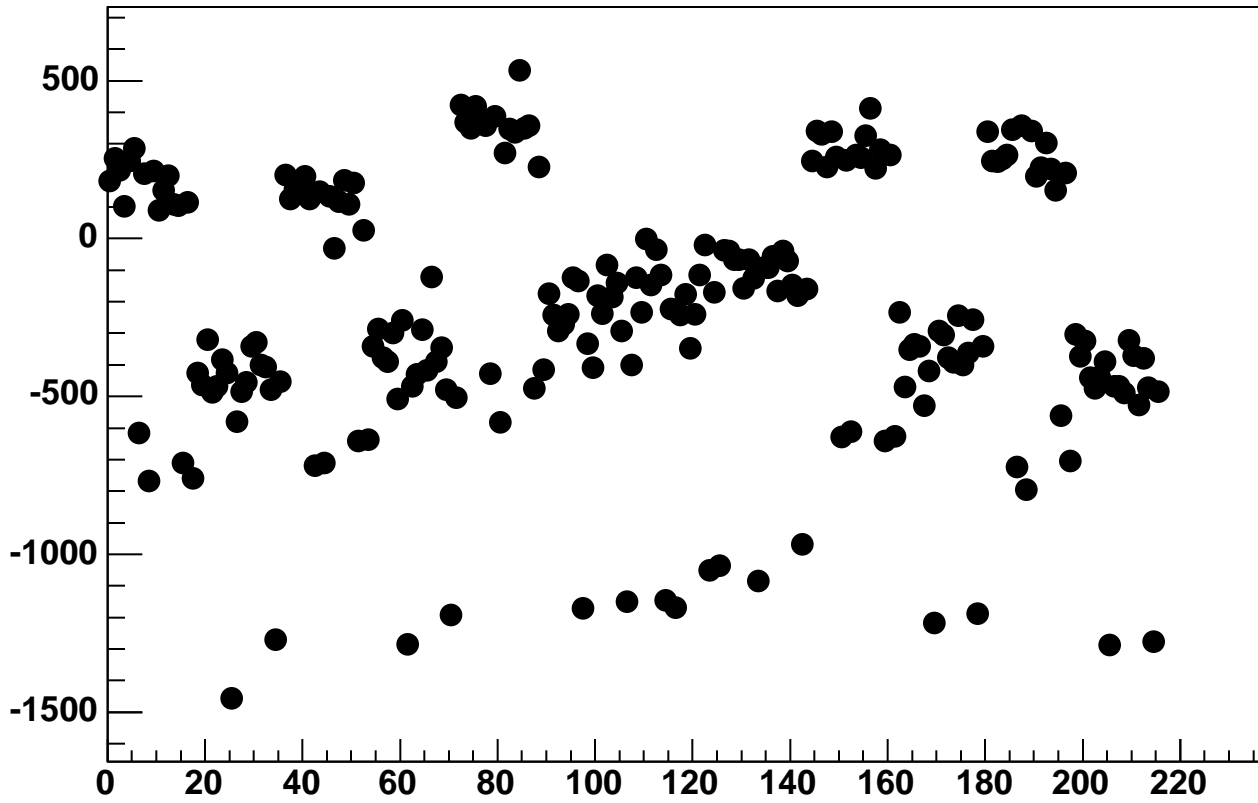
Enable 2, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



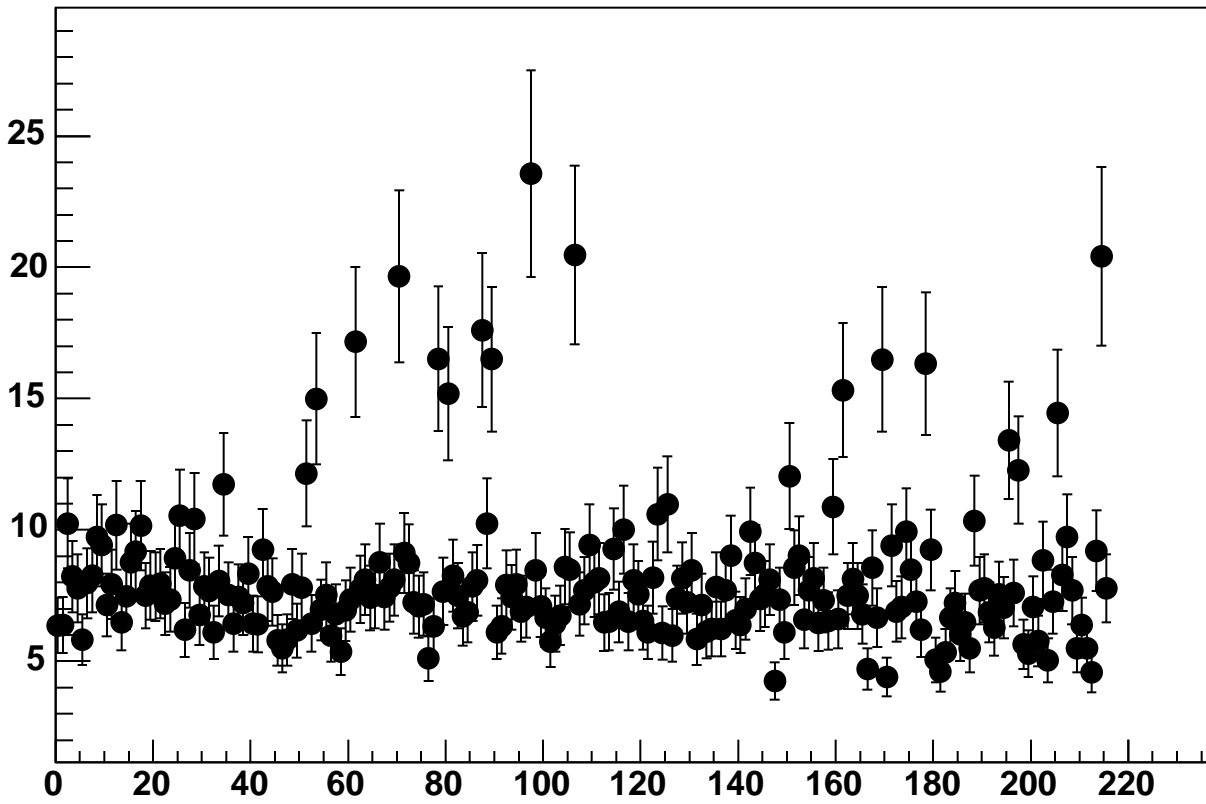
Enable 2, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



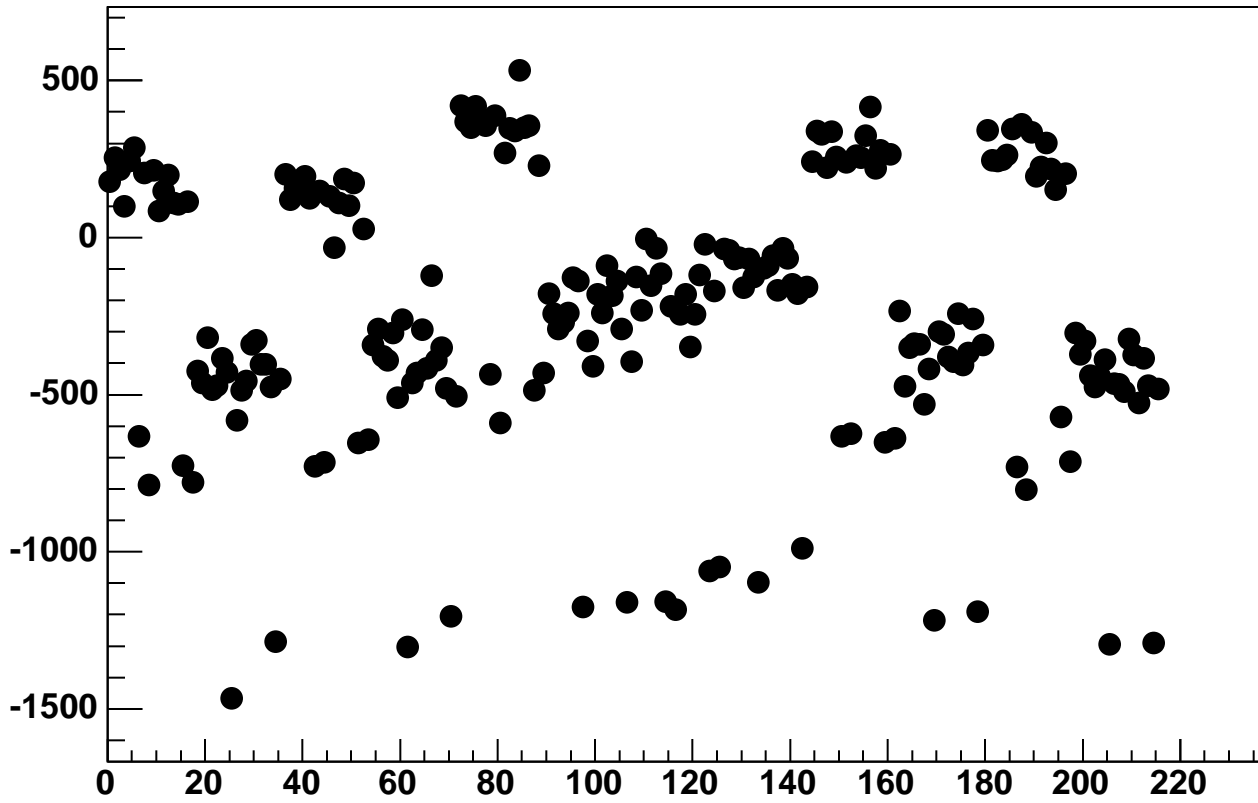
Enable 2, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



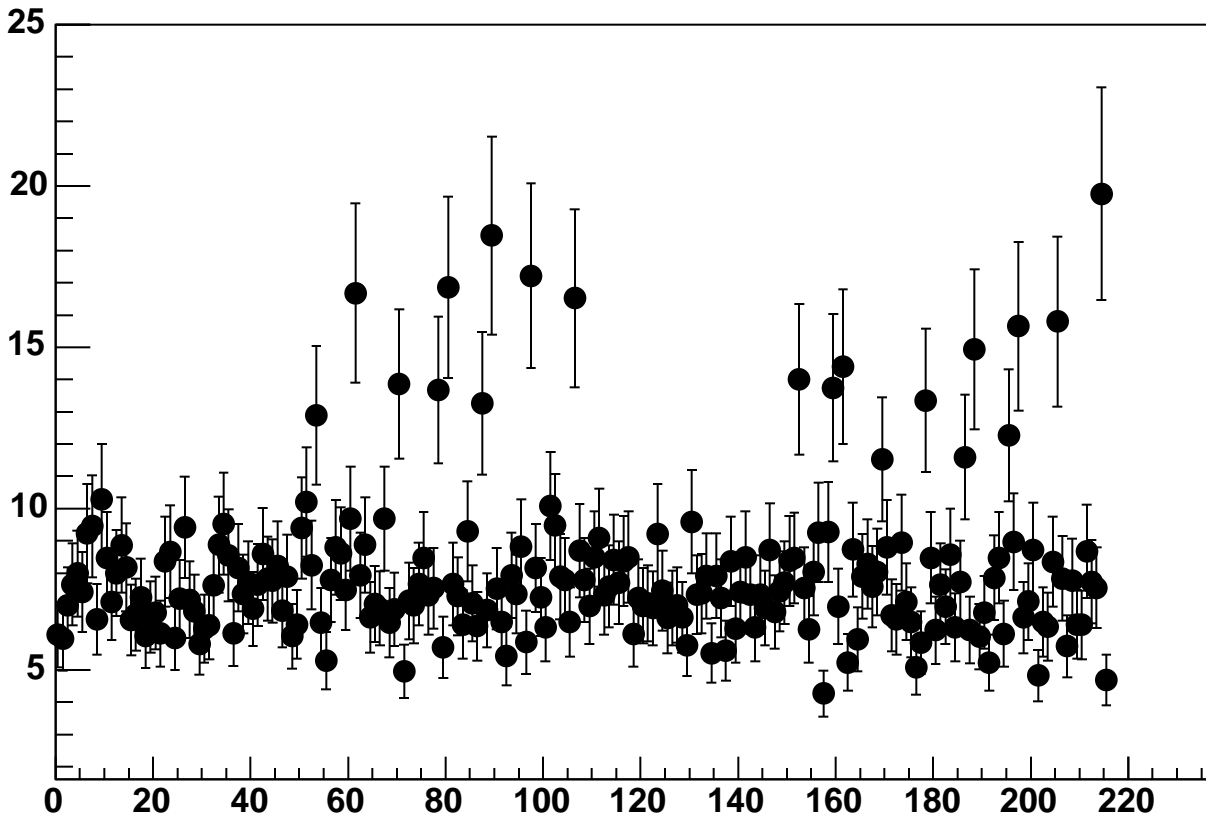
Enable 2, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



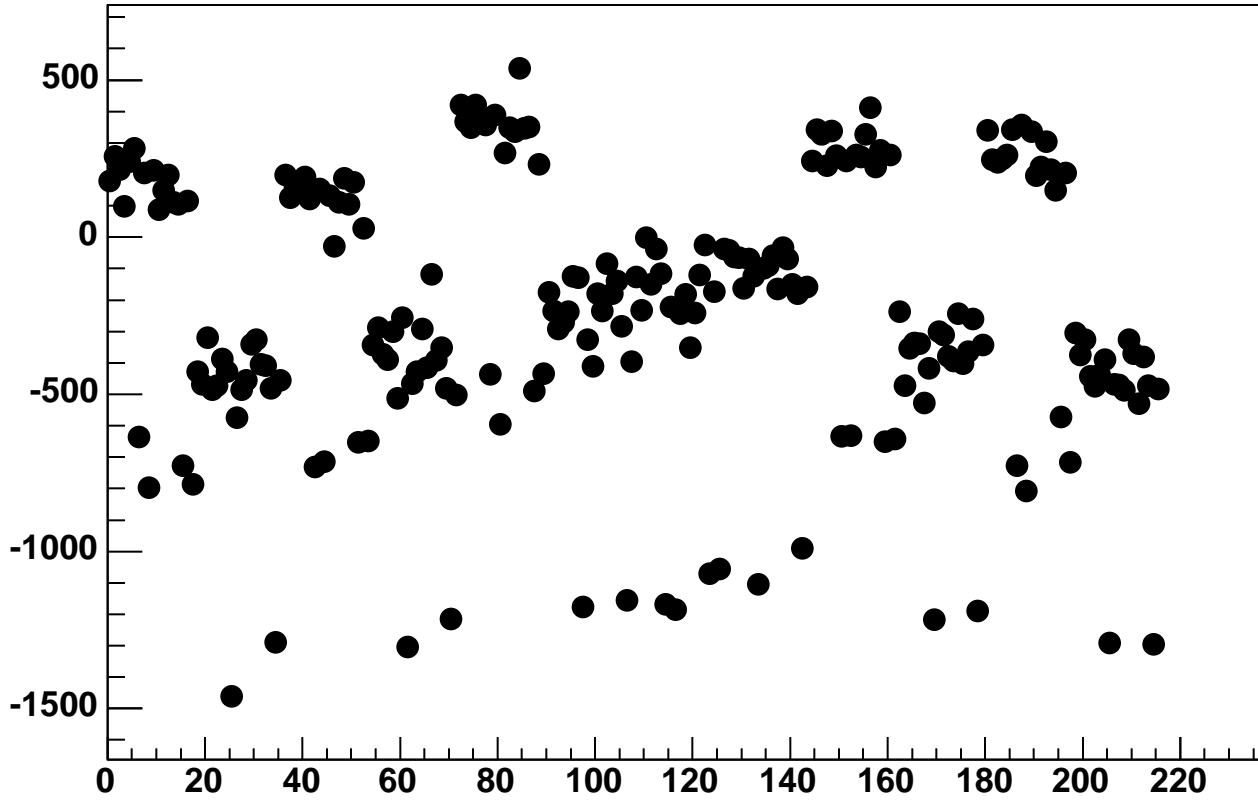
Enable 2, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



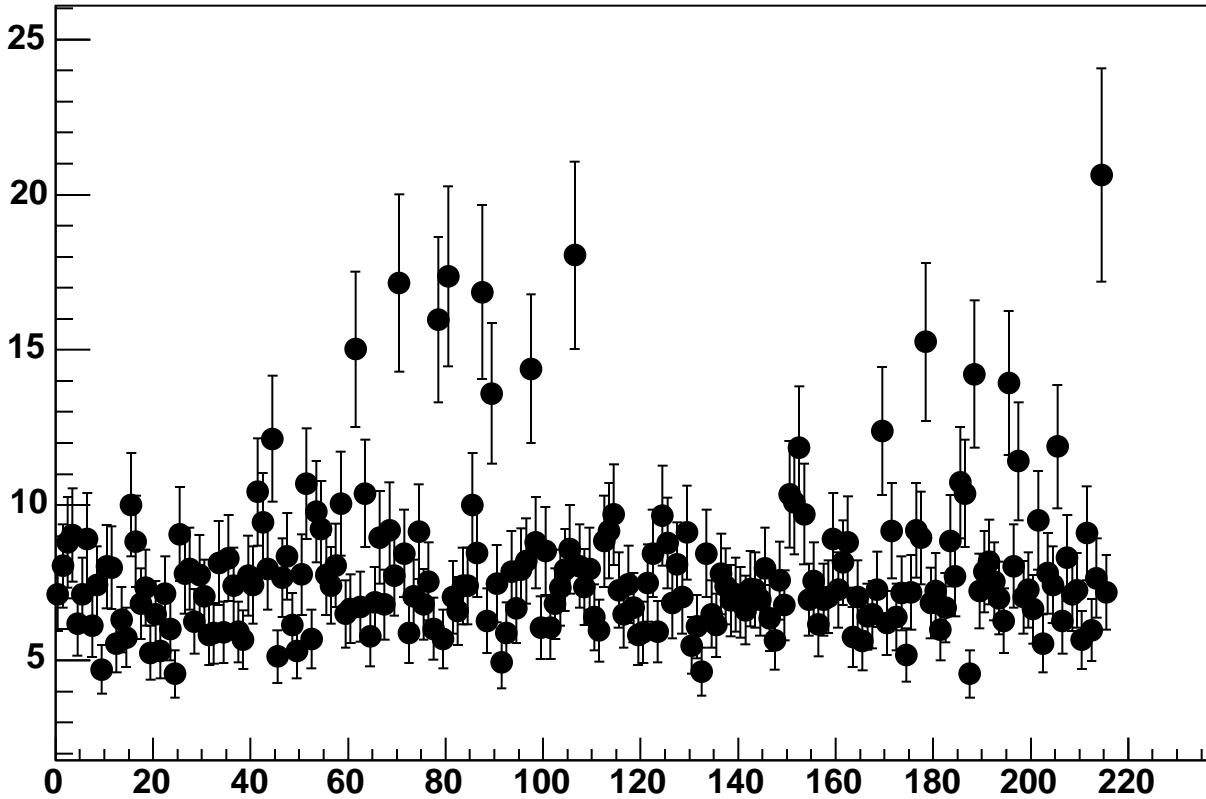
Enable 2, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



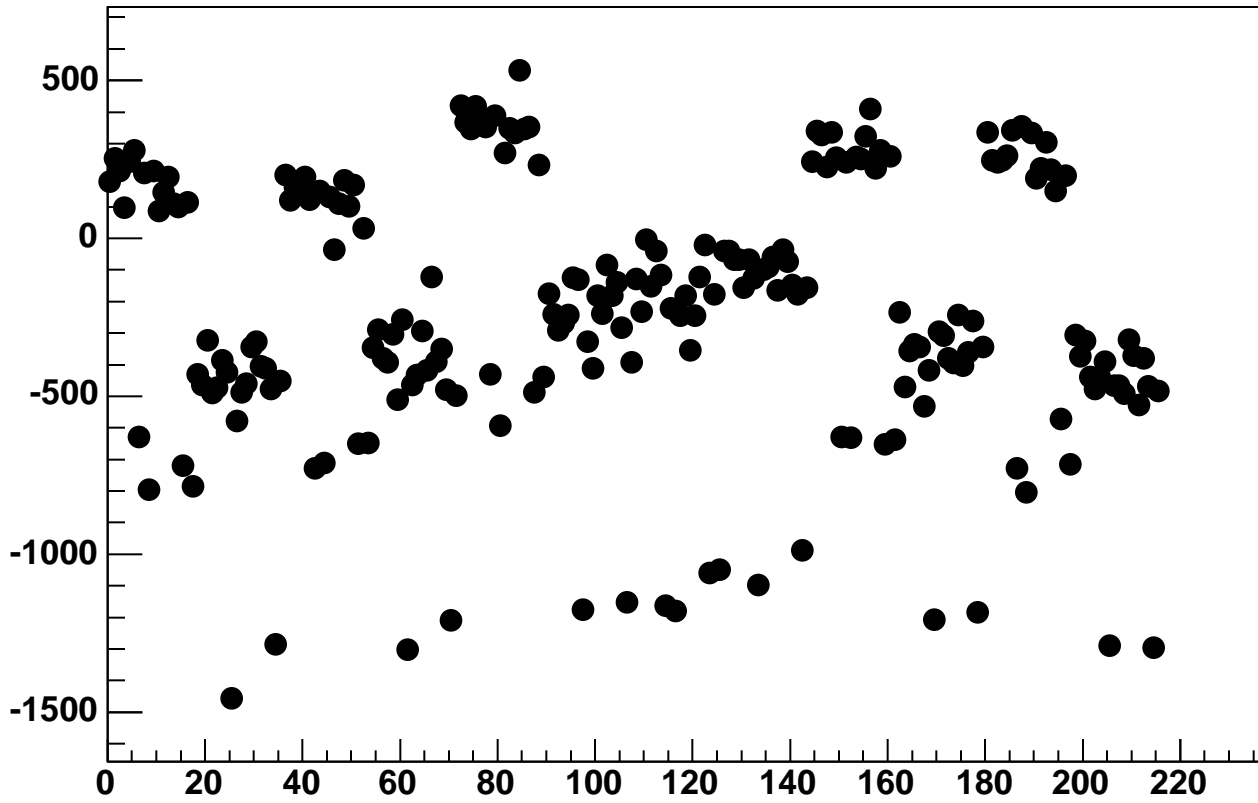
Enable 2, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



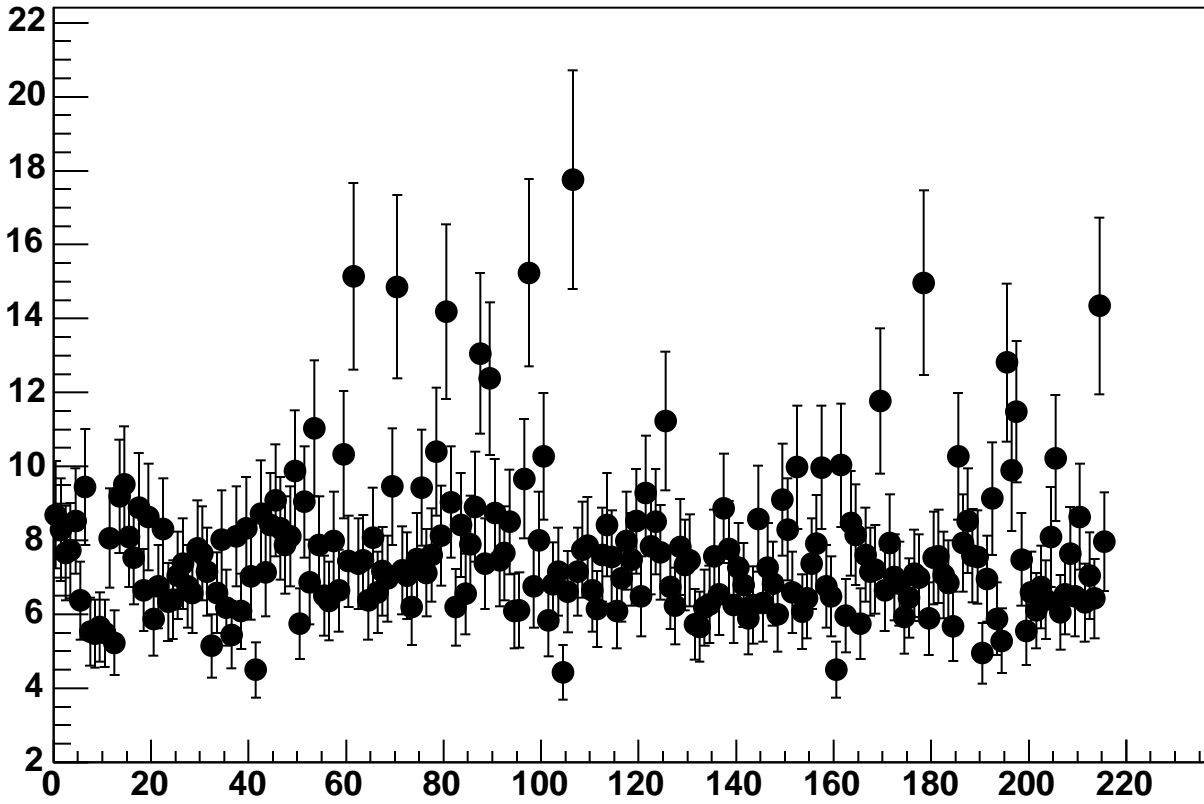
Enable 2, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



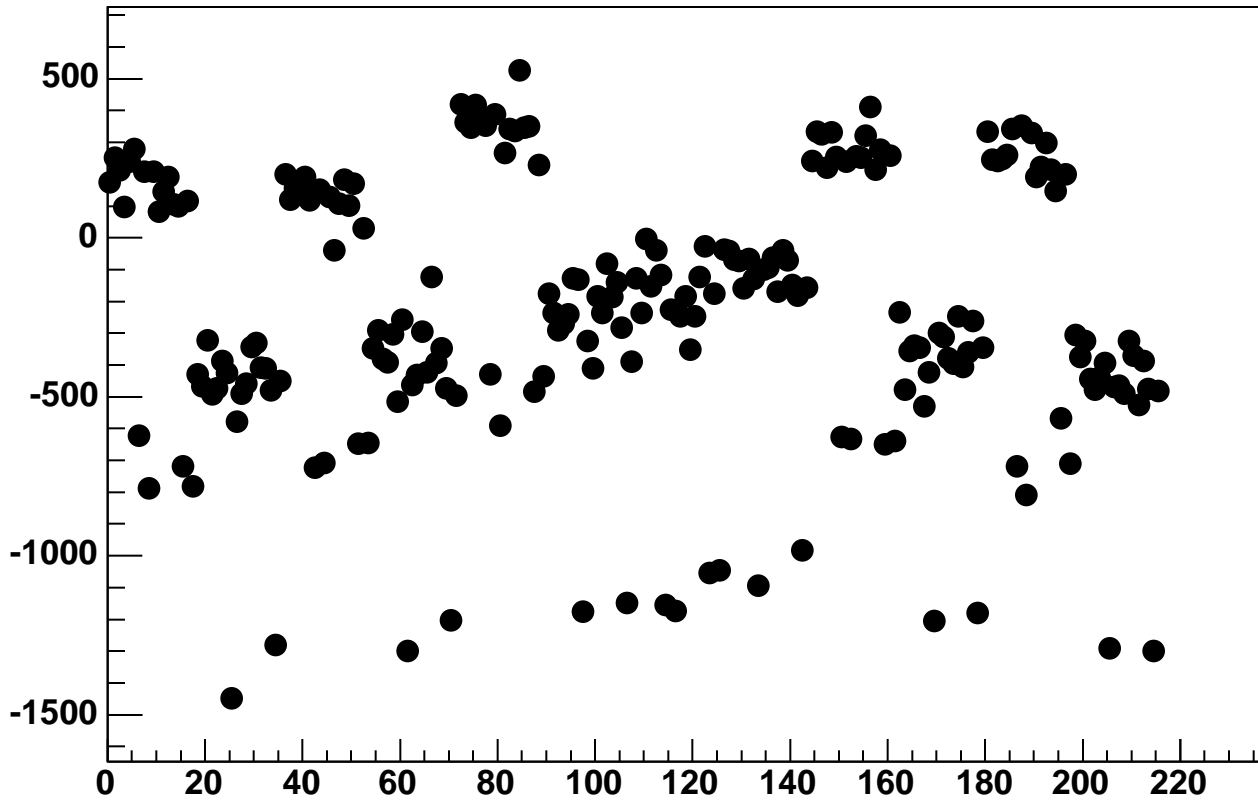
Enable 2, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



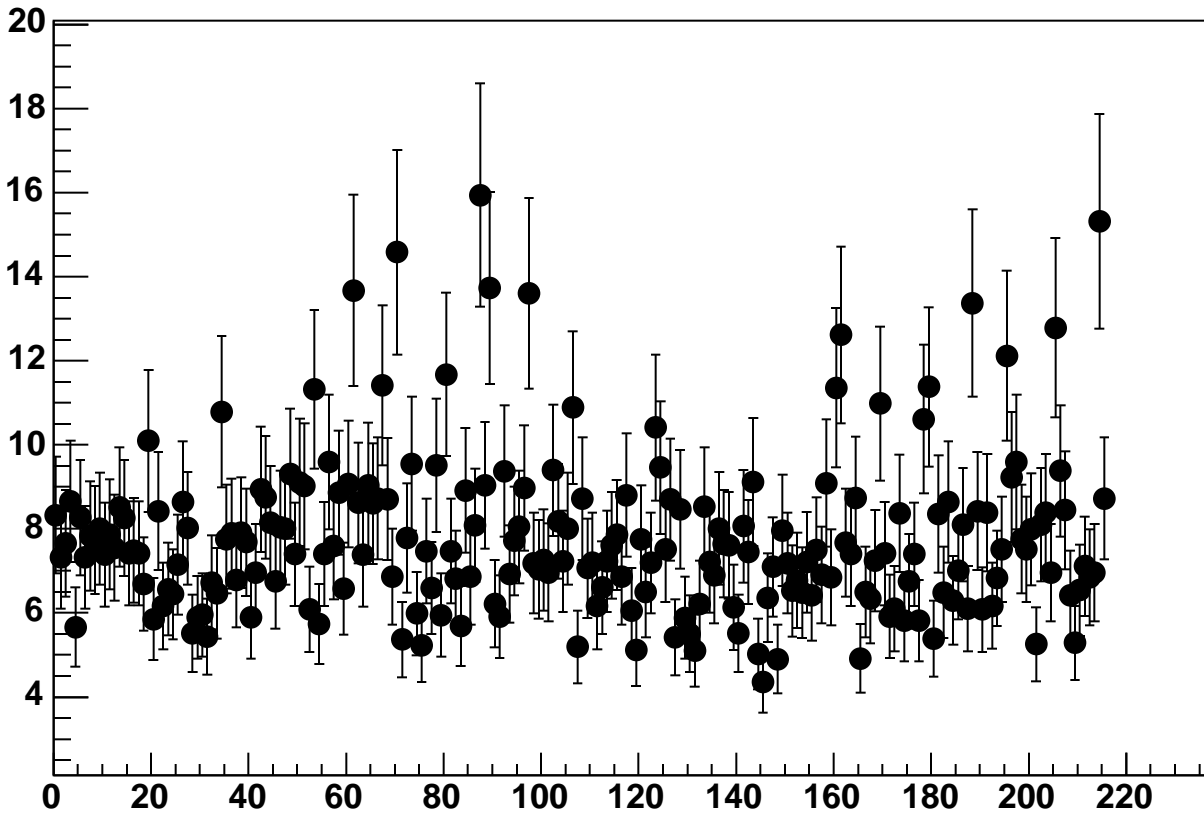
Enable 2, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



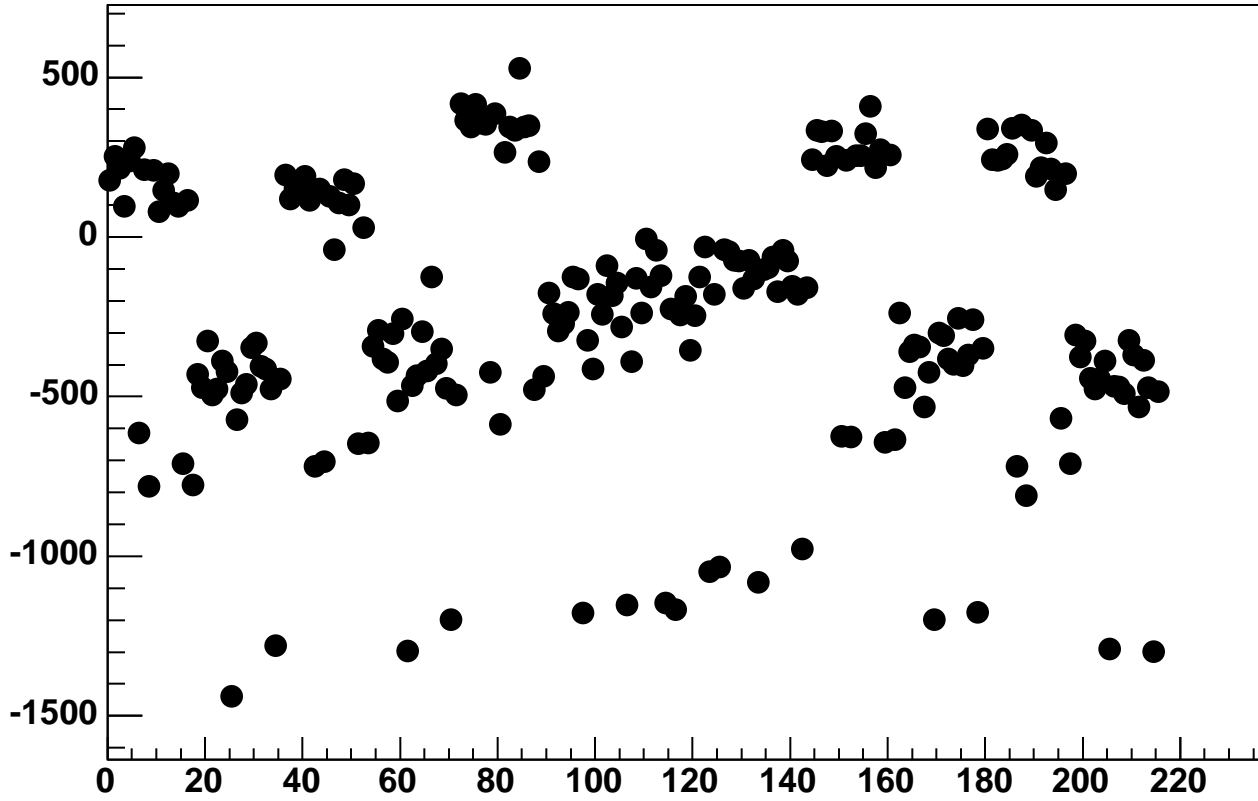
Enable 2, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



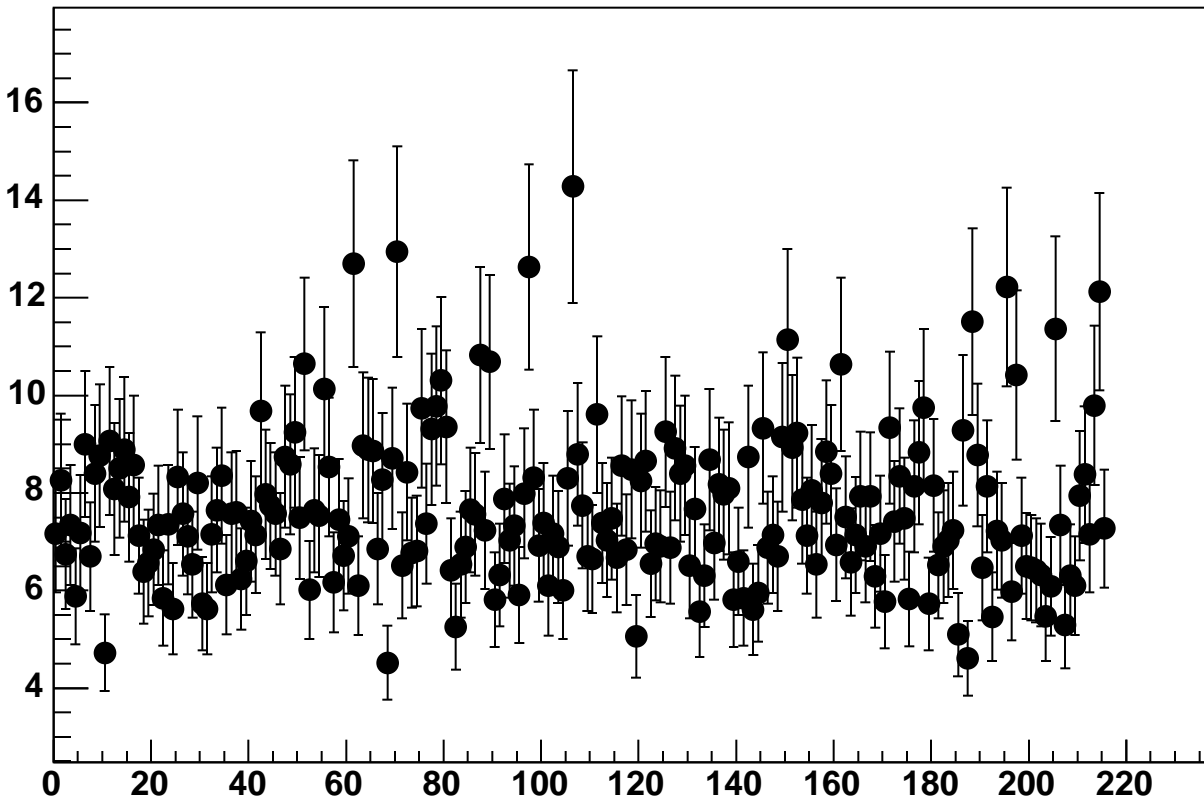
Enable 2, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan



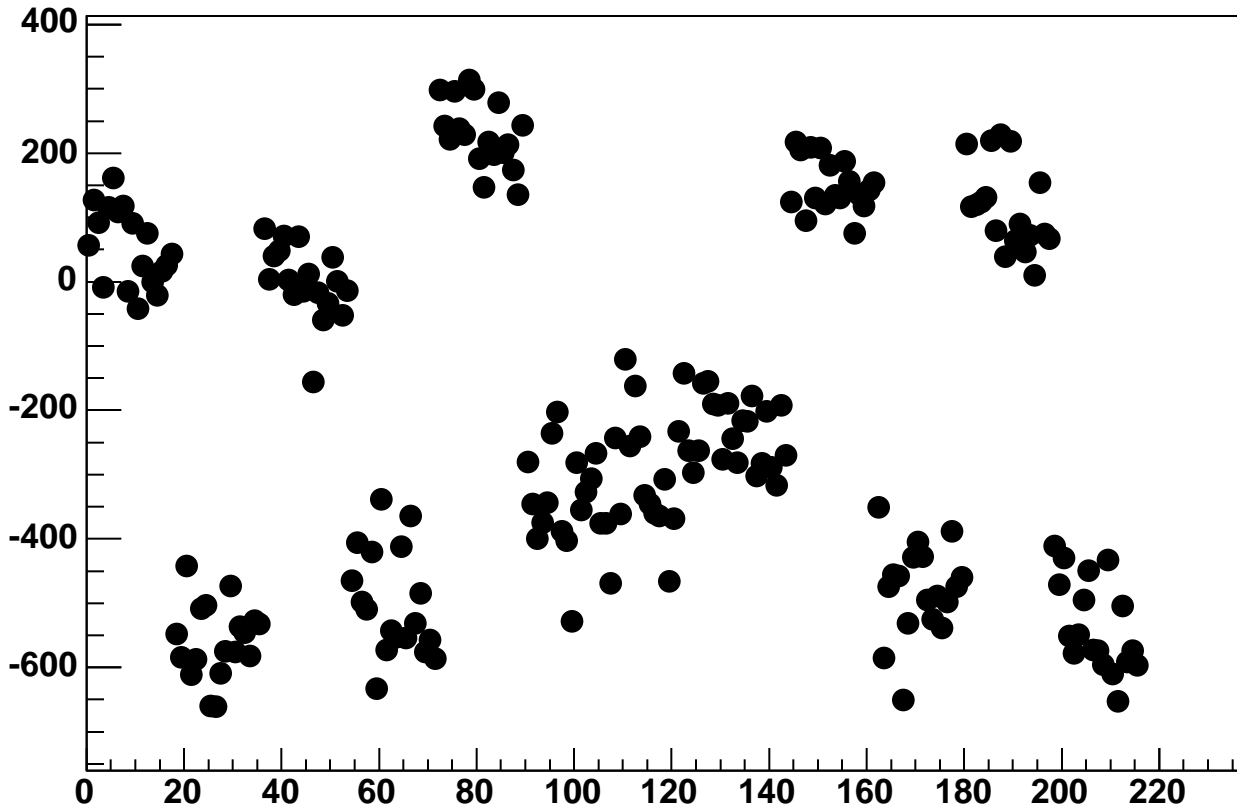
Enable 2, DAC=4000, Hold=245, ADC Mean vs 18*Chip+Chan



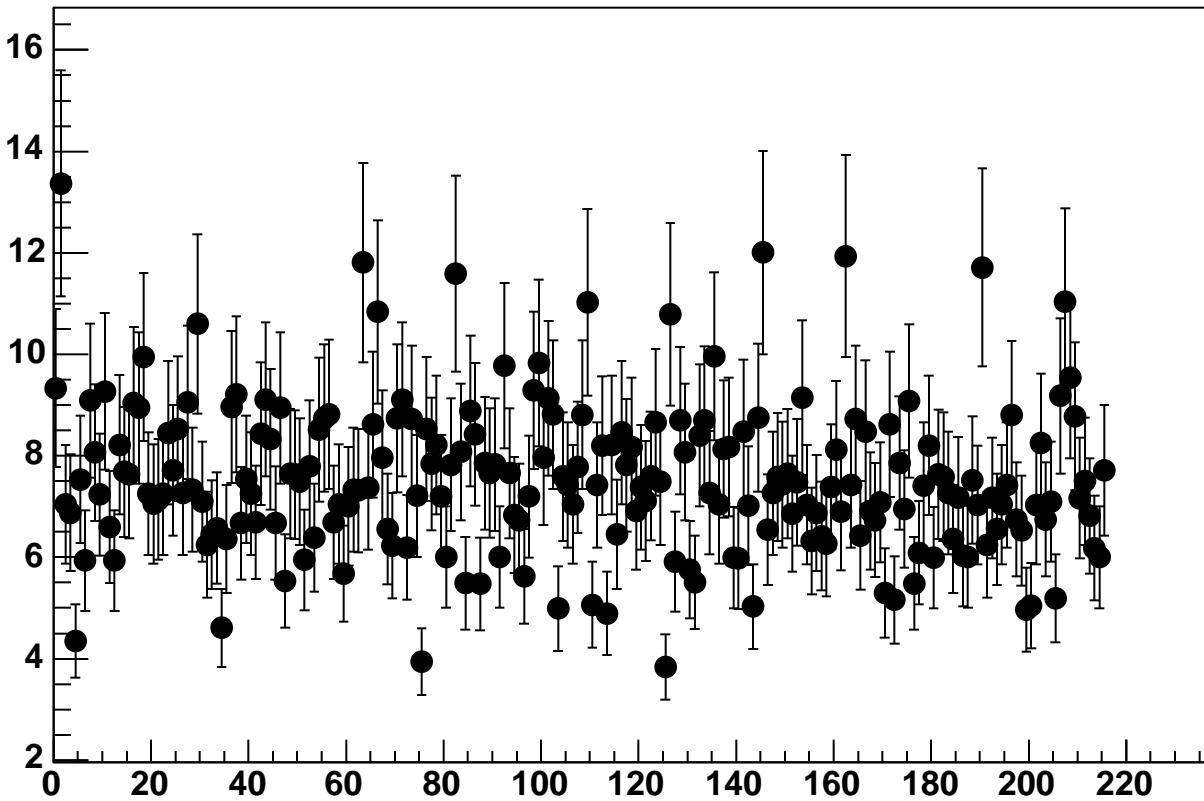
Enable 2, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



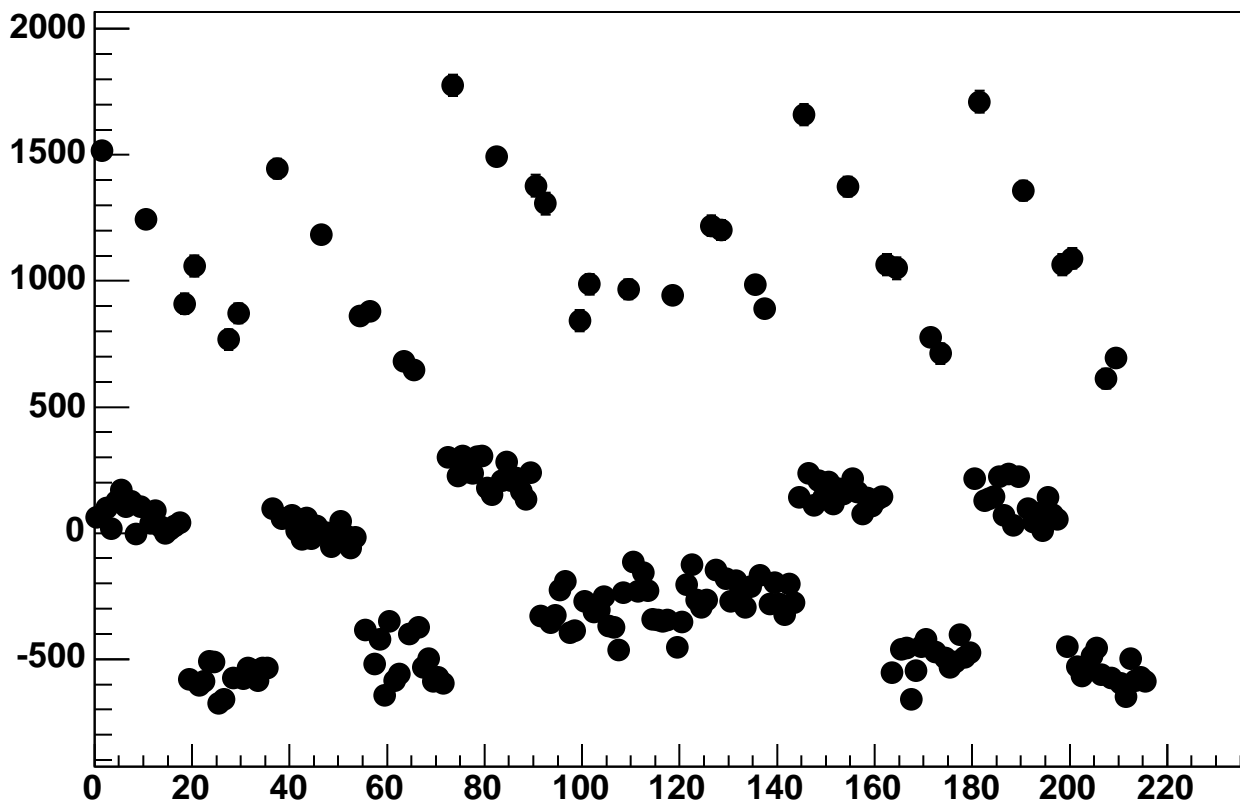
Enable 3, DAC=4000, Hold=0, ADC Mean vs 18*Chip+Chan



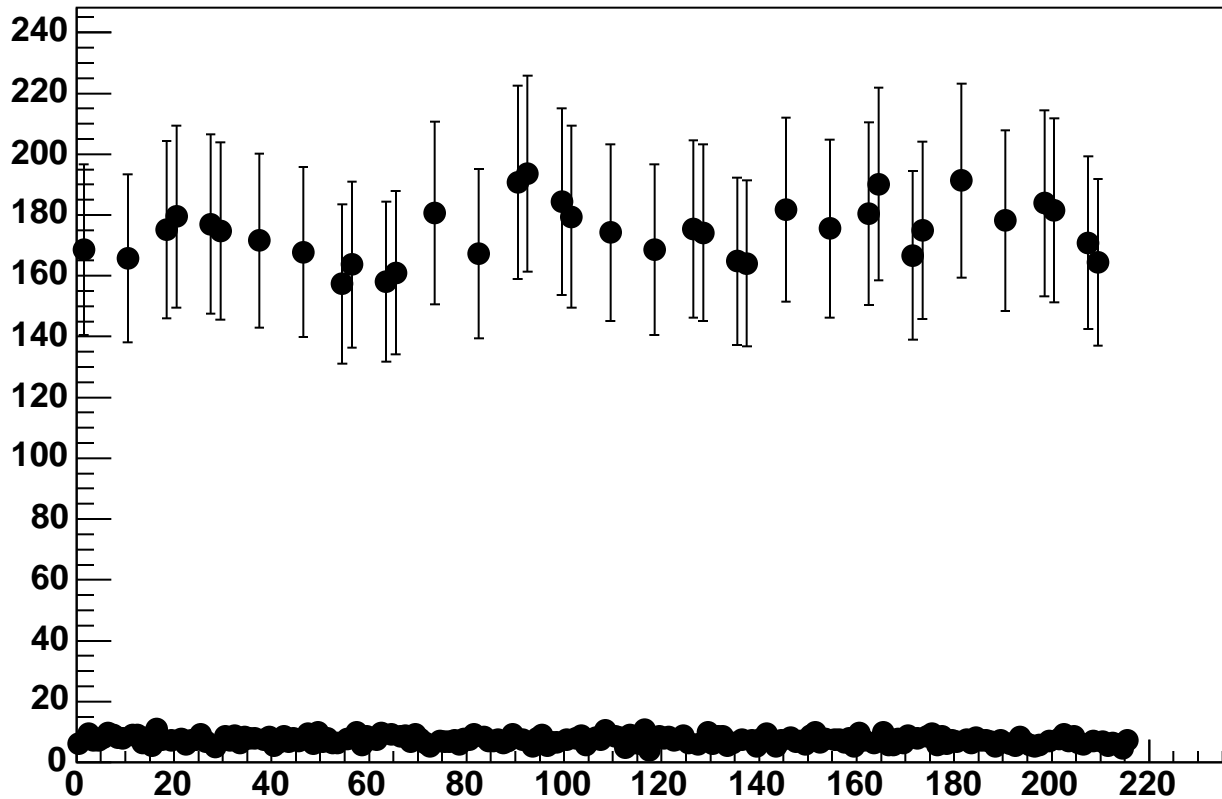
Enable 3, DAC=4000, Hold=0, ADC Noise vs 18*Chip+Chan



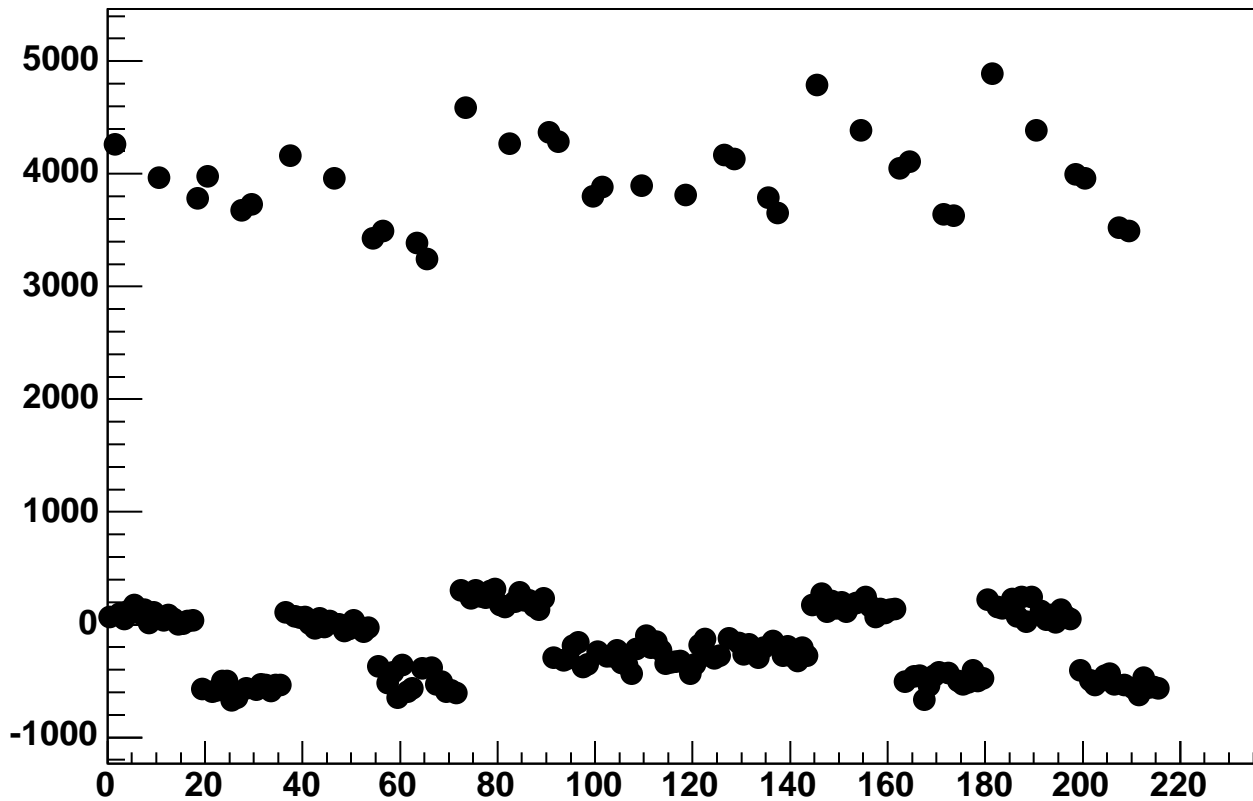
Enable 3, DAC=4000, Hold=5, ADC Mean vs 18*Chip+Chan



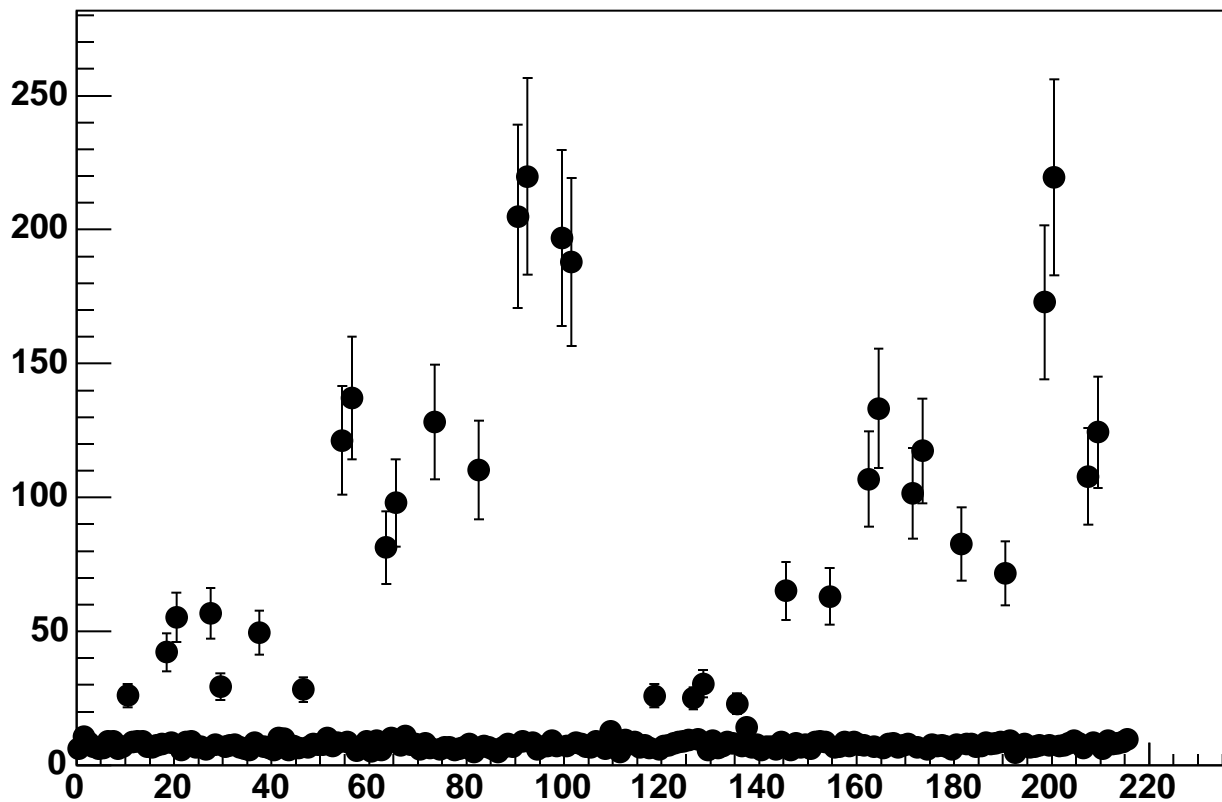
Enable 3, DAC=4000, Hold=5, ADC Noise vs 18*Chip+Chan



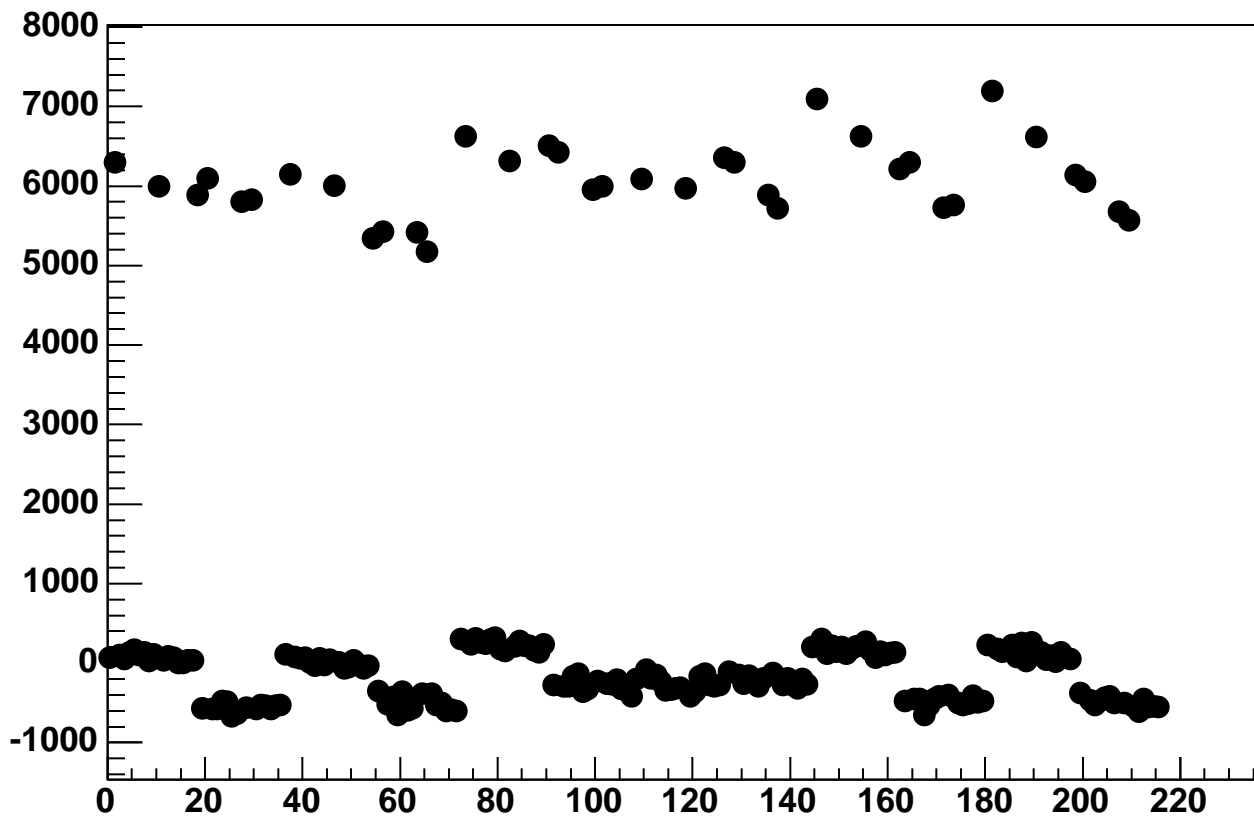
Enable 3, DAC=4000, Hold=10, ADC Mean vs 18*Chip+Chan



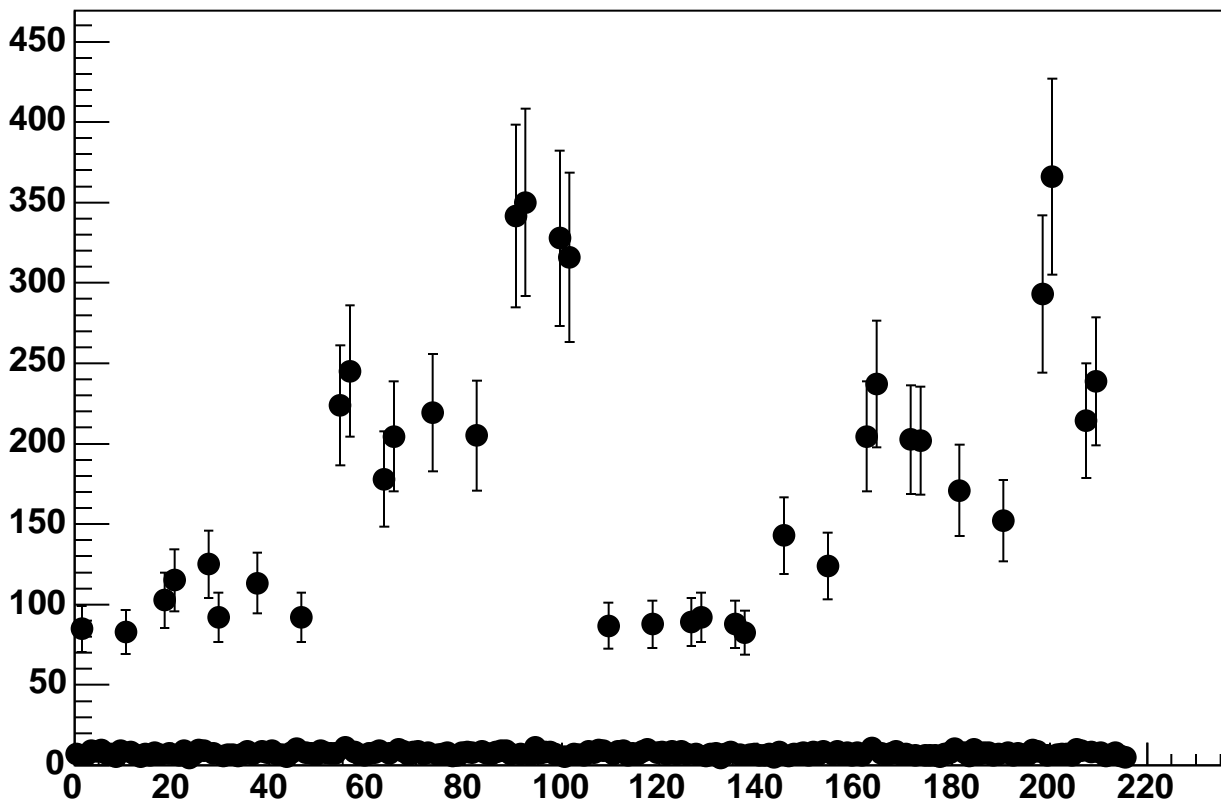
Enable 3, DAC=4000, Hold=10, ADC Noise vs 18*Chip+Chan



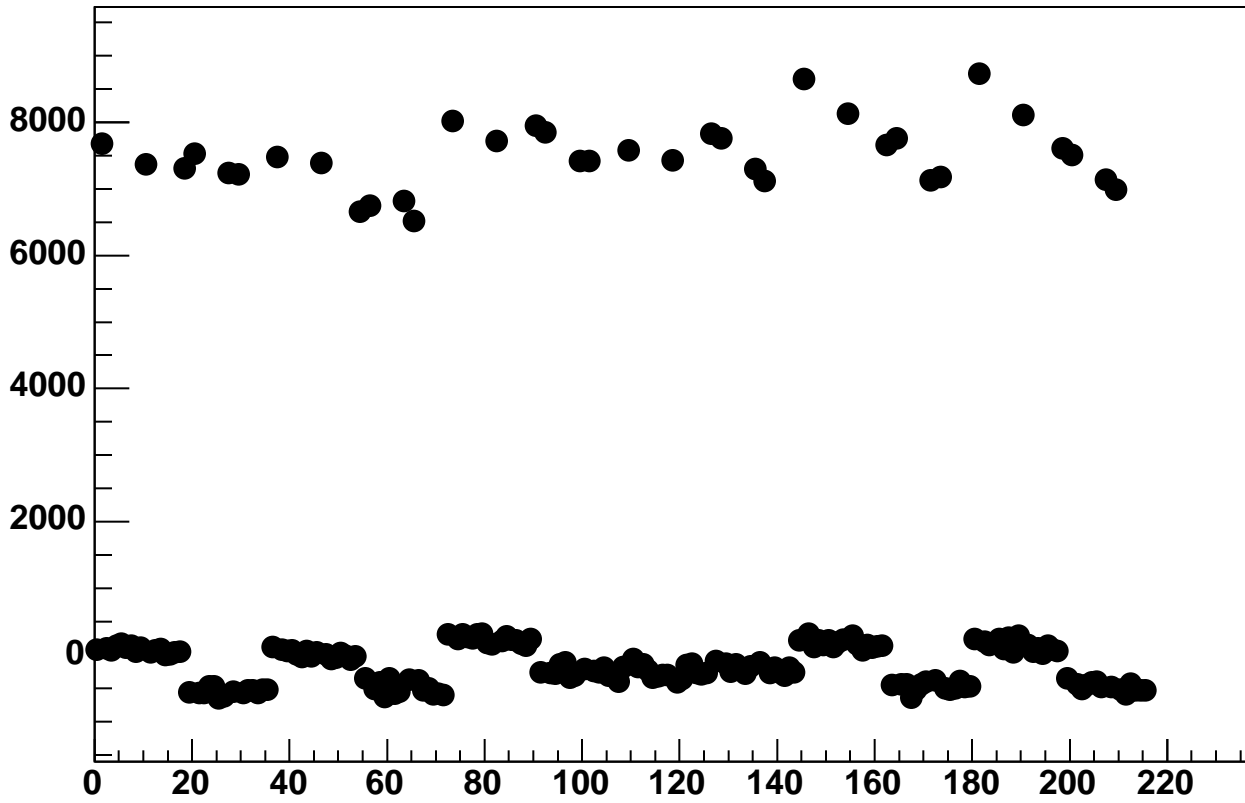
Enable 3, DAC=4000, Hold=15, ADC Mean vs 18*Chip+Chan



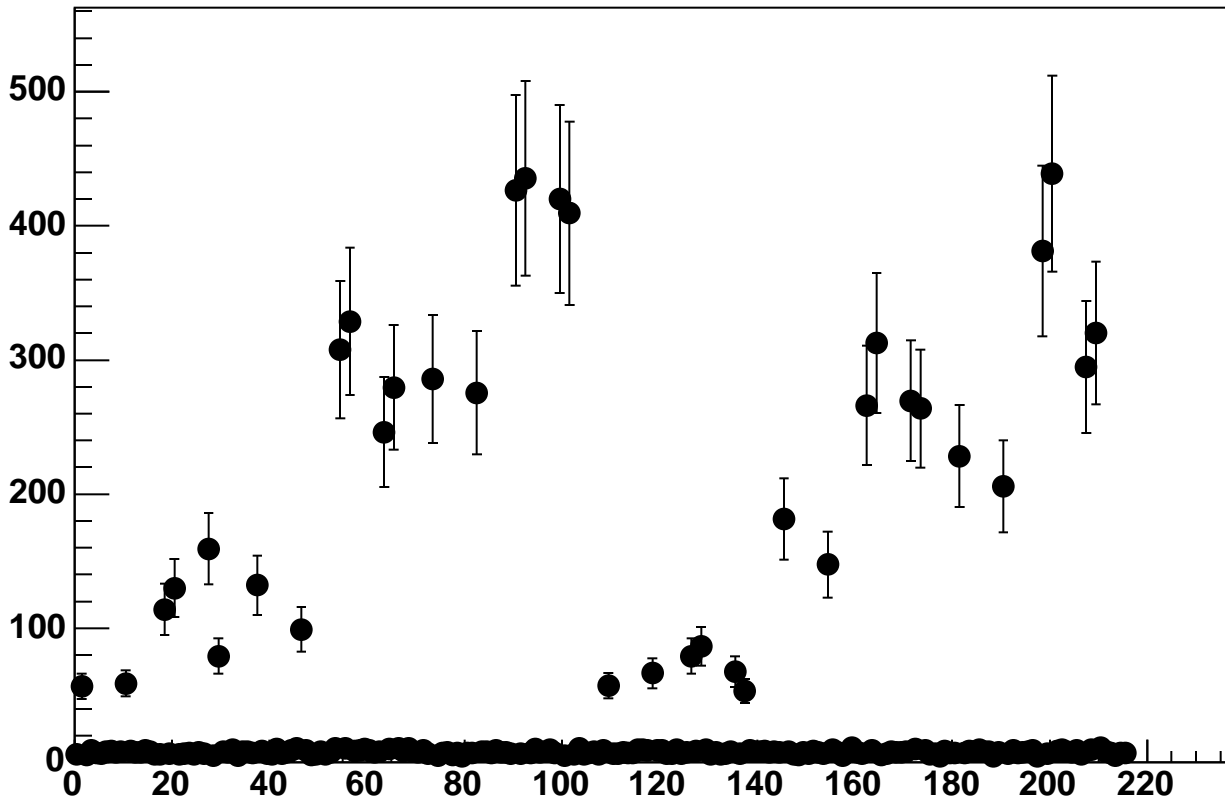
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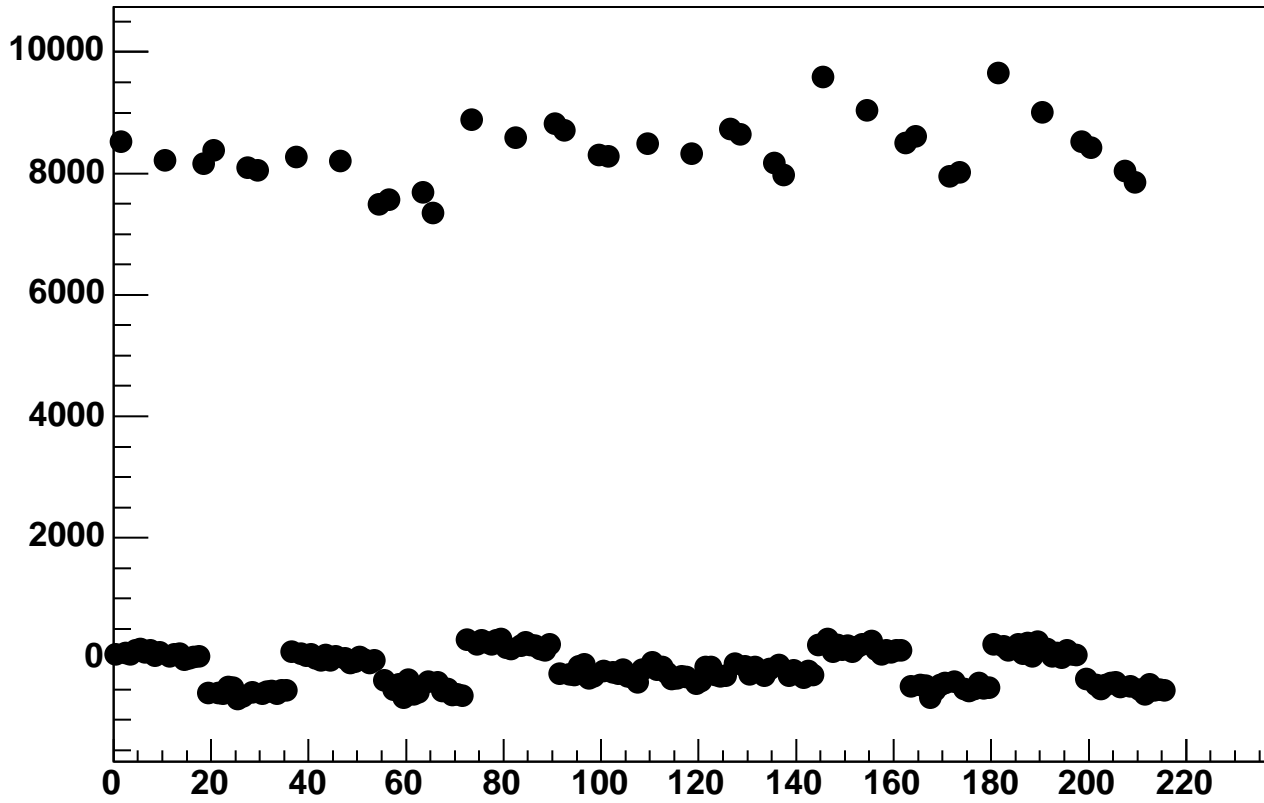
Enable 3, DAC=4000, Hold=20, ADC Mean vs 18*Chip+Chan



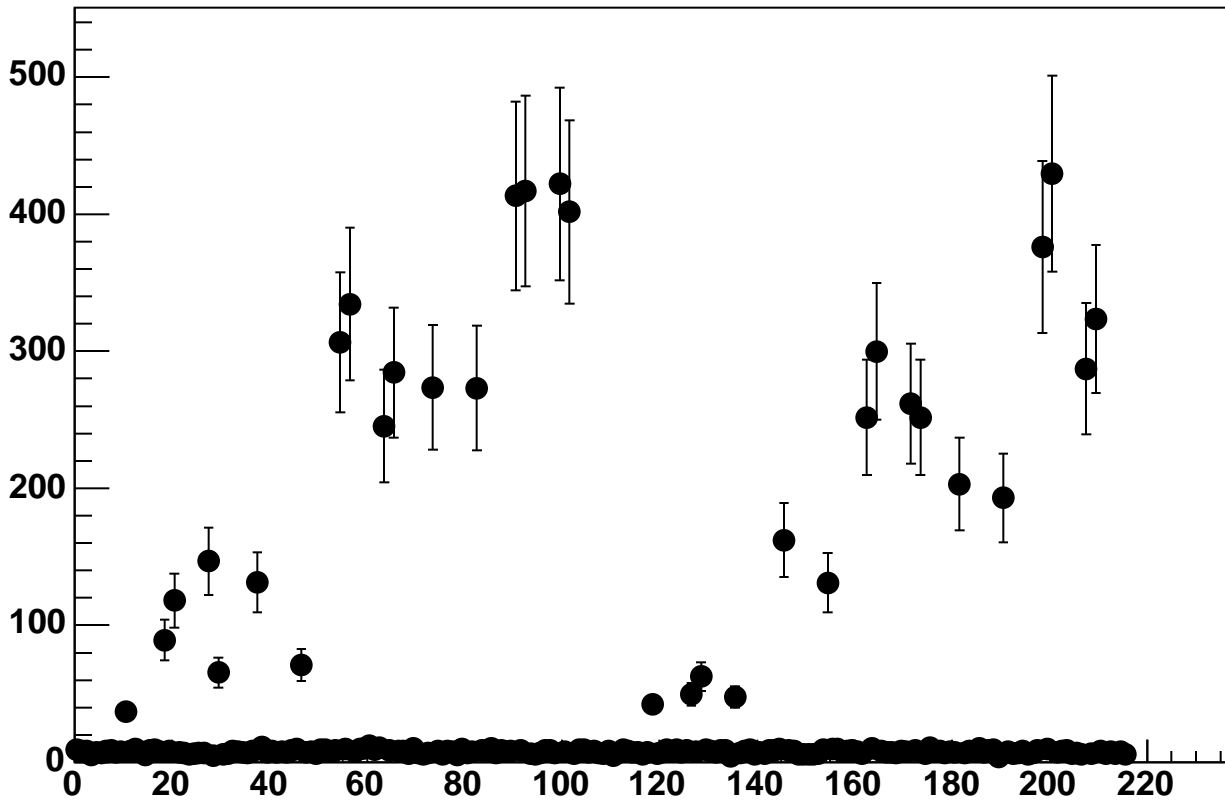
Enable 3, DAC=4000, Hold=20, ADC Noise vs 18*Chip+Chan



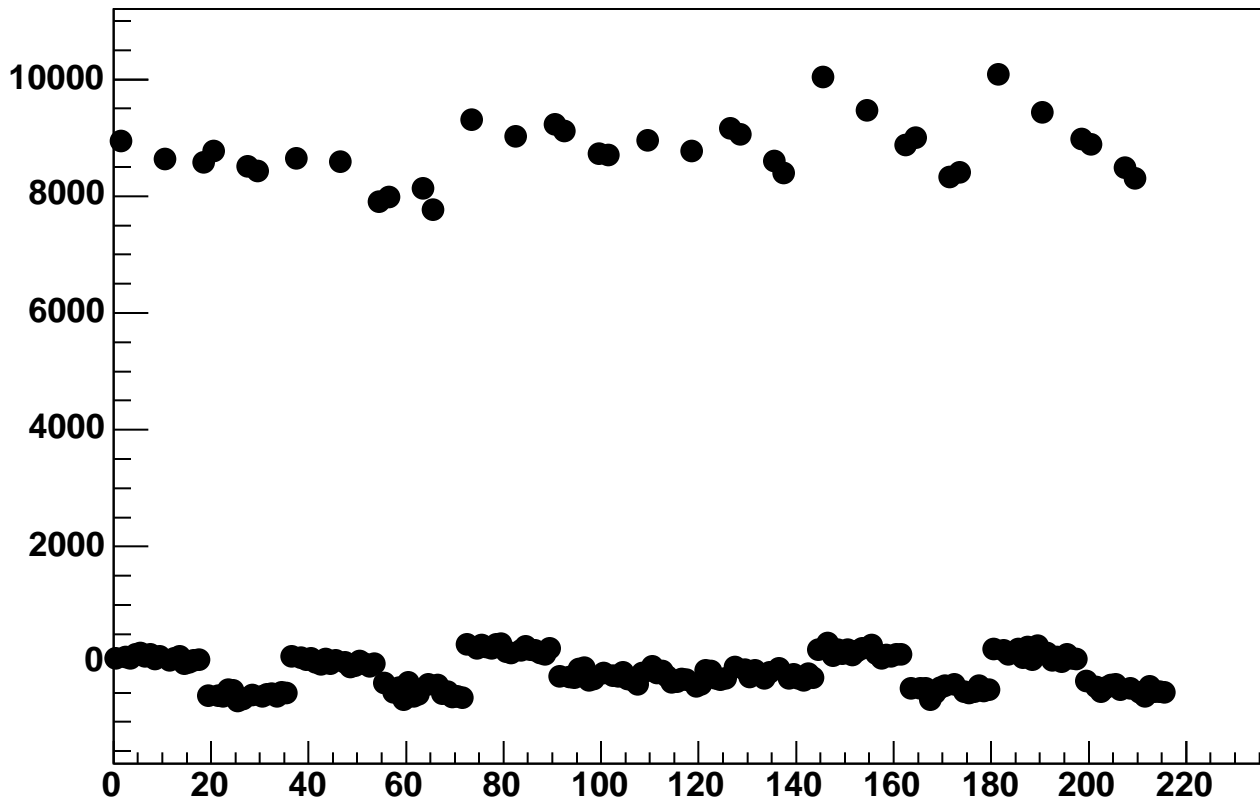
Enable 3, DAC=4000, Hold=25, ADC Mean vs 18*Chip+Chan



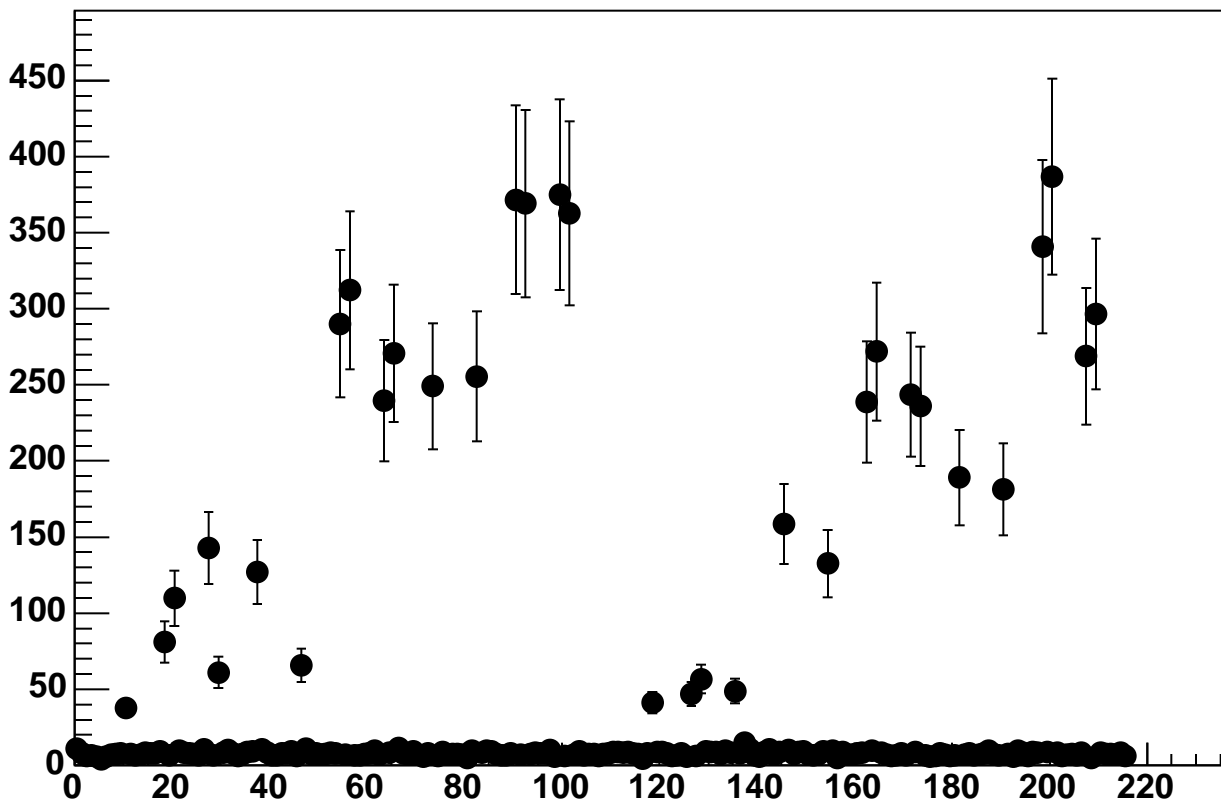
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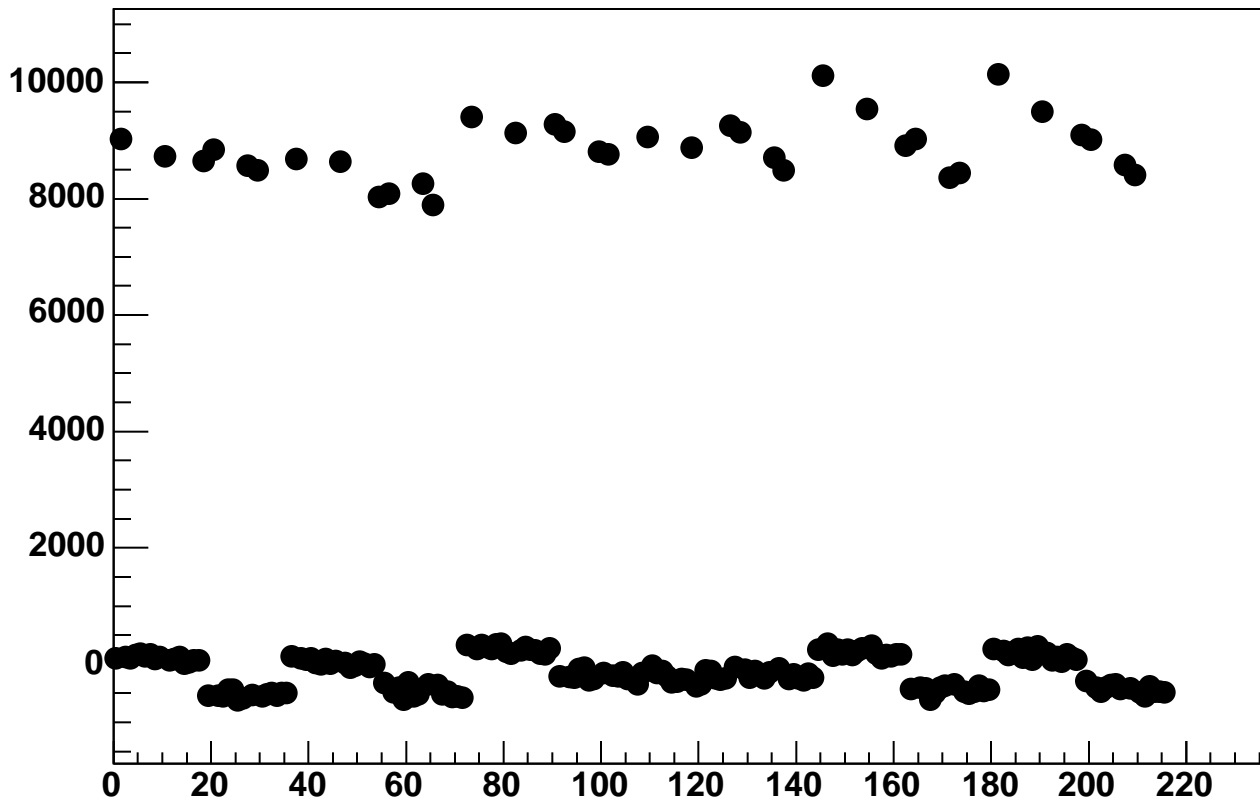
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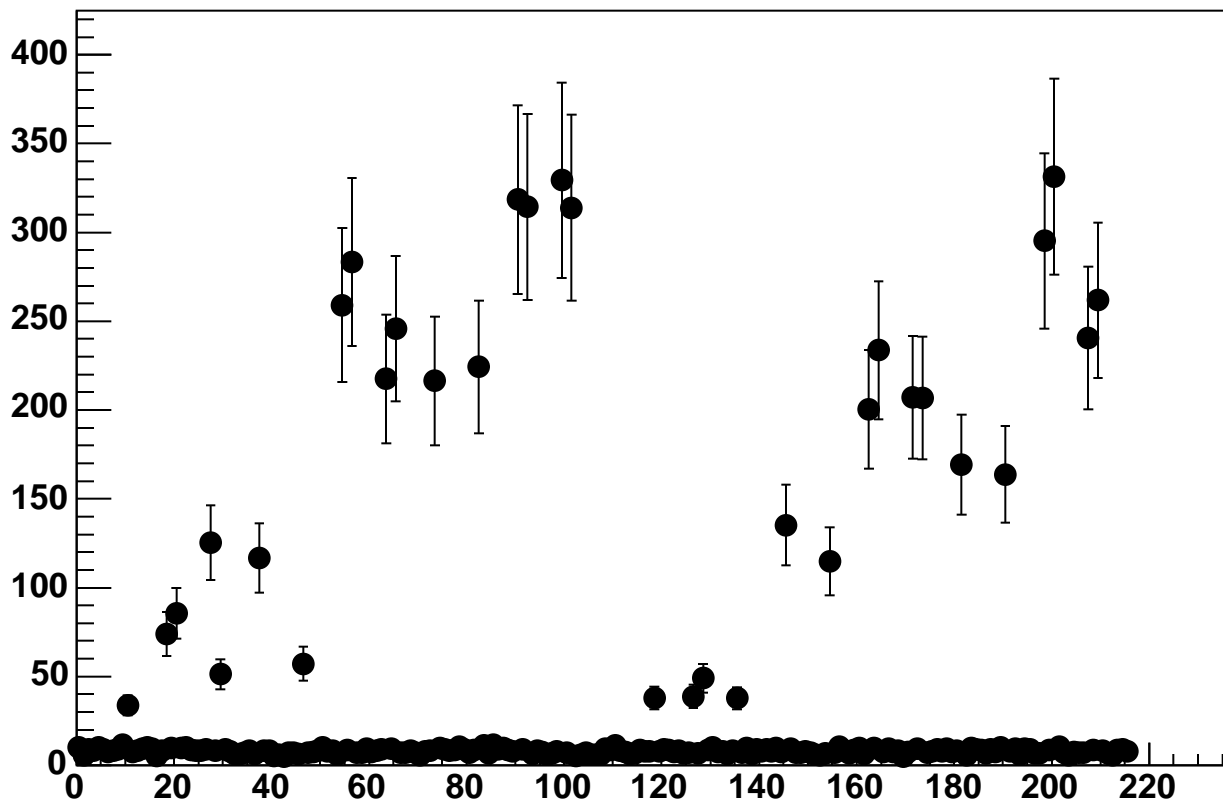
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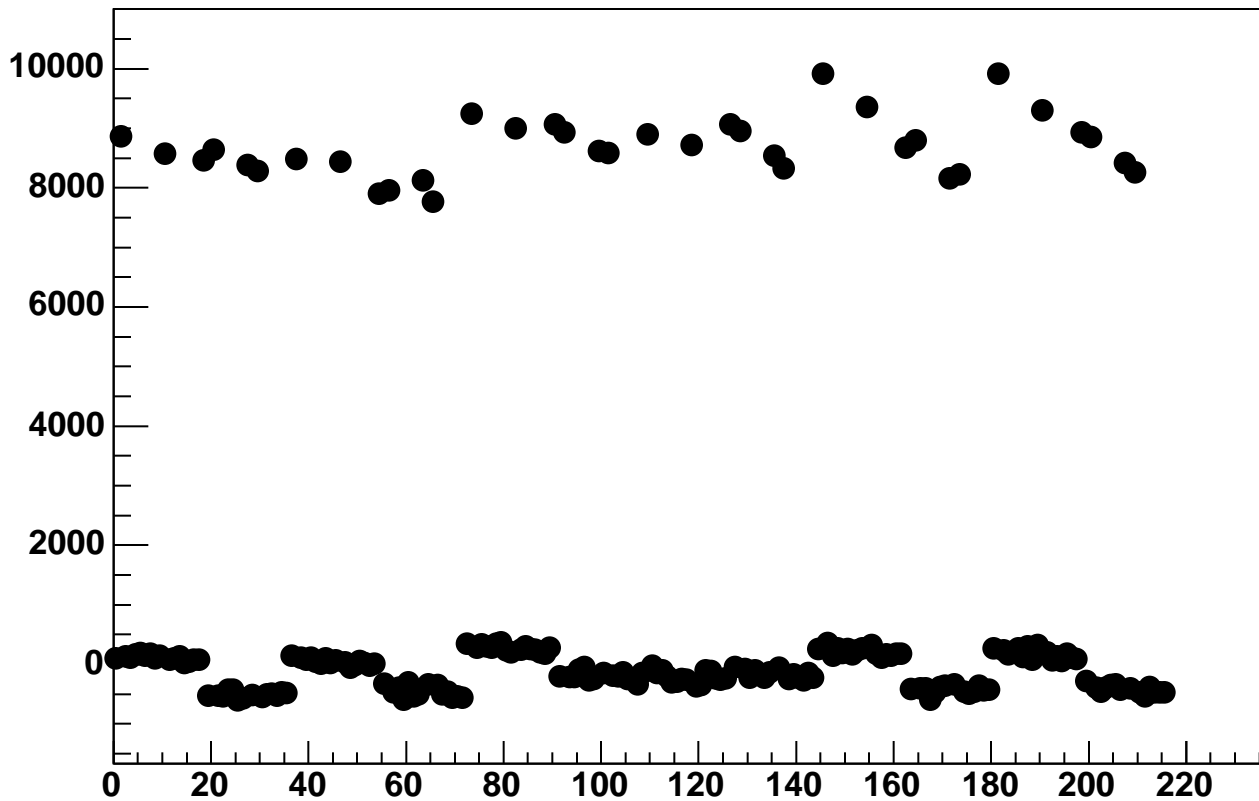
Enable 3, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



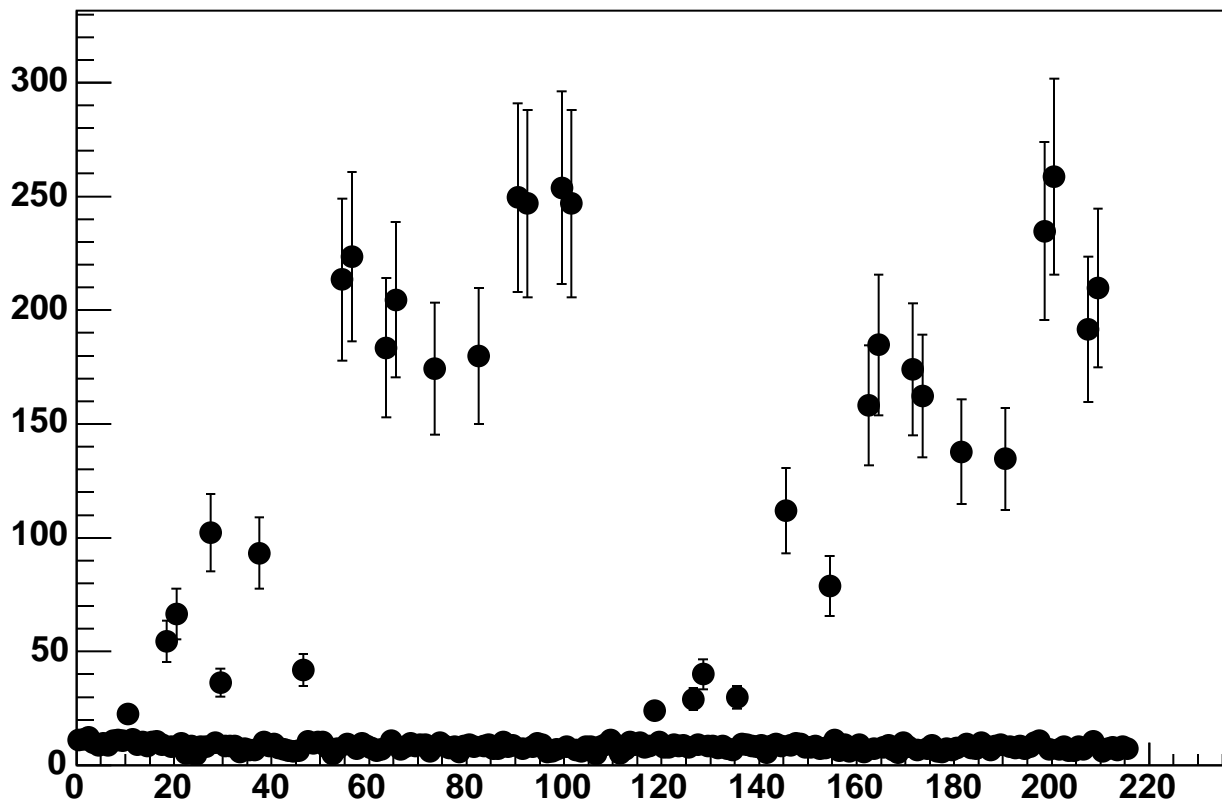
Enable 3, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



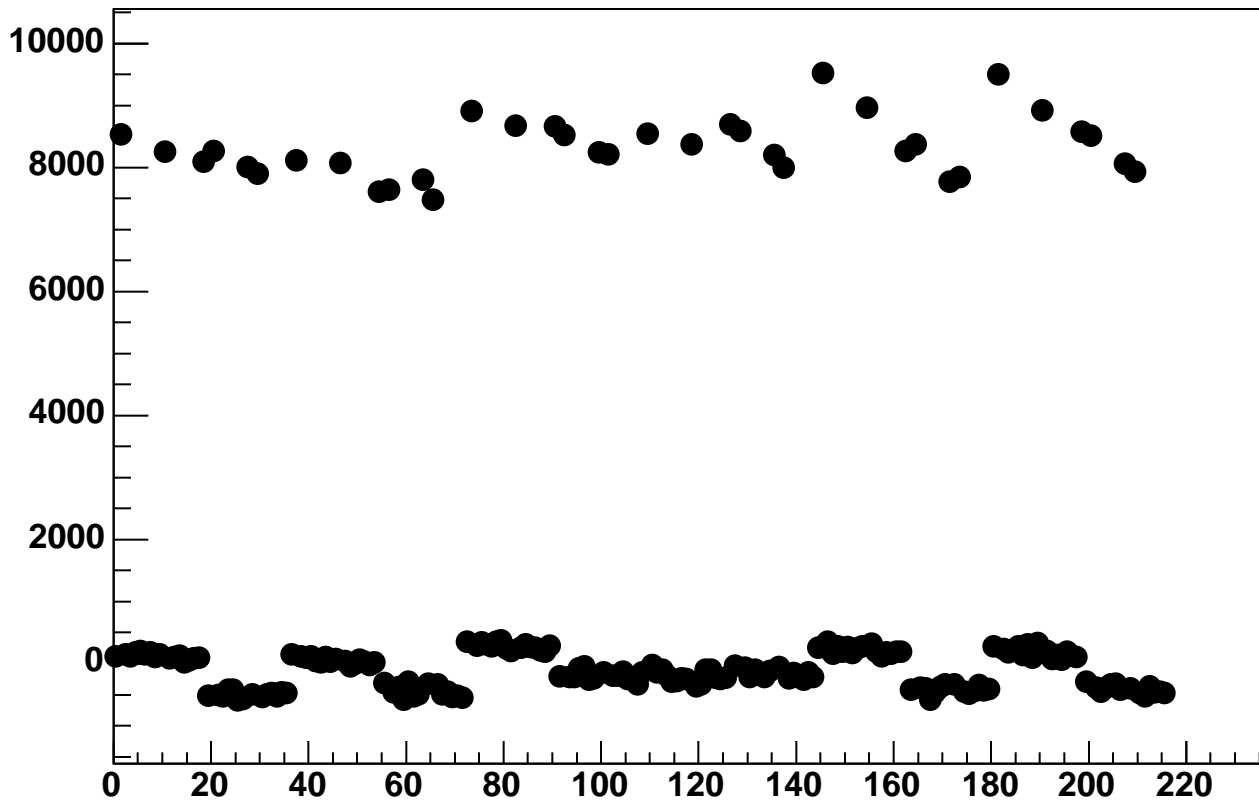
Enable 3, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



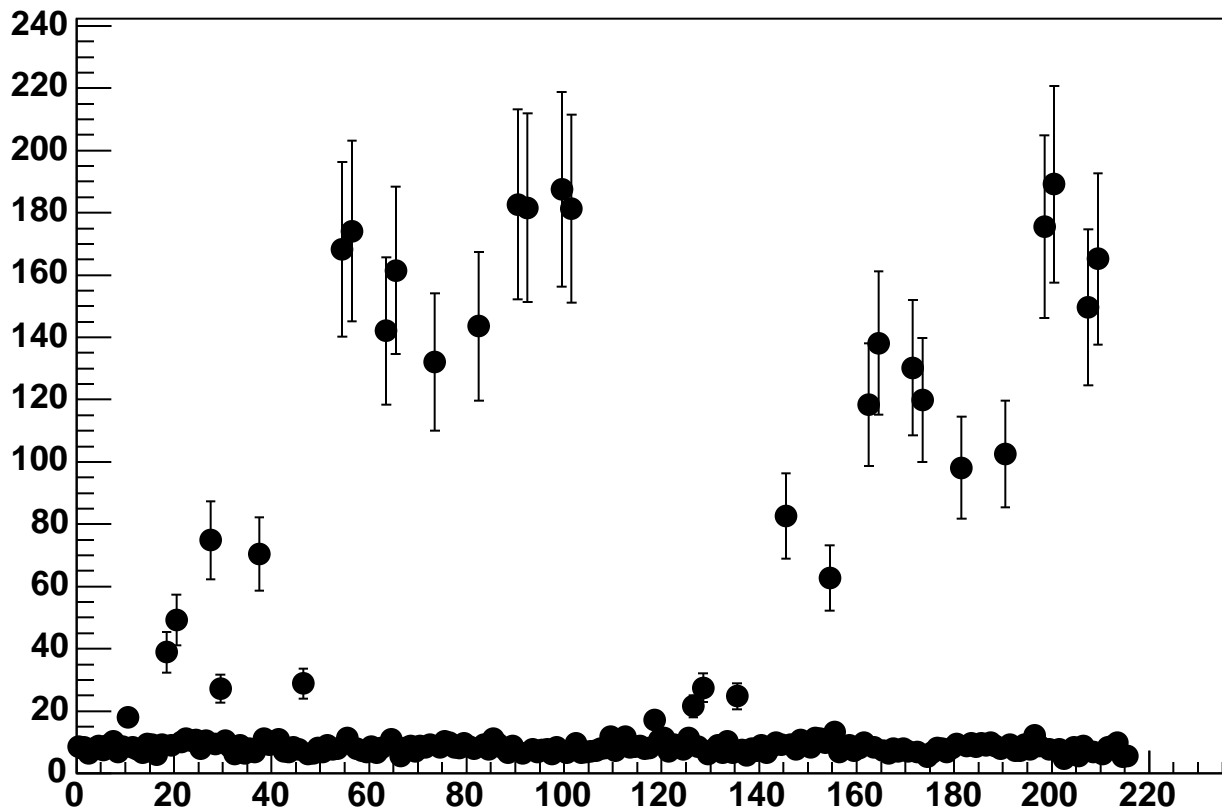
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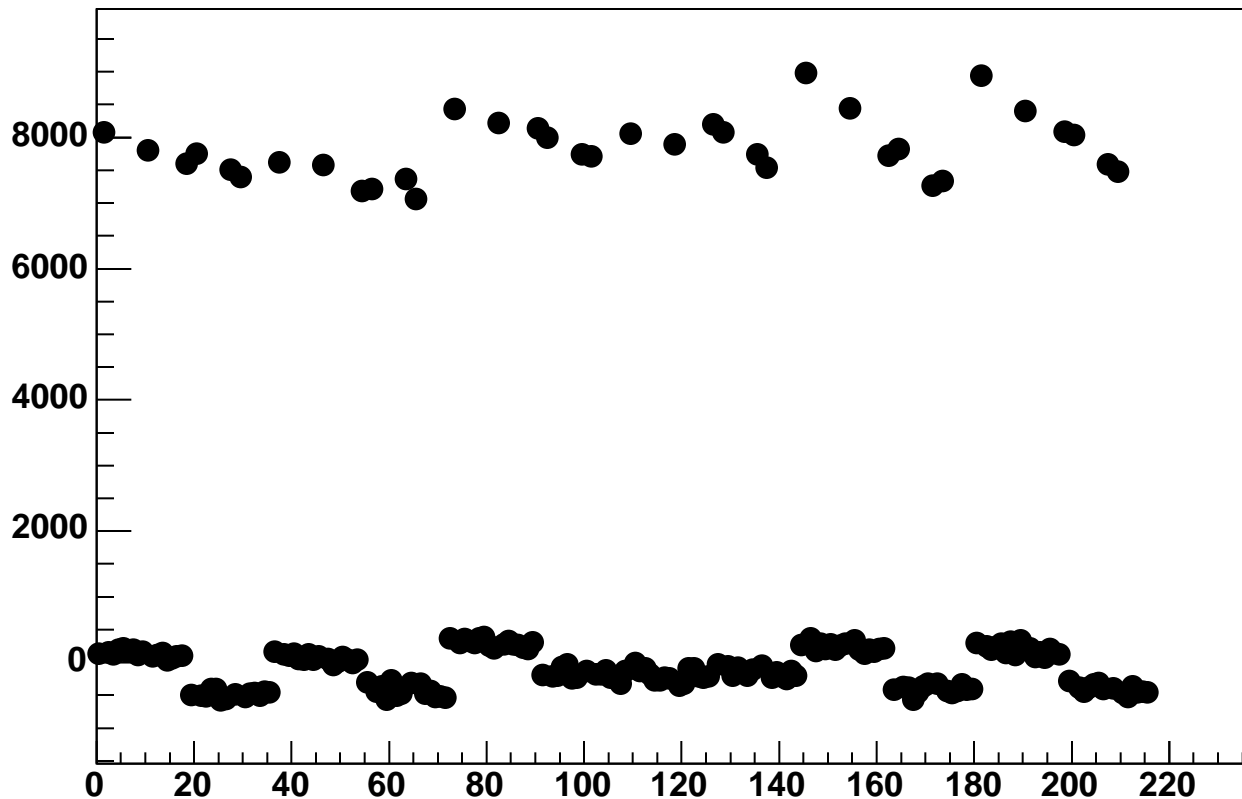
Enable 3, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



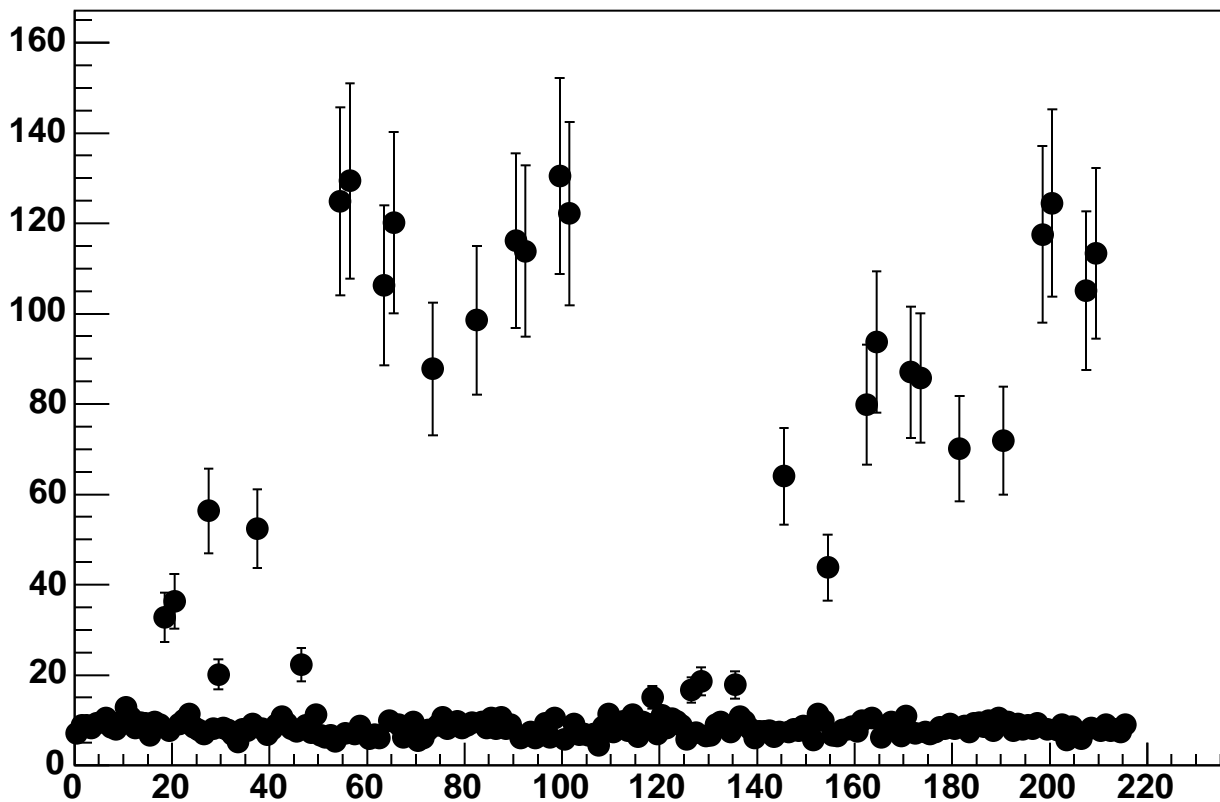
Enable 3, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



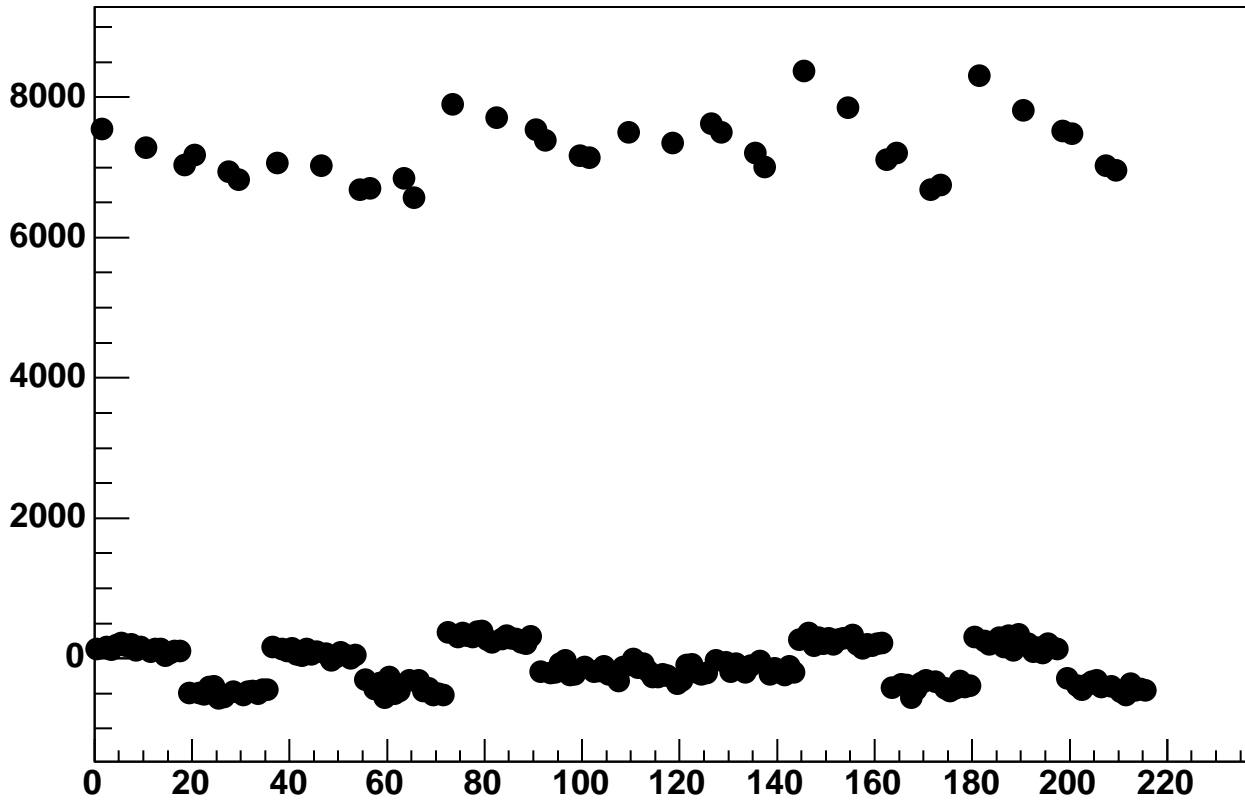
Enable 3, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



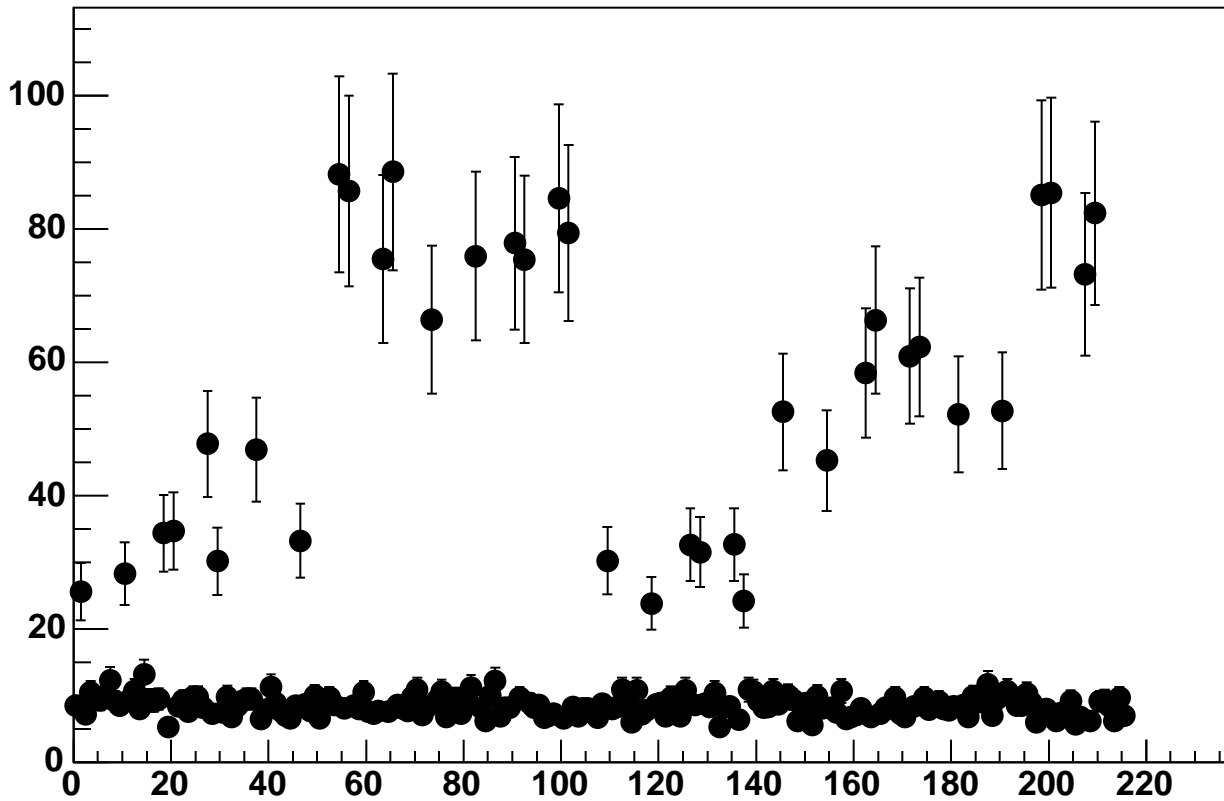
Enable 3, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



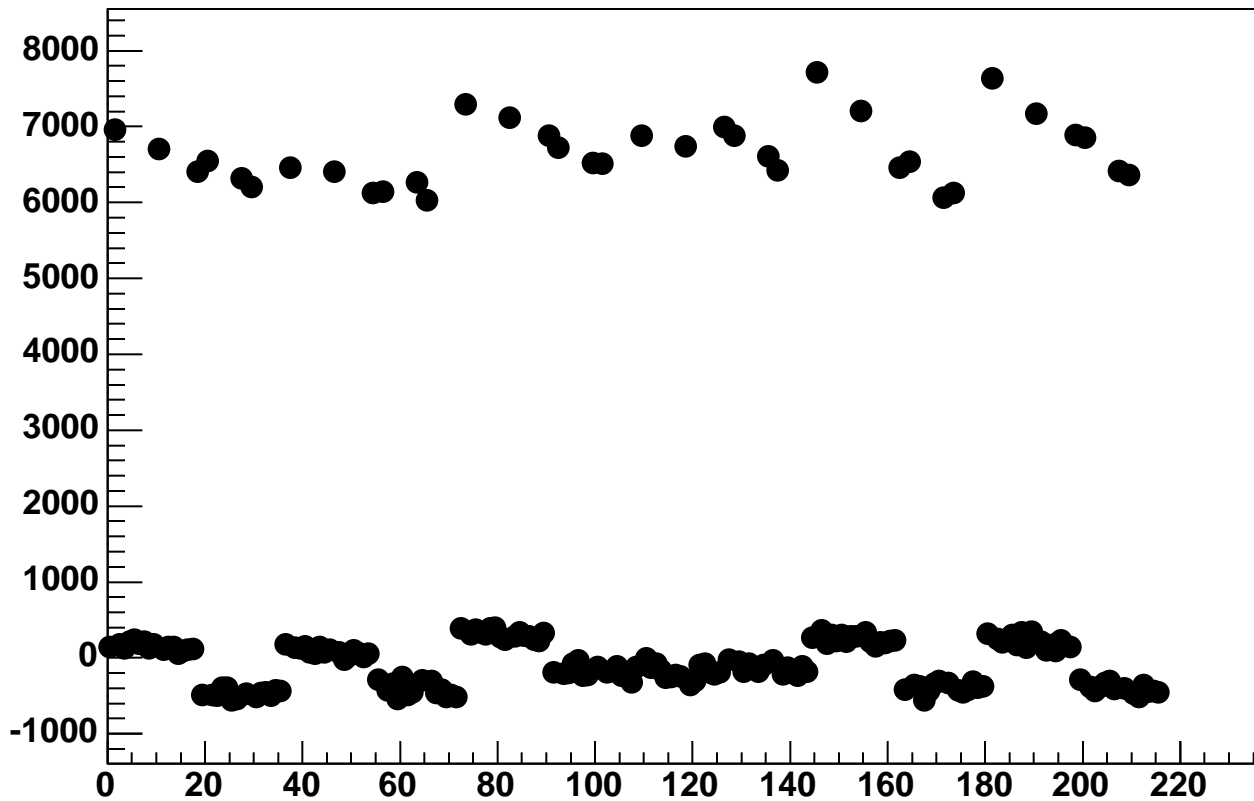
Enable 3, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



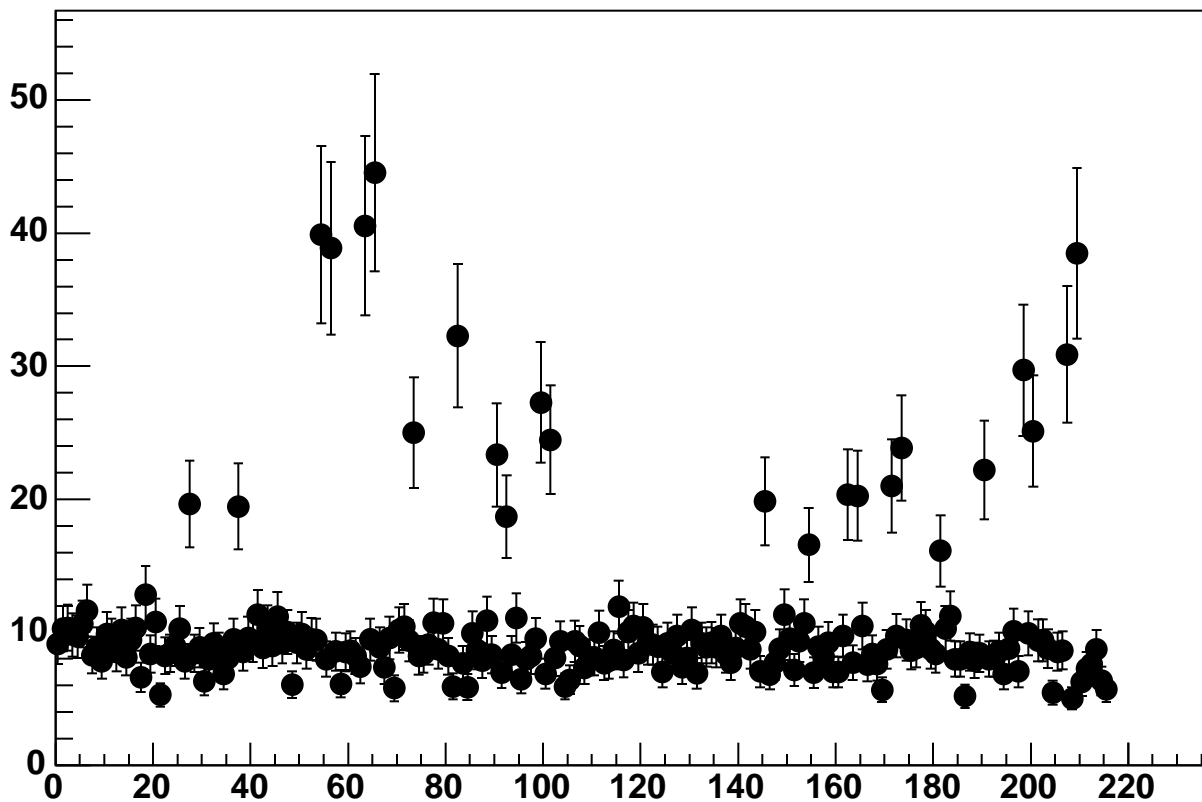
Enable 3, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



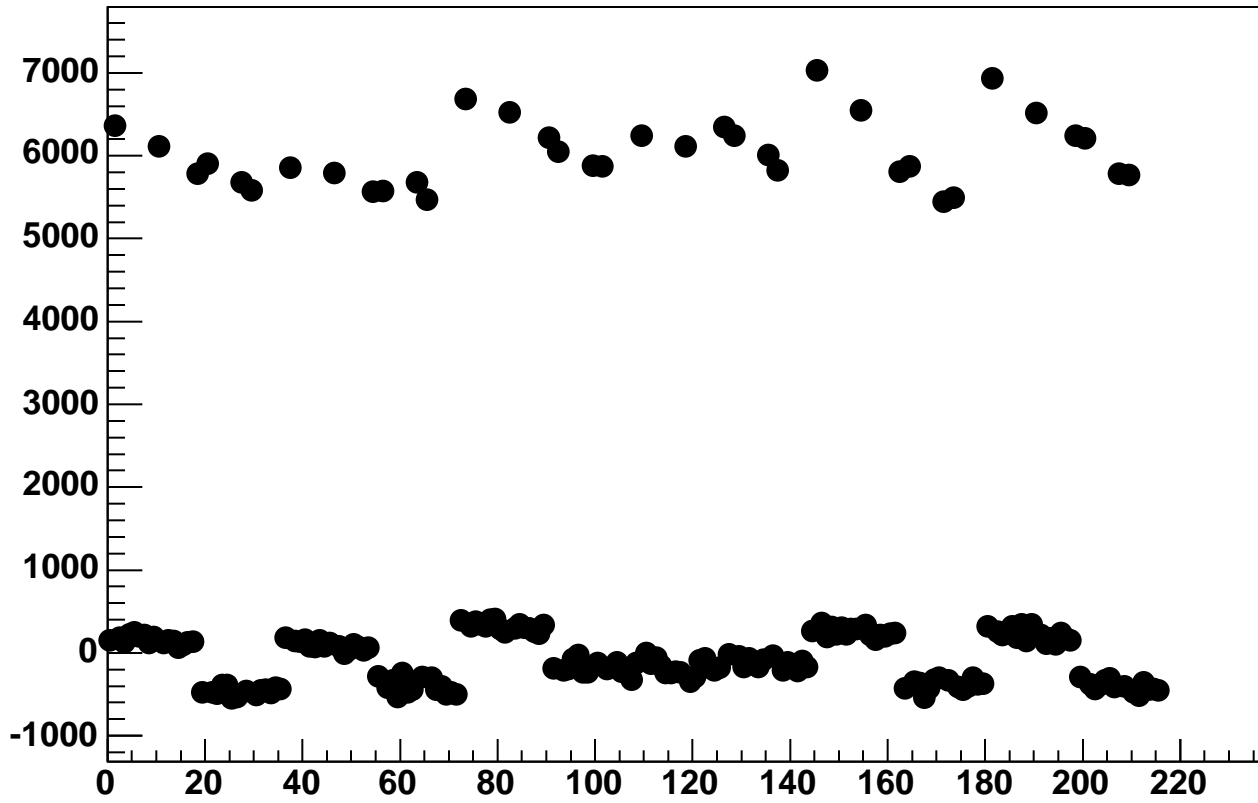
Enable 3, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



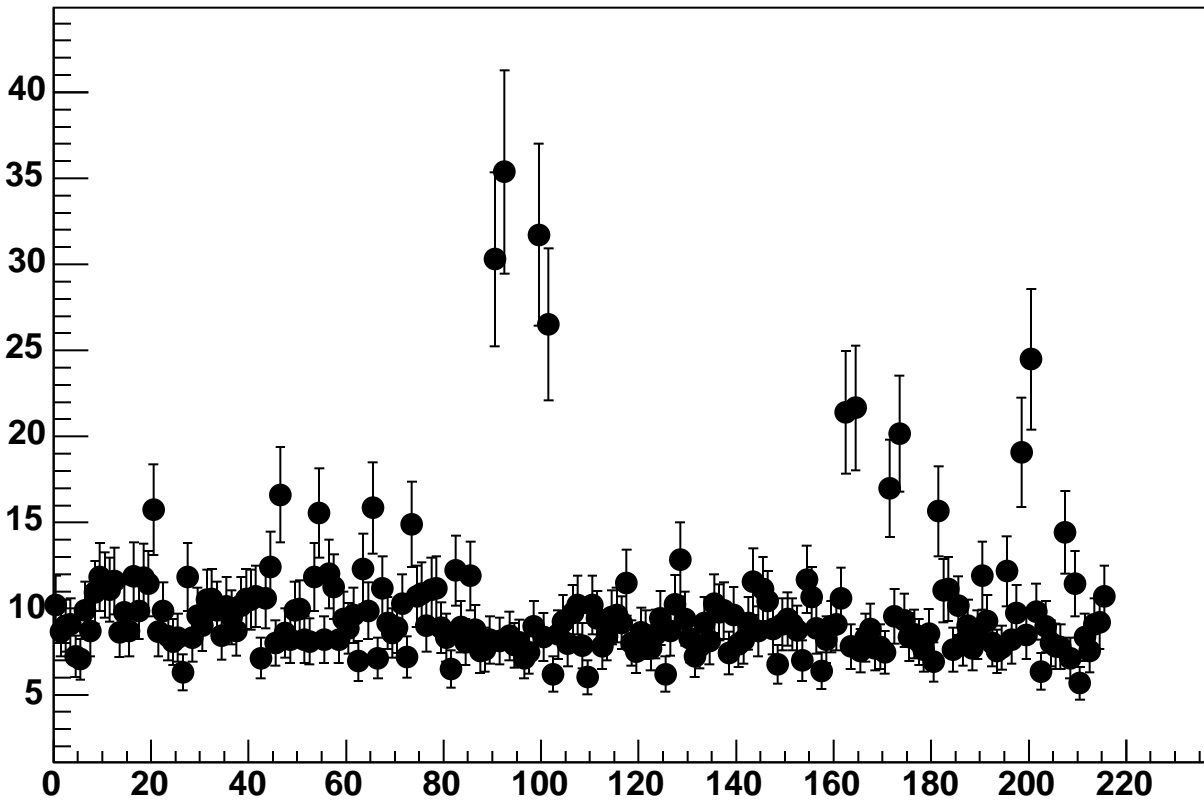
Enable 3, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



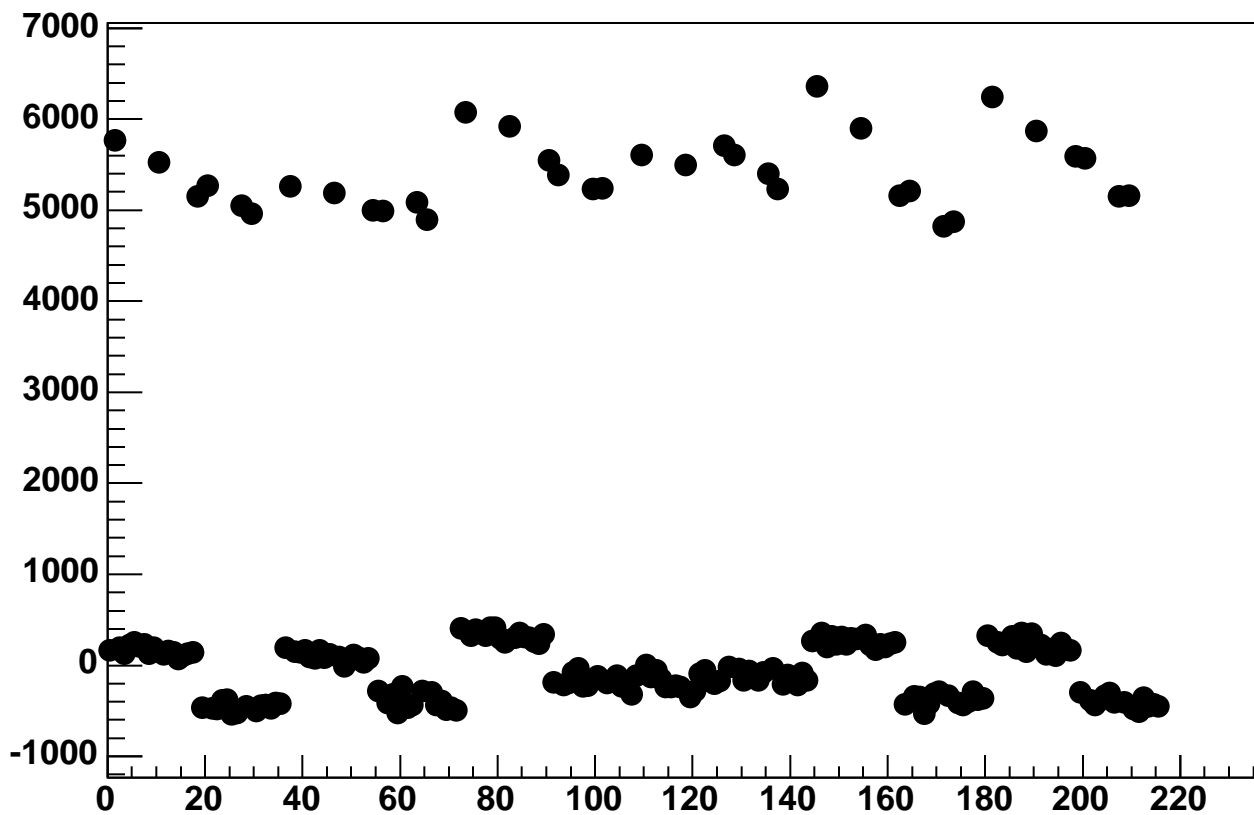
Enable 3, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



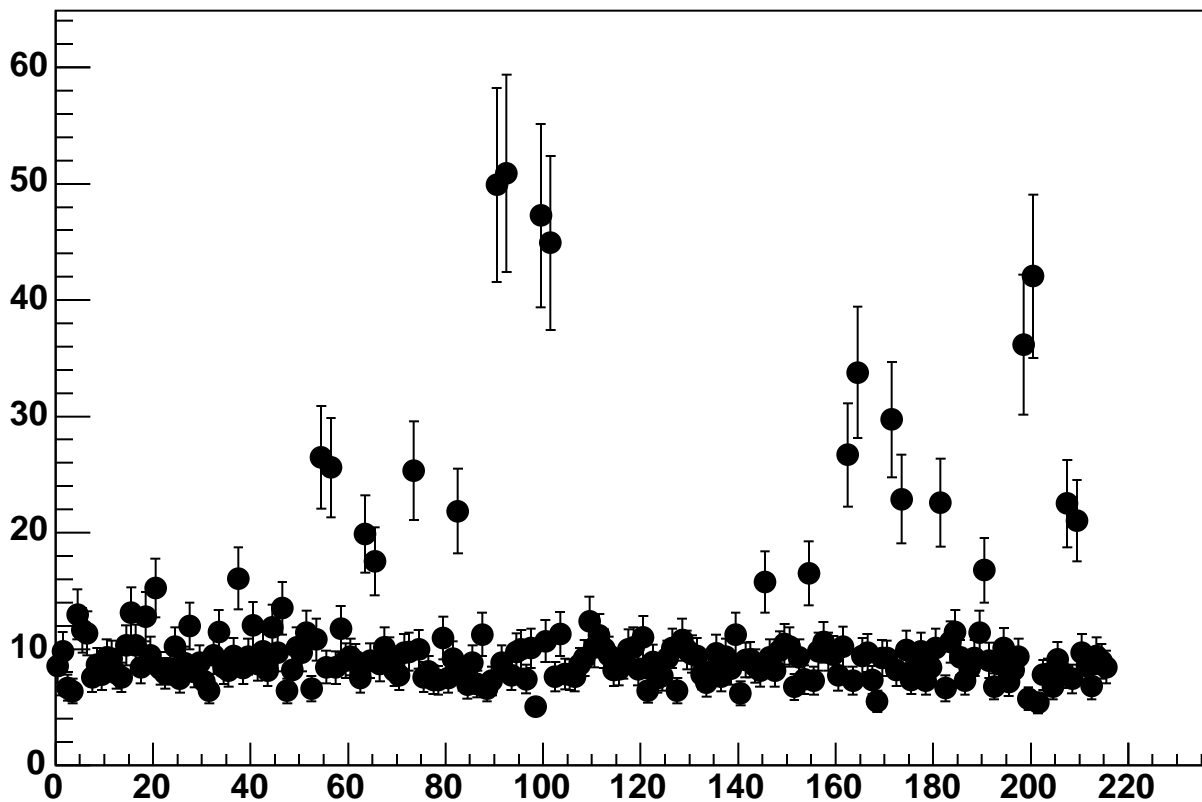
Enable 3, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



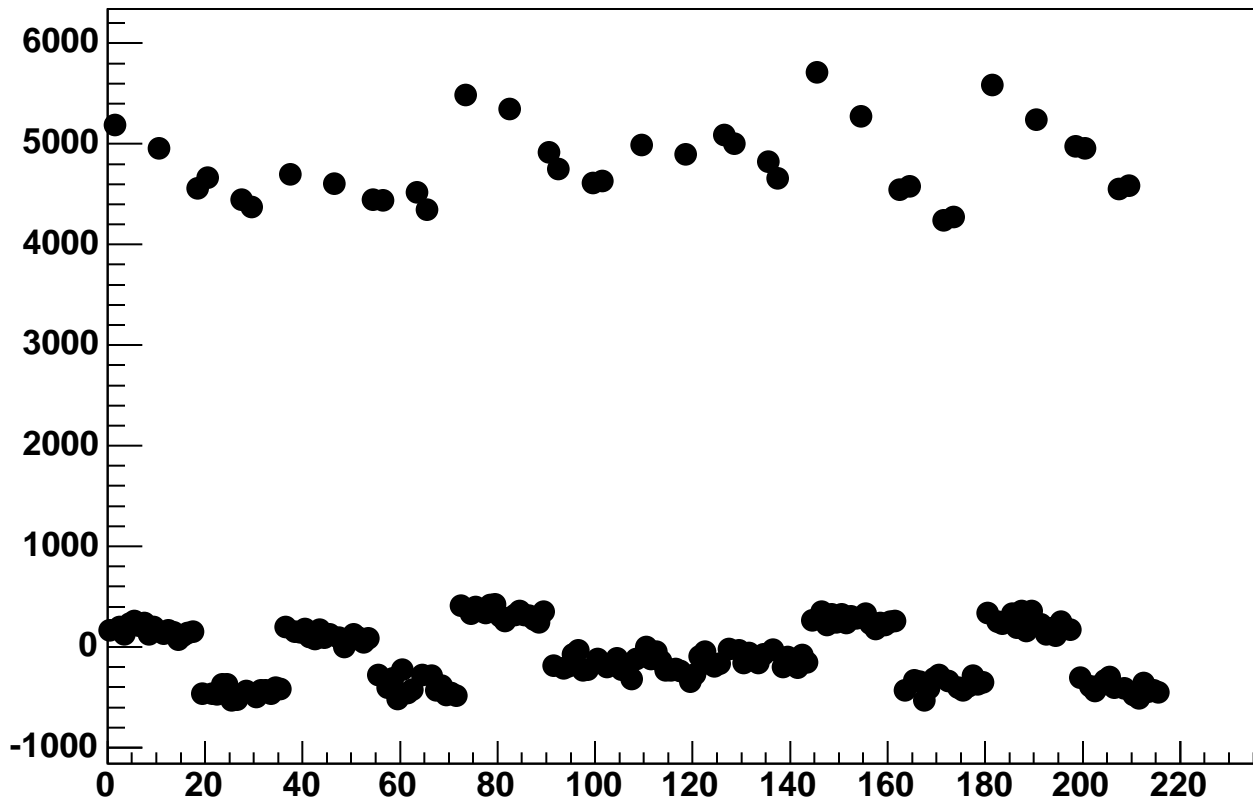
Enable 3, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



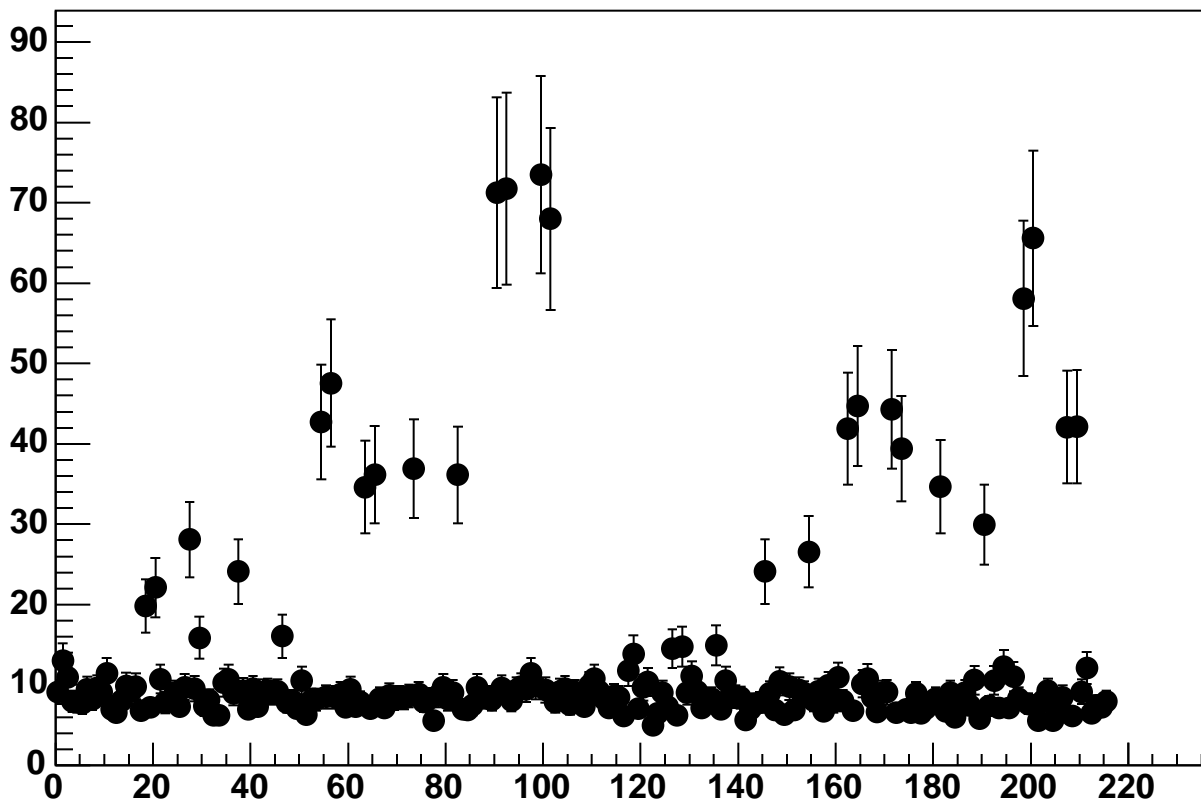
Enable 3, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



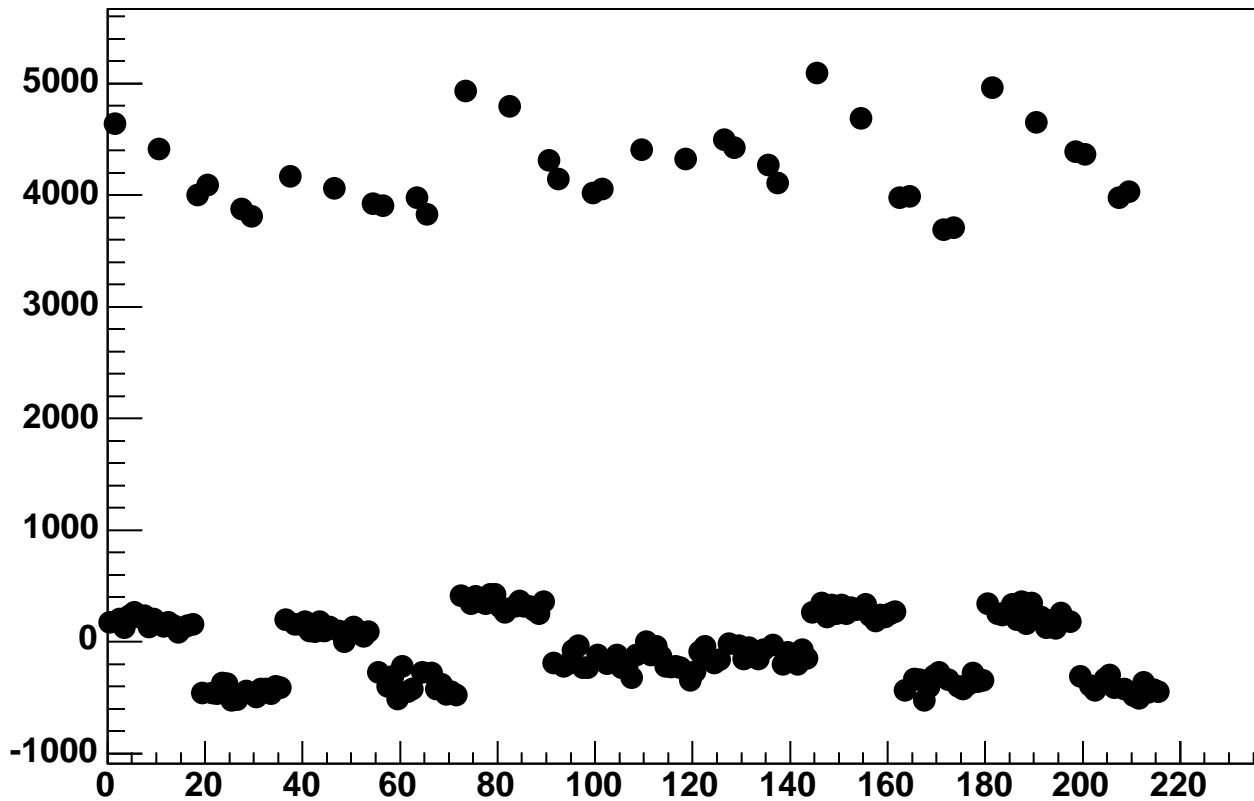
Enable 3, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



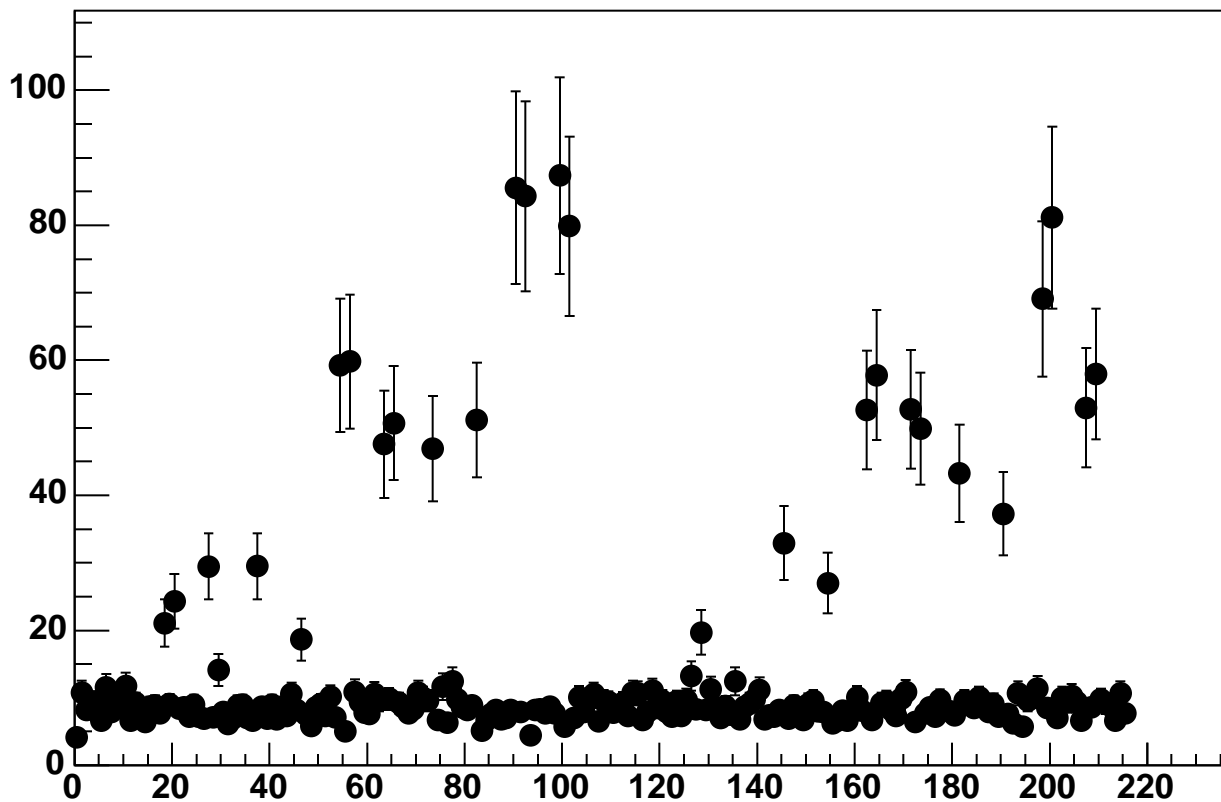
Enable 3, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



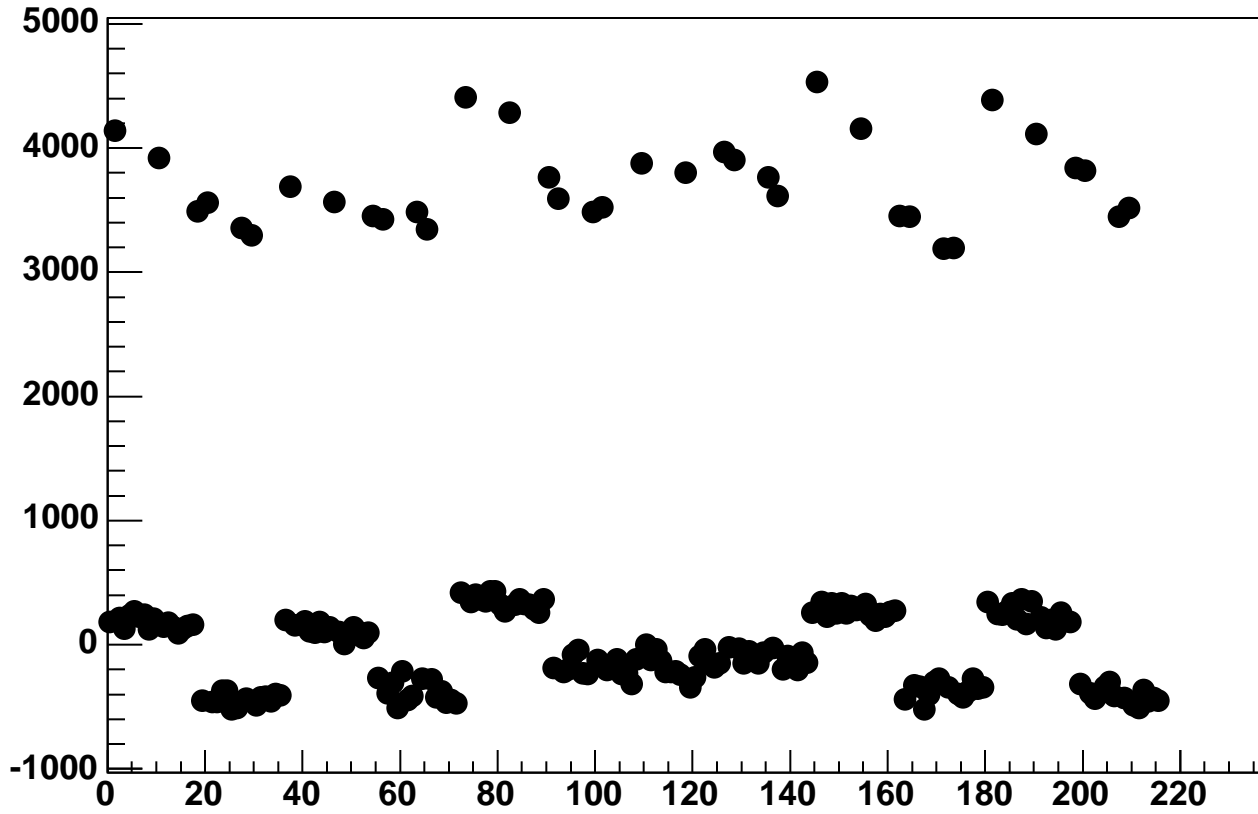
Enable 3, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



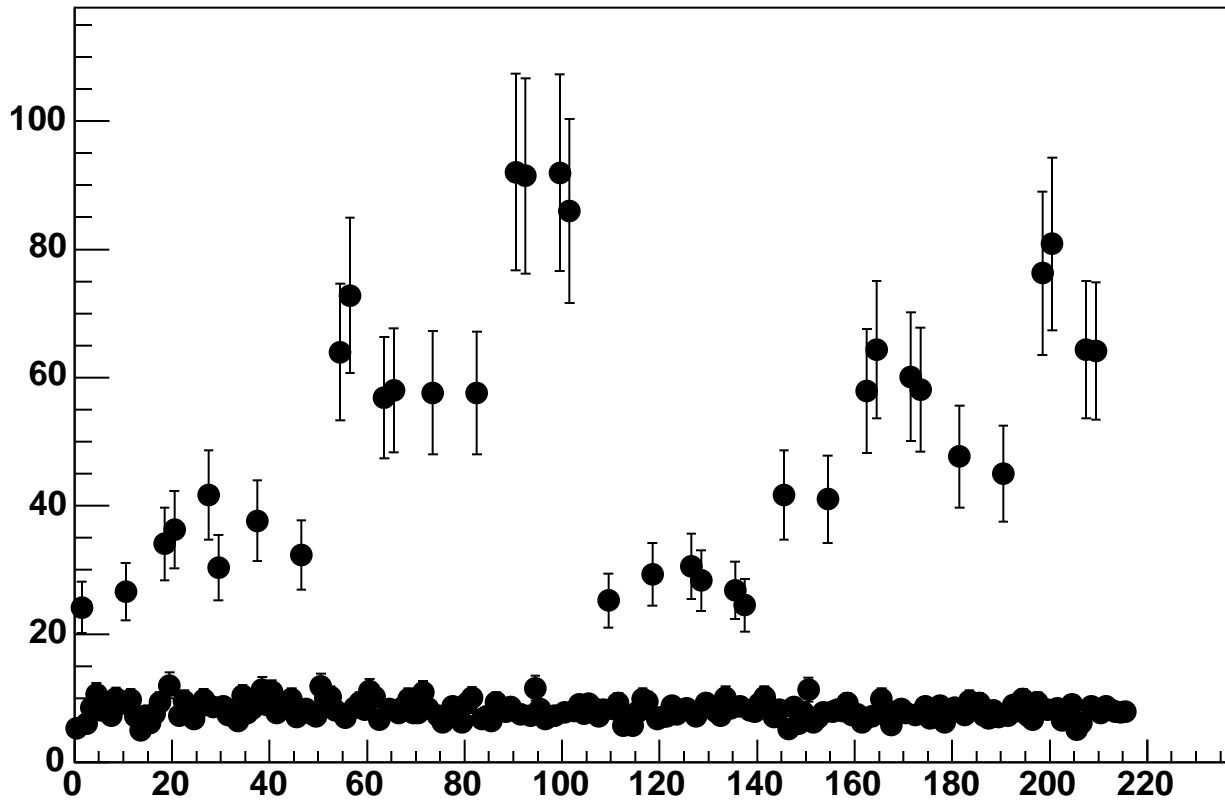
Enable 3, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



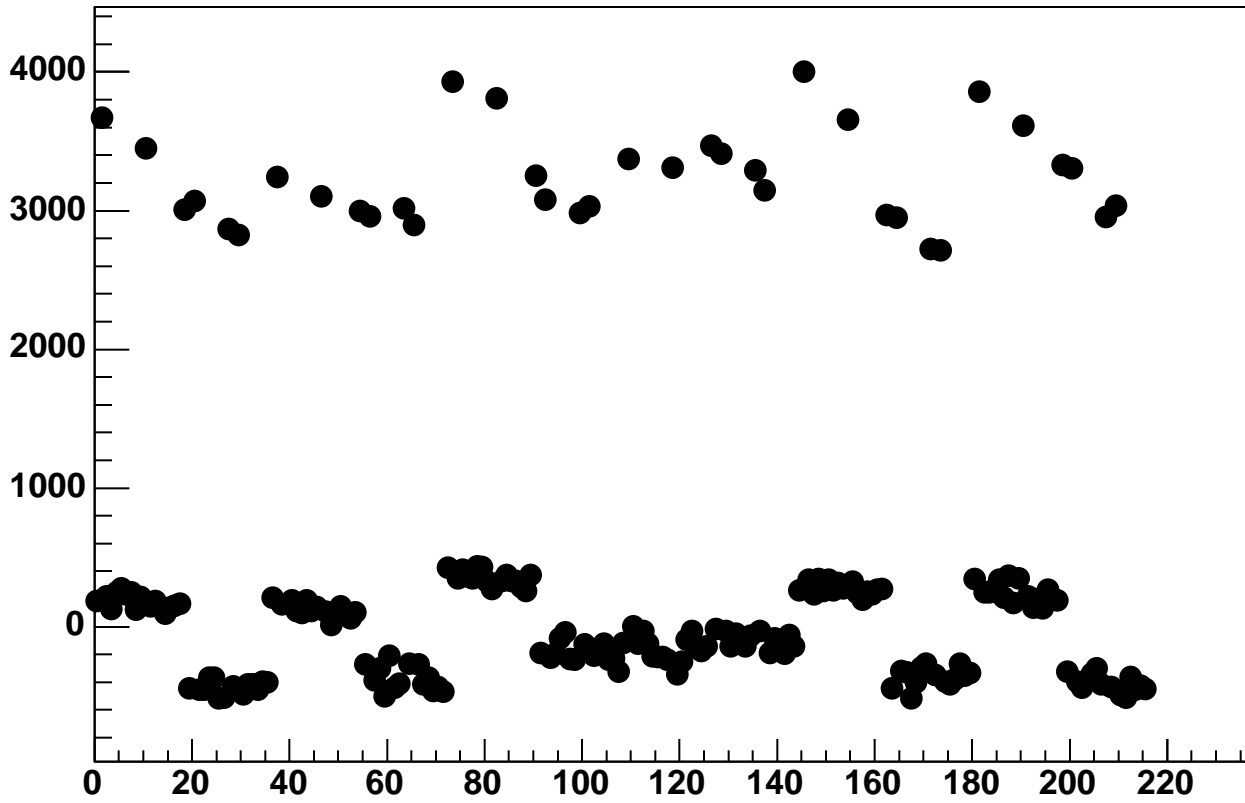
Enable 3, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



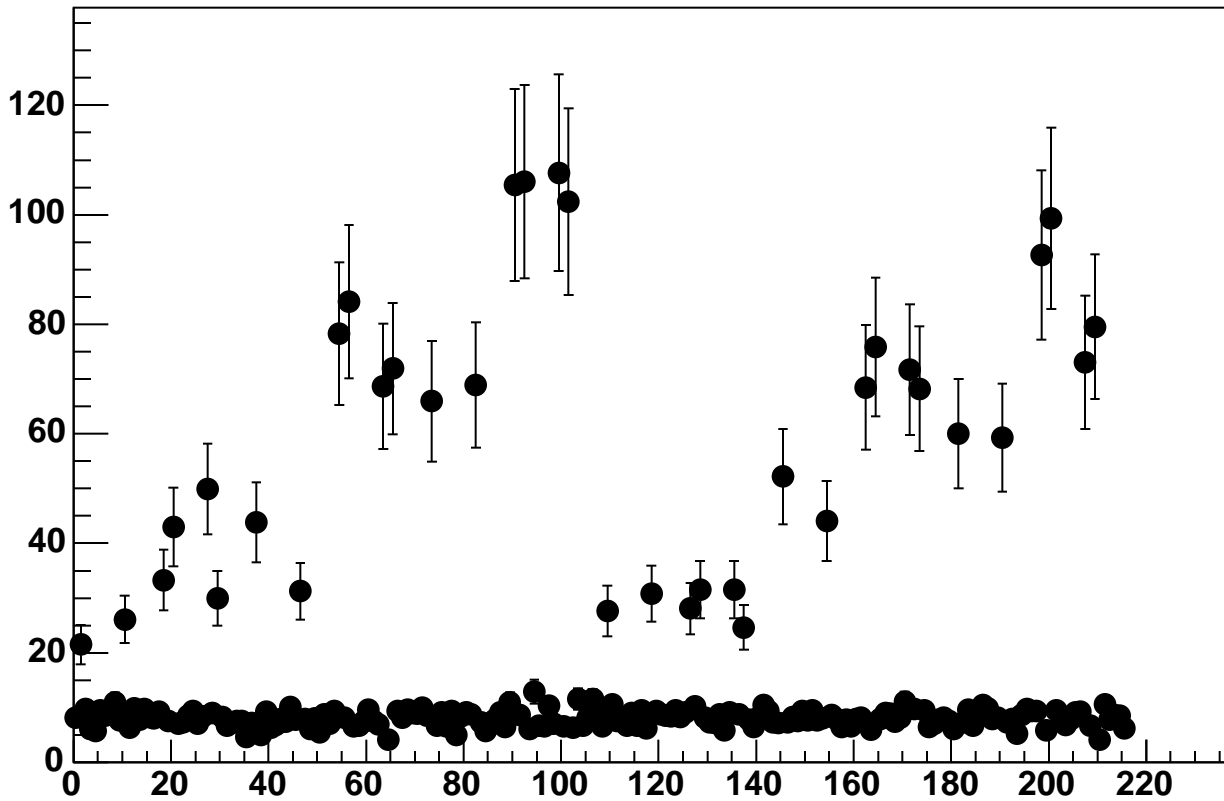
Enable 3, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



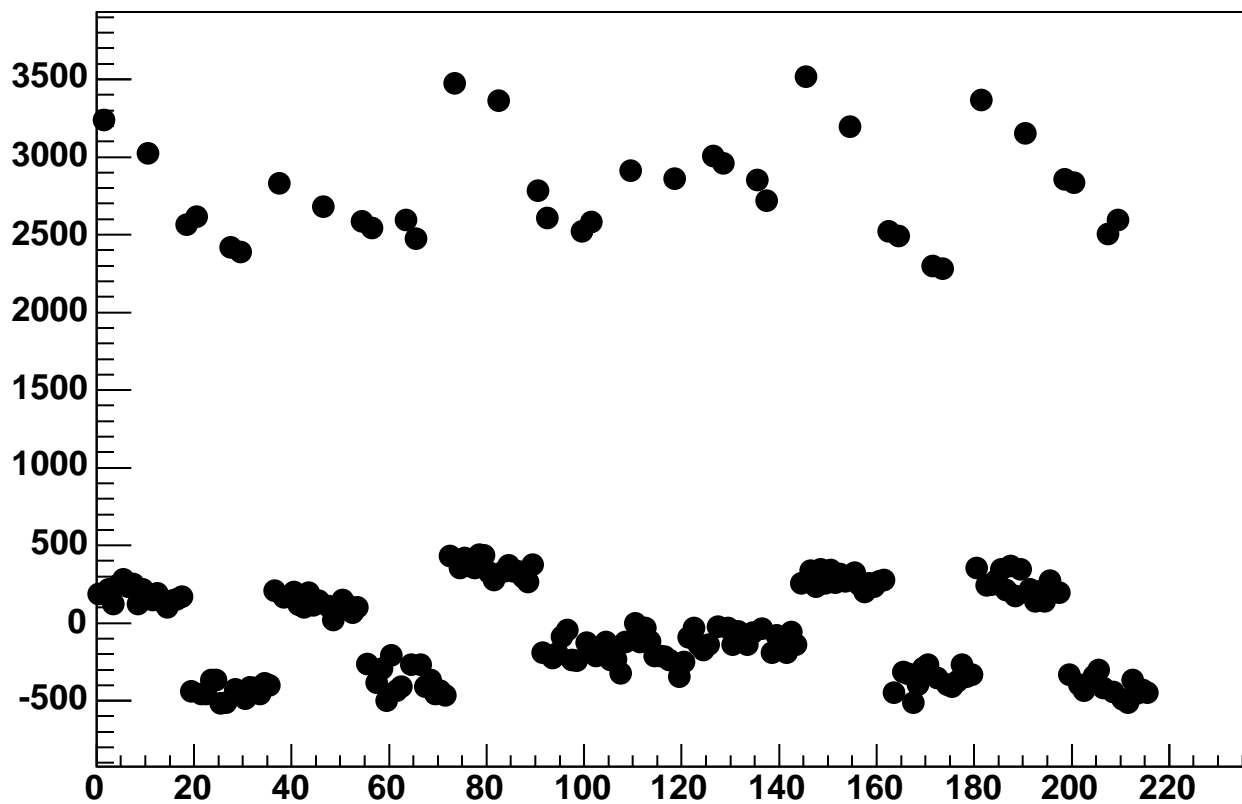
Enable 3, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



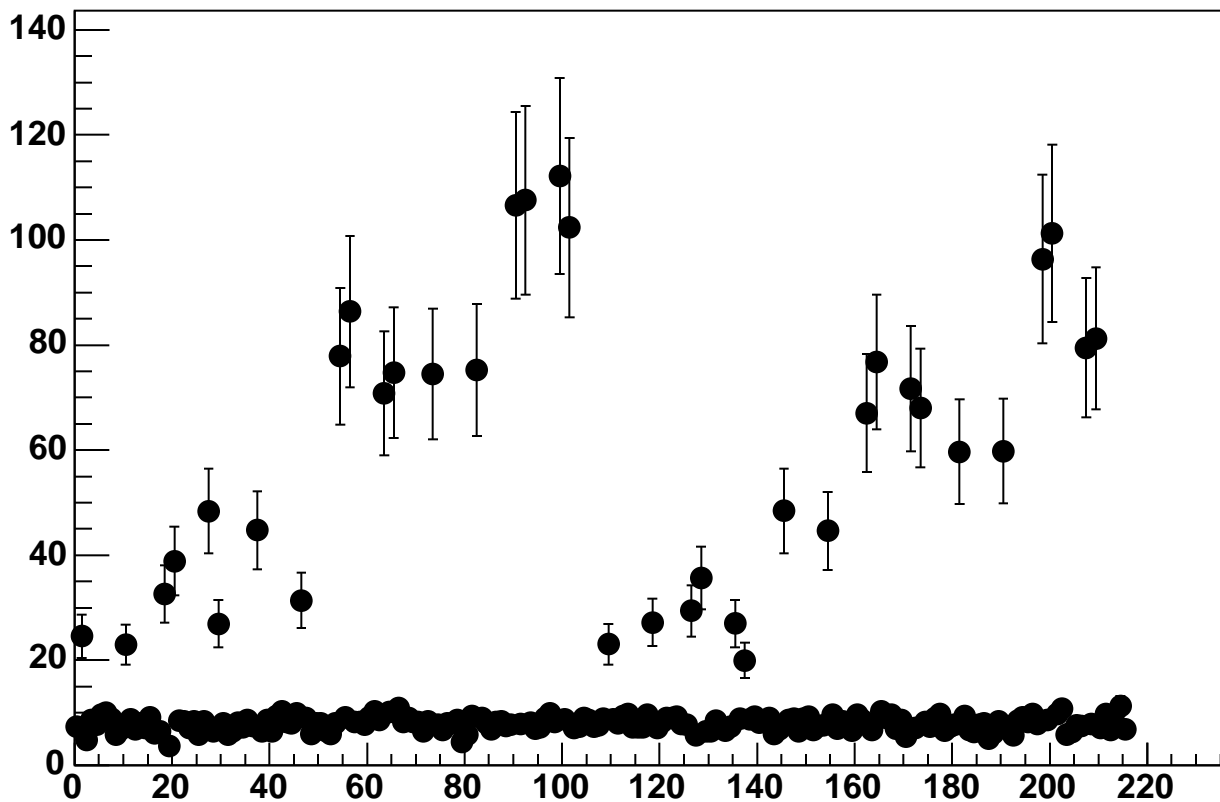
Enable 3, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



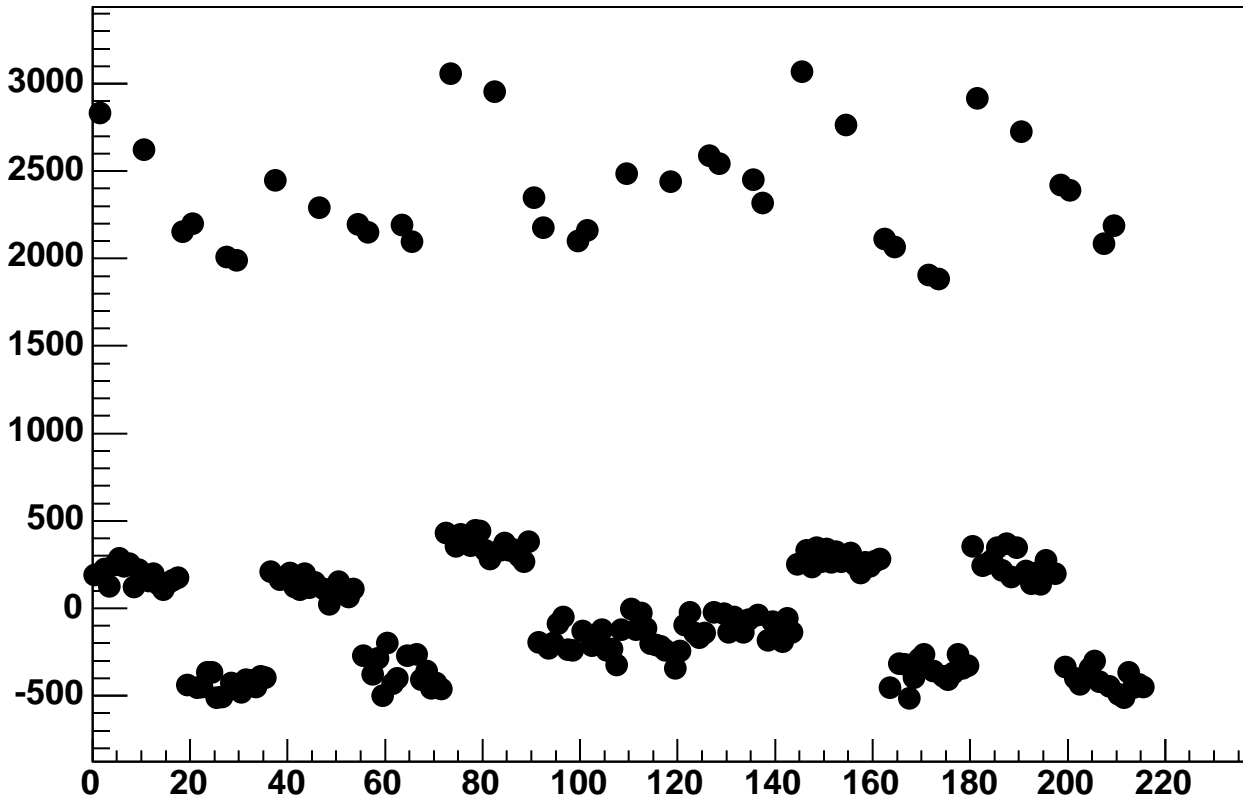
Enable 3, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



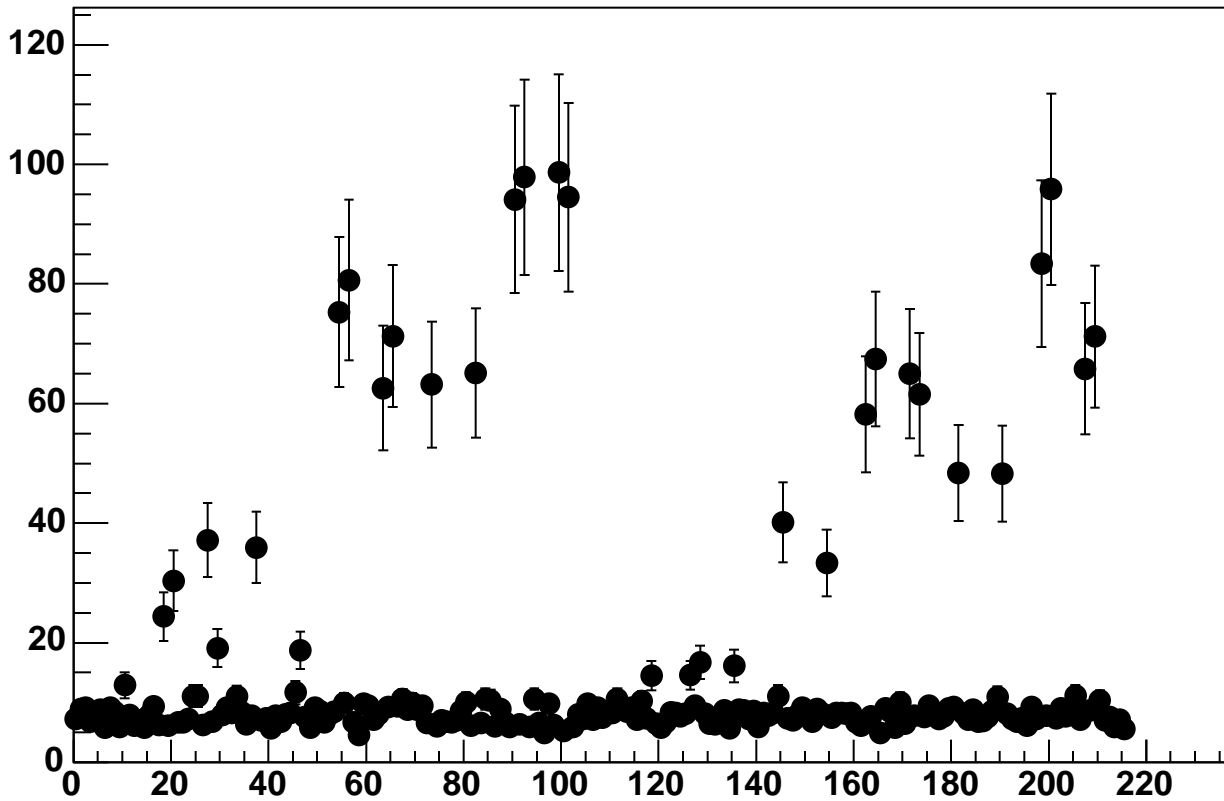
Enable 3, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



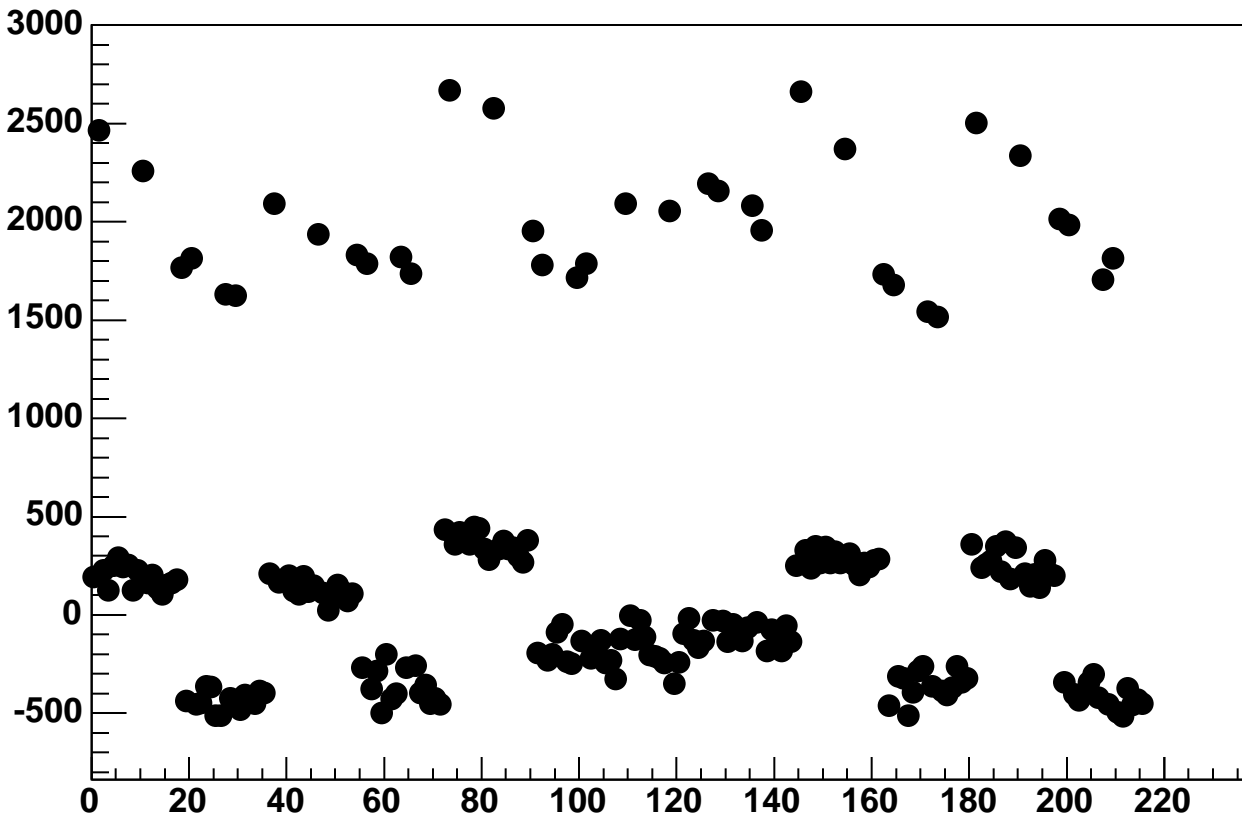
Enable 3, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



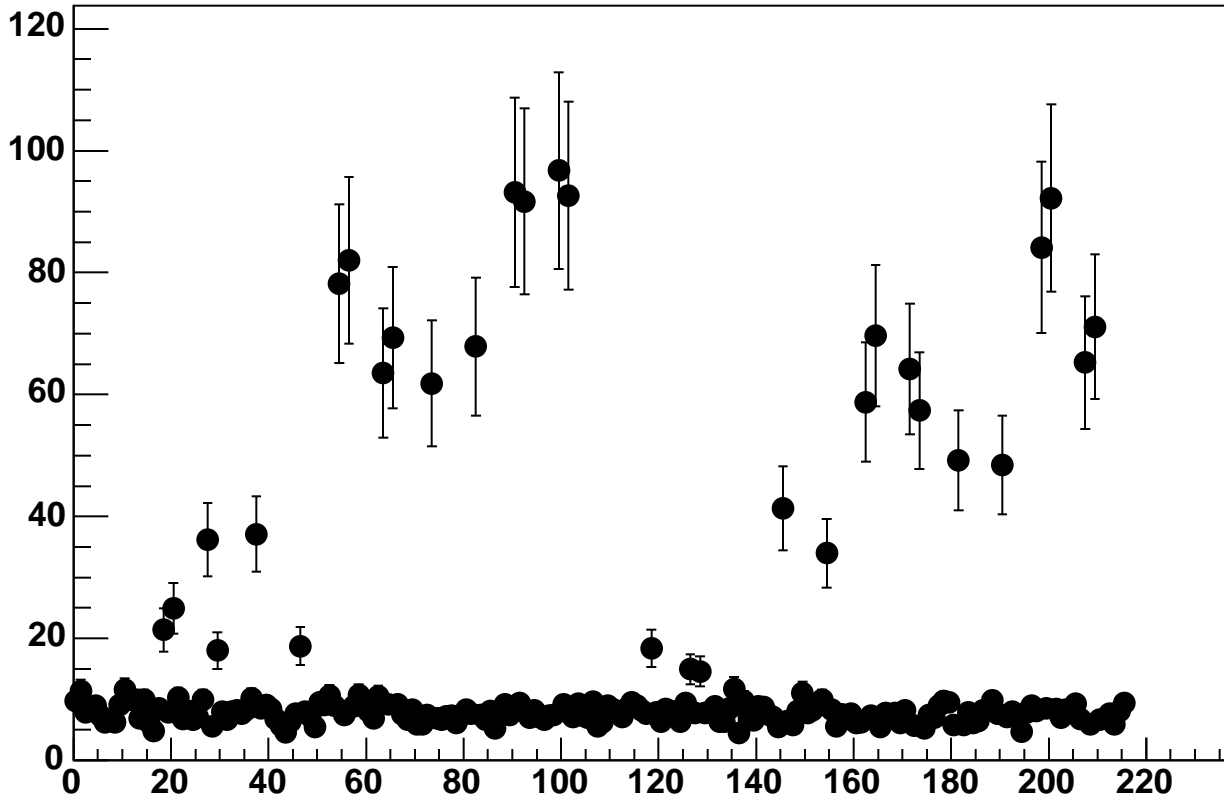
Enable 3, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



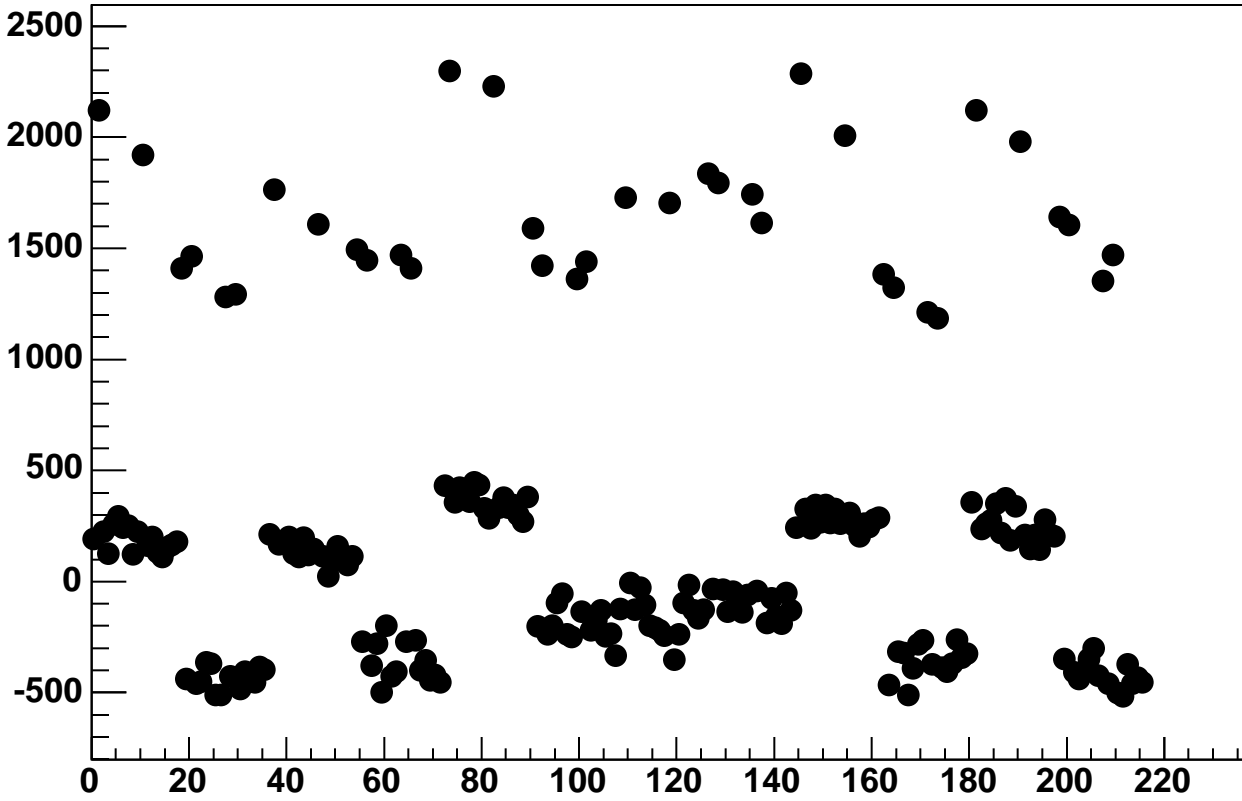
Enable 3, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



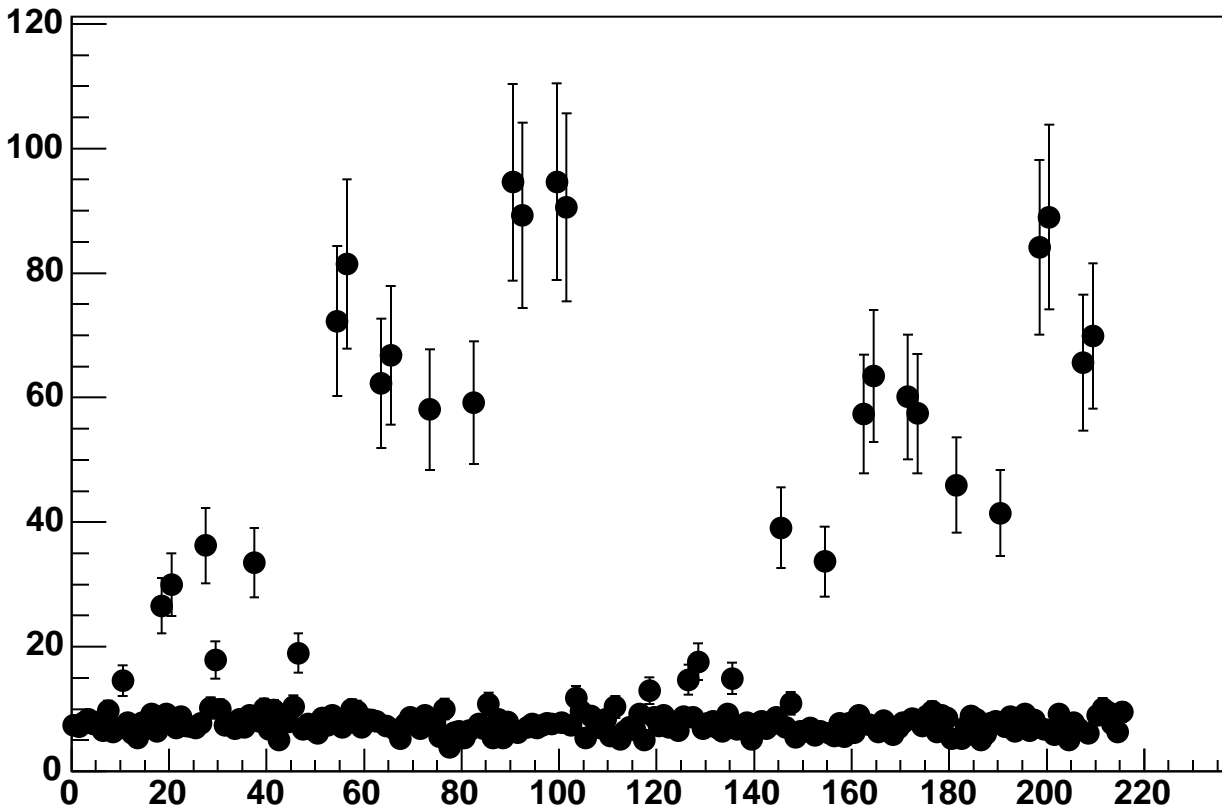
Enable 3, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



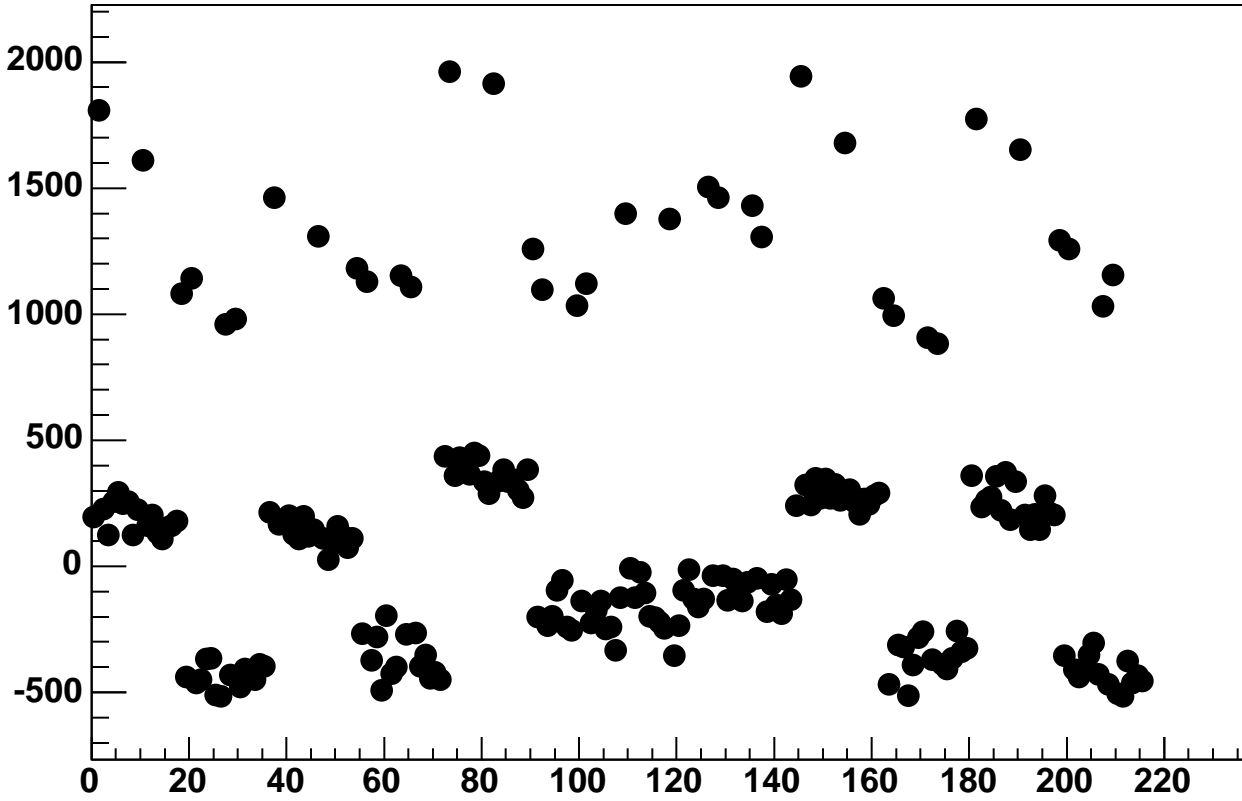
Enable 3, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



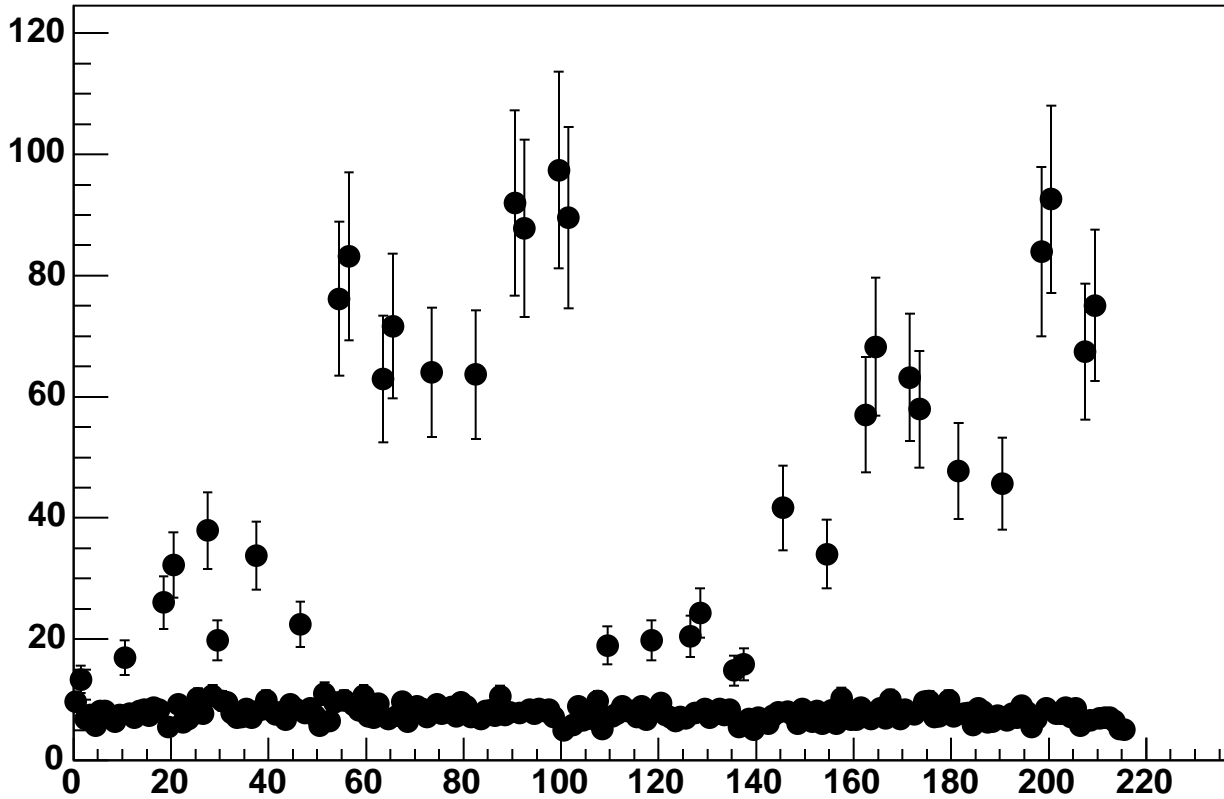
Enable 3, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



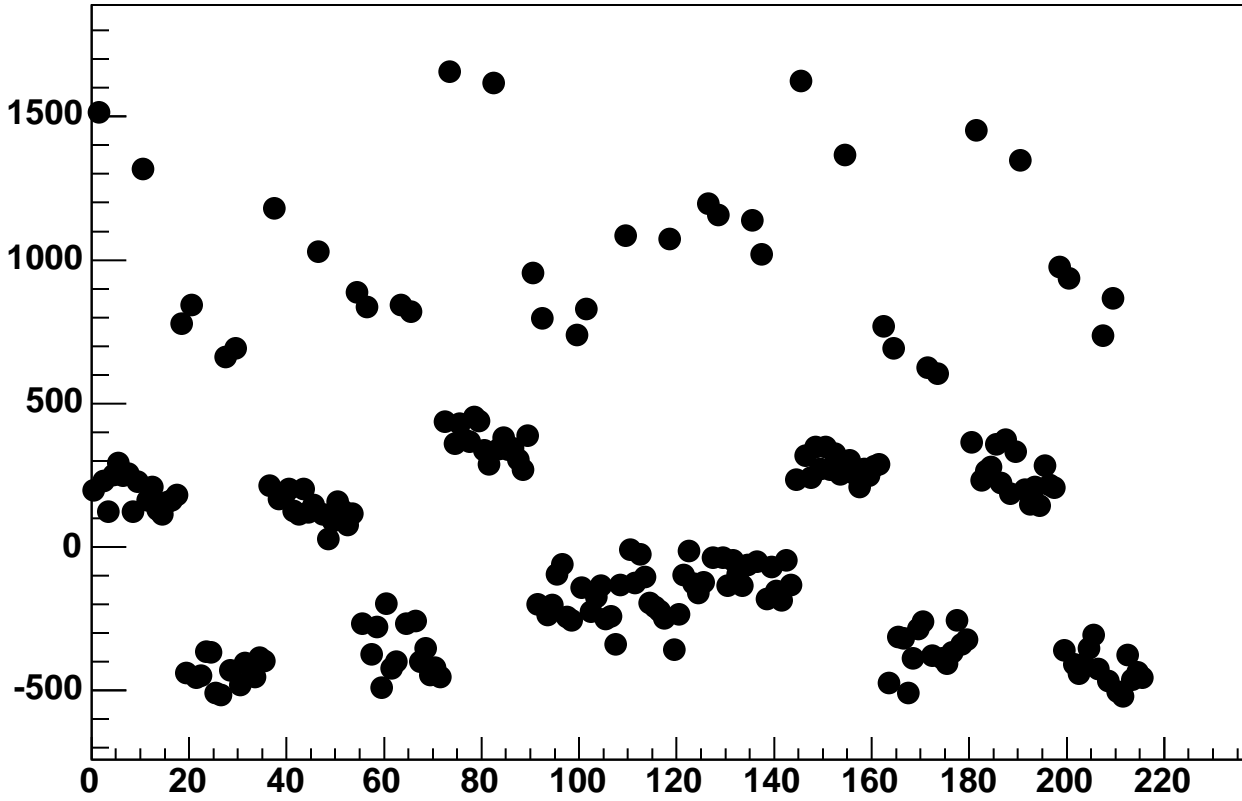
Enable 3, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



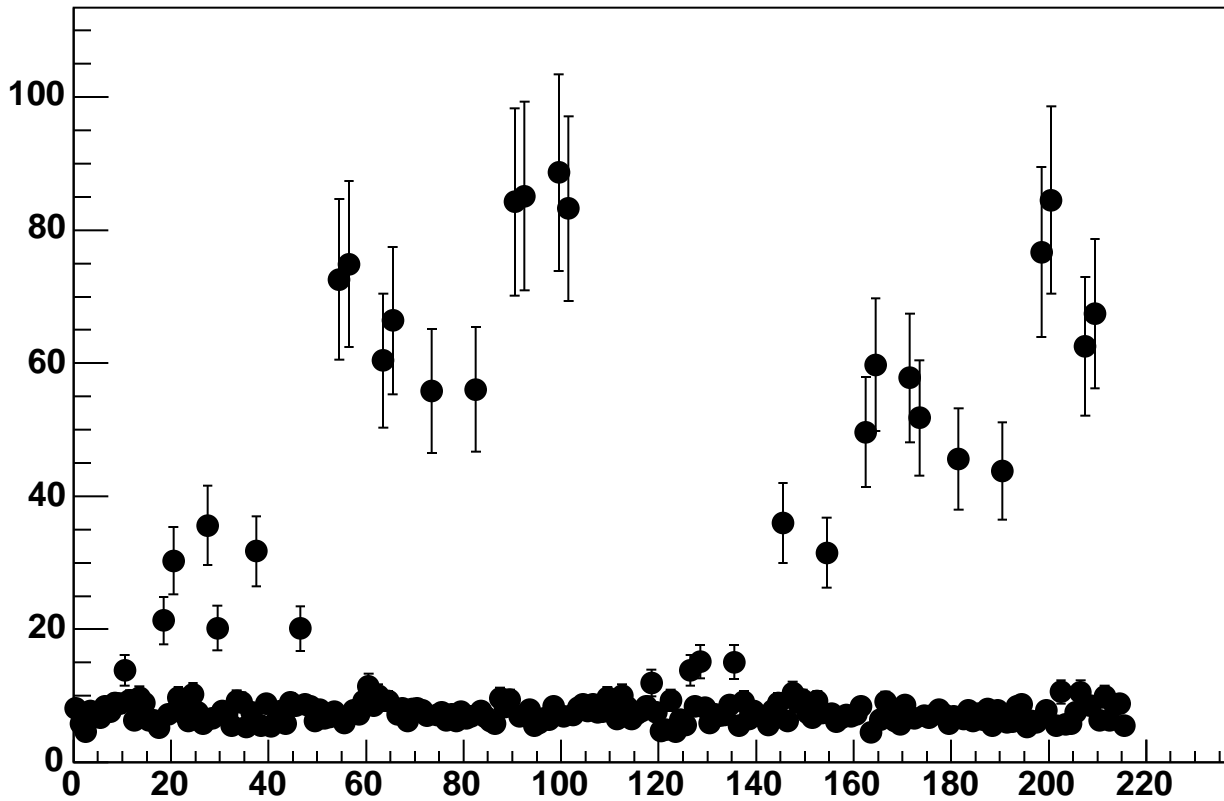
Enable 3, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



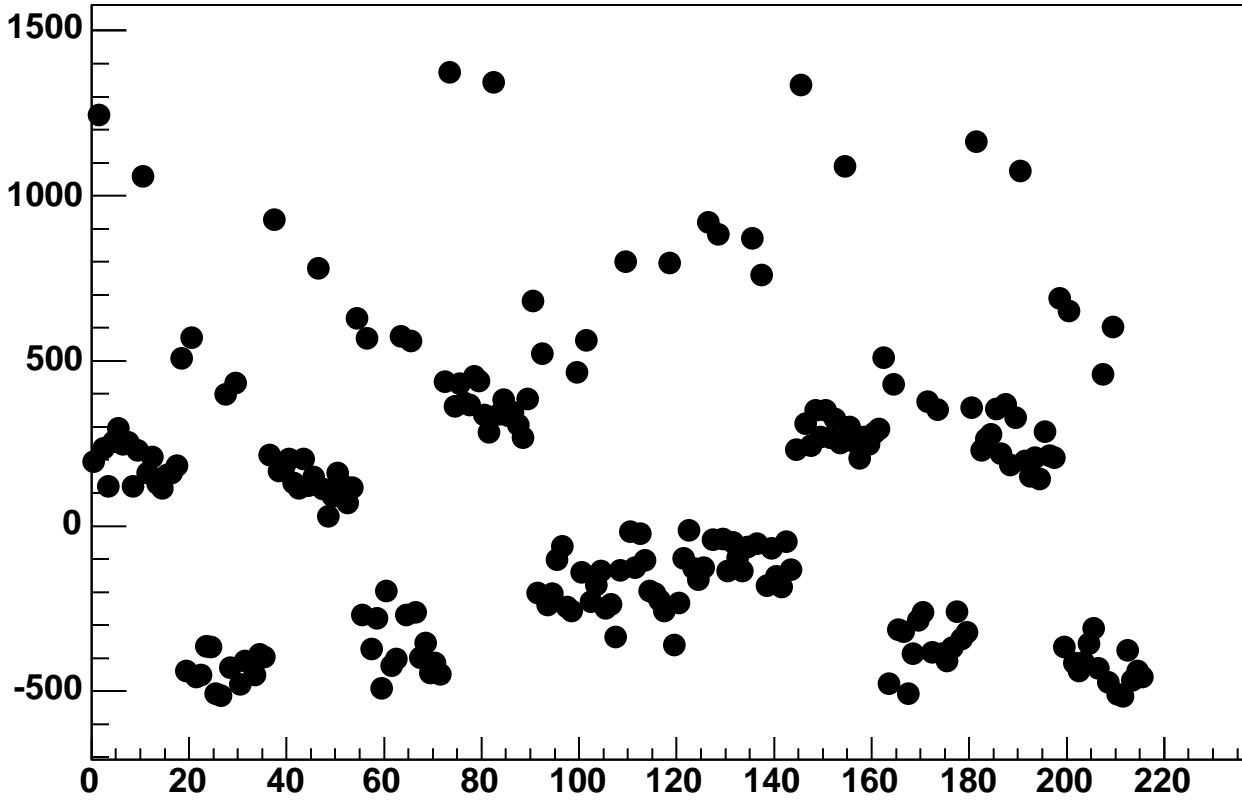
Enable 3, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



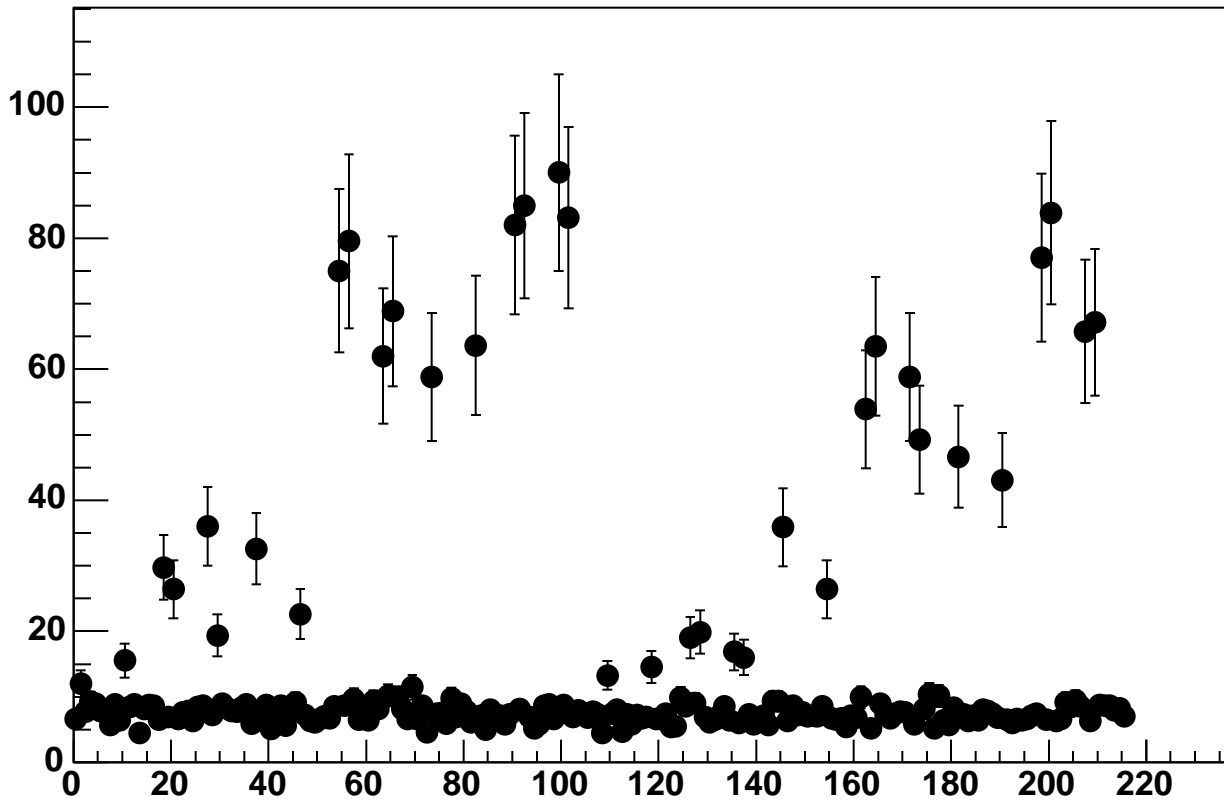
Enable 3, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



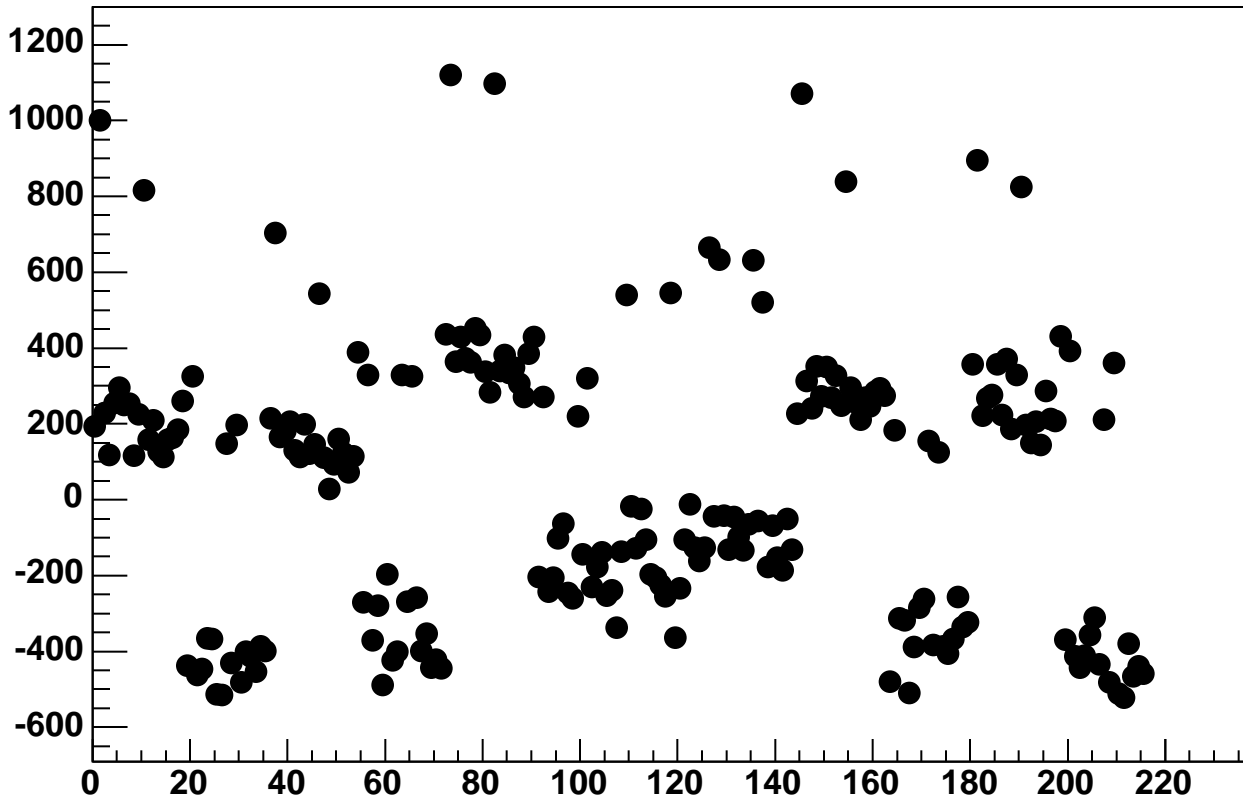
Enable 3, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



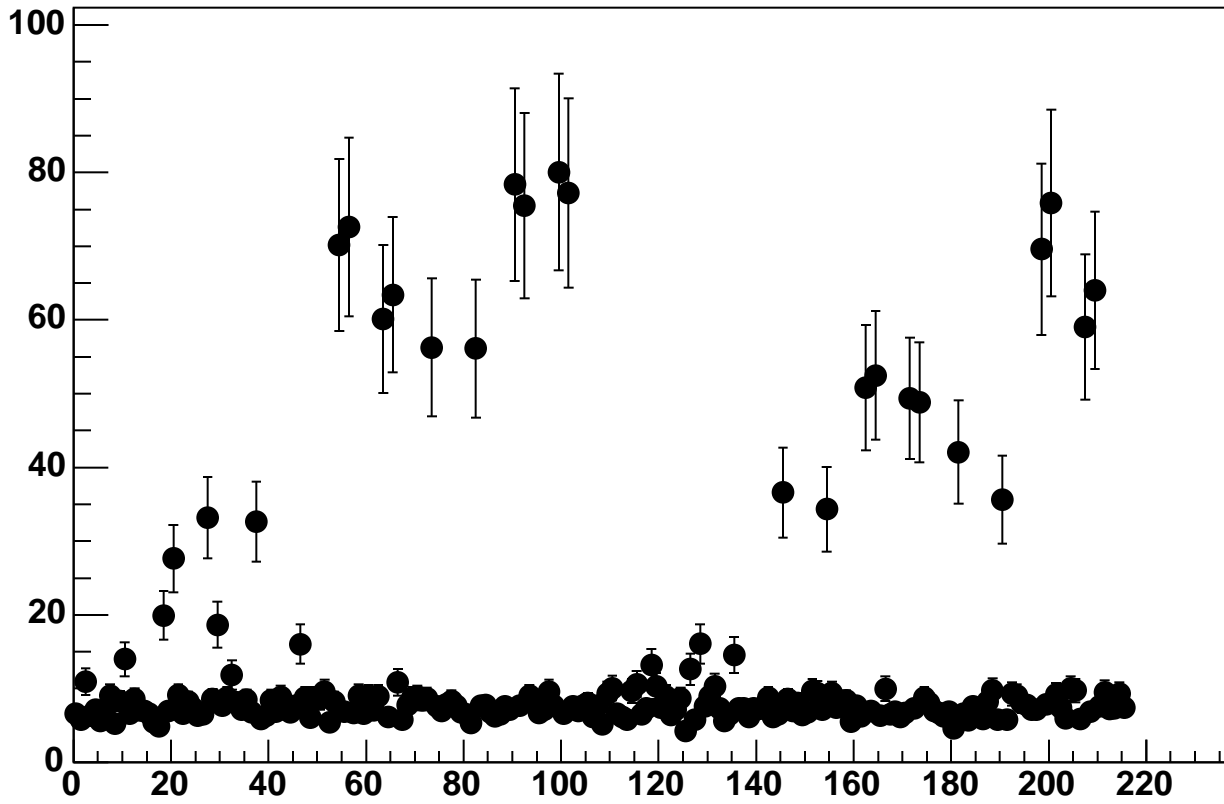
Enable 3, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



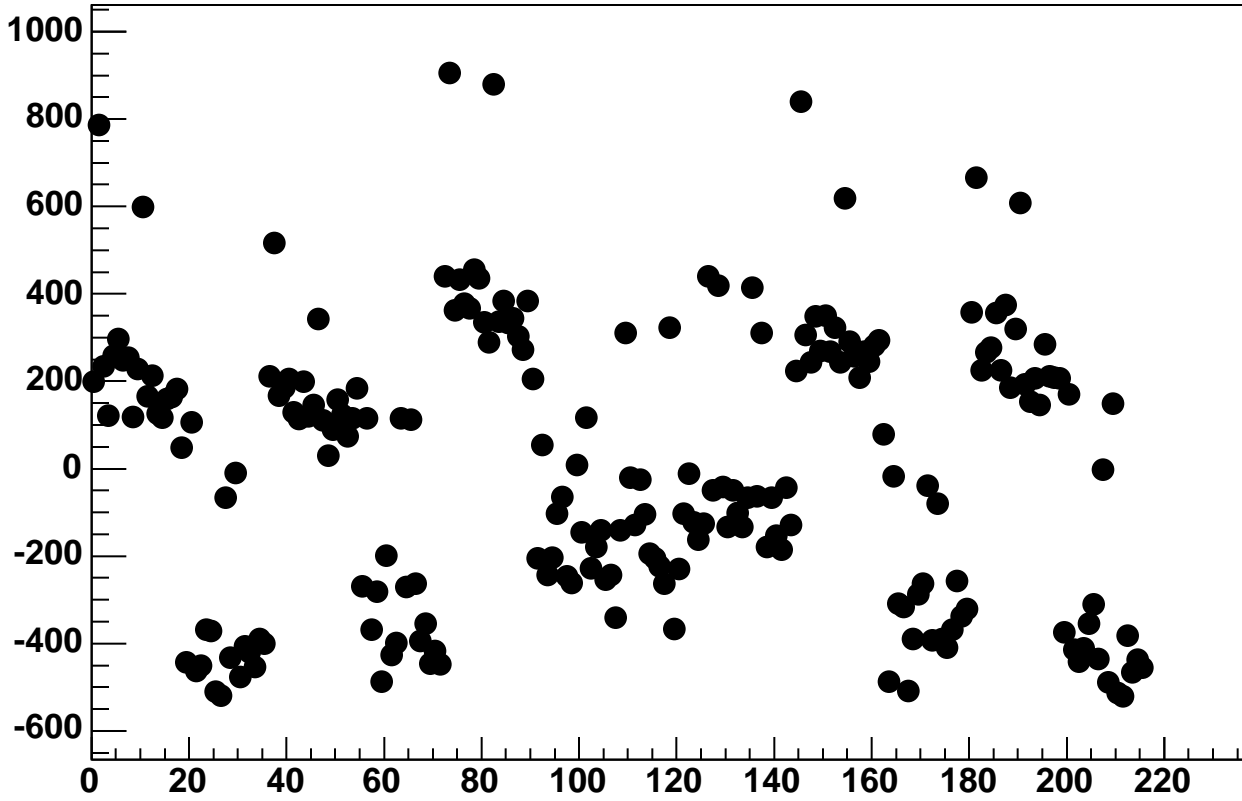
Enable 3, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



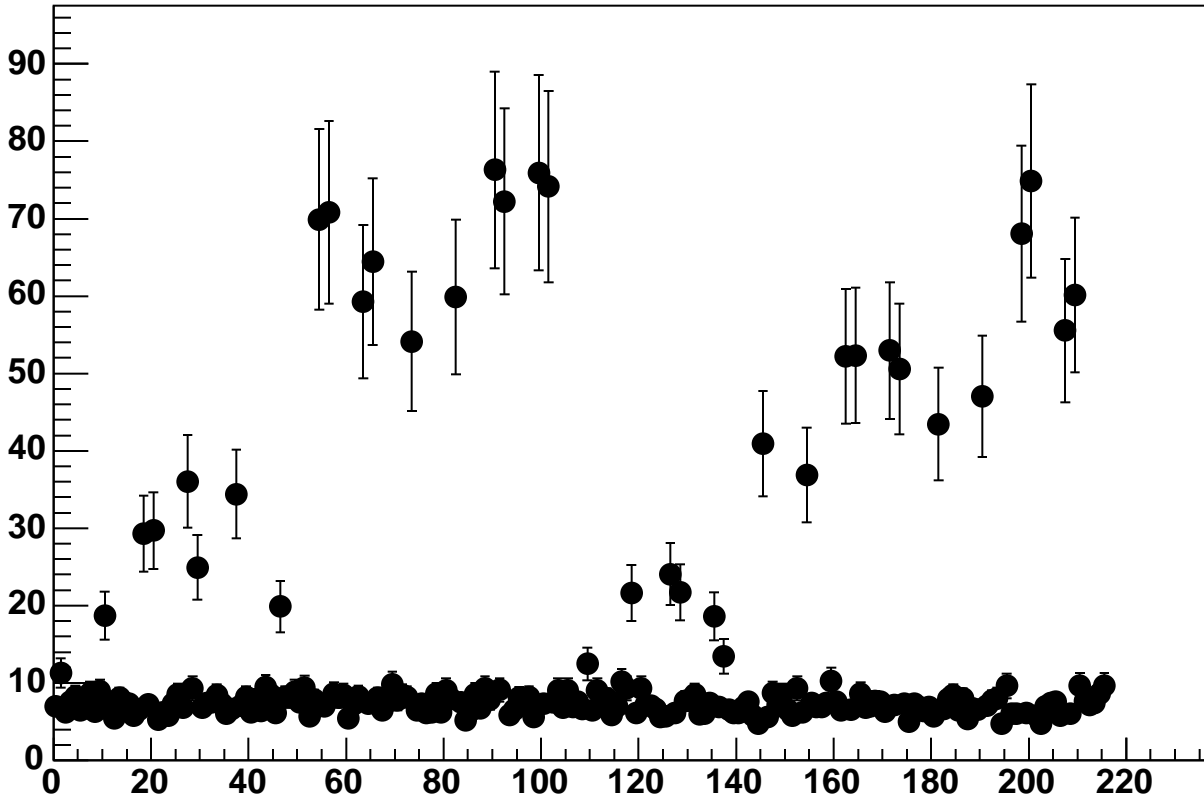
Enable 3, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



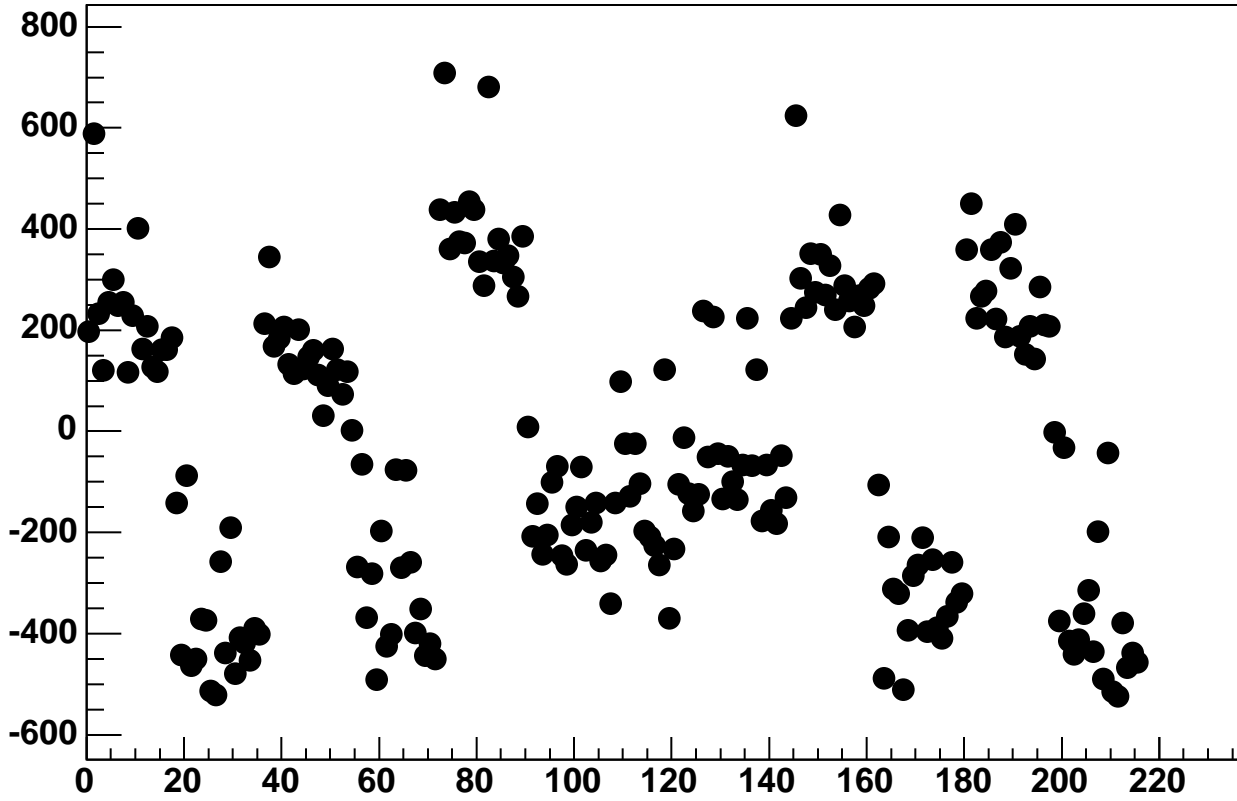
Enable 3, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



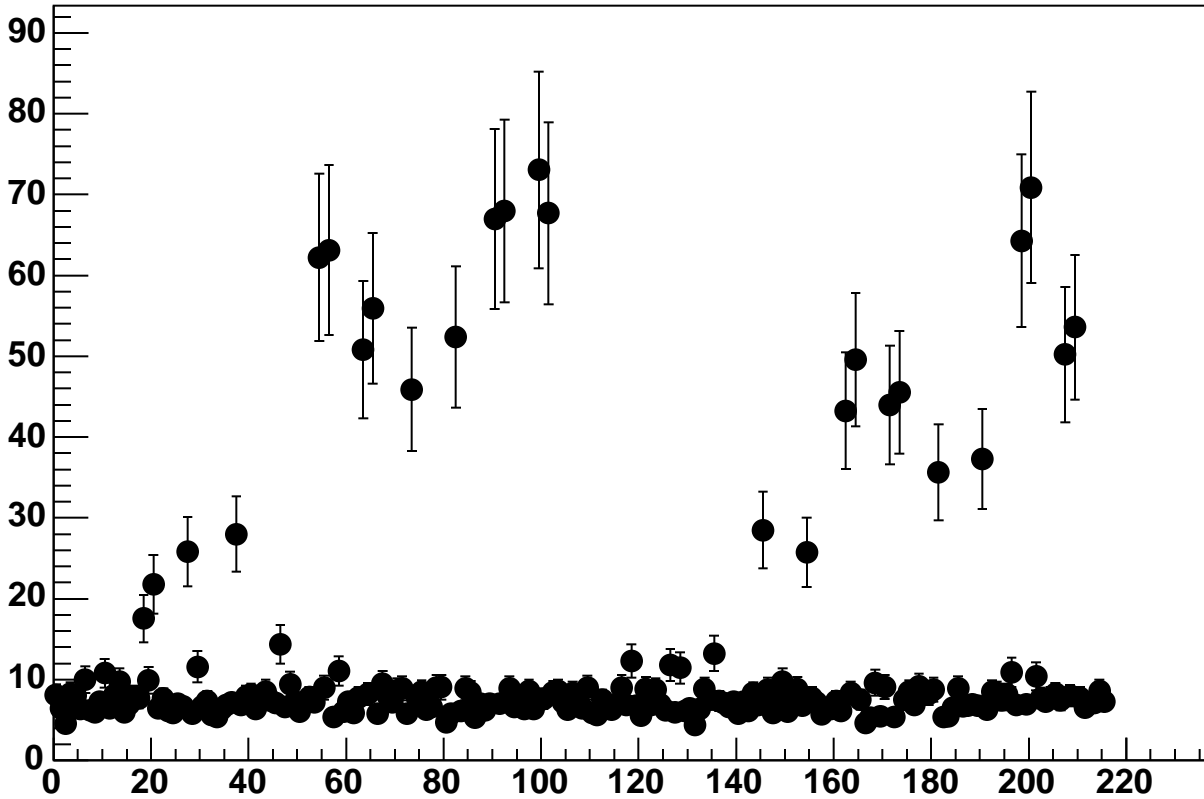
Enable 3, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



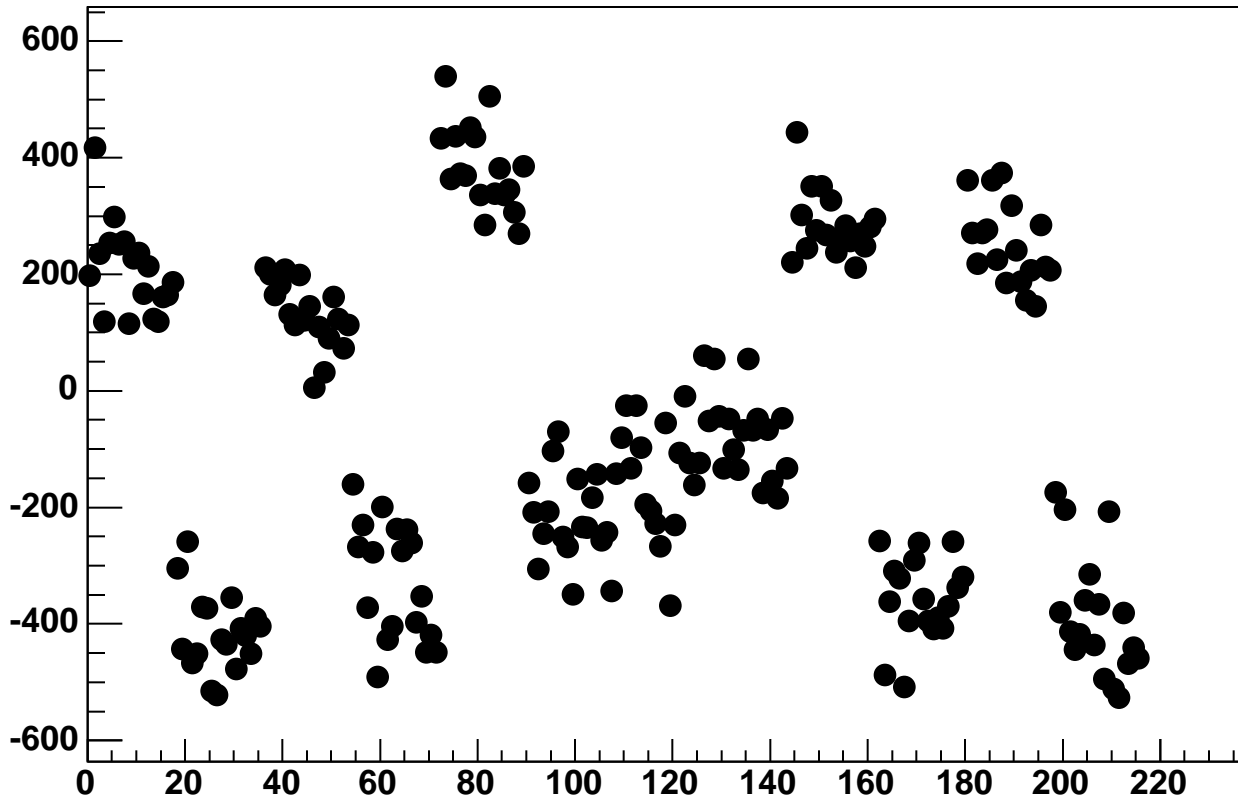
Enable 3, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



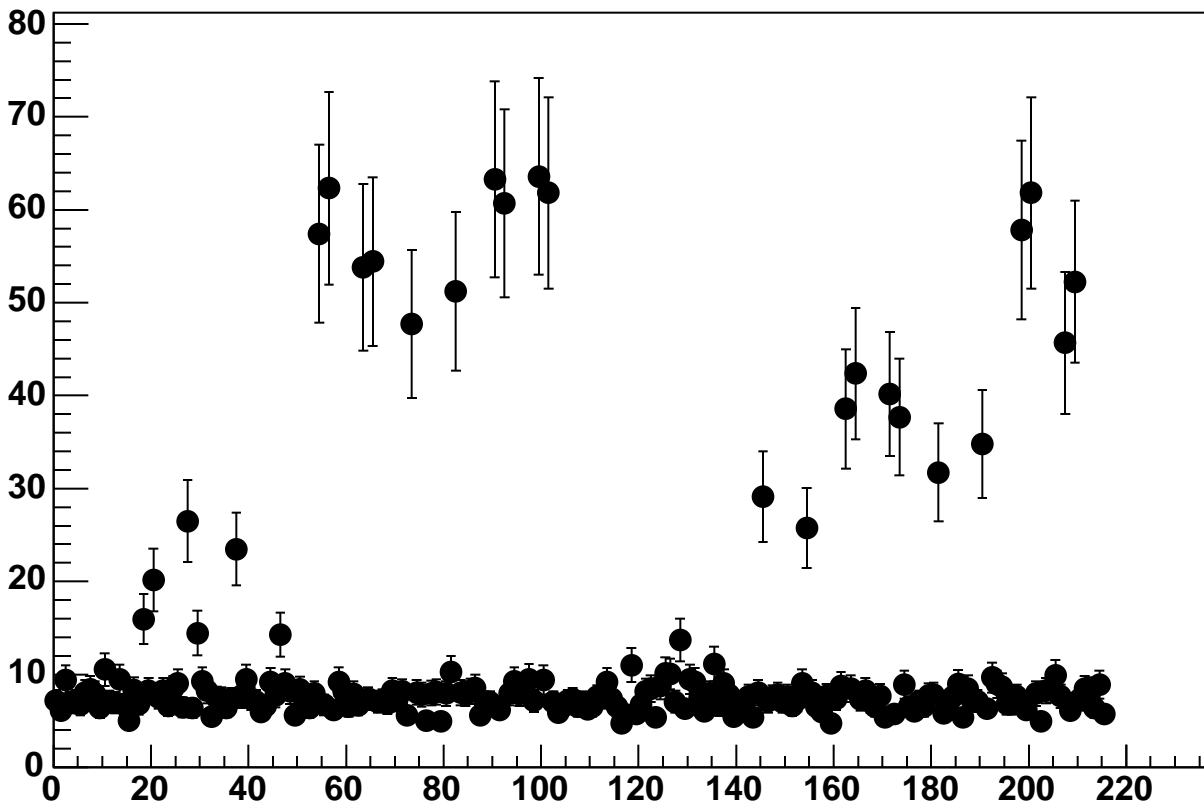
Enable 3, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



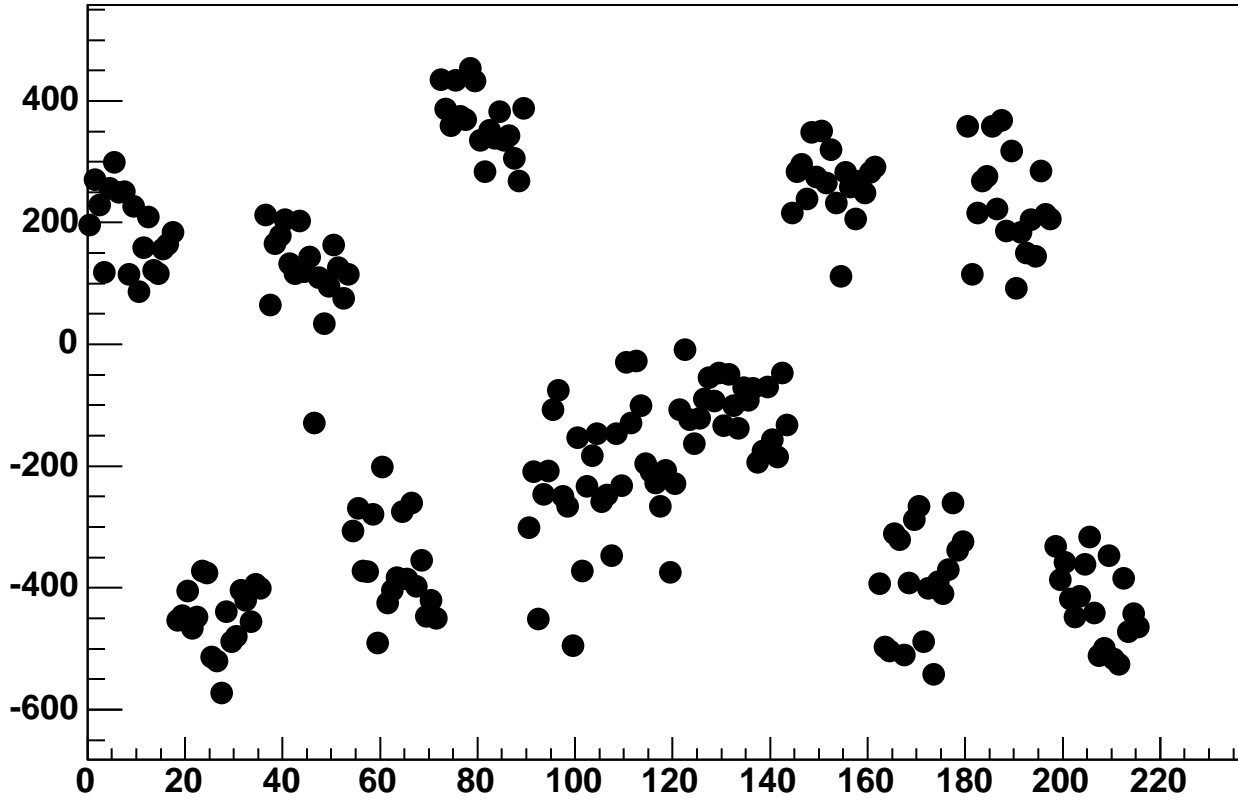
Enable 3, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



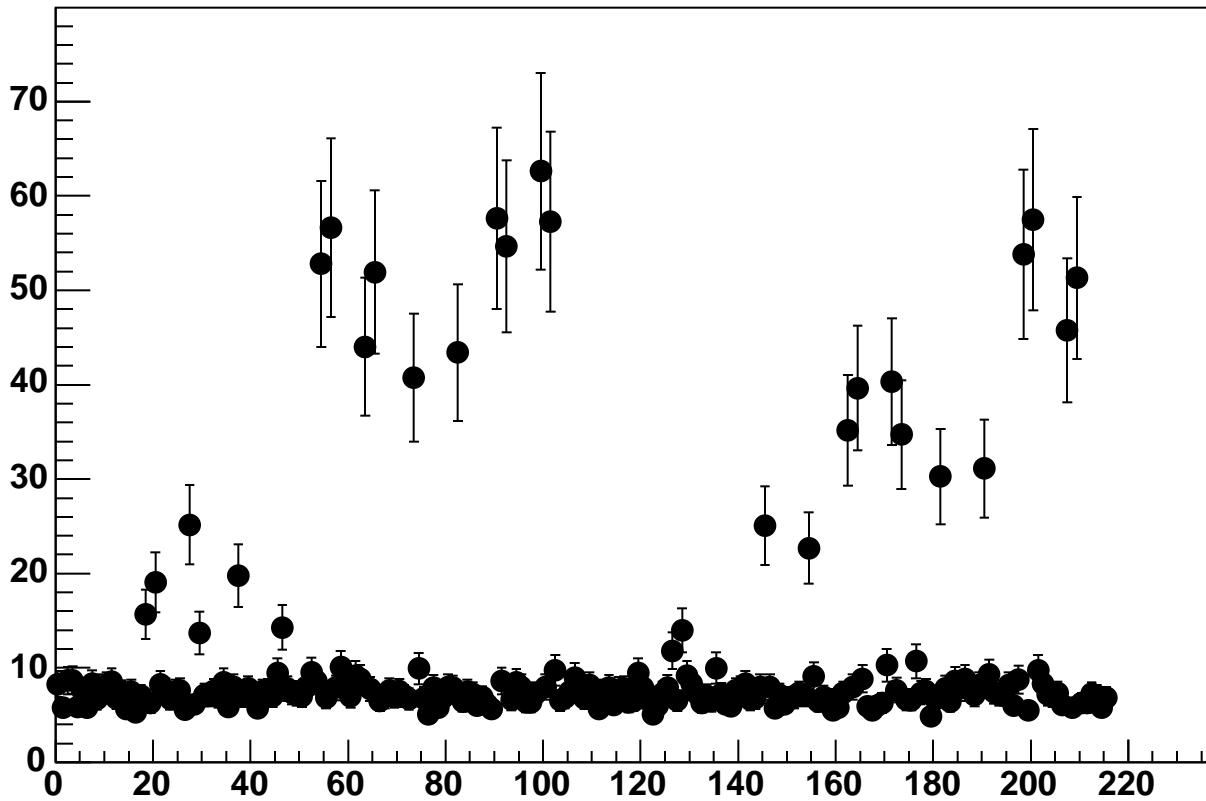
Enable 3, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



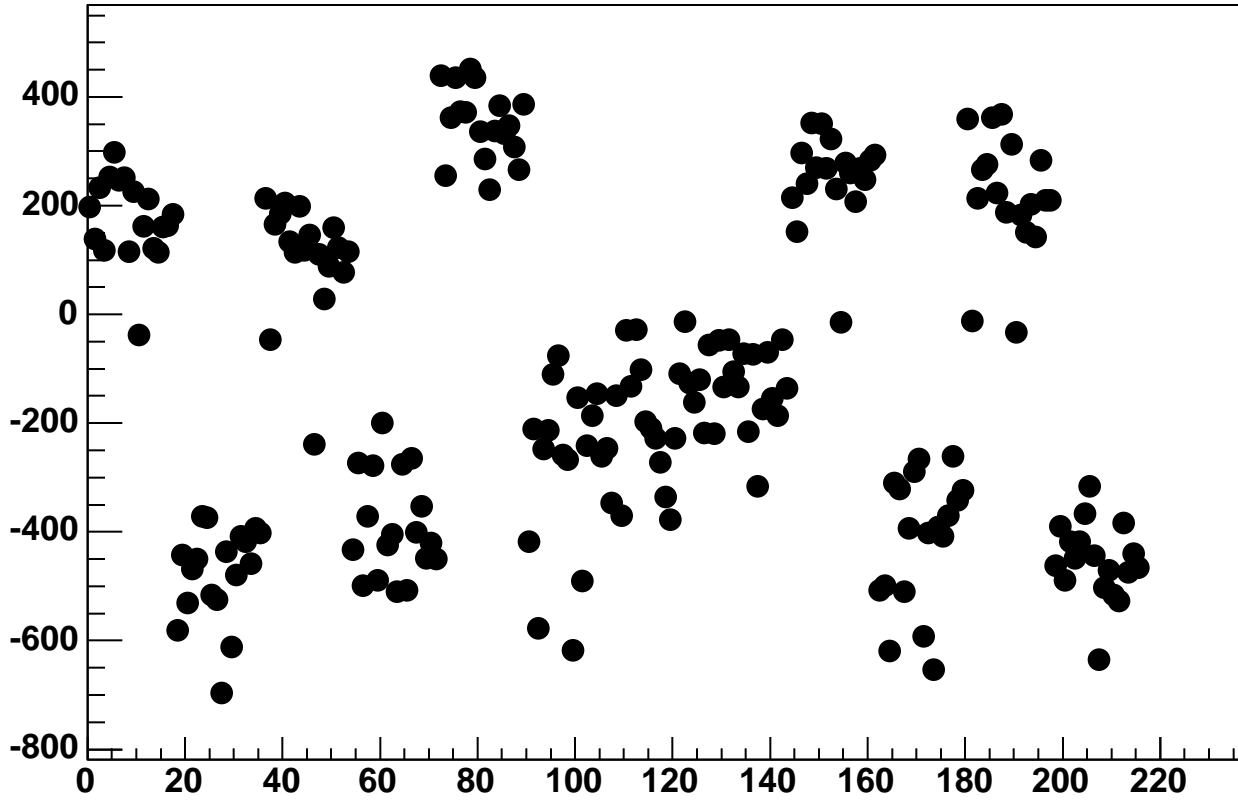
Enable 3, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



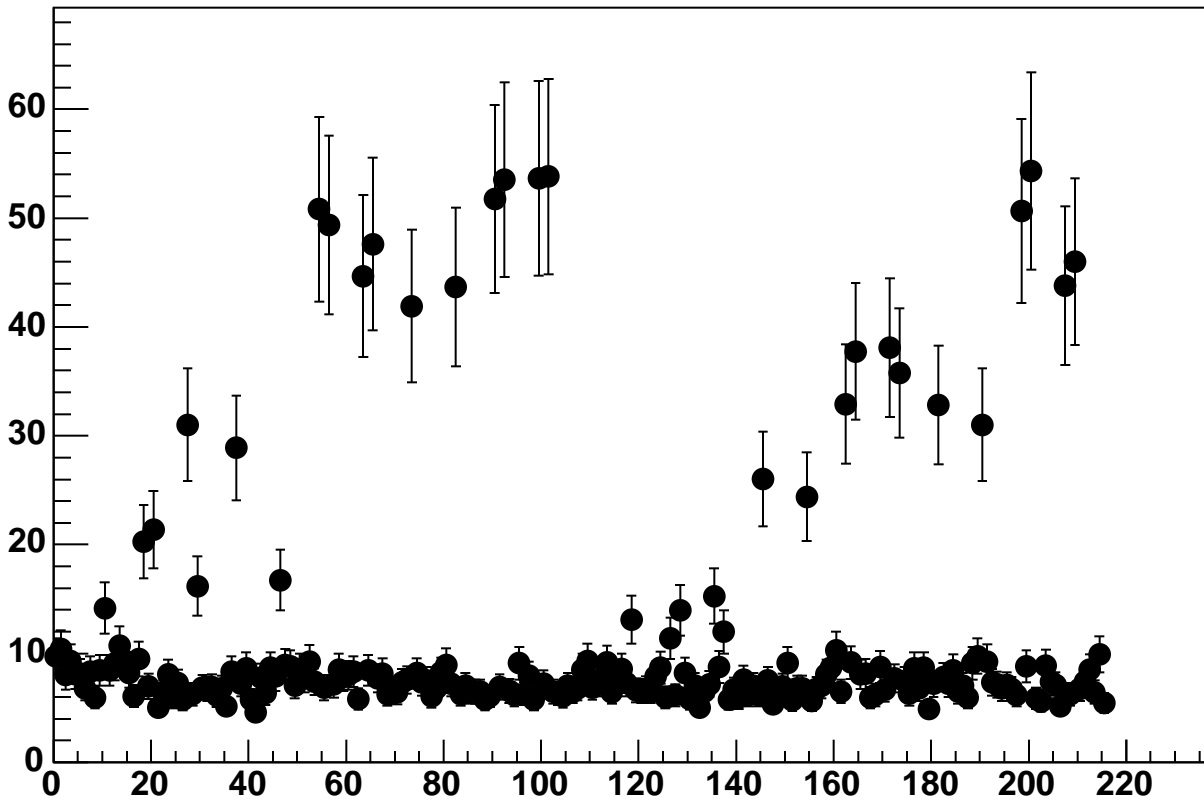
Enable 3, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



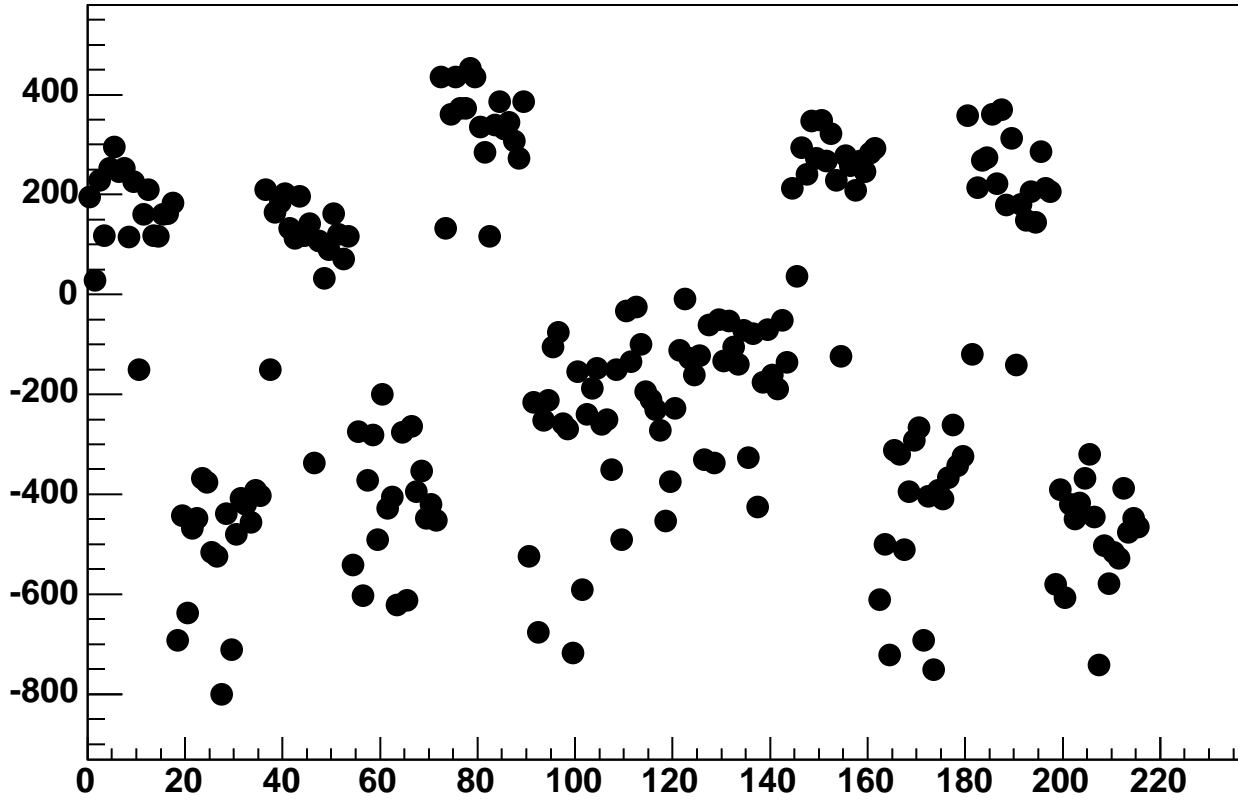
Enable 3, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



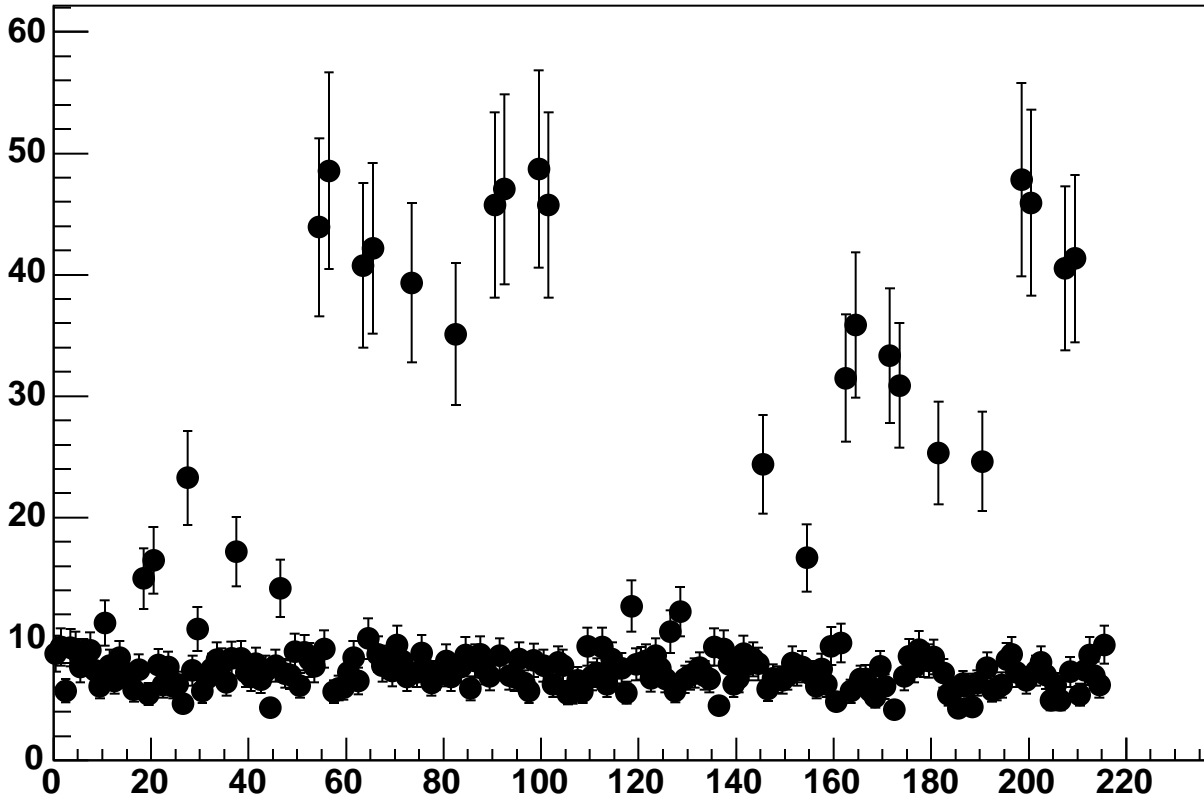
Enable 3, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



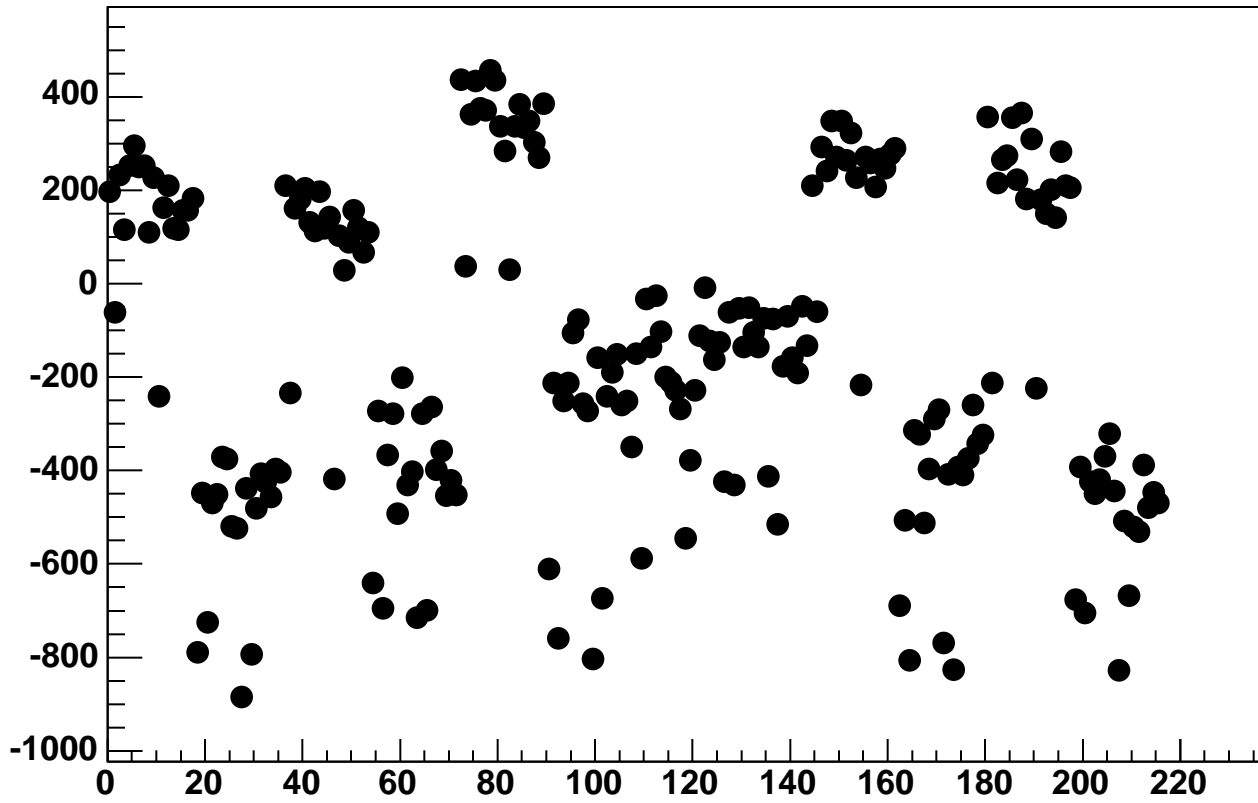
Enable 3, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



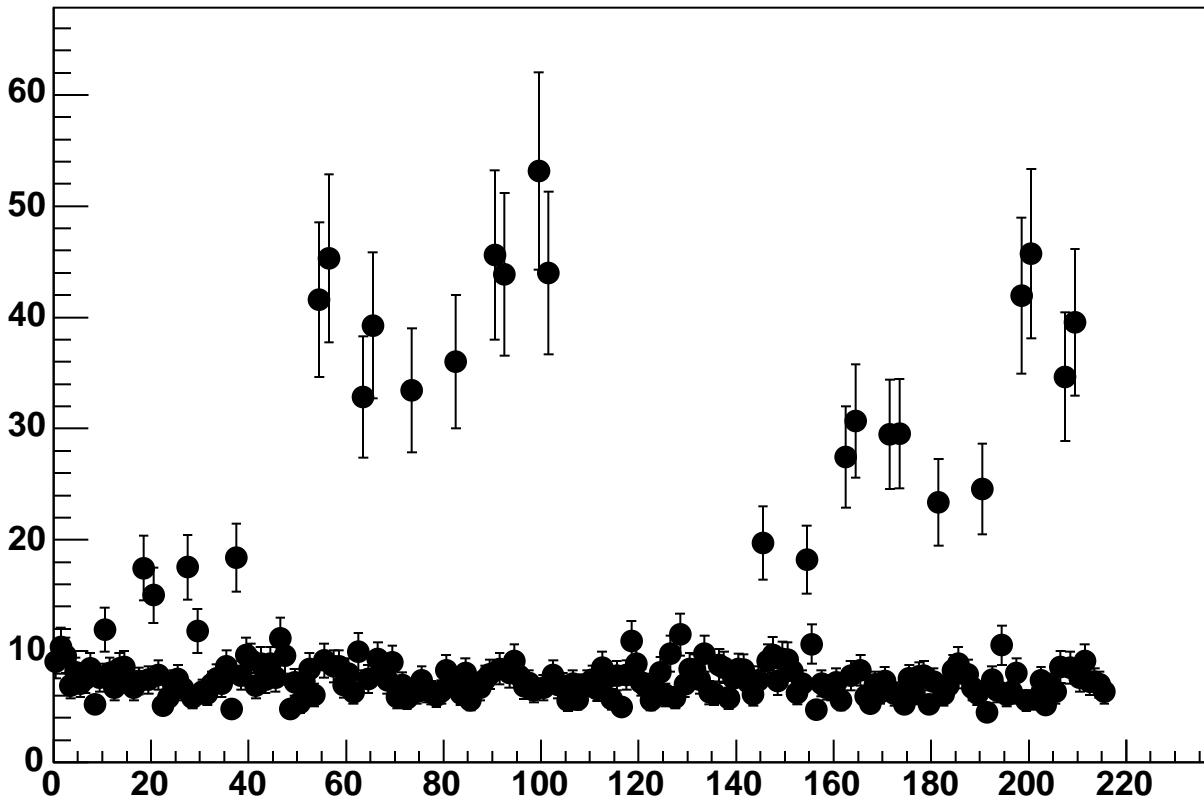
Enable 3, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



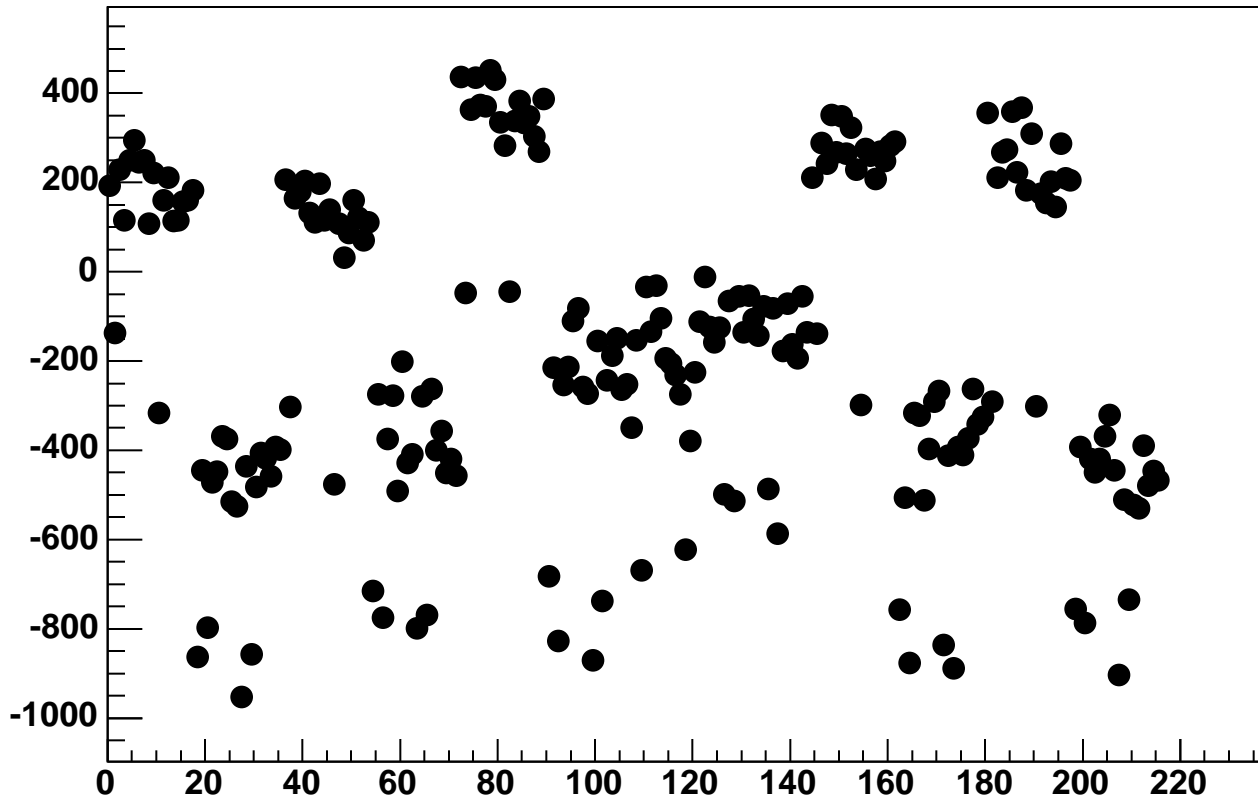
Enable 3, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



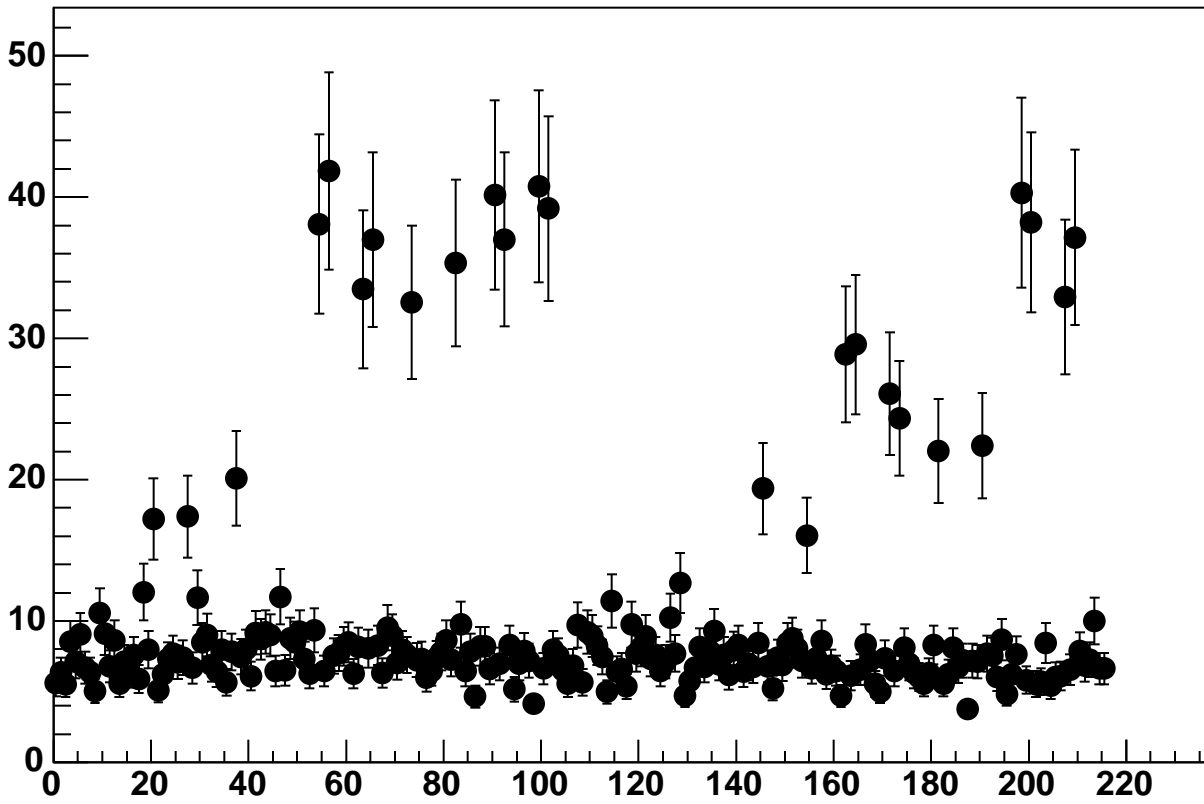
Enable 3, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



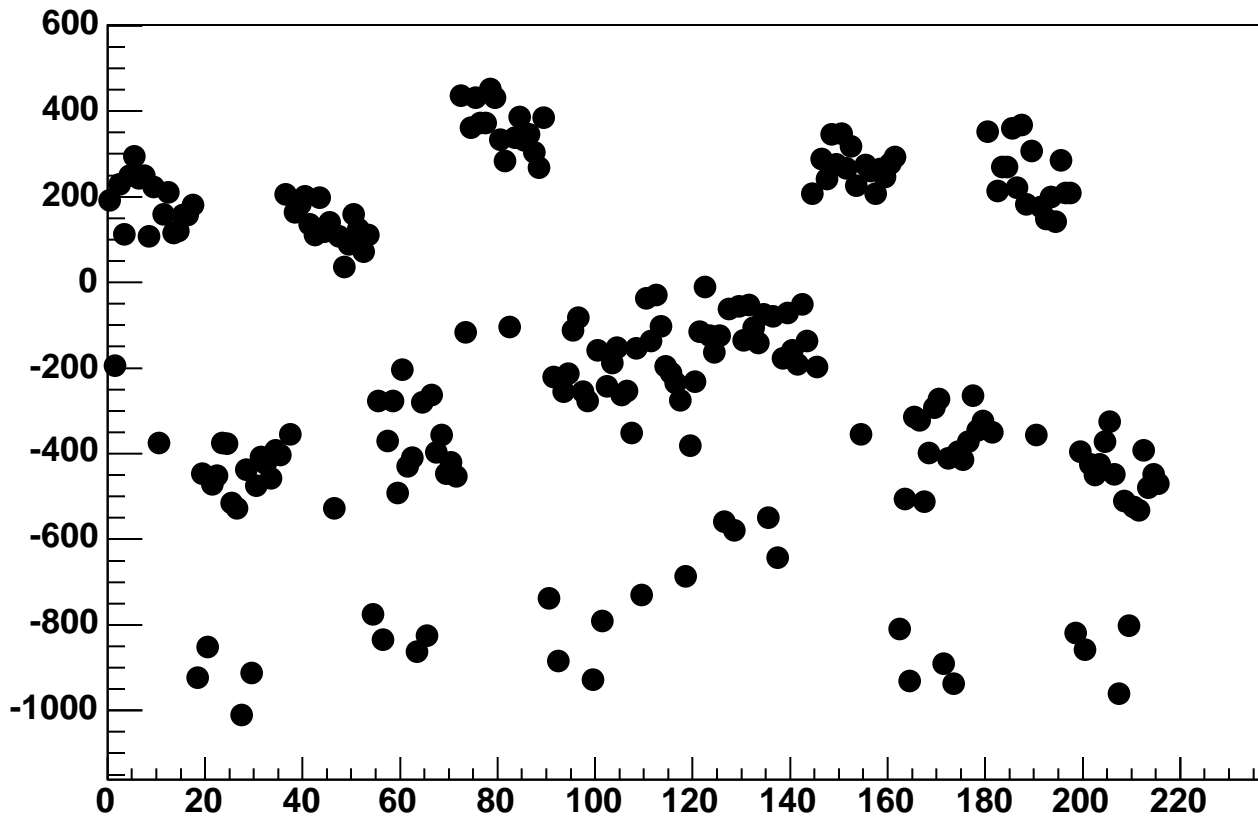
Enable 3, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



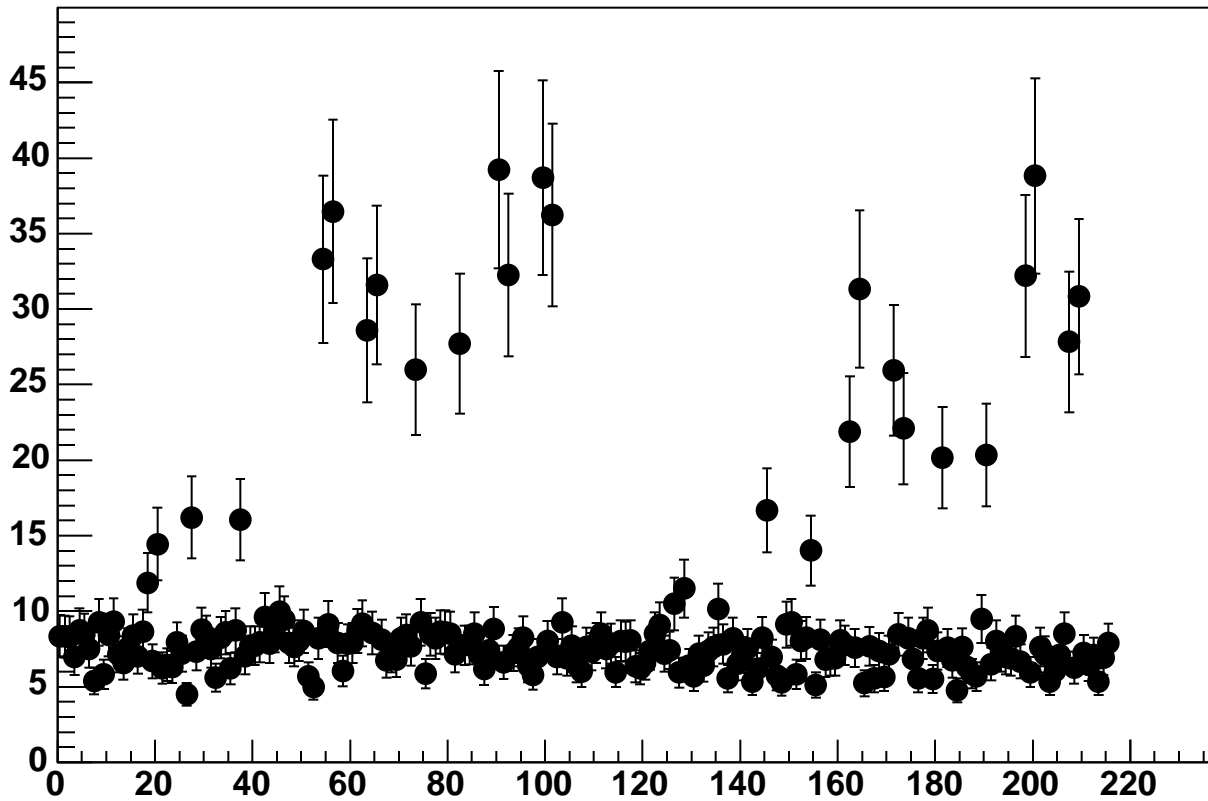
Enable 3, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



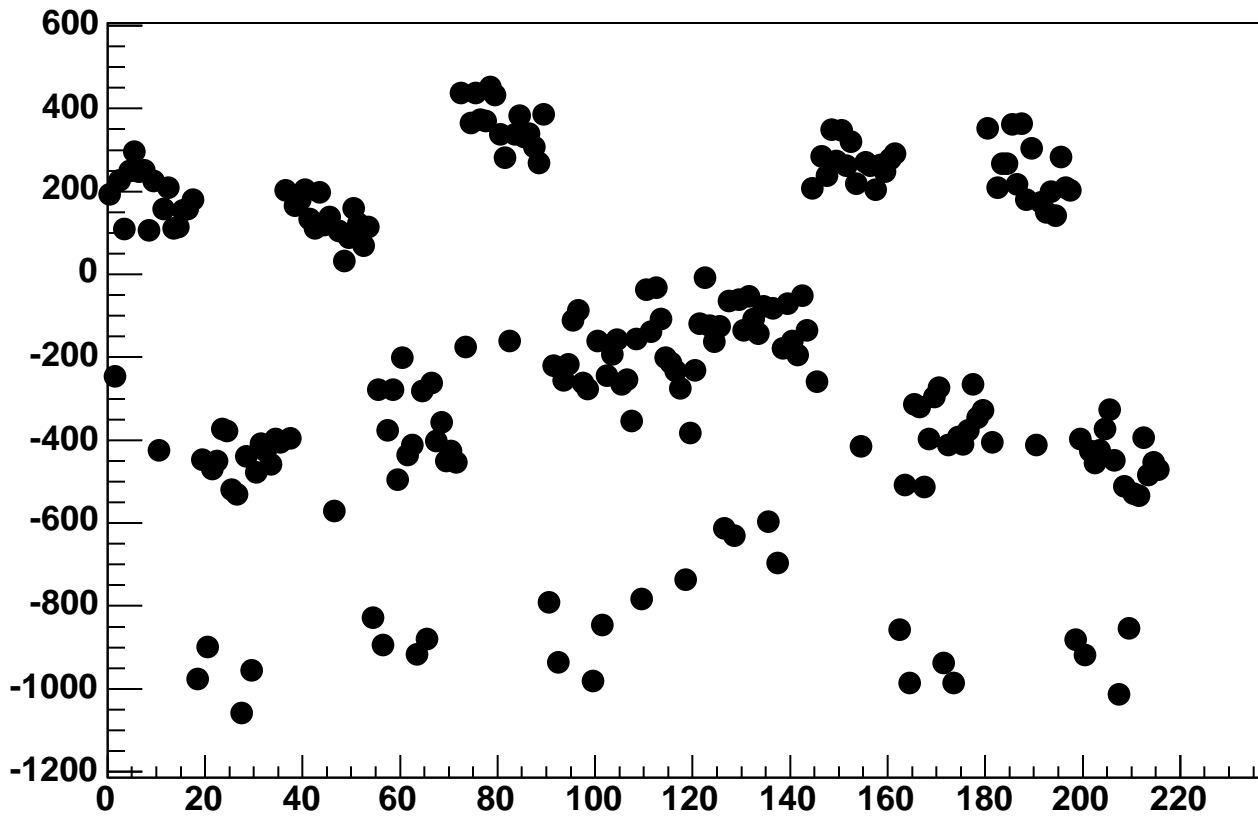
Enable 3, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



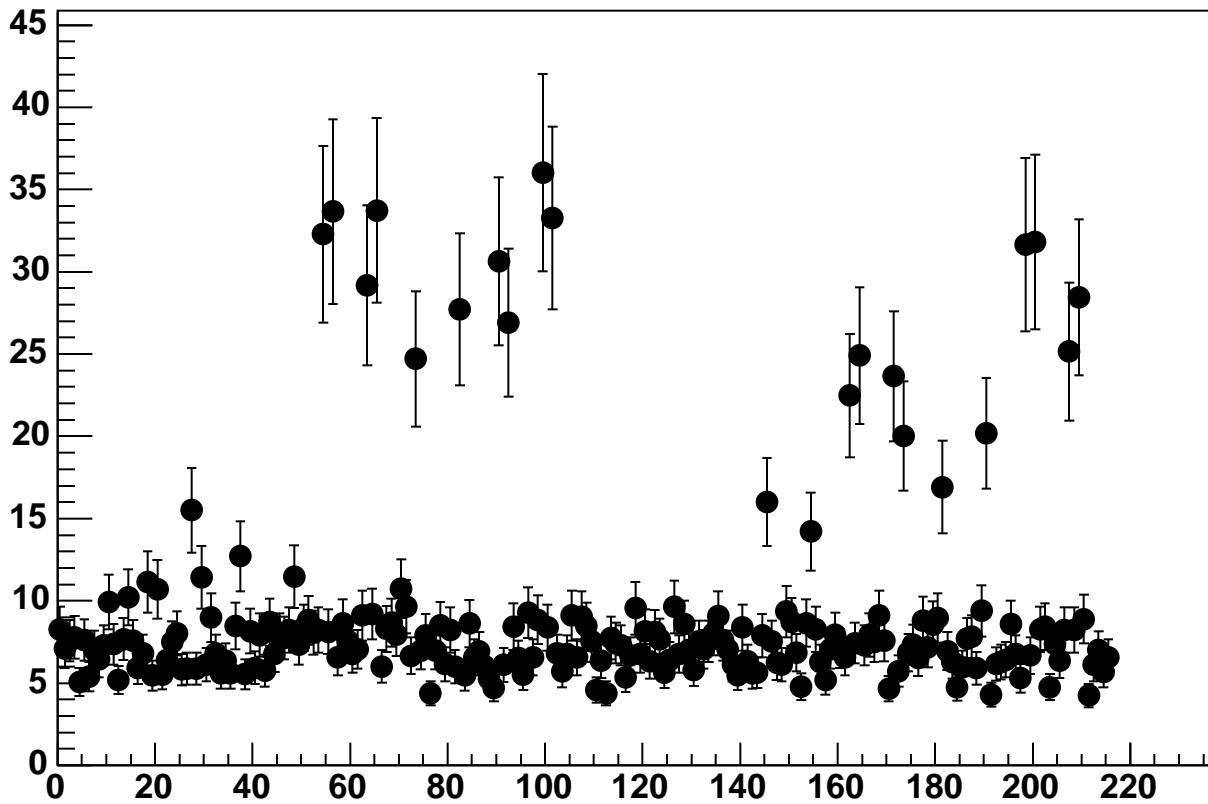
Enable 3, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



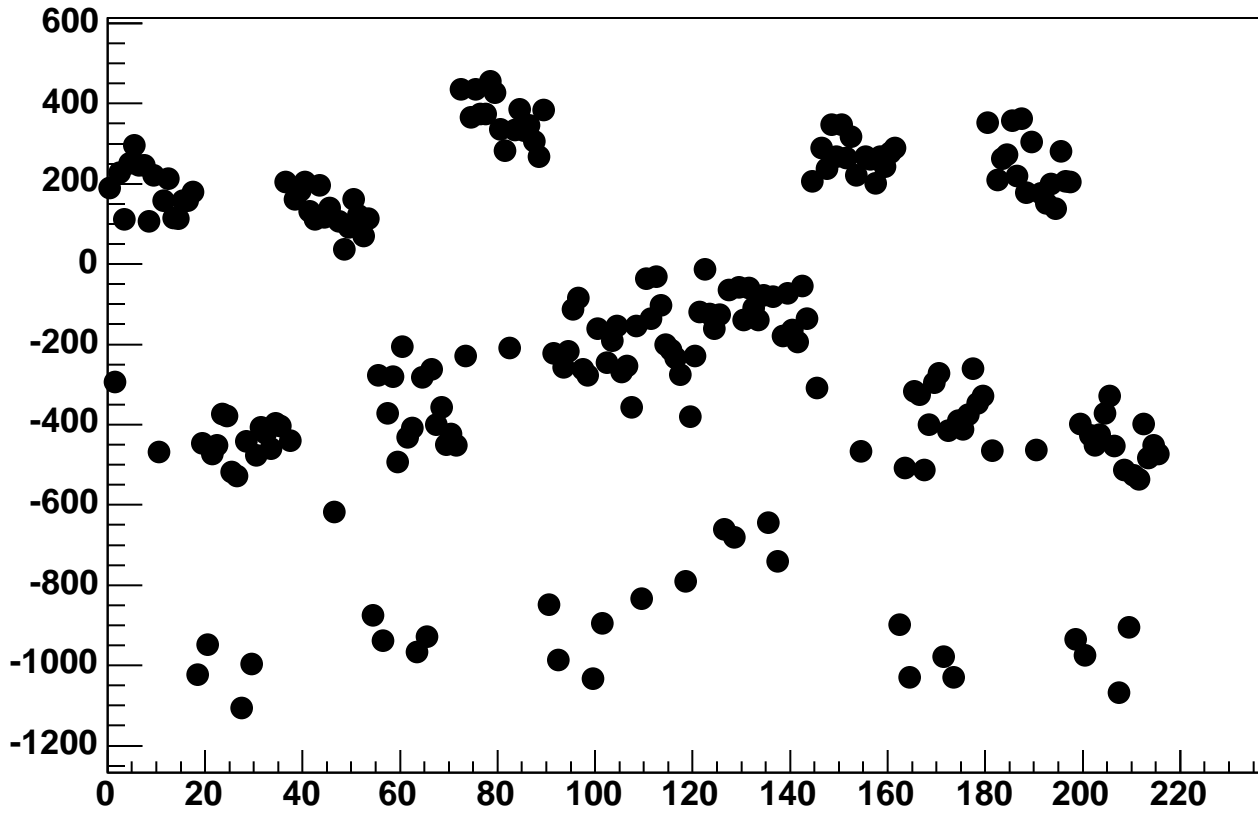
Enable 3, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



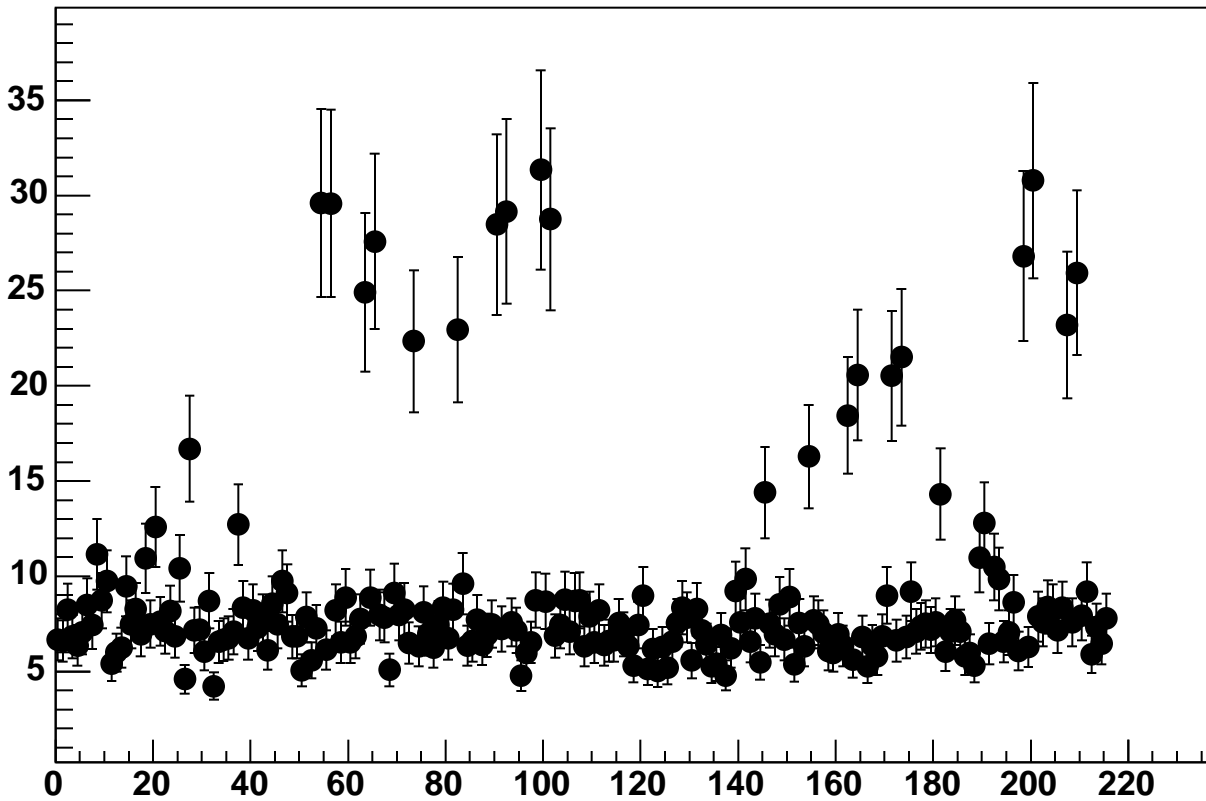
Enable 3, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



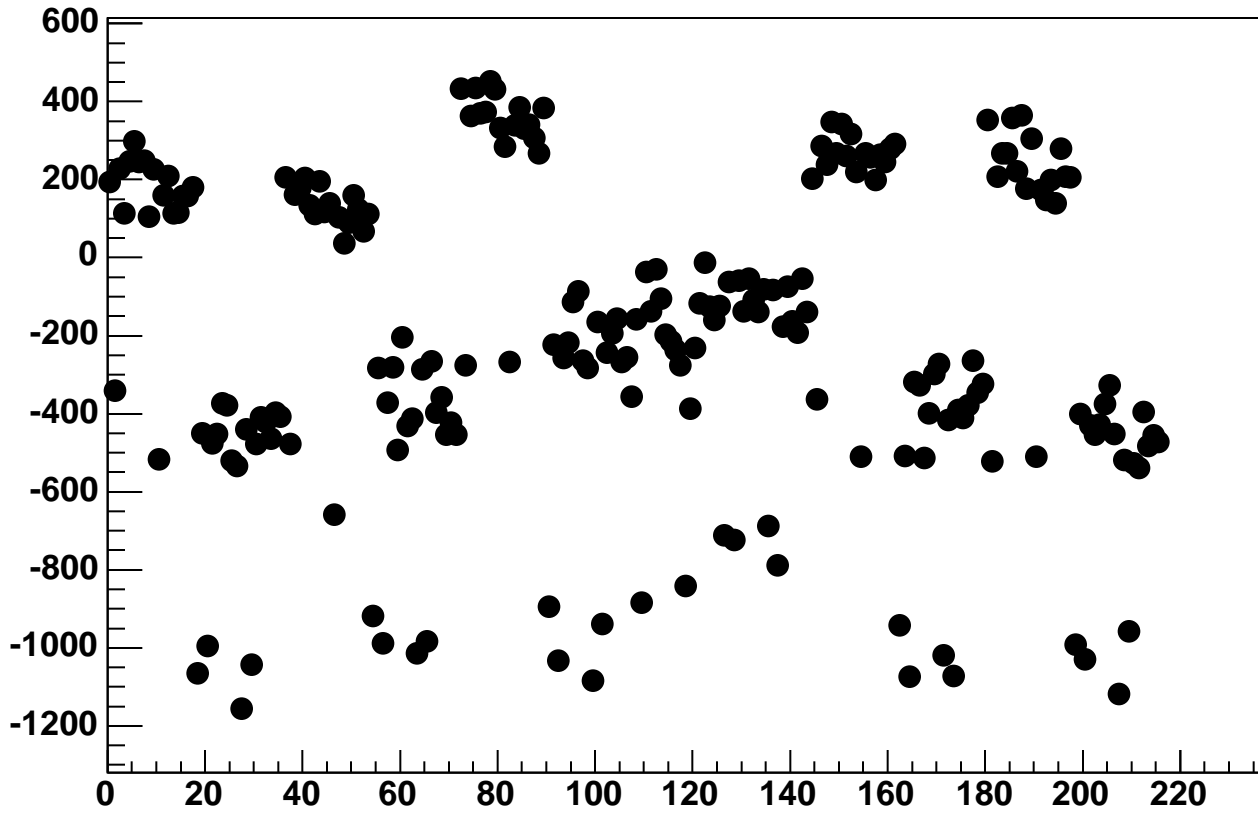
Enable 3, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



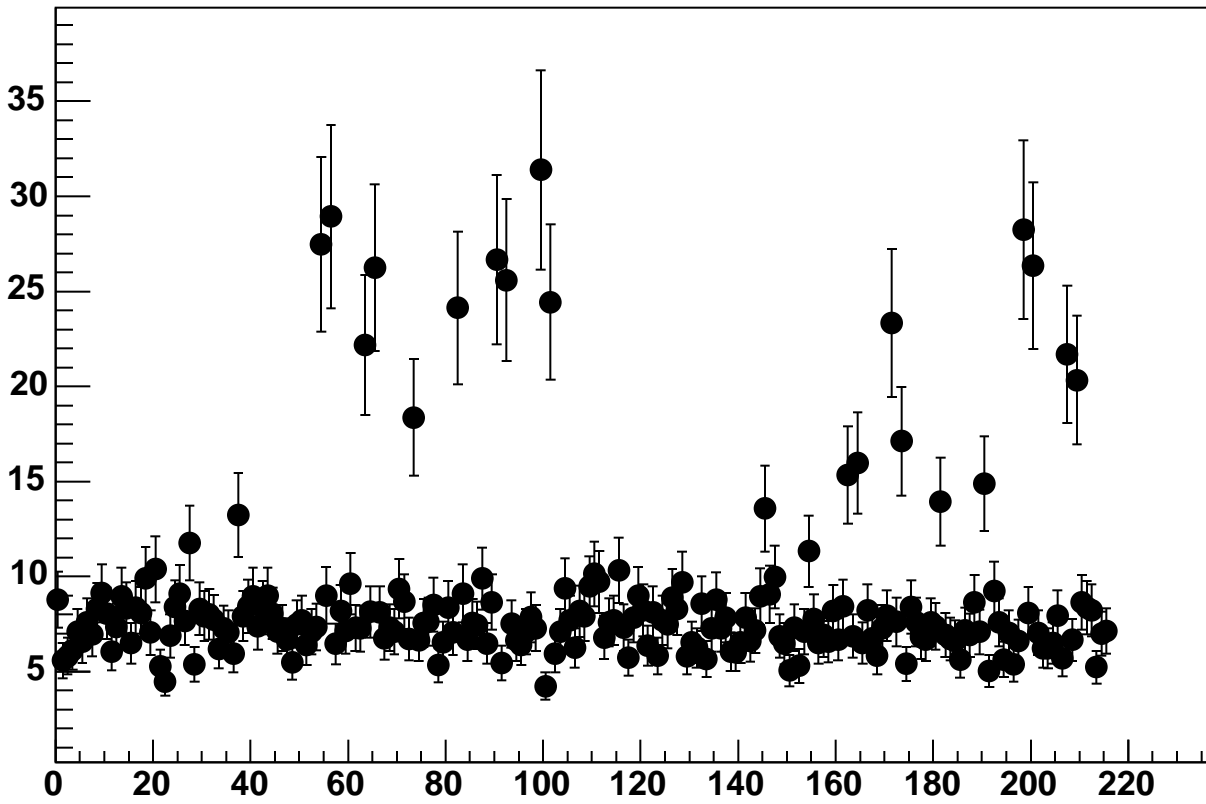
Enable 3, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



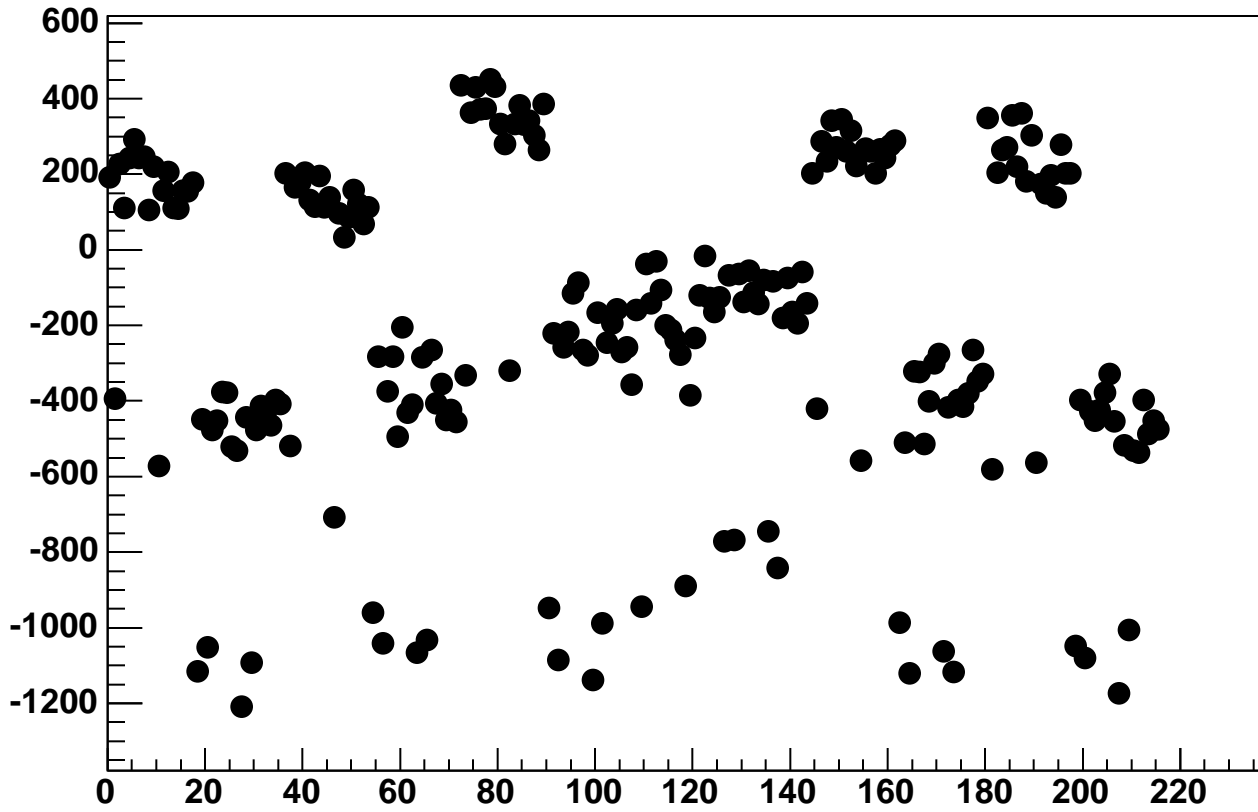
Enable 3, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



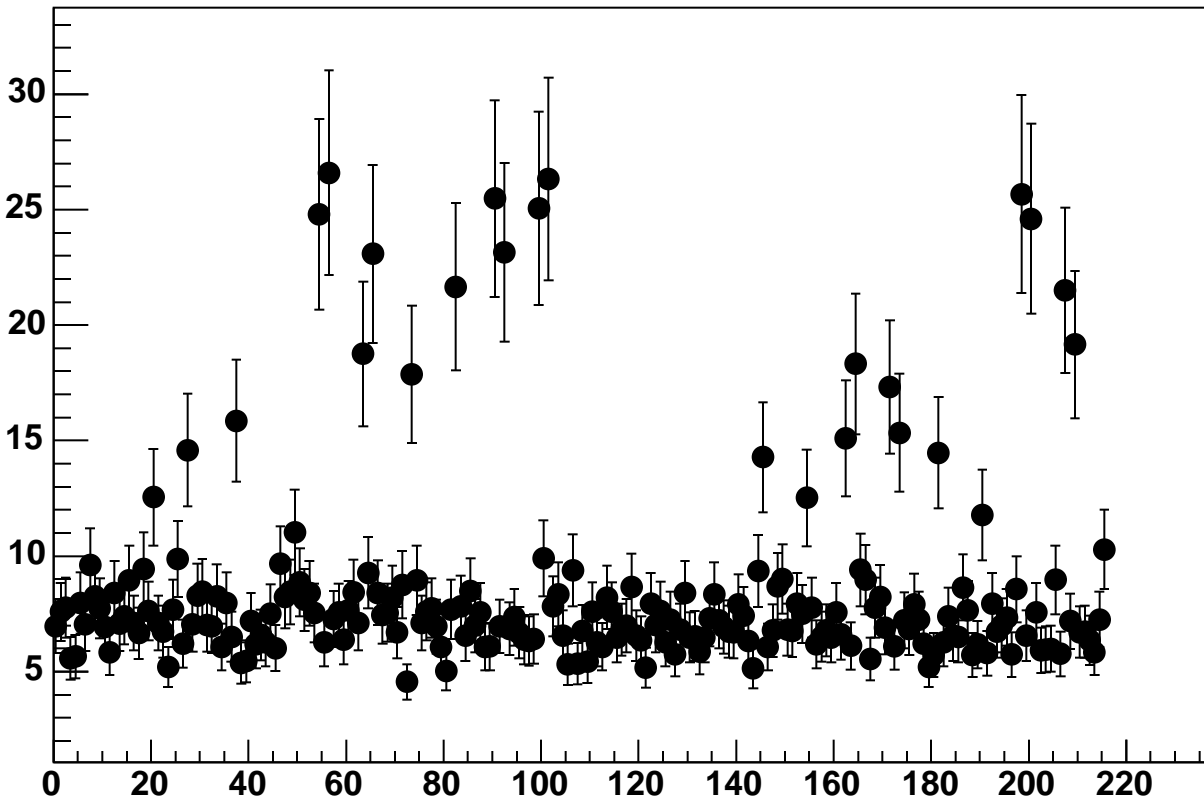
Enable 3, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



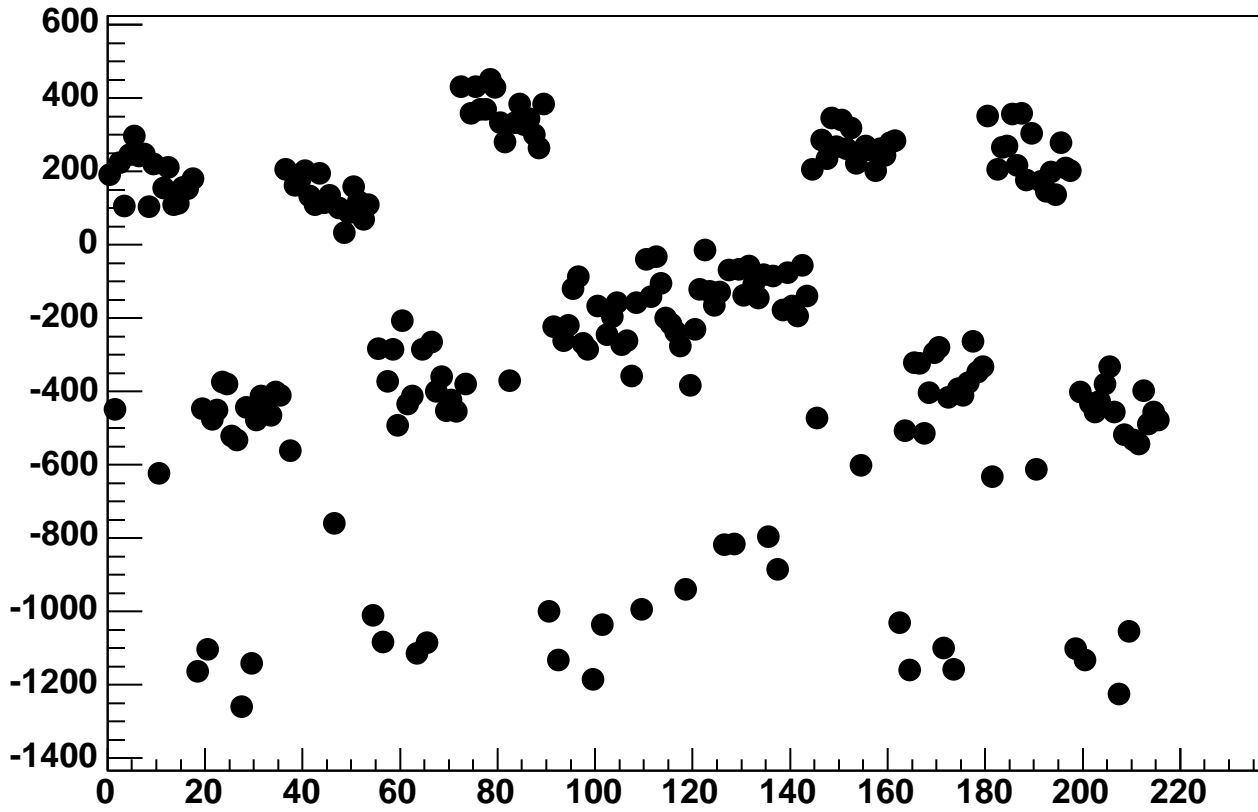
Enable 3, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



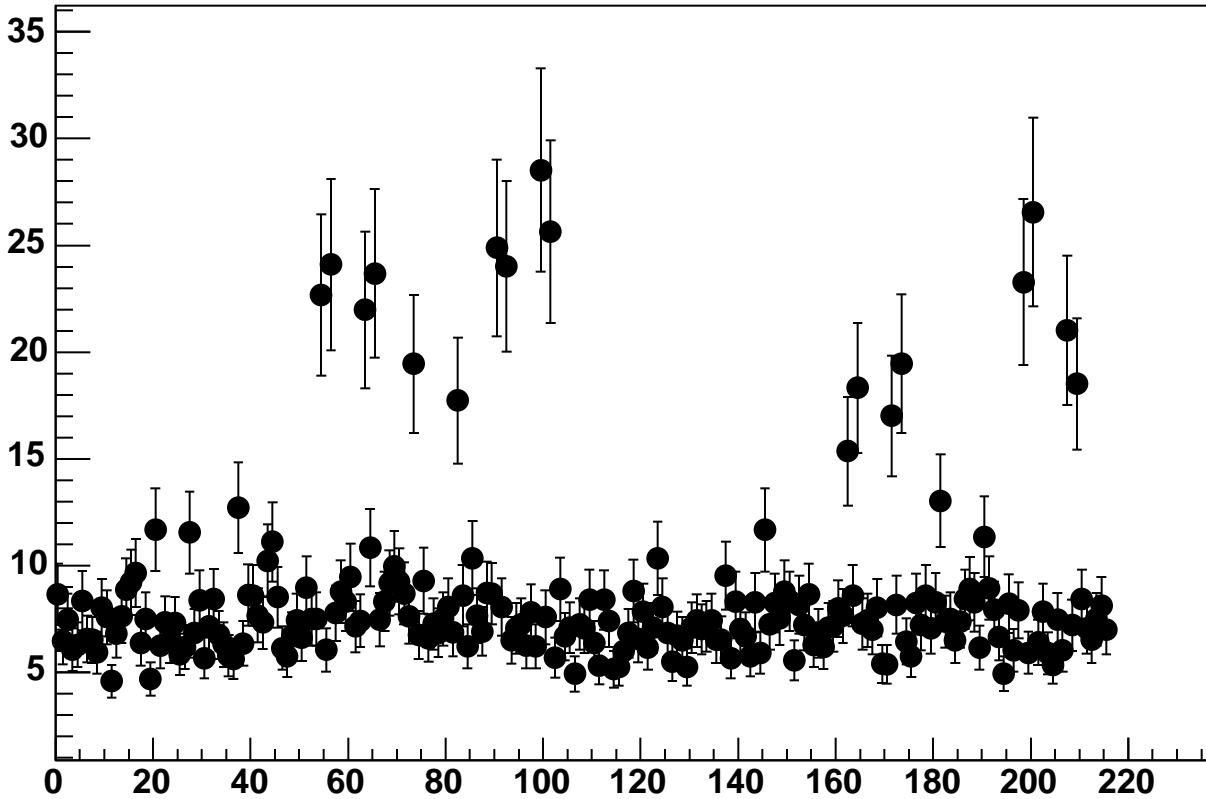
Enable 3, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



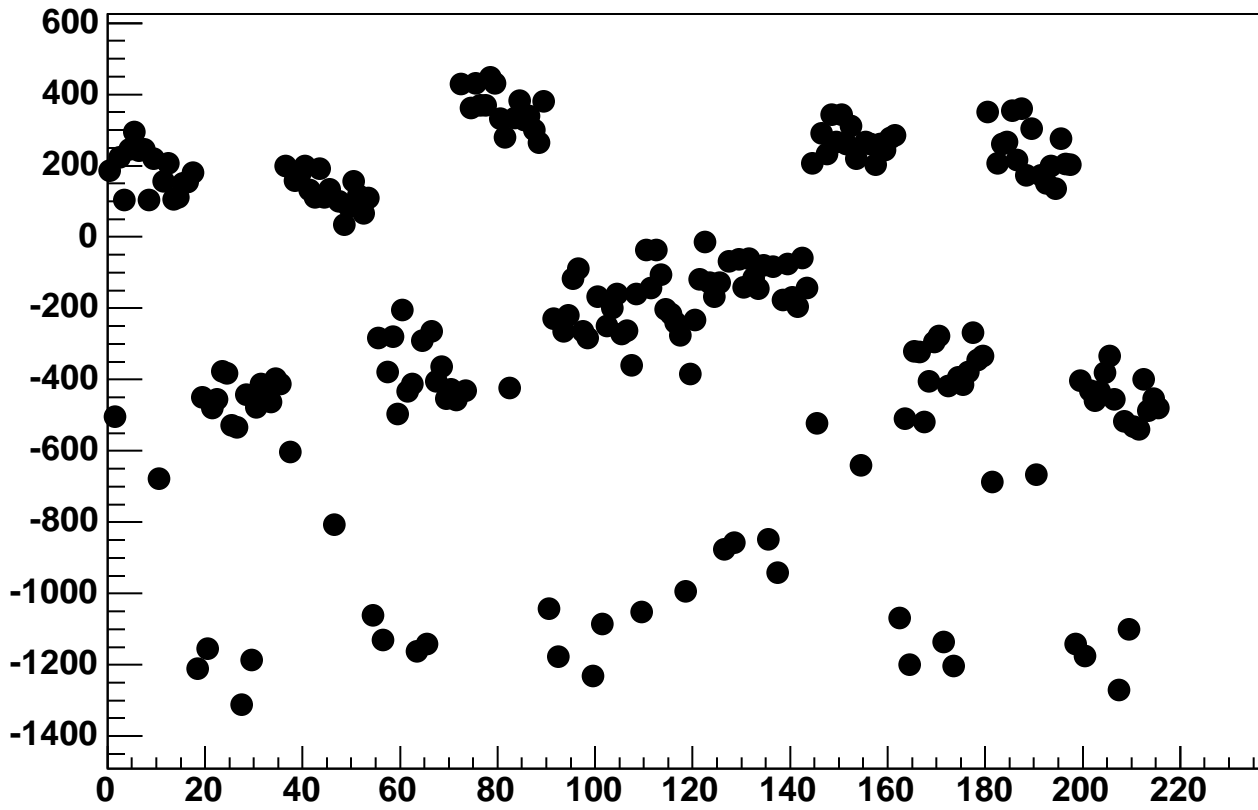
Enable 3, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



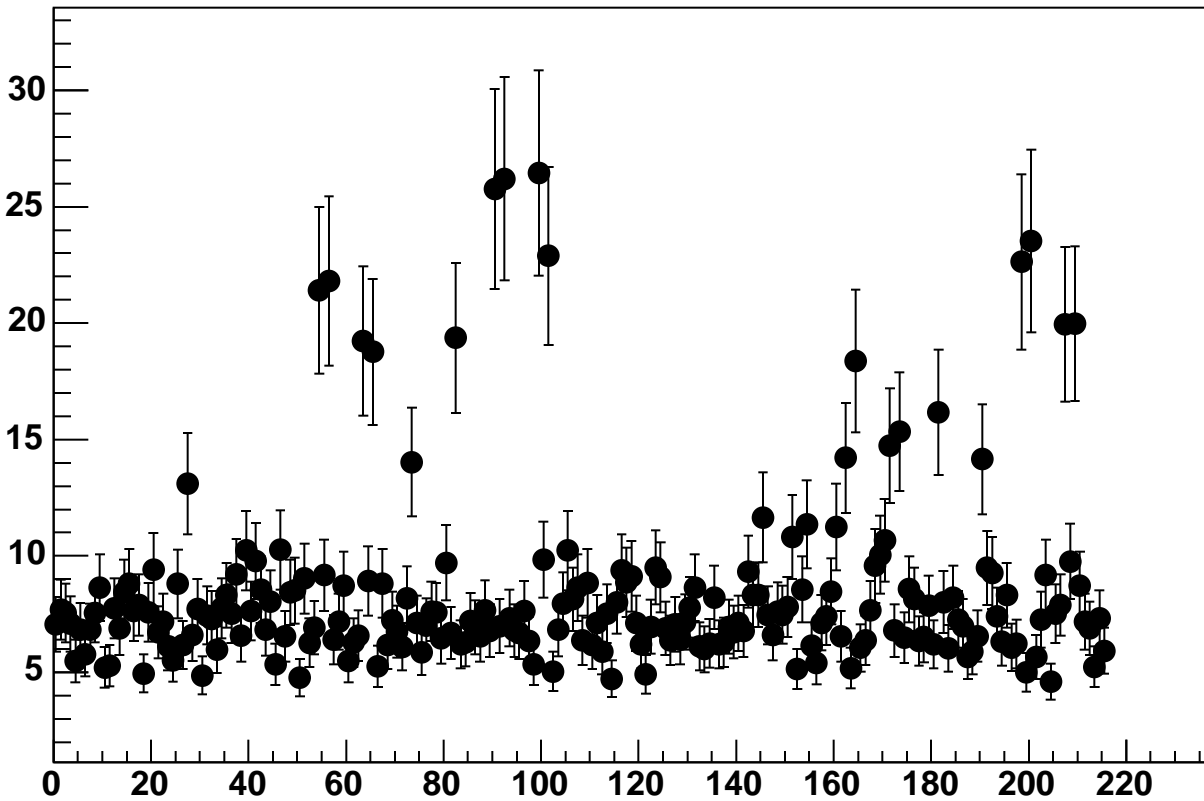
Enable 3, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



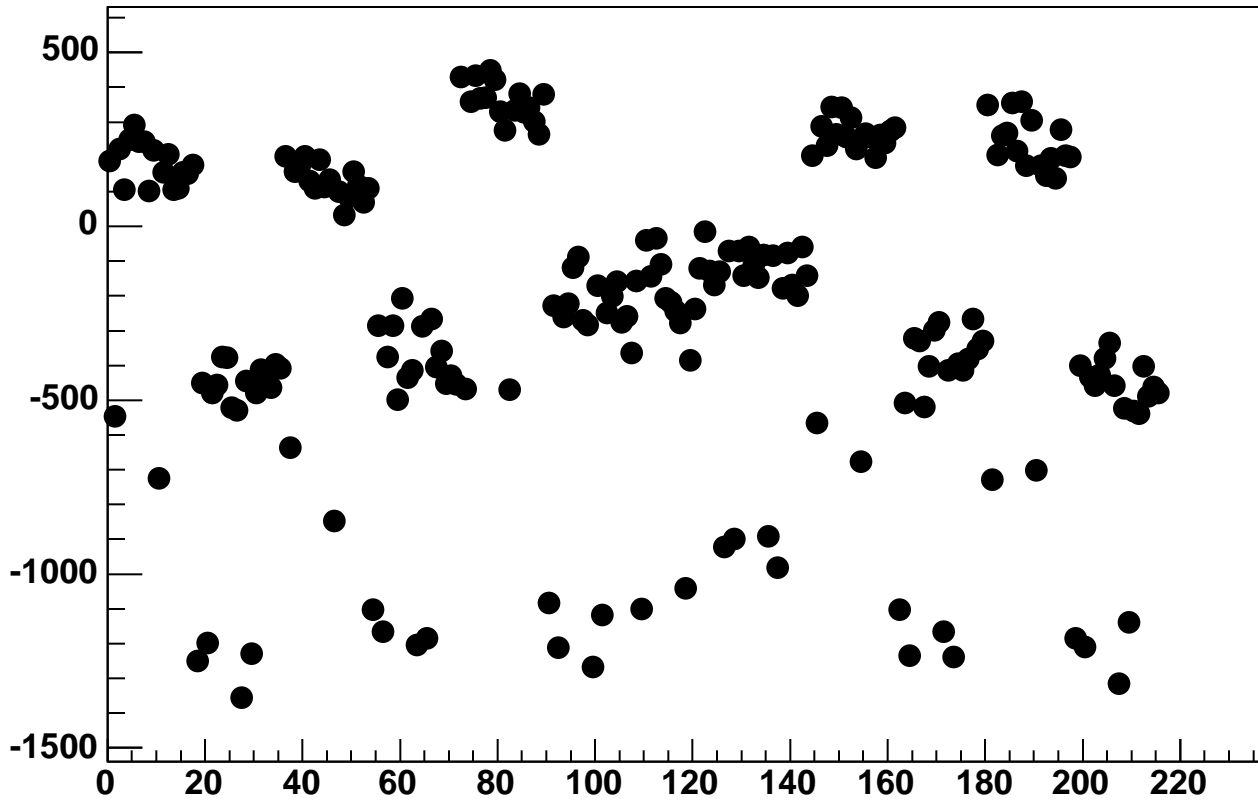
Enable 3, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



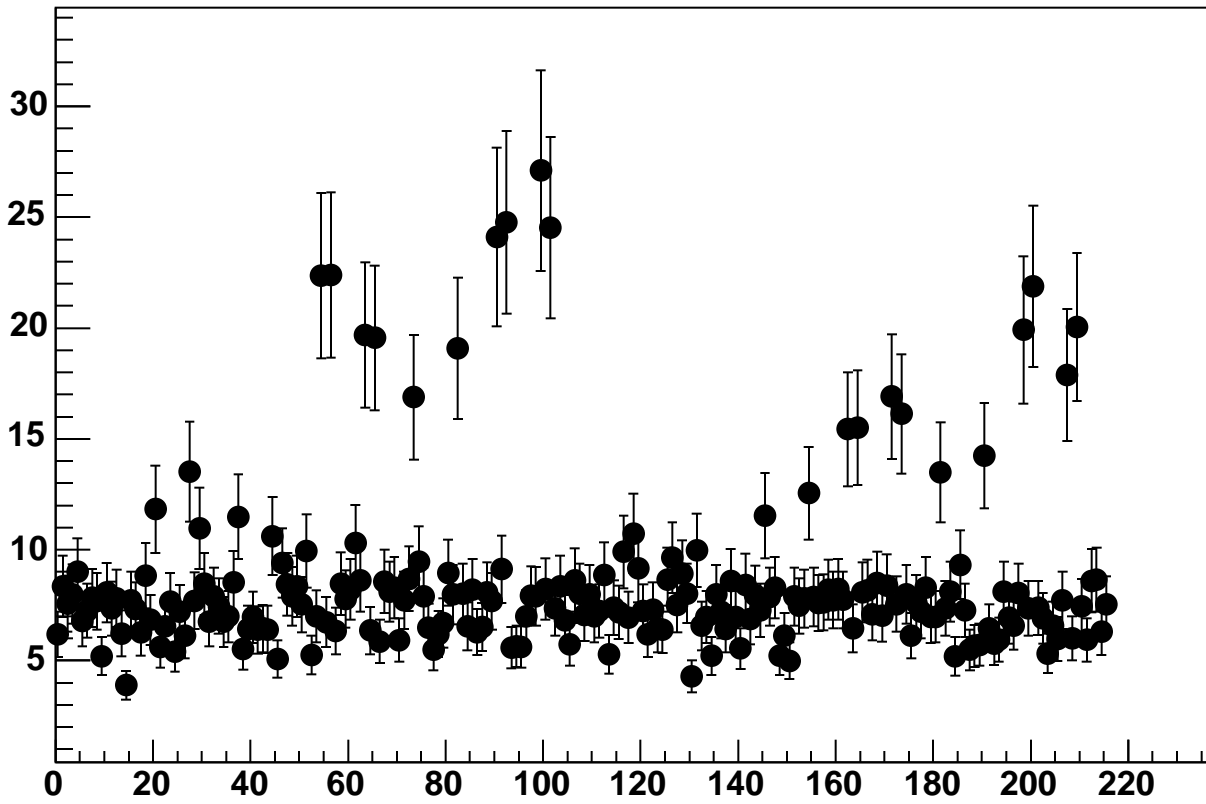
Enable 3, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



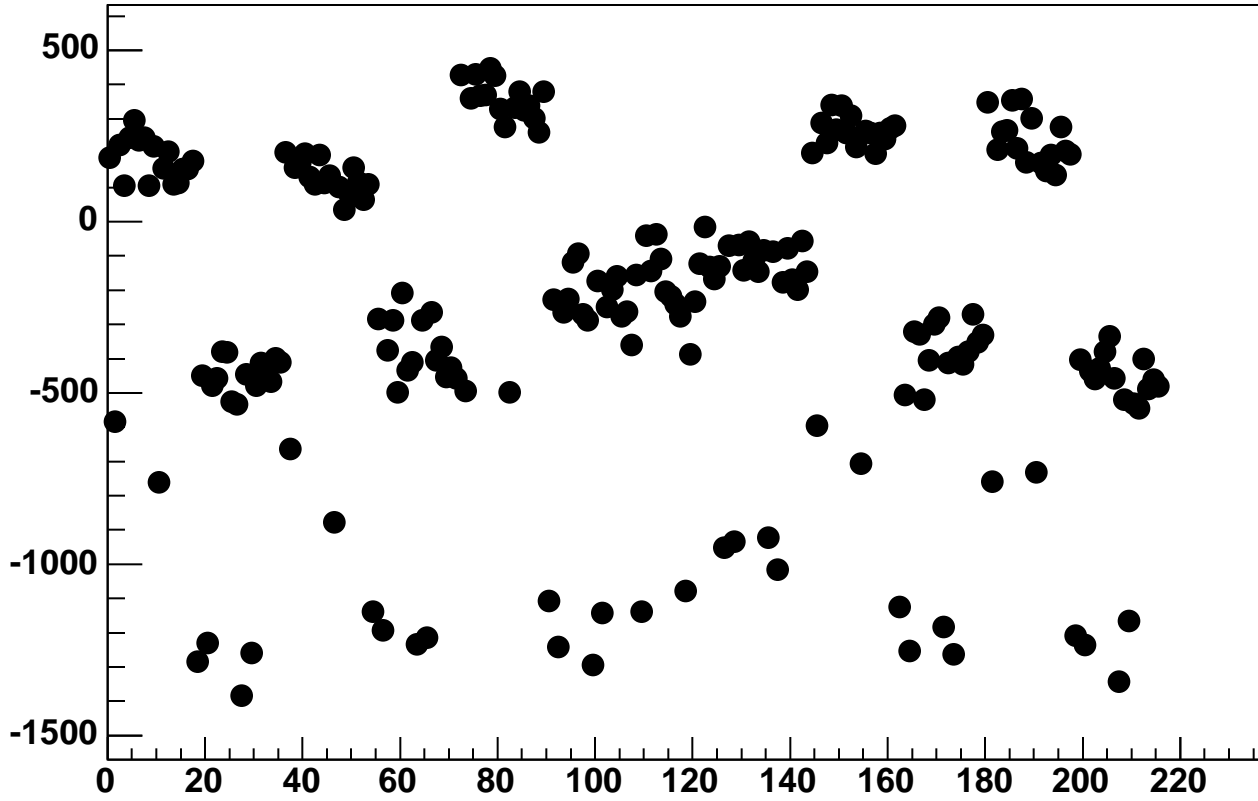
Enable 3, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



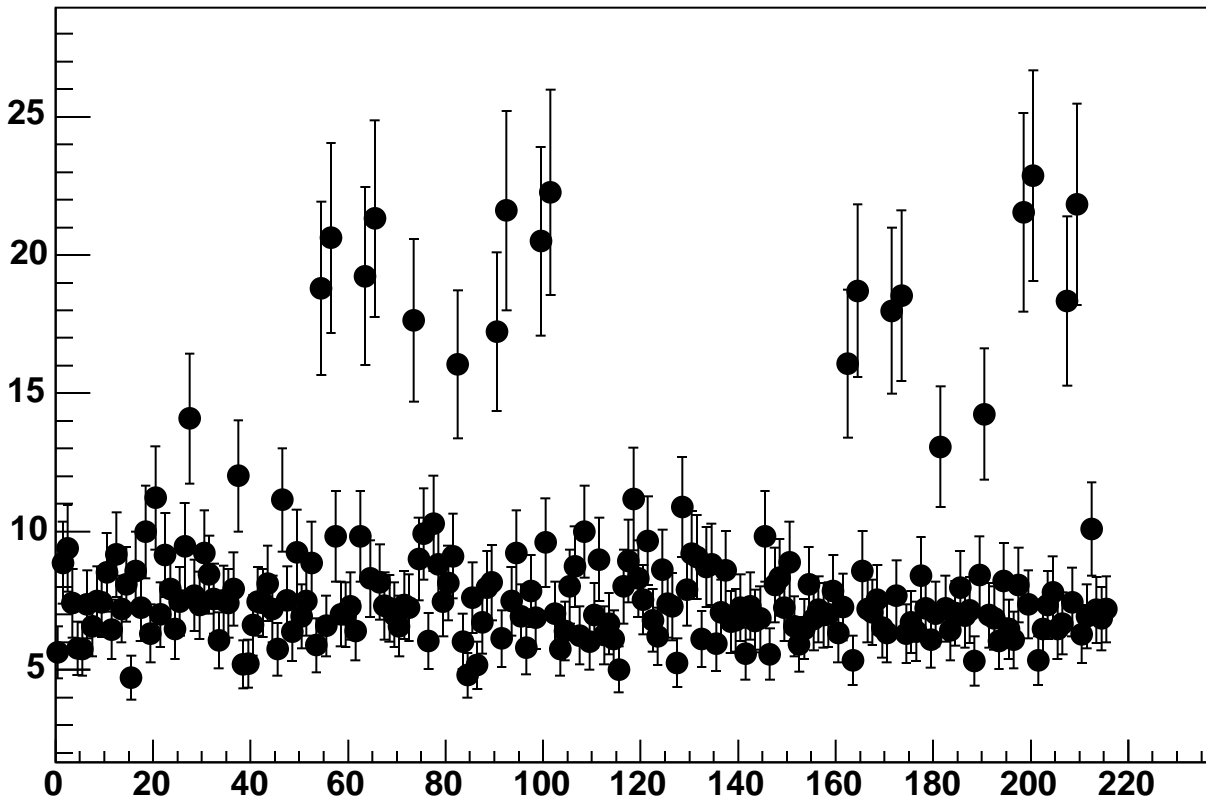
Enable 3, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



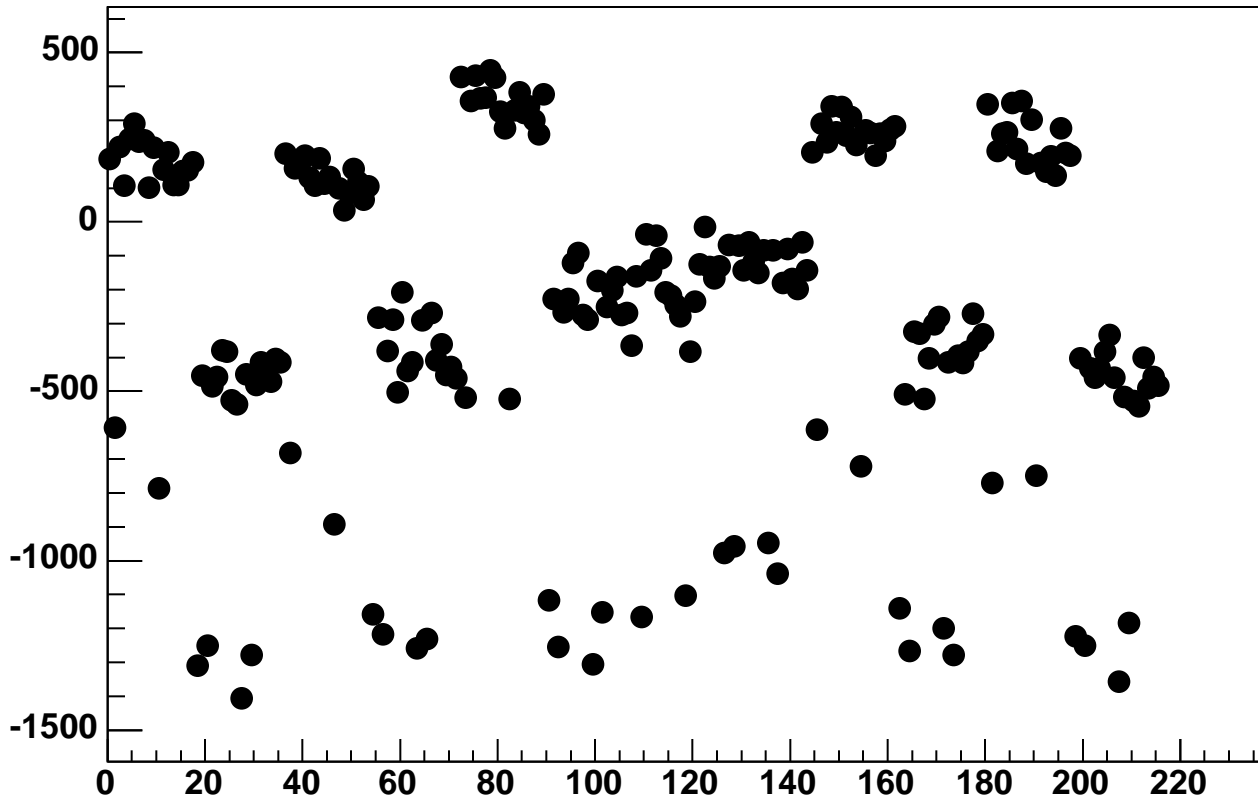
Enable 3, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



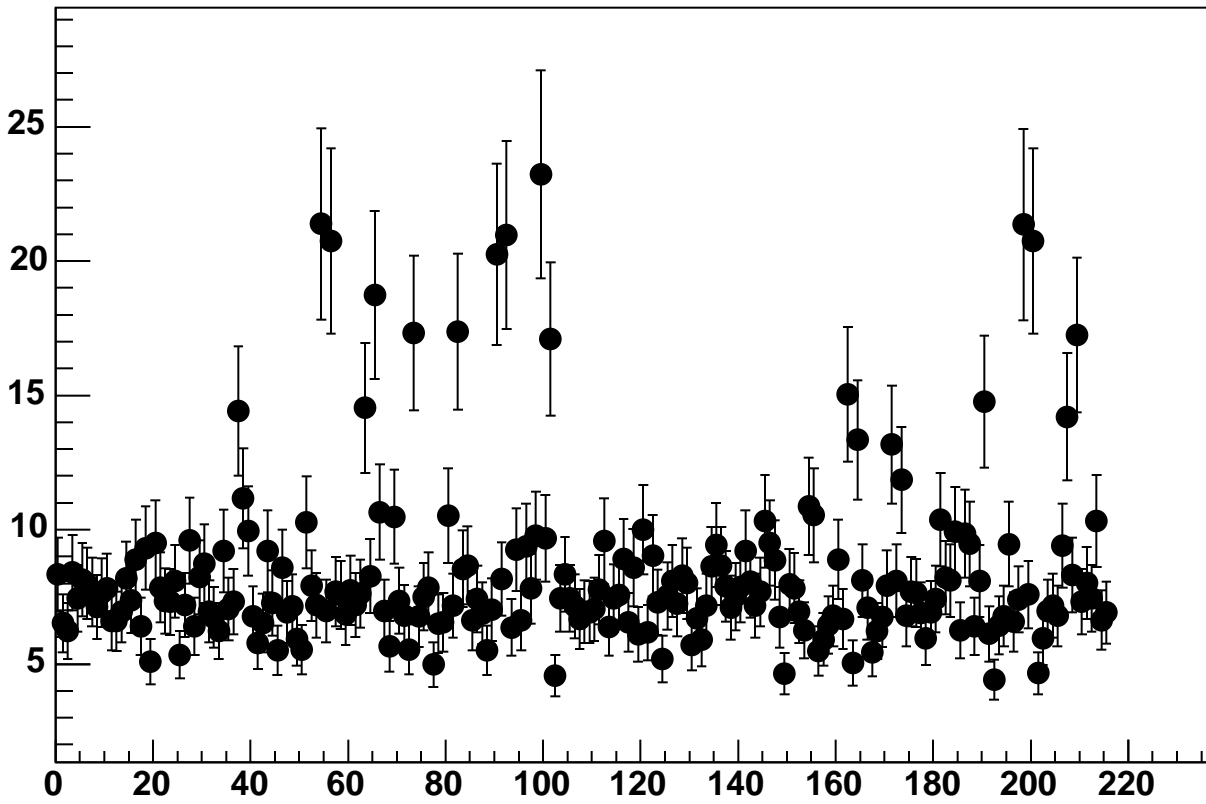
Enable 3, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



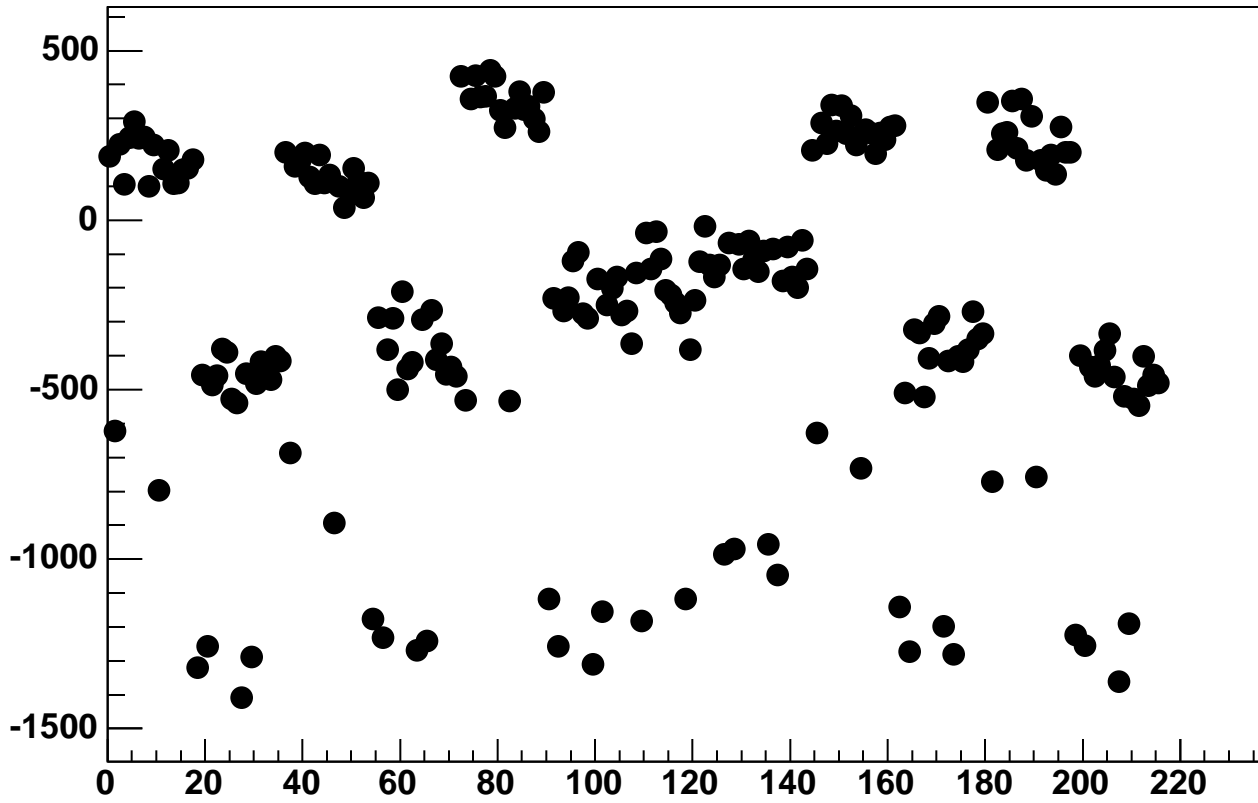
Enable 3, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



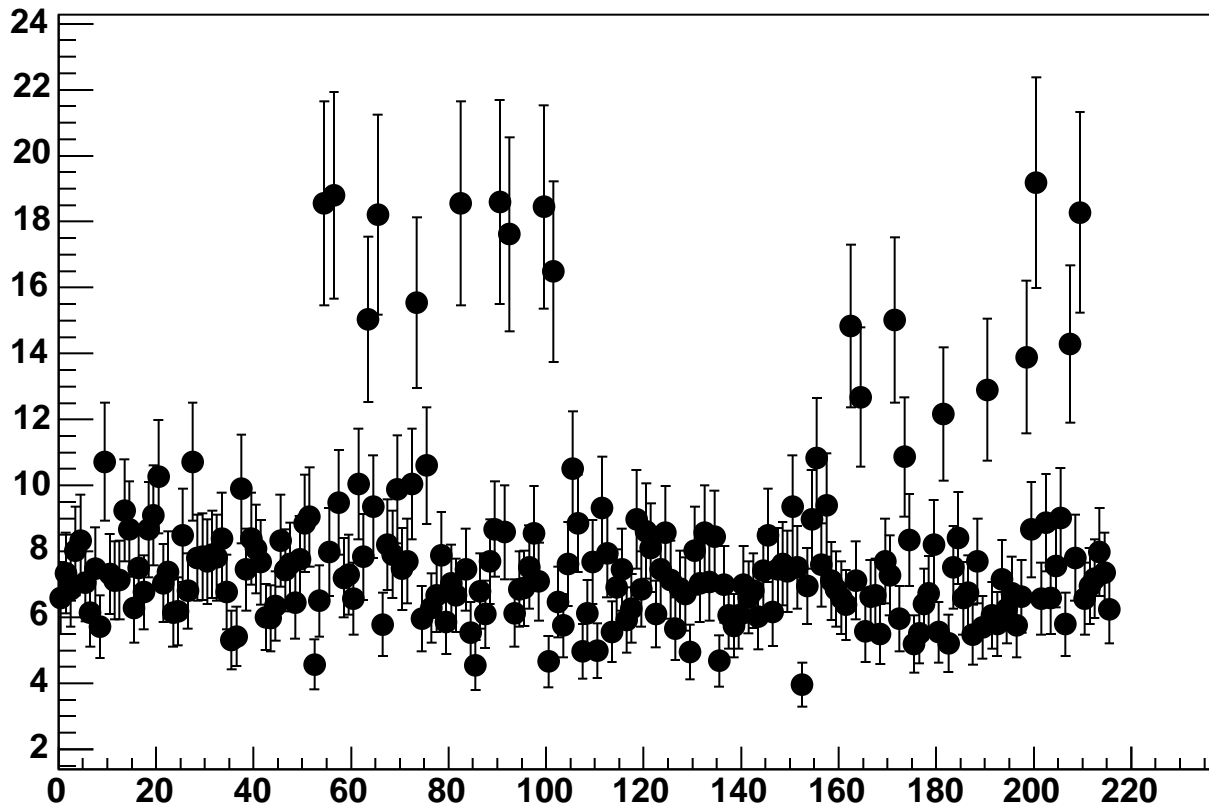
Enable 3, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



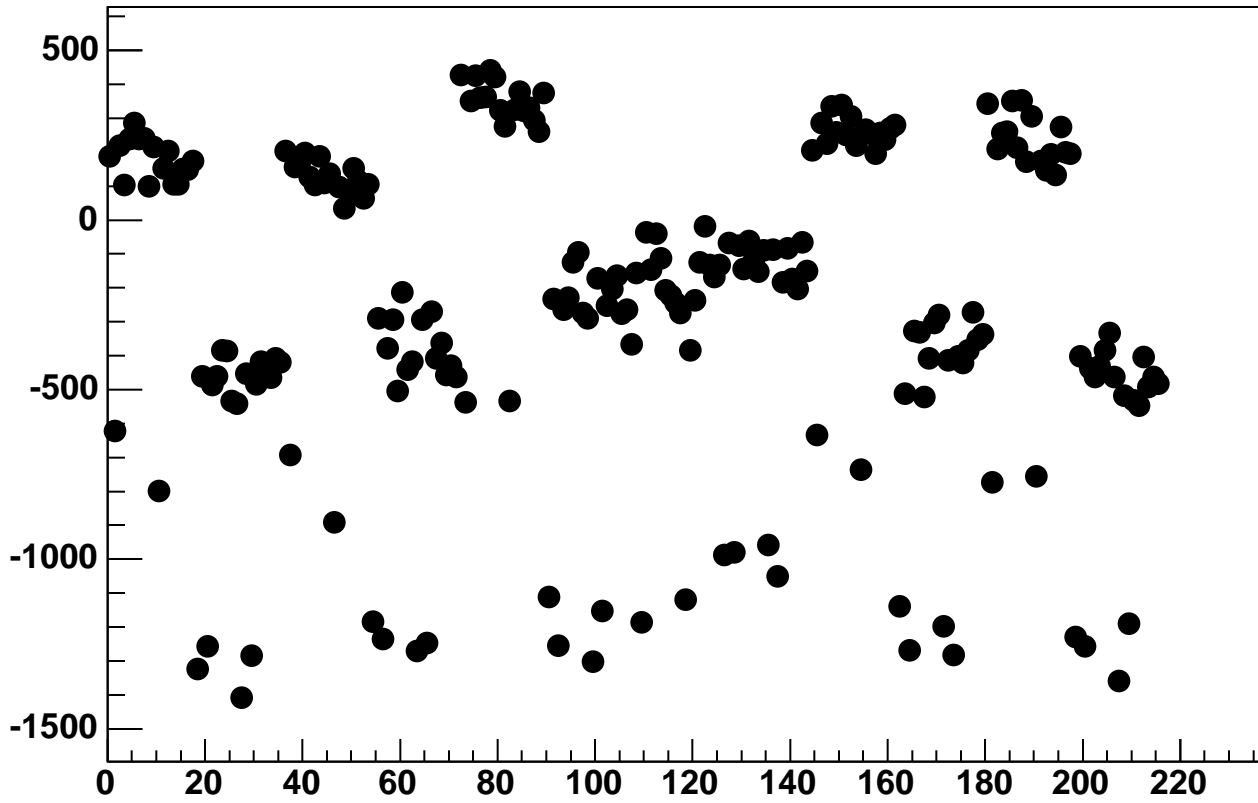
Enable 3, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



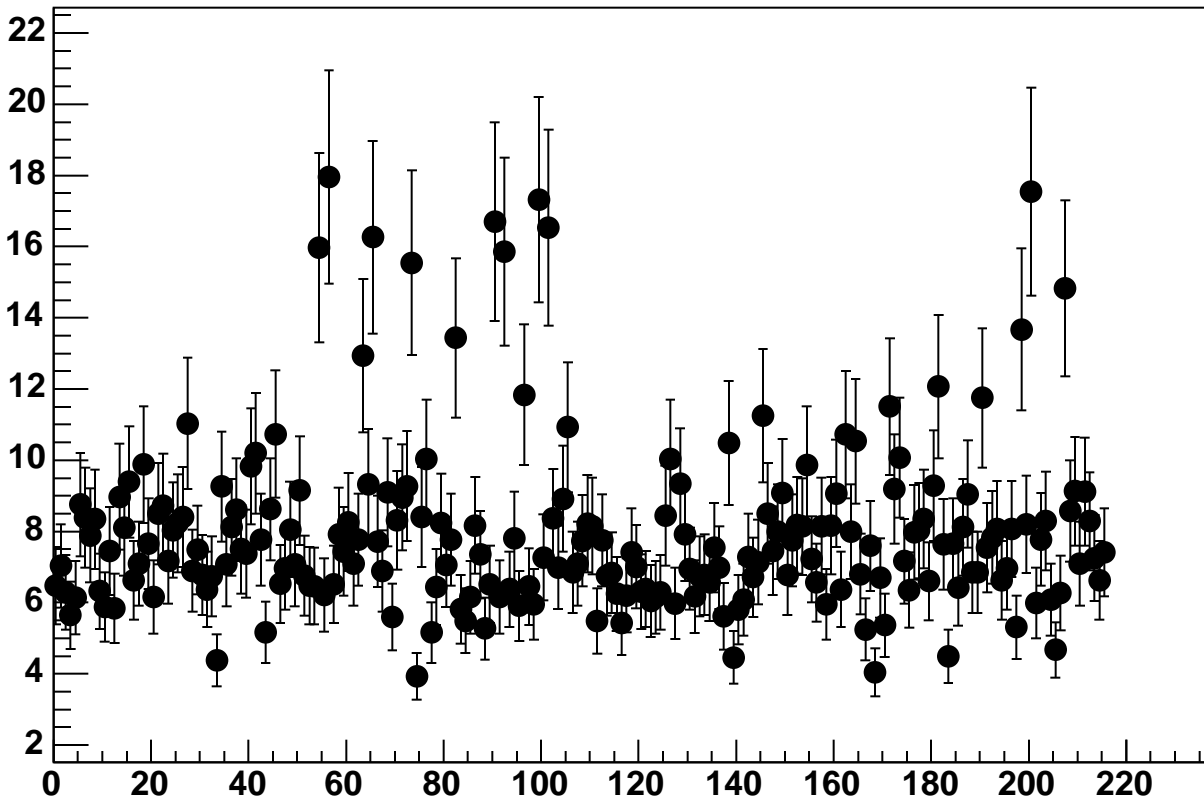
Enable 3, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



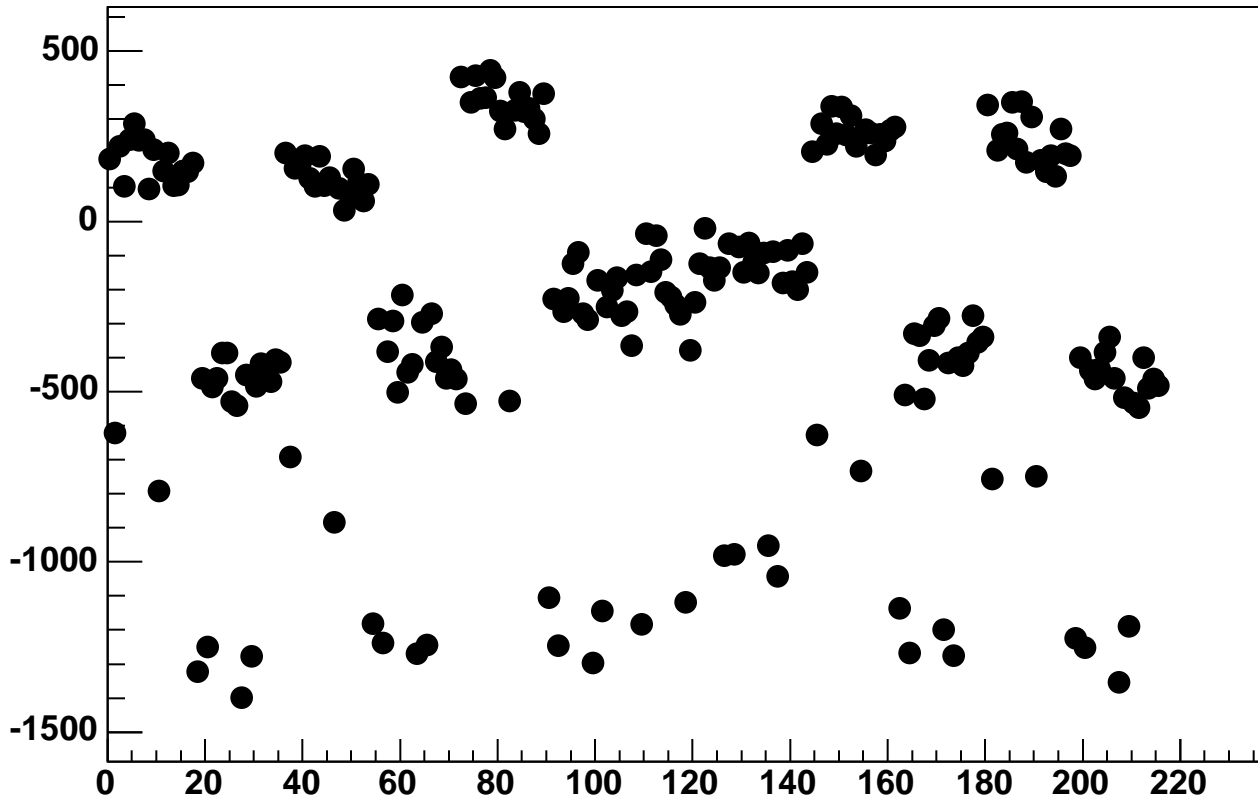
Enable 3, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



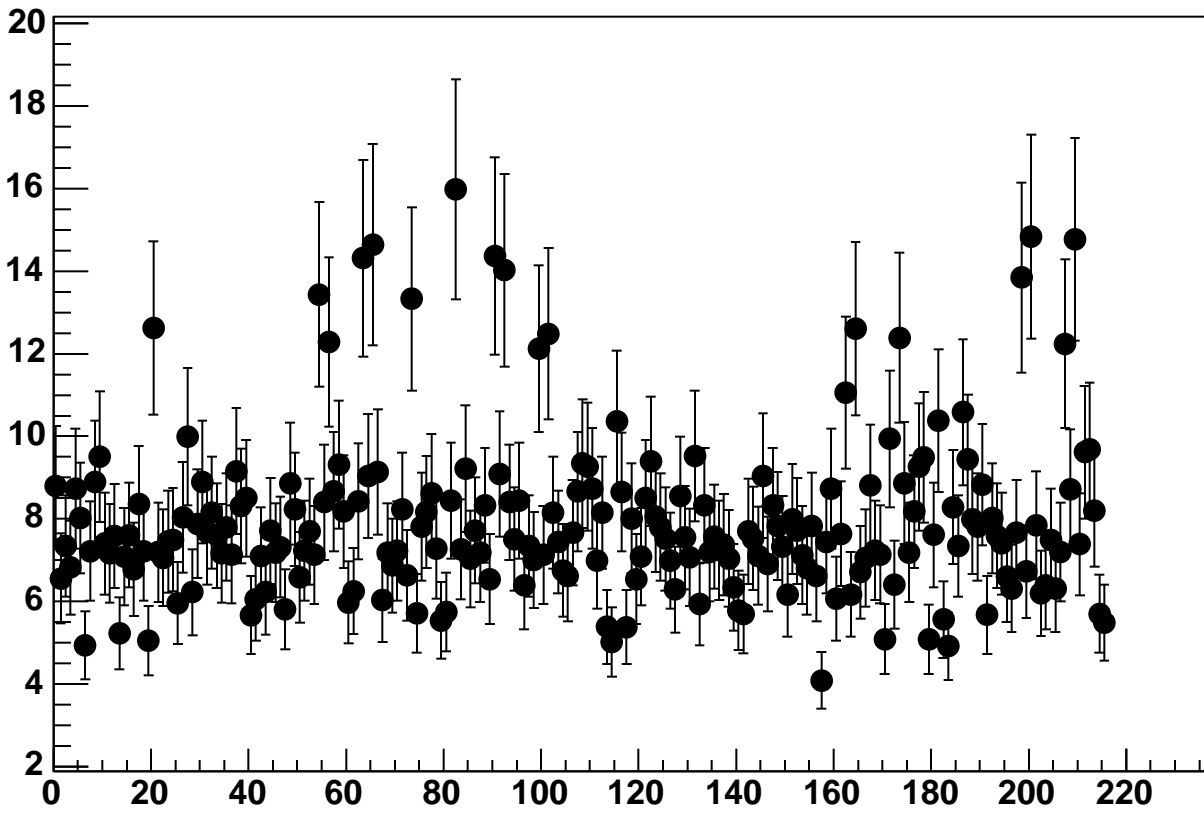
Enable 3, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



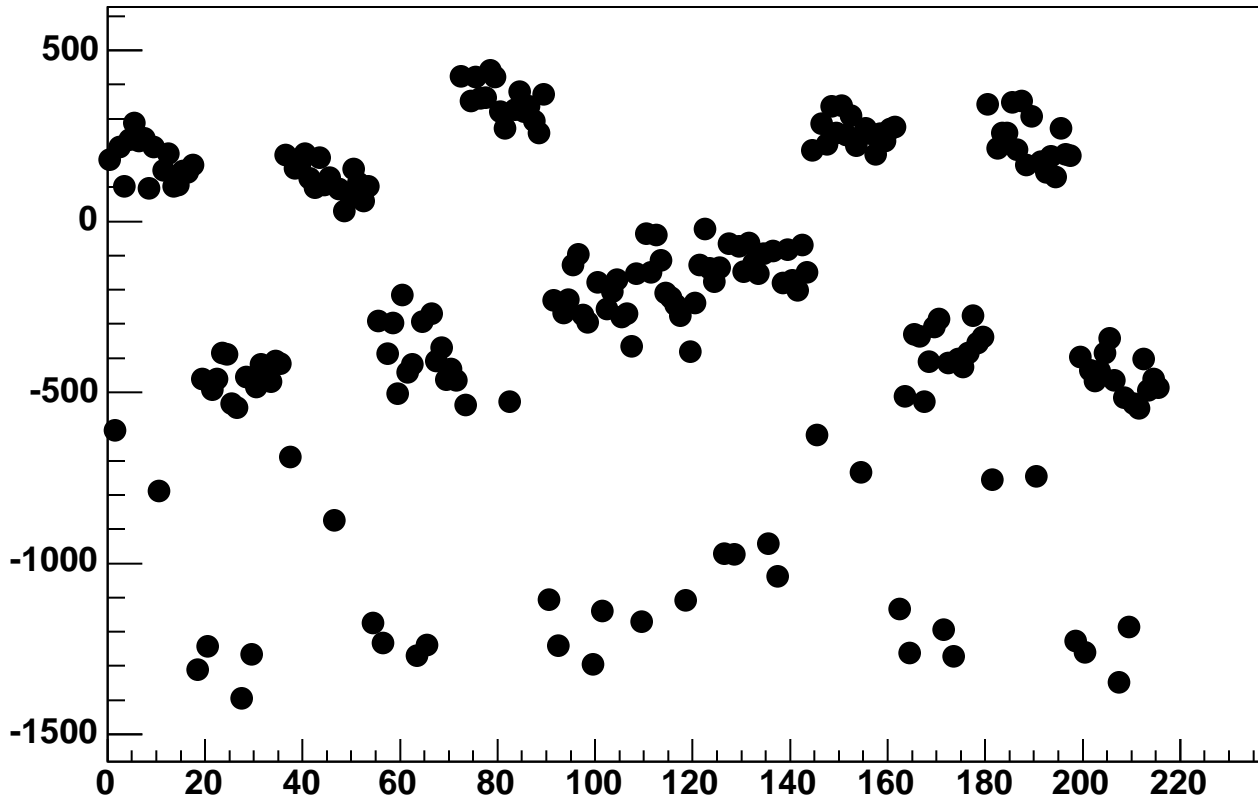
Enable 3, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



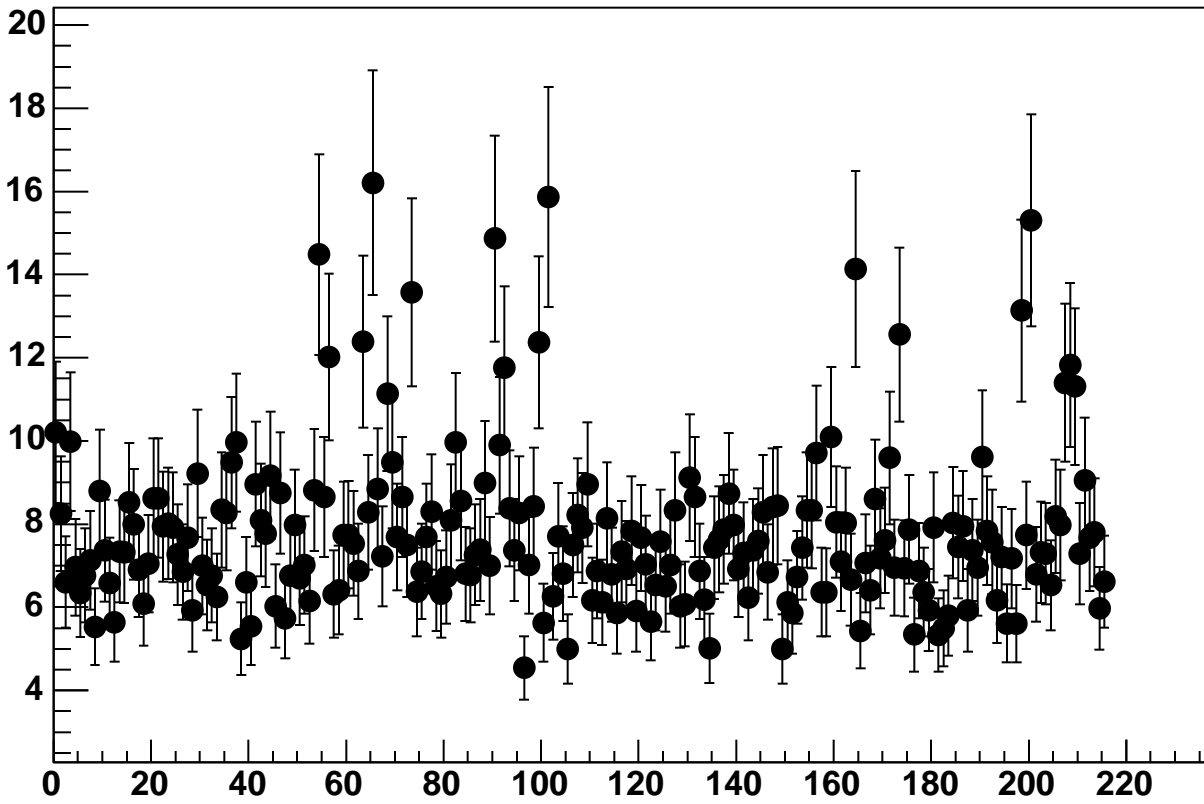
Enable 3, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



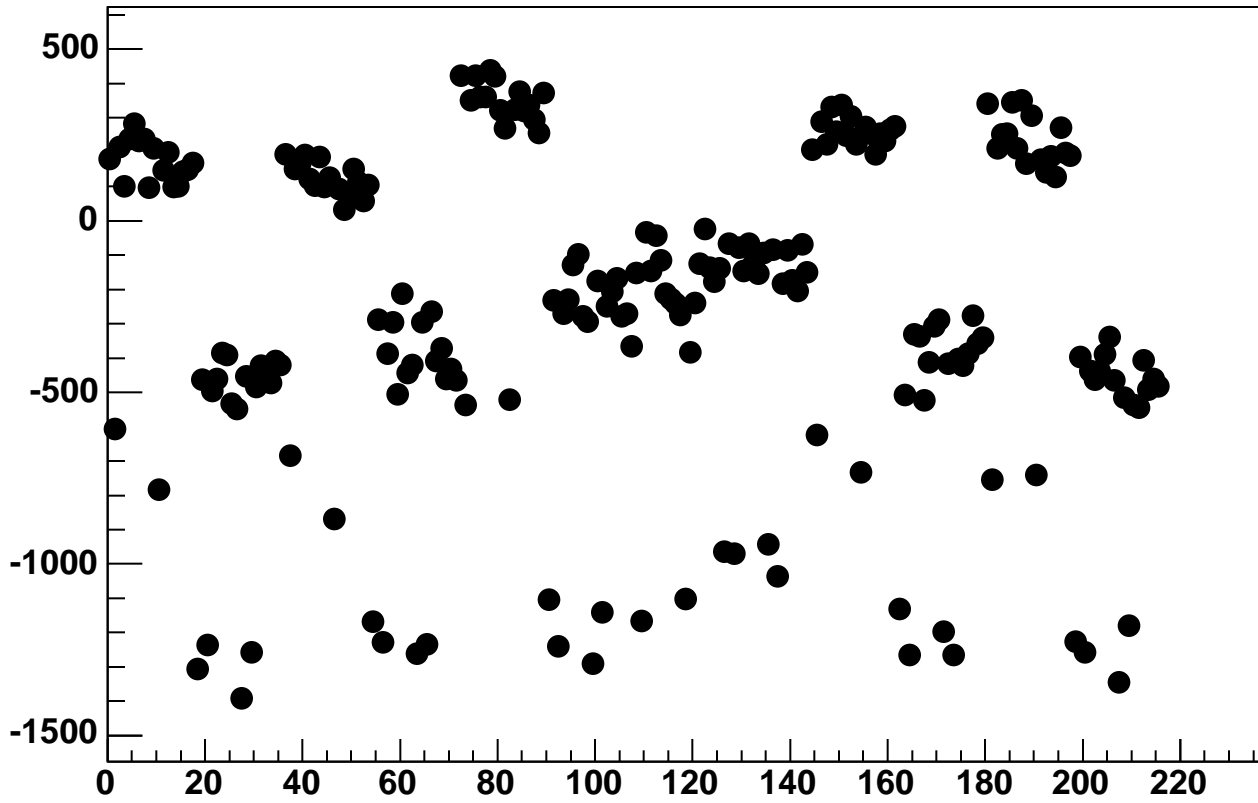
Enable 3, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



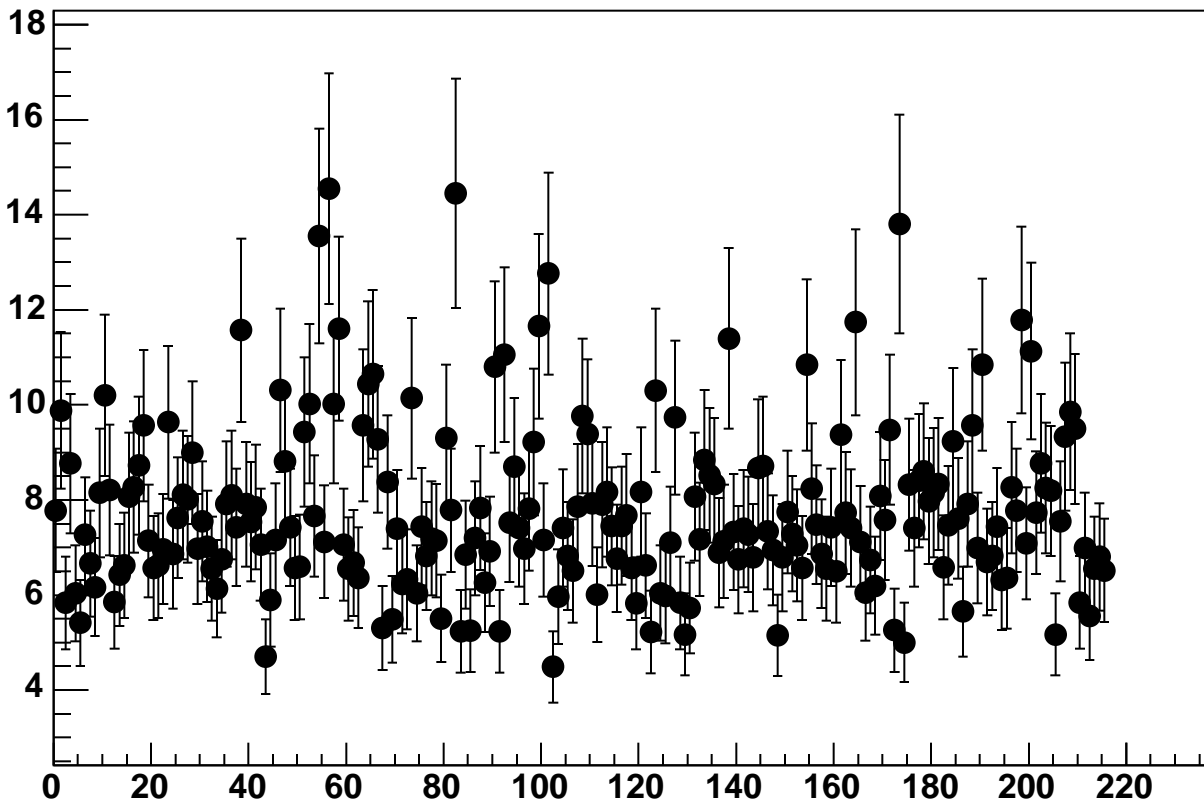
Enable 3, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan



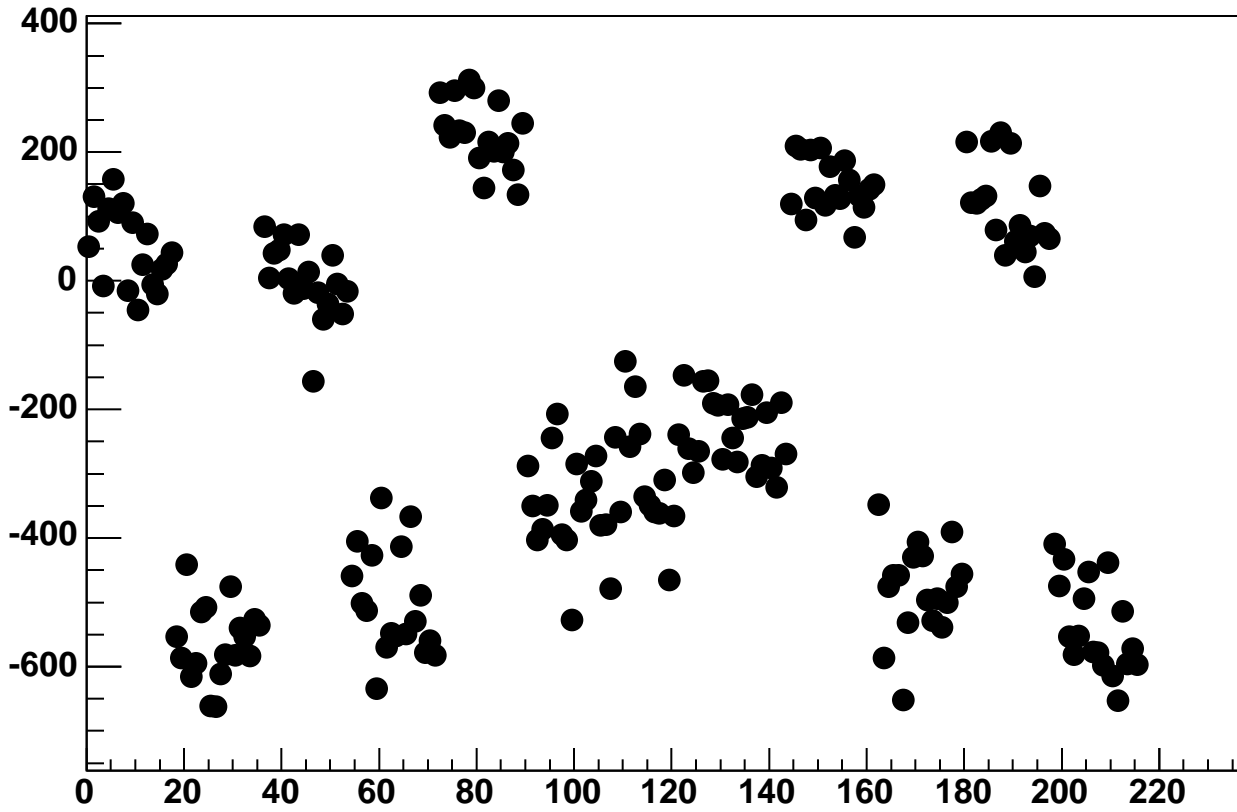
Enable 3, DAC=4000, Hold=245, ADC Mean vs 18*Chip+Chan



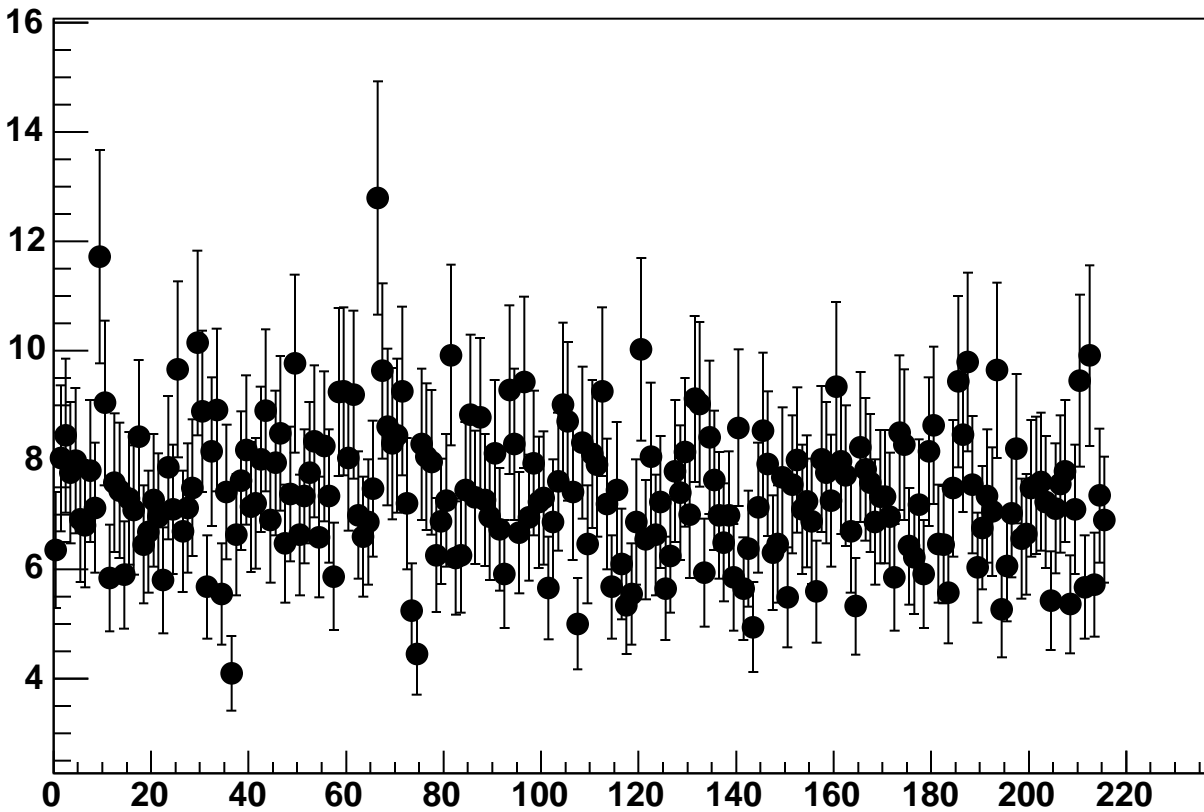
Enable 3, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



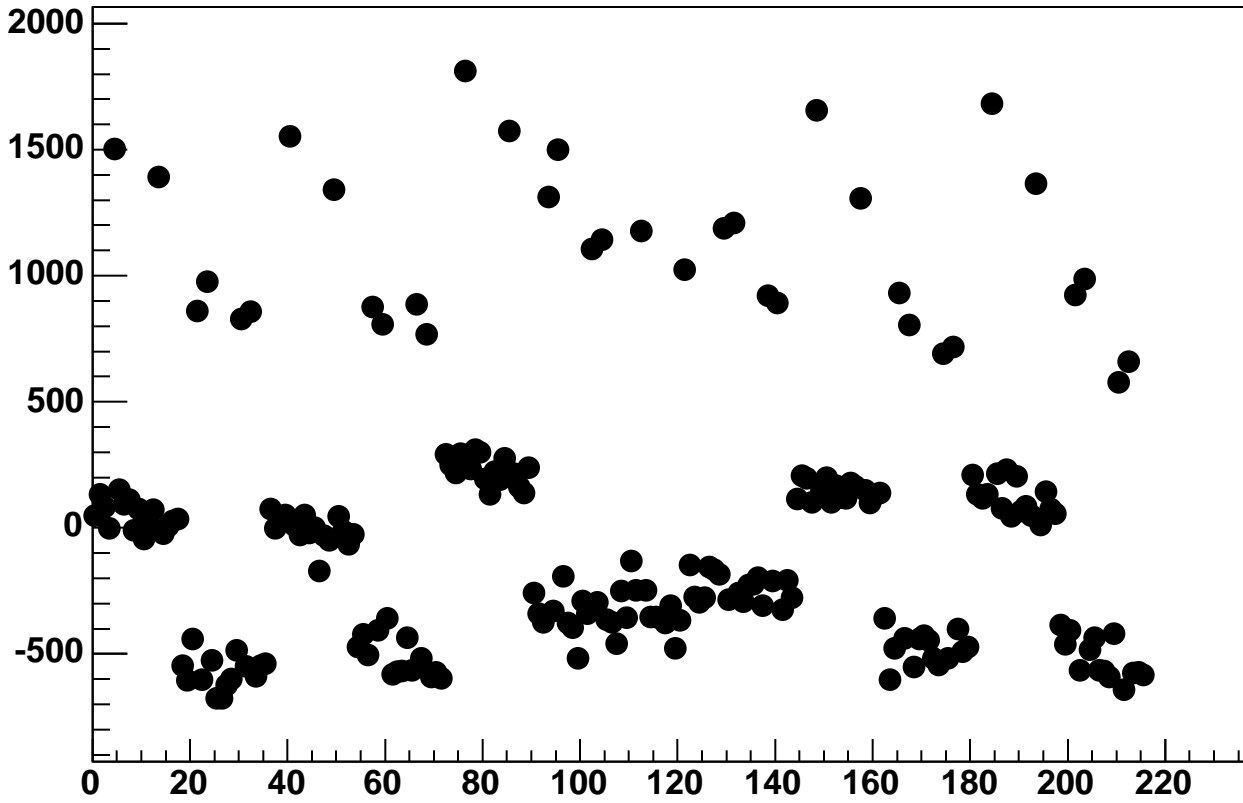
Enable 4, DAC=4000, Hold=0, ADC Mean vs 18*Chip+Chan



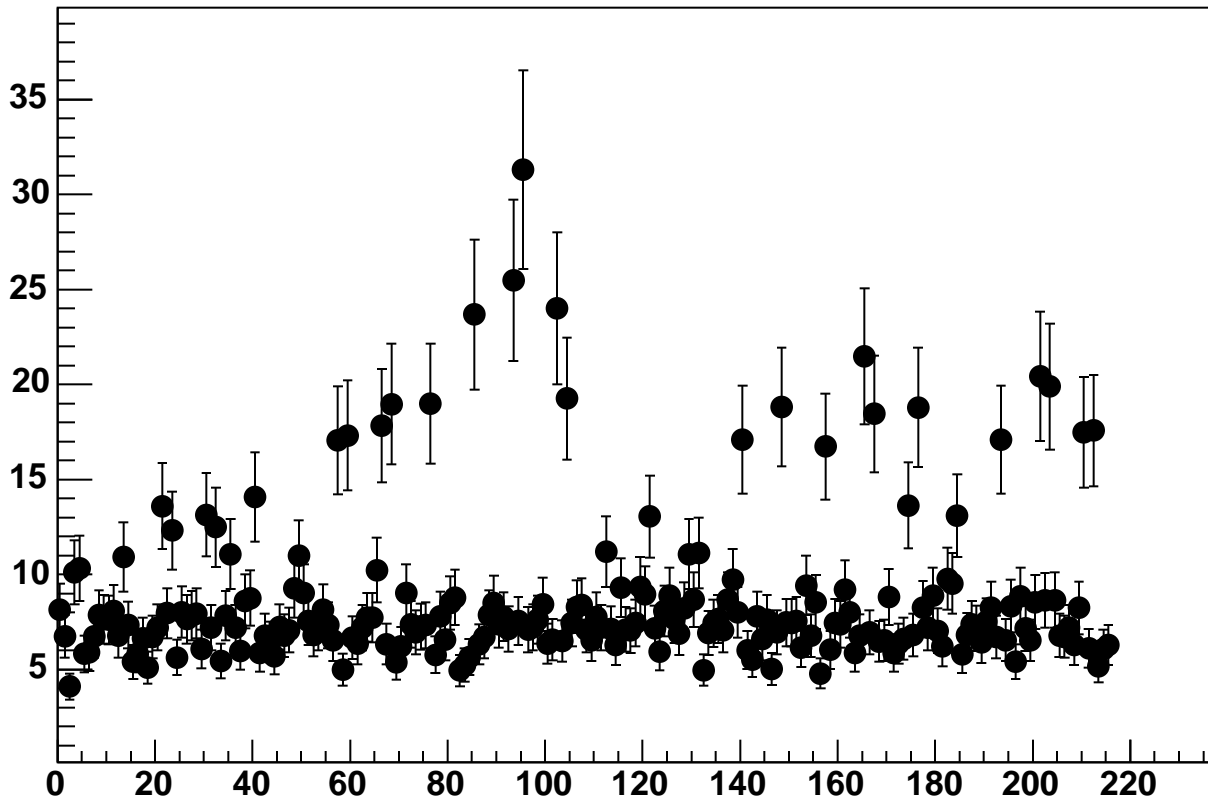
Enable 4, DAC=4000, Hold=0, ADC Noise vs 18*Chip+Chan



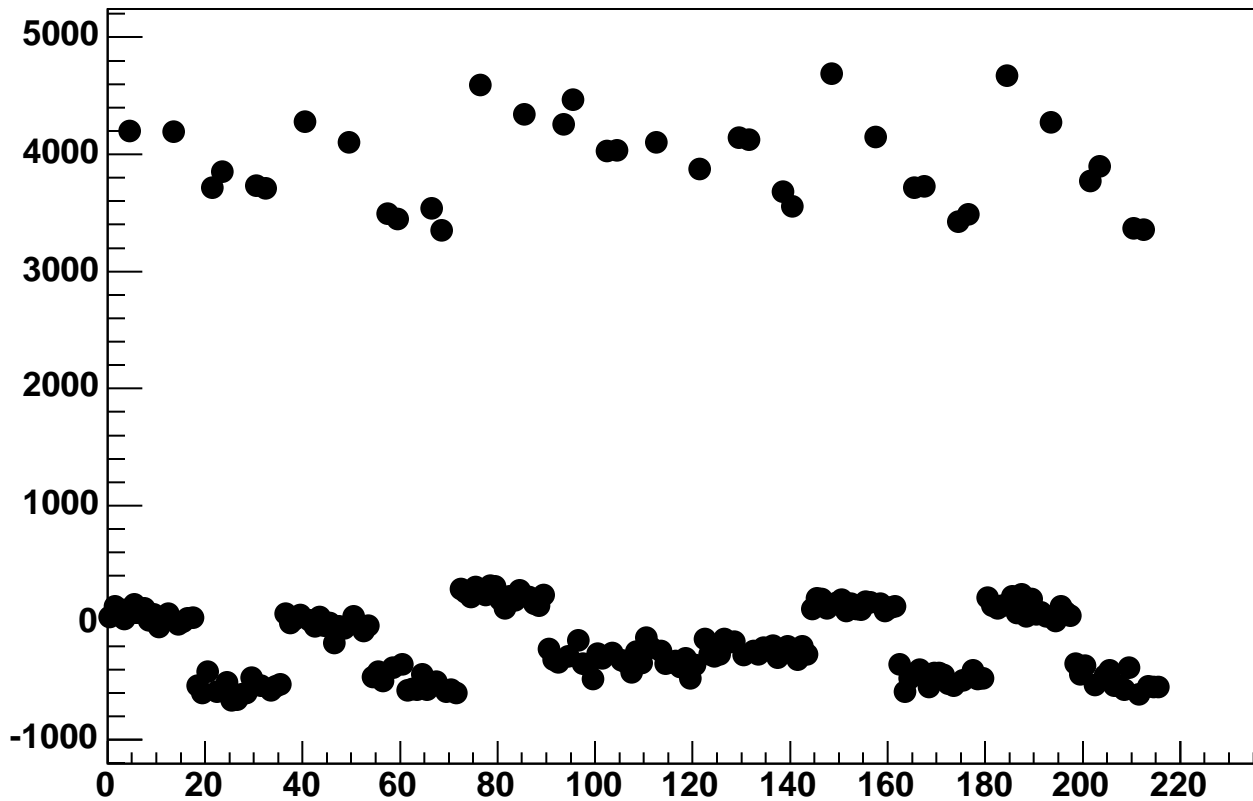
Enable 4, DAC=4000, Hold=5, ADC Mean vs 18*Chip+Chan



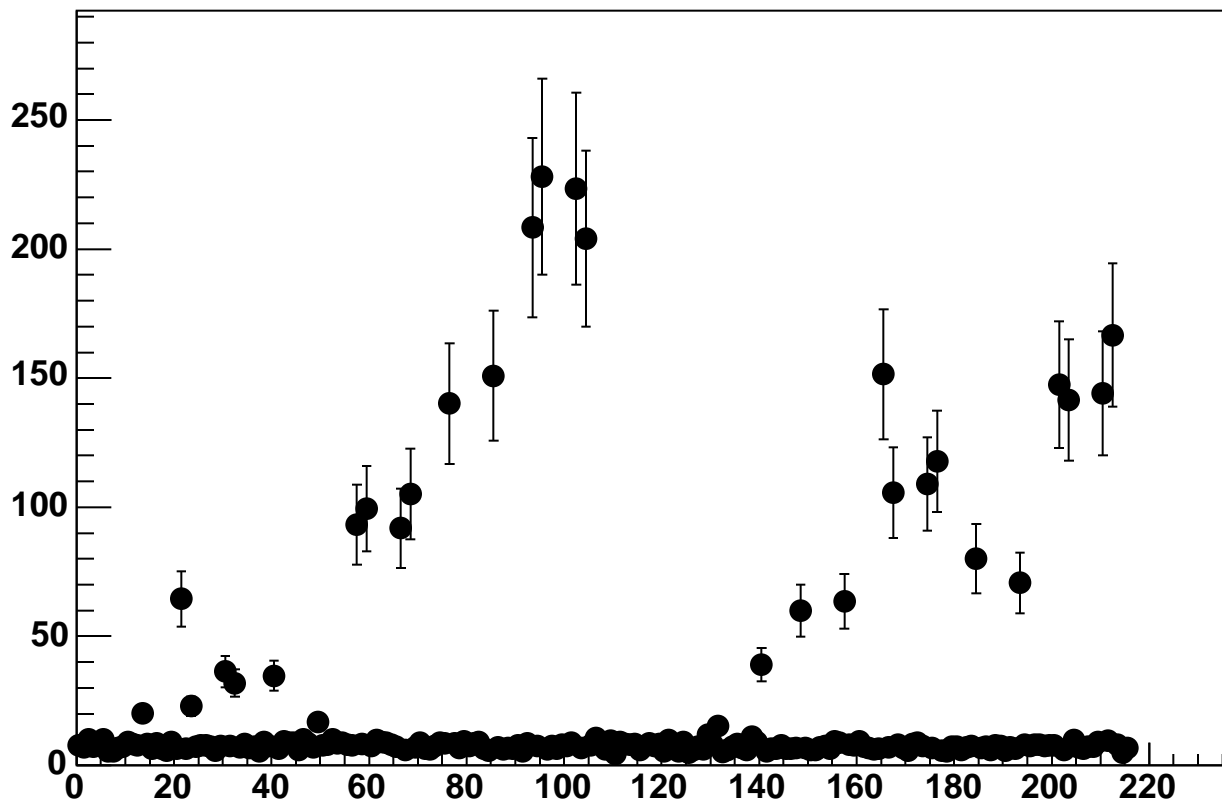
Enable 4, DAC=4000, Hold=5, ADC Noise vs 18*Chip+Chan



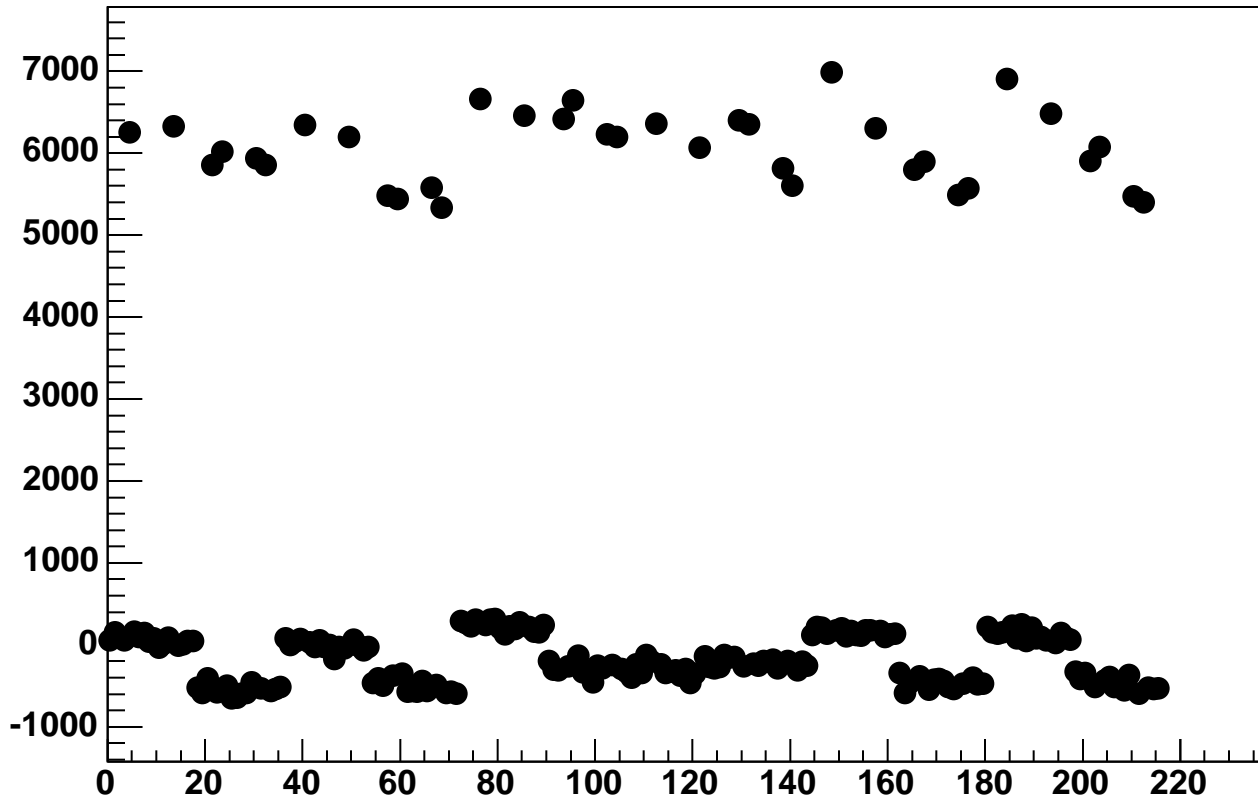
Enable 4, DAC=4000, Hold=10, ADC Mean vs 18*Chip+Chan



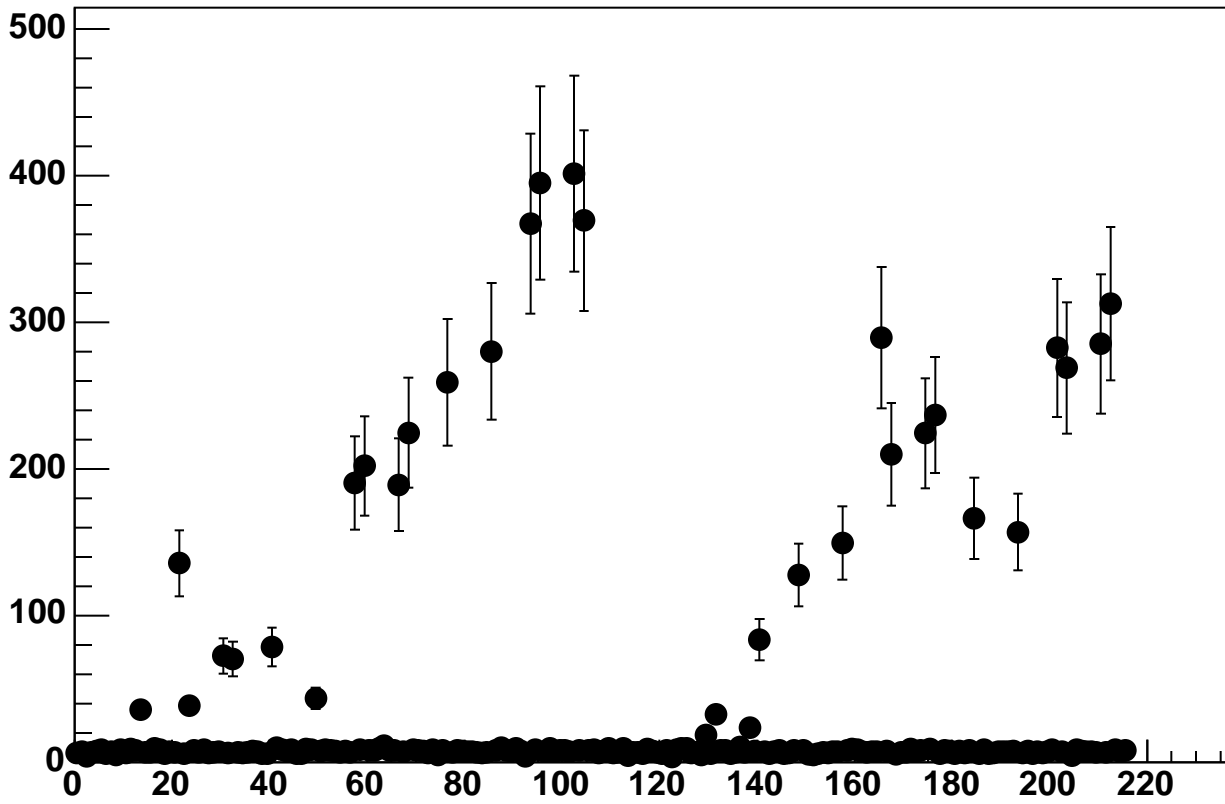
Enable 4, DAC=4000, Hold=10, ADC Noise vs 18*Chip+Chan



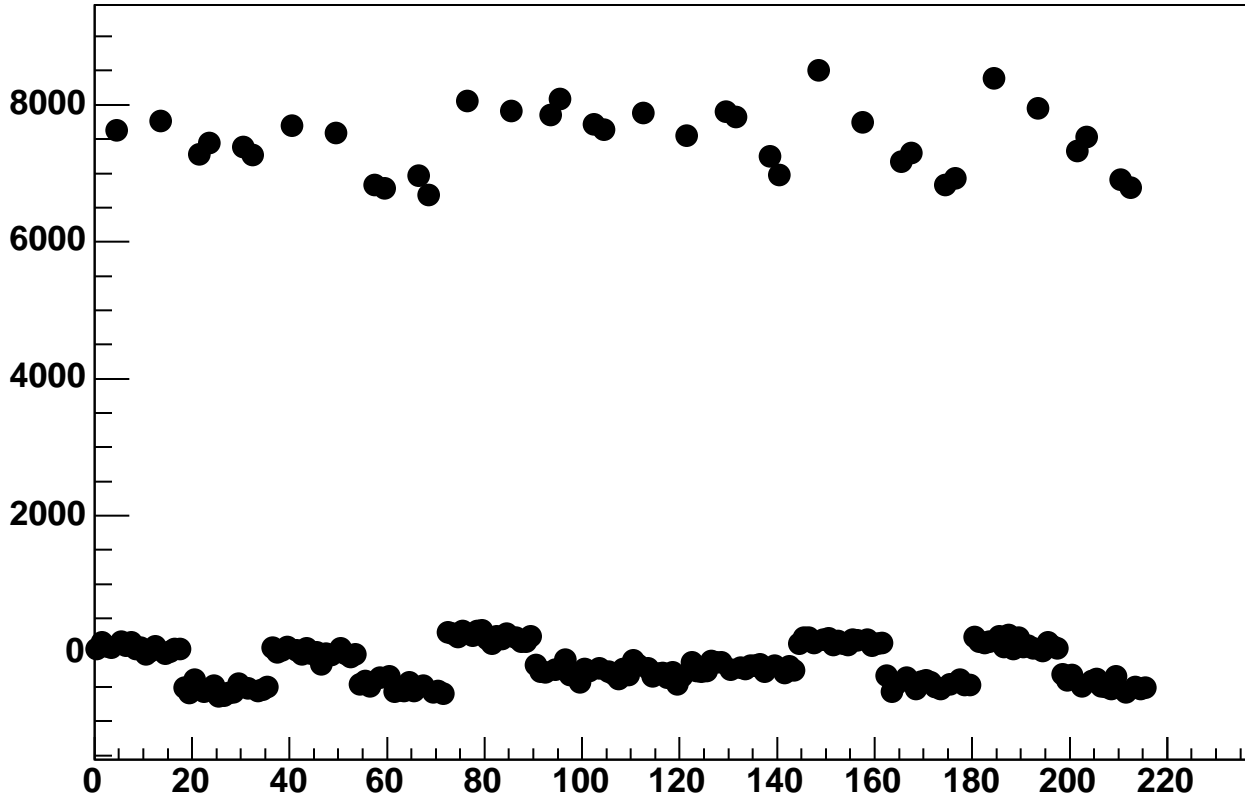
Enable 4, DAC=4000, Hold=15, ADC Mean vs 18*Chip+Chan



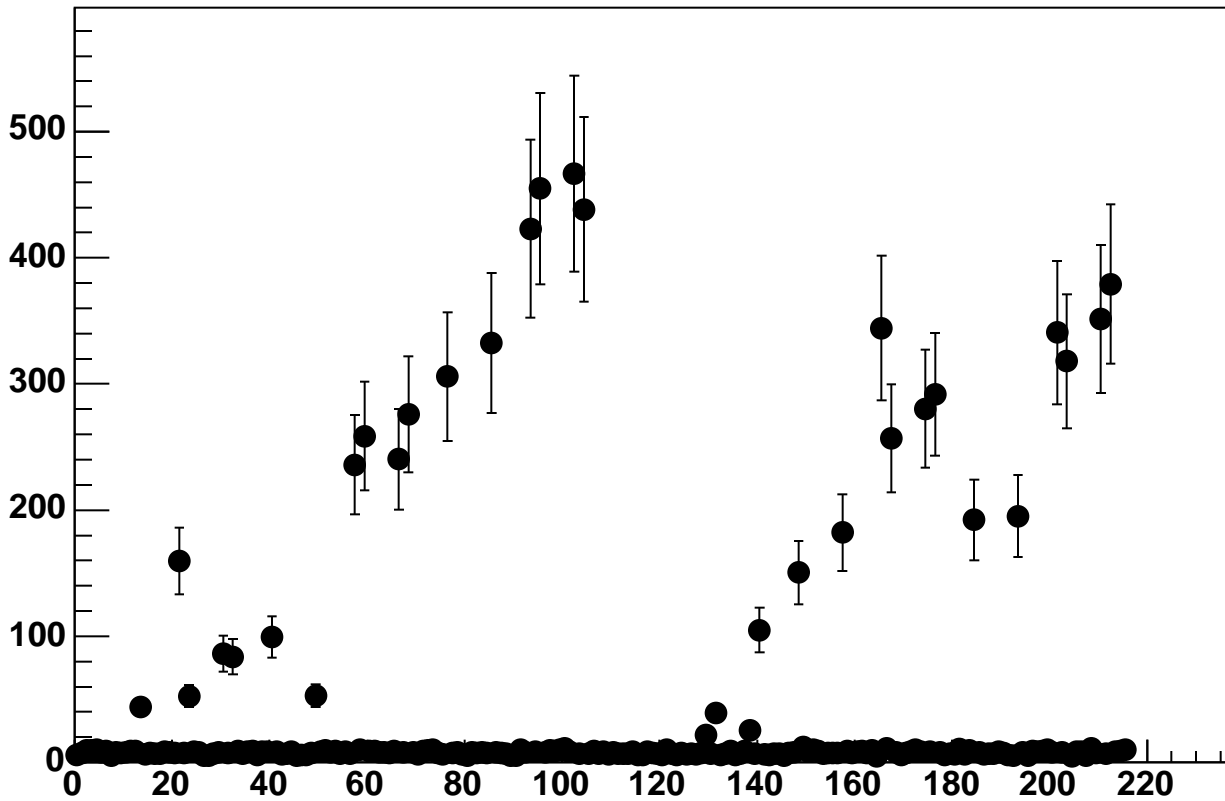
Enable 4, DAC=4000, Hold=15, ADC Noise vs 18*Chip+Chan



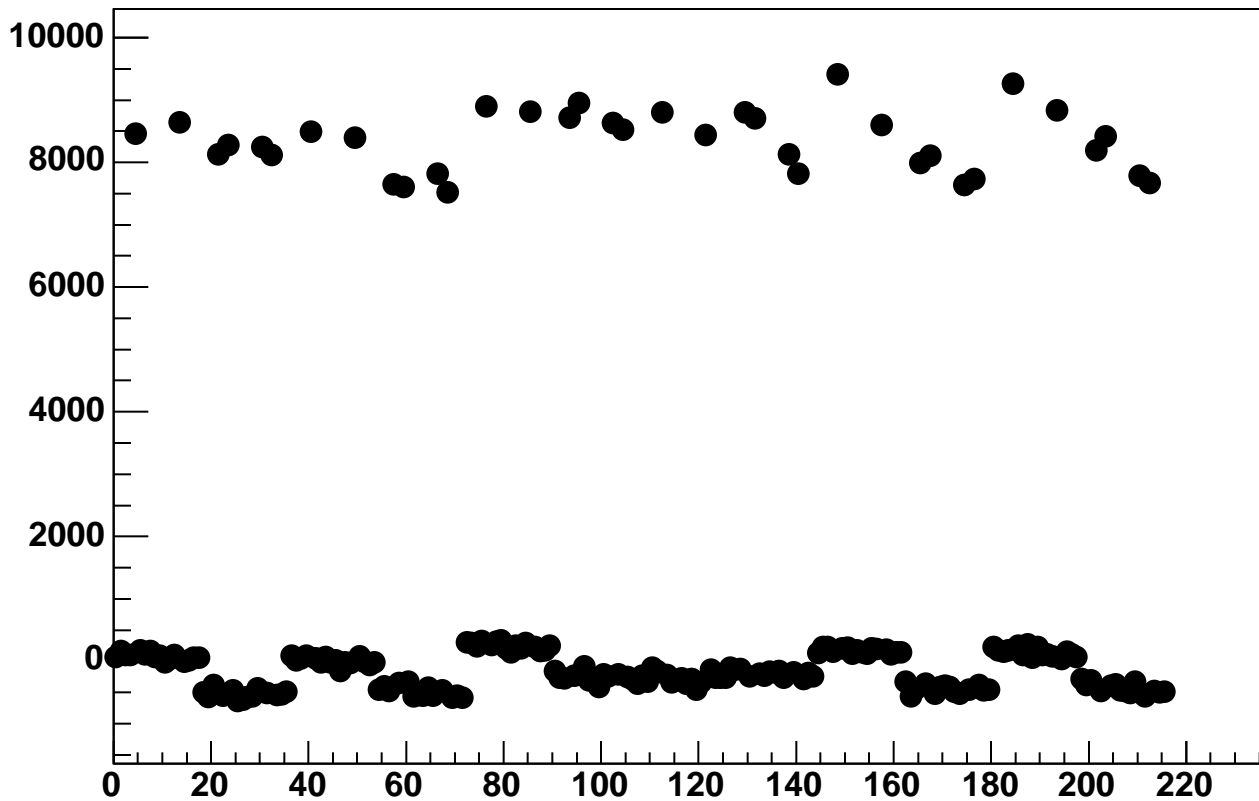
Enable 4, DAC=4000, Hold=20, ADC Mean vs 18*Chip+Chan



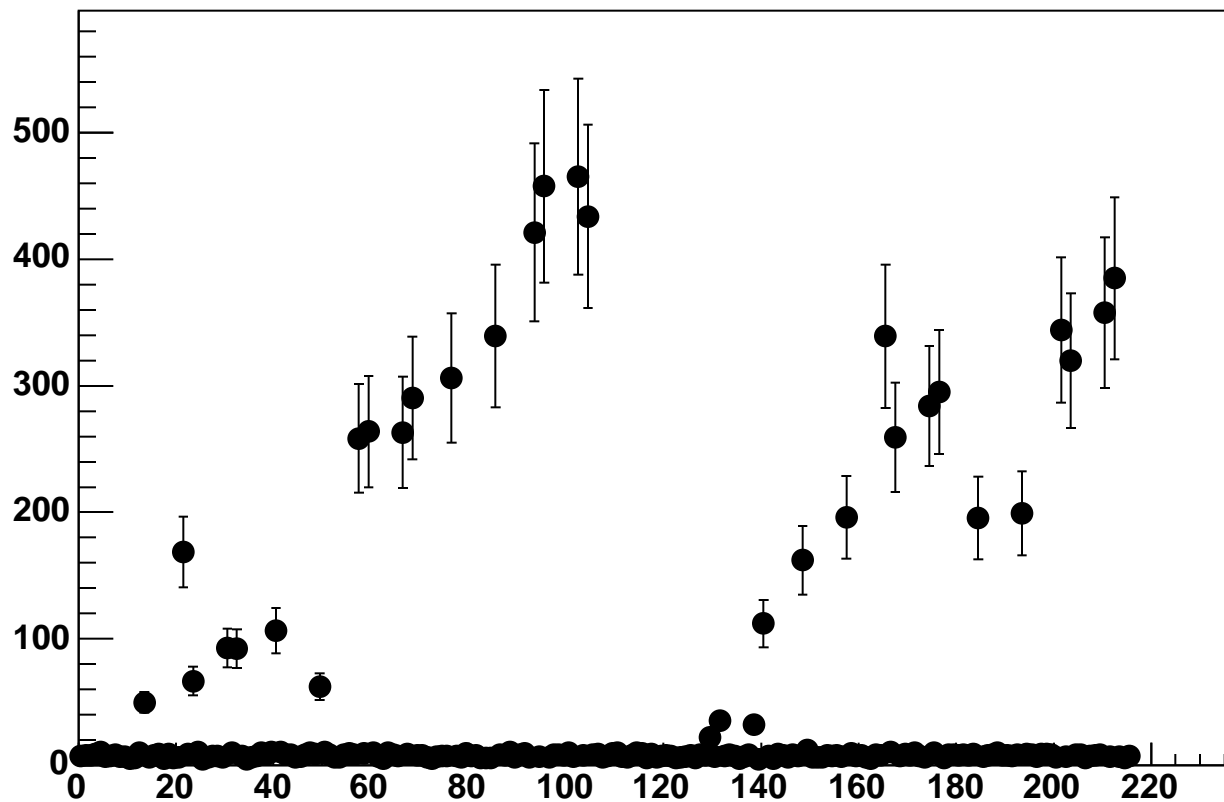
Enable 4, DAC=4000, Hold=20, ADC Noise vs 18*Chip+Chan



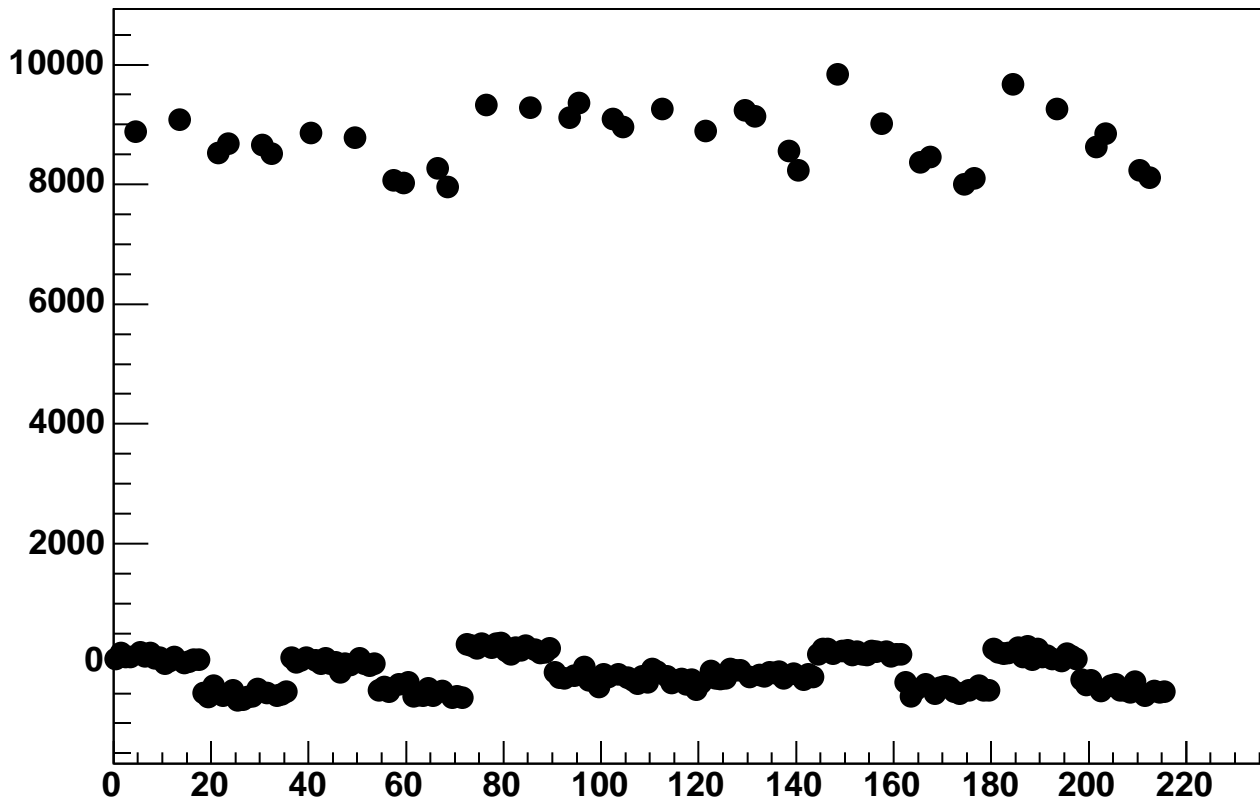
Enable 4, DAC=4000, Hold=25, ADC Mean vs 18*Chip+Chan



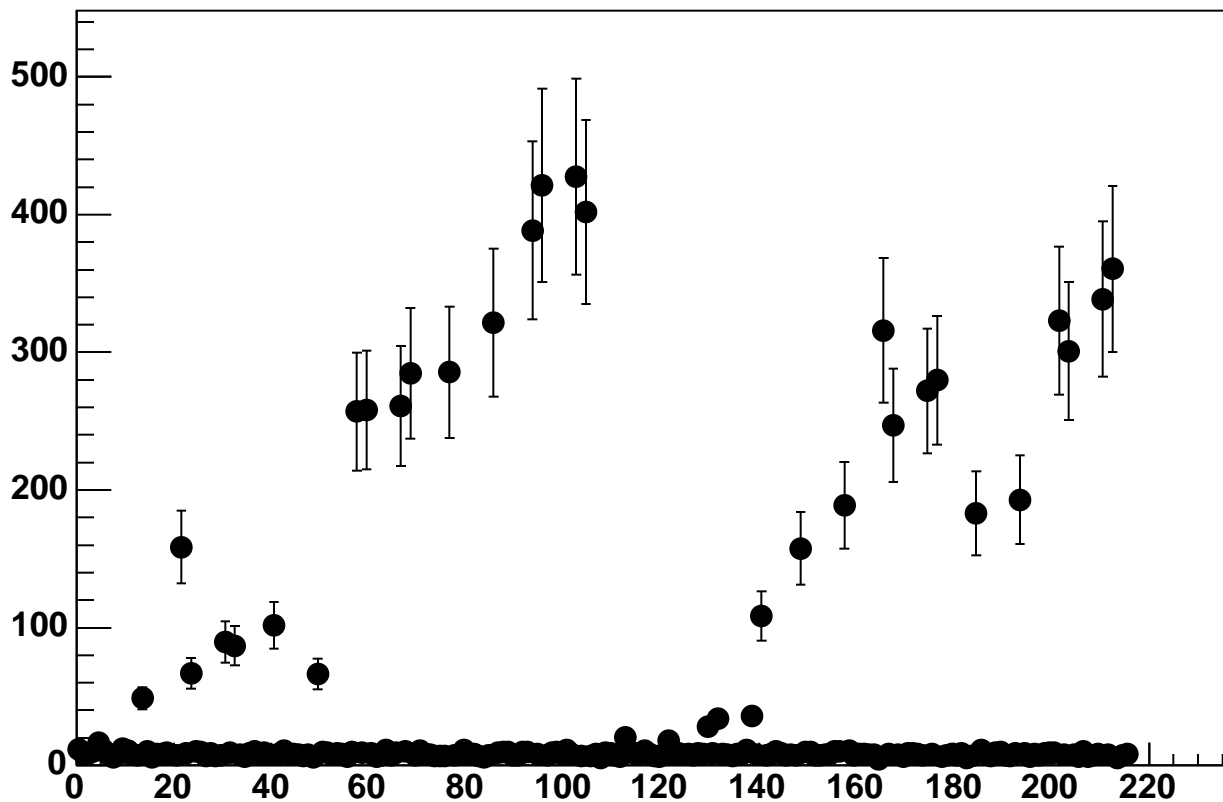
Enable 4, DAC=4000, Hold=25, ADC Noise vs 18*Chip+Chan



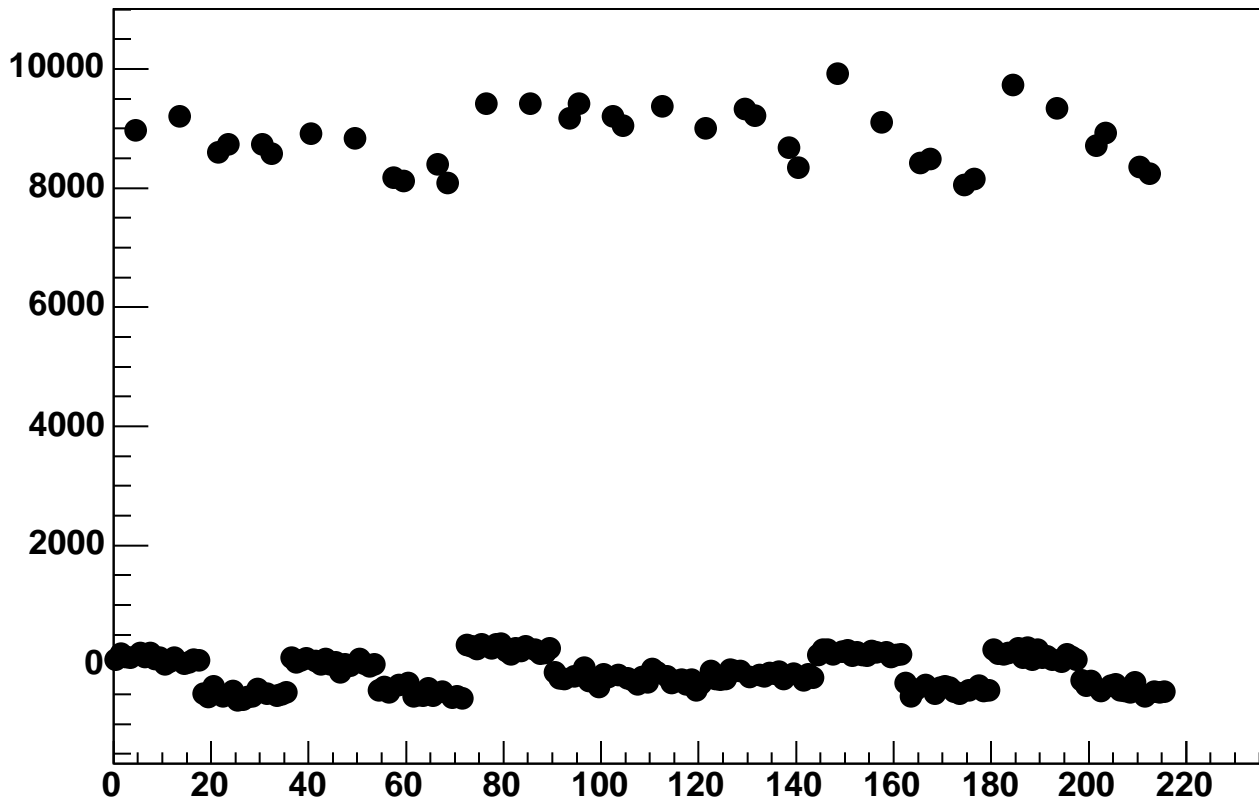
Enable 4, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



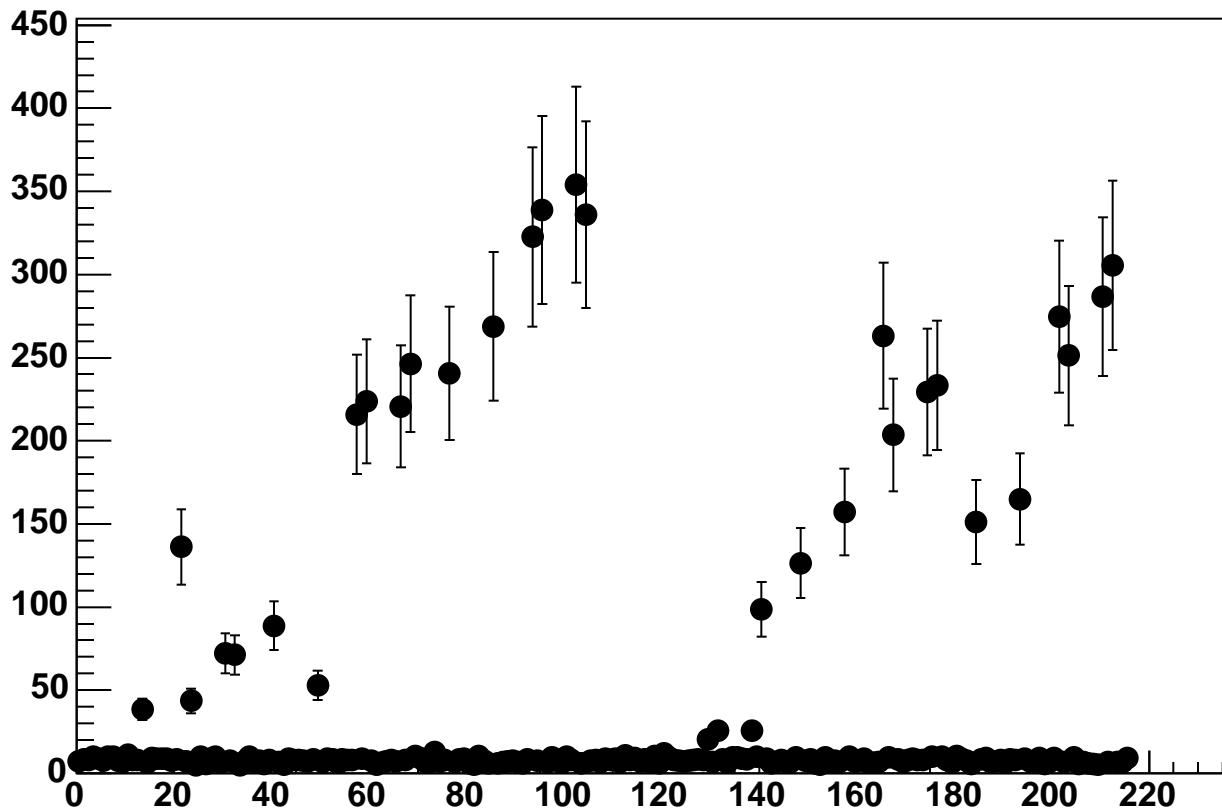
Enable 4, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



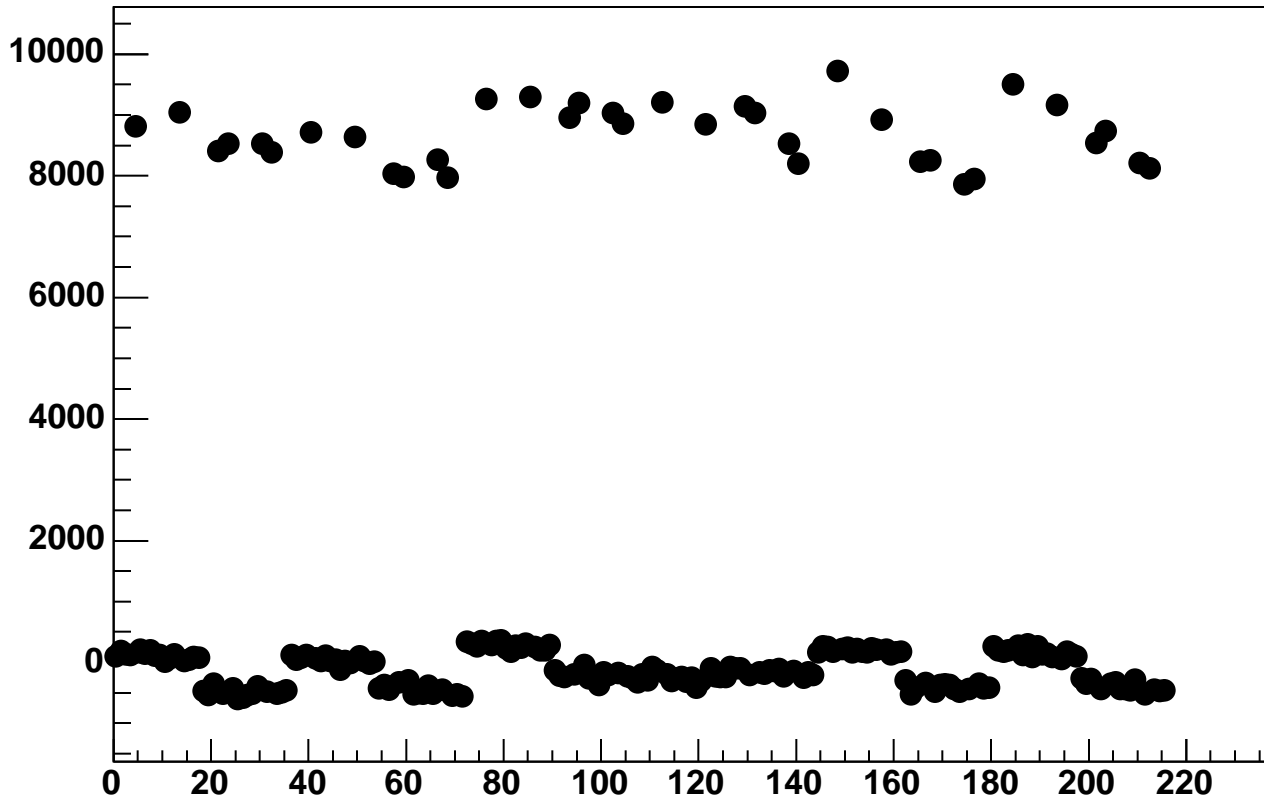
Enable 4, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



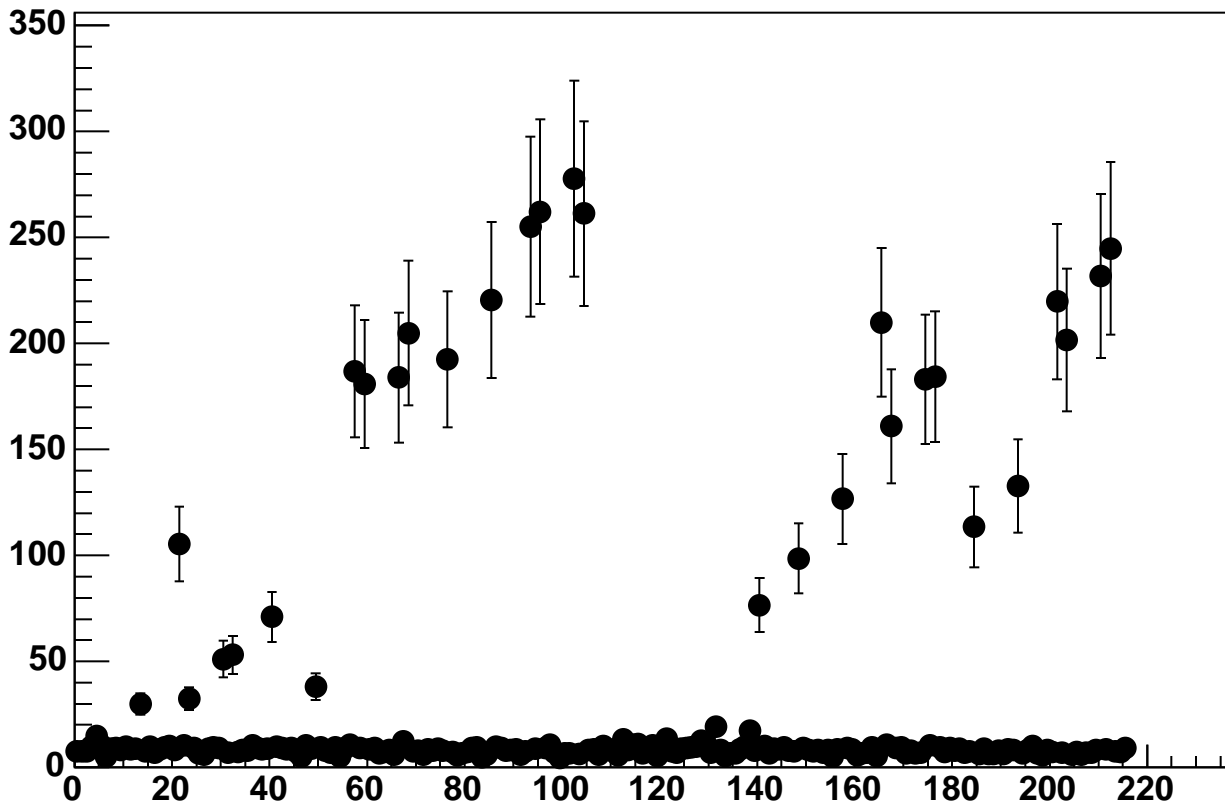
Enable 4, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



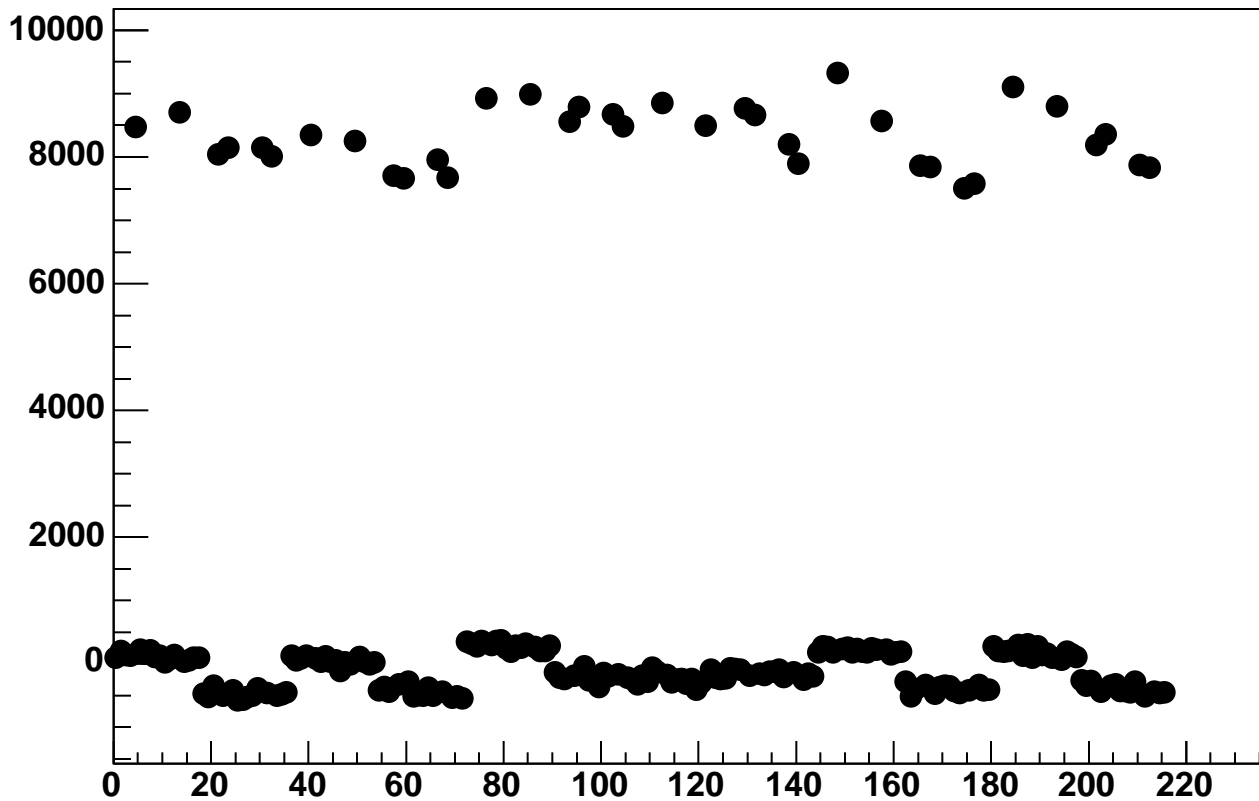
Enable 4, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



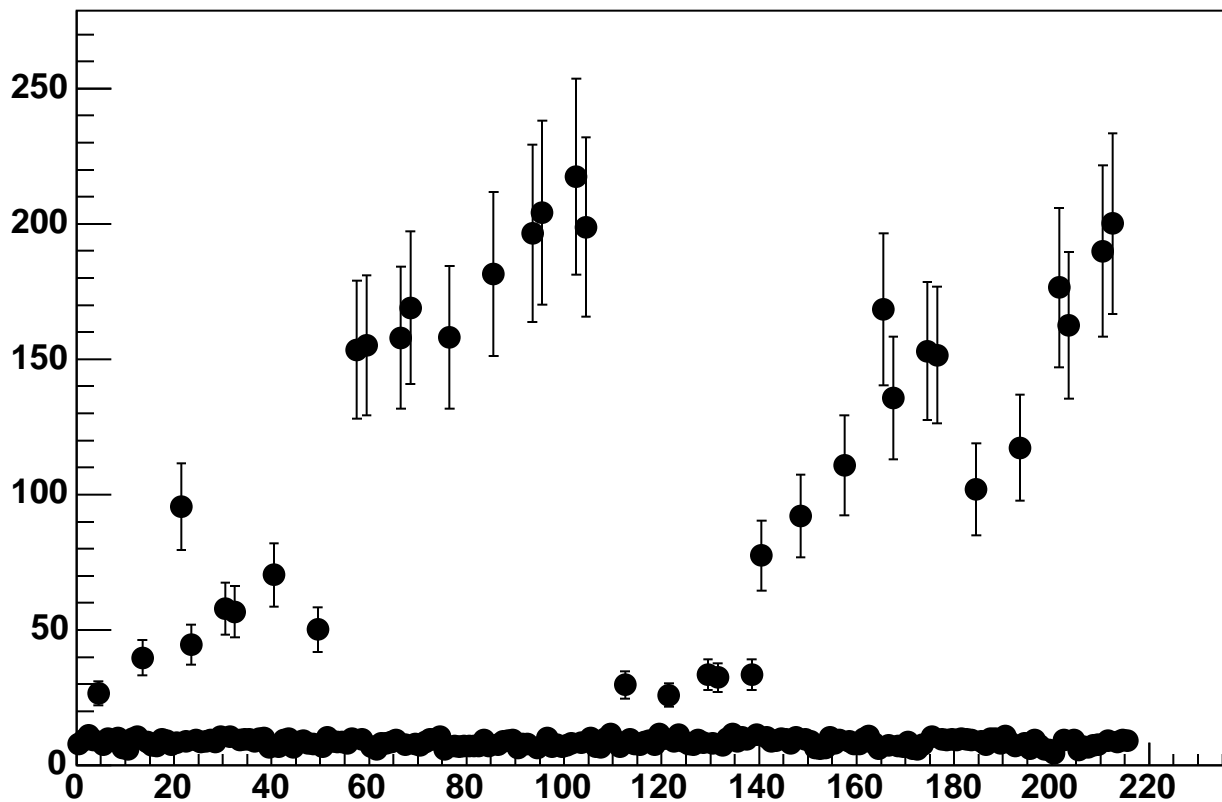
Enable 4, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



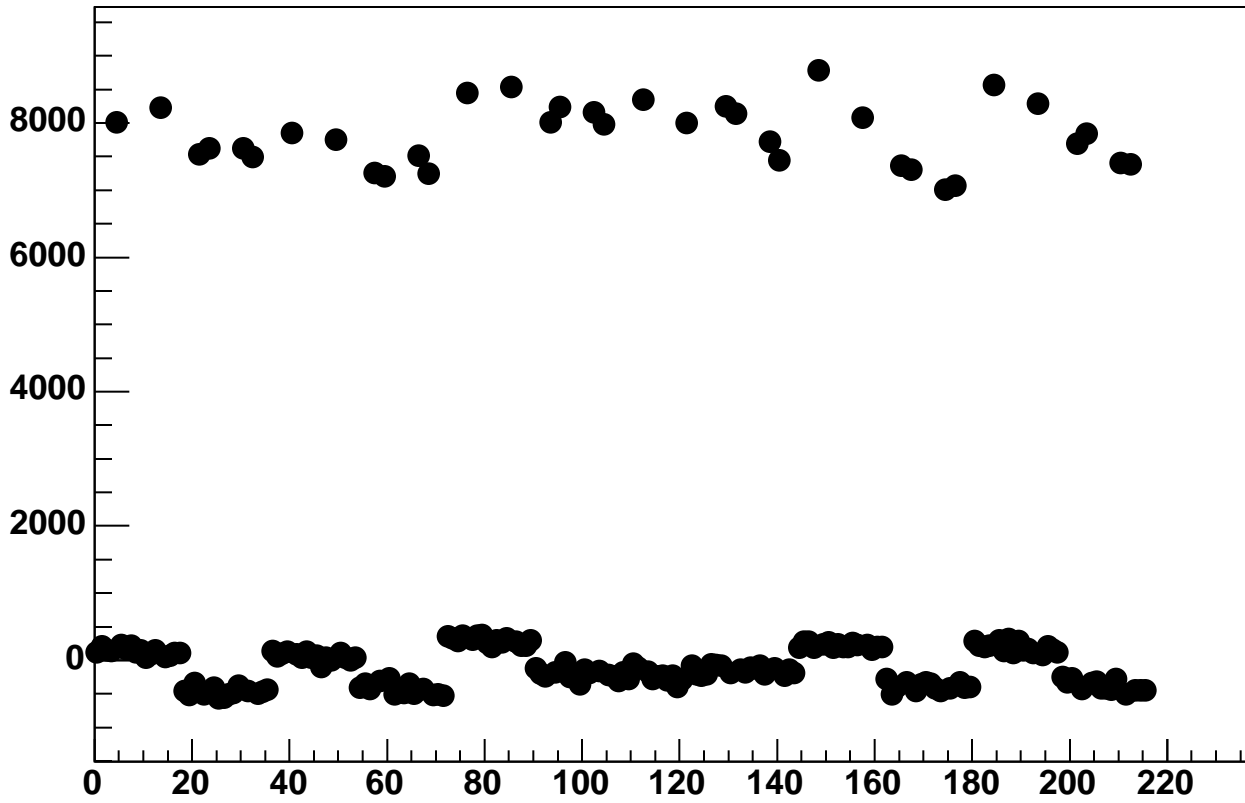
Enable 4, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



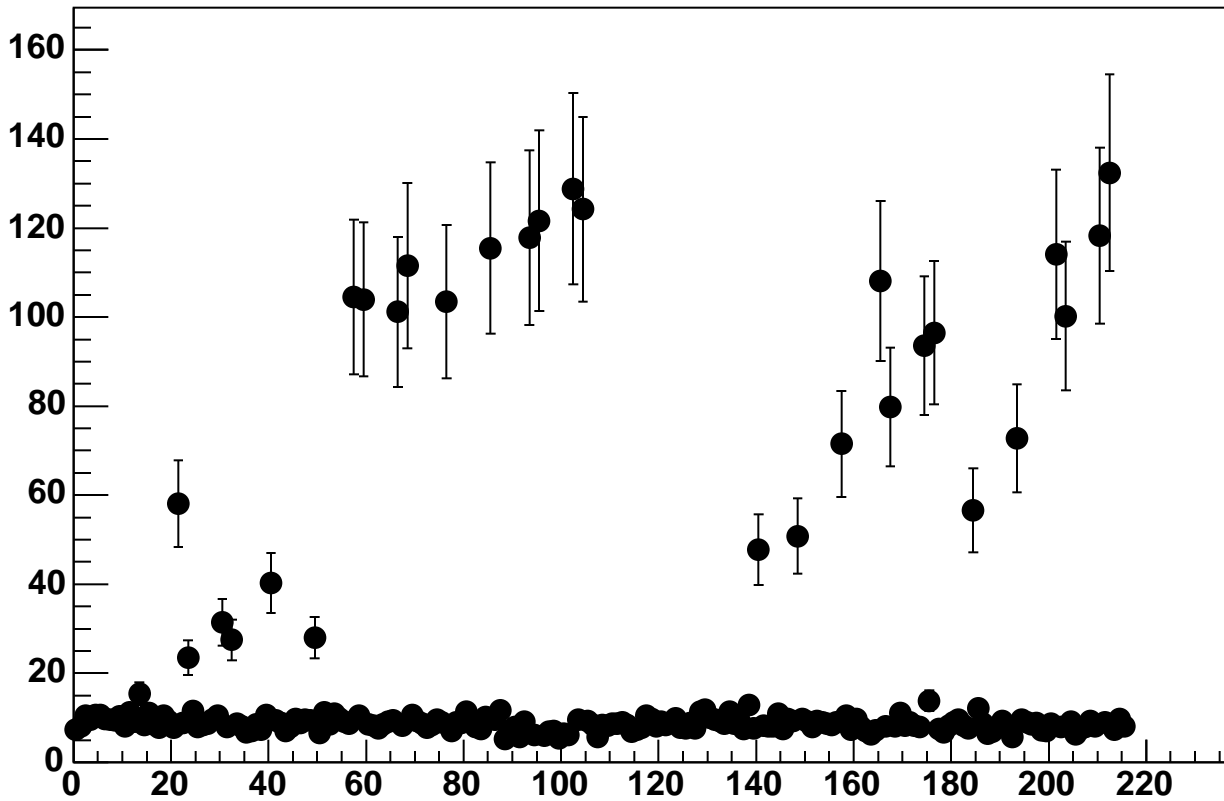
Enable 4, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



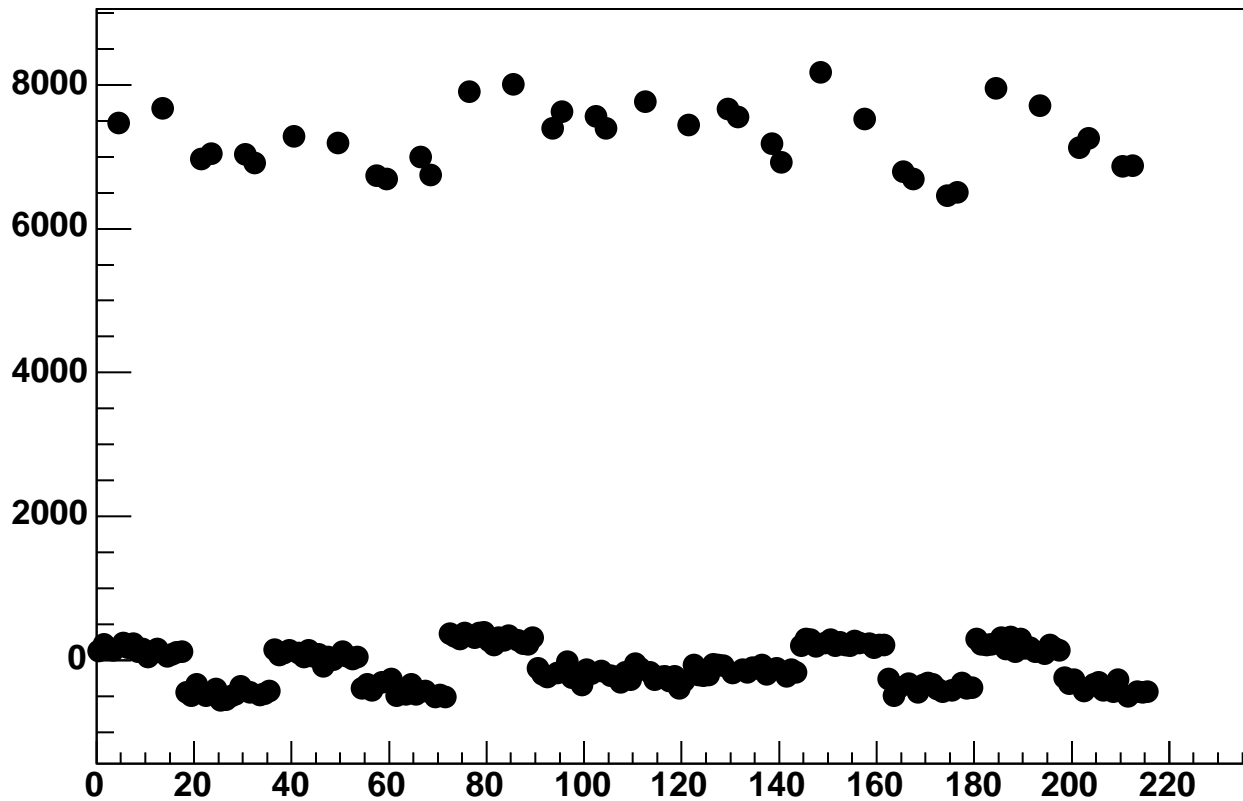
Enable 4, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



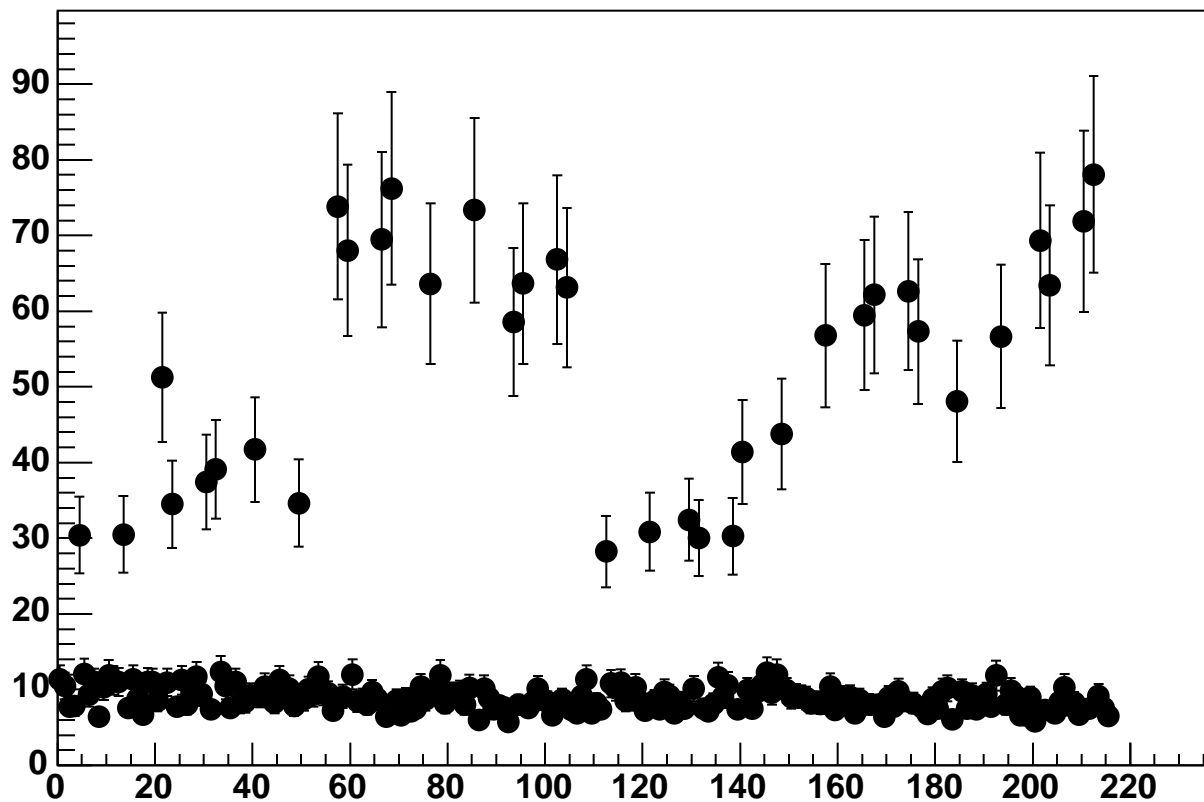
Enable 4, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



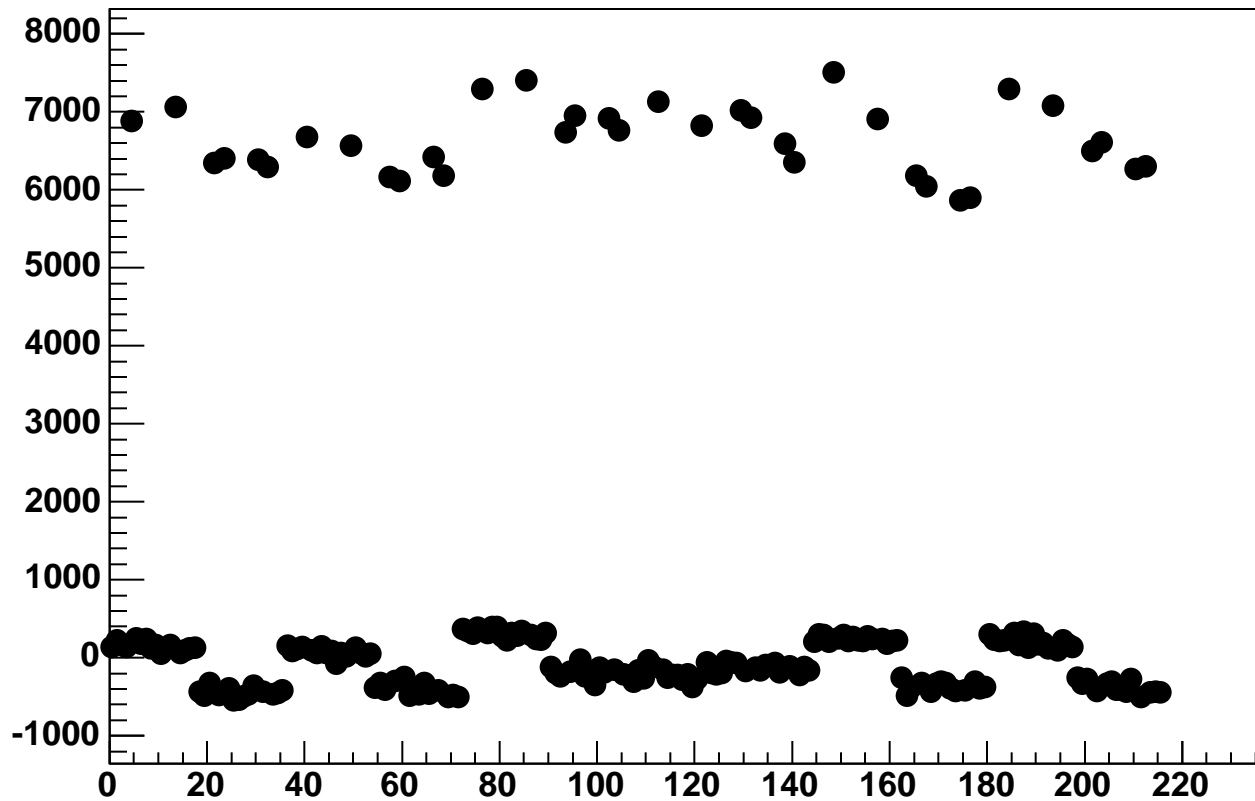
Enable 4, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



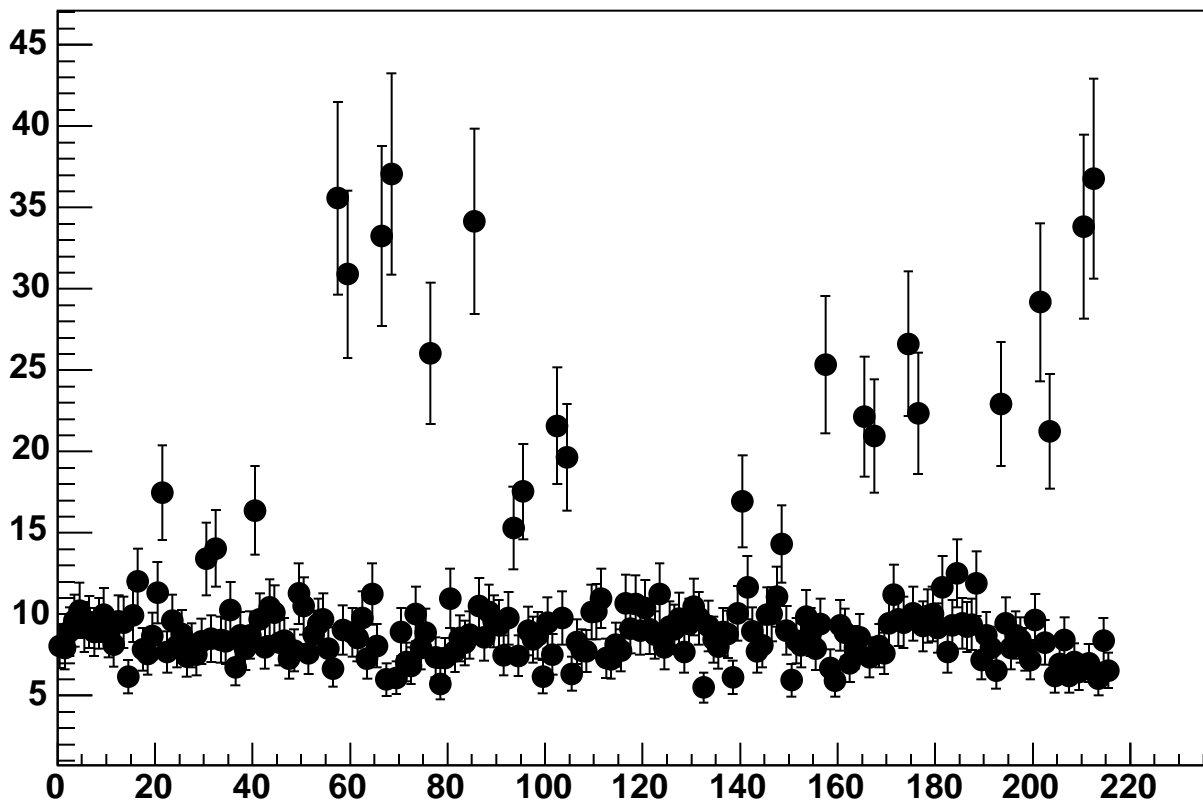
Enable 4, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



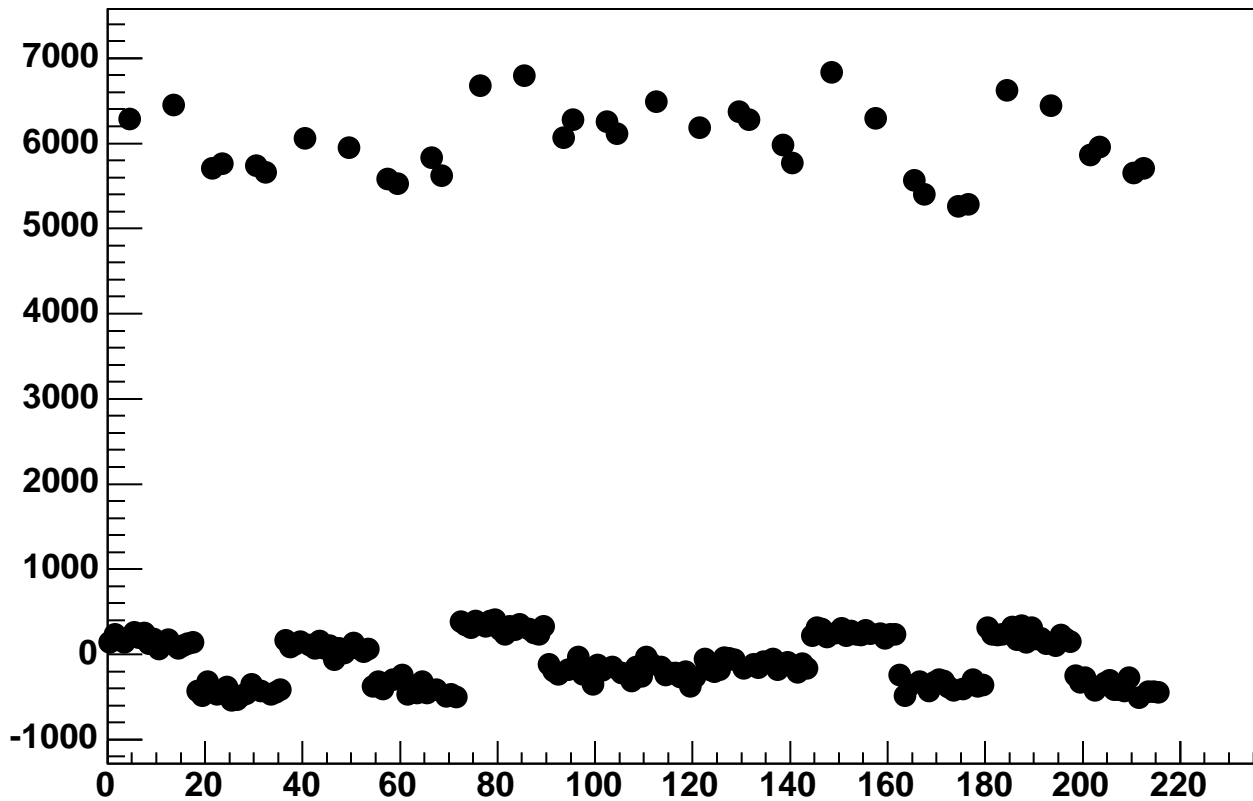
Enable 4, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



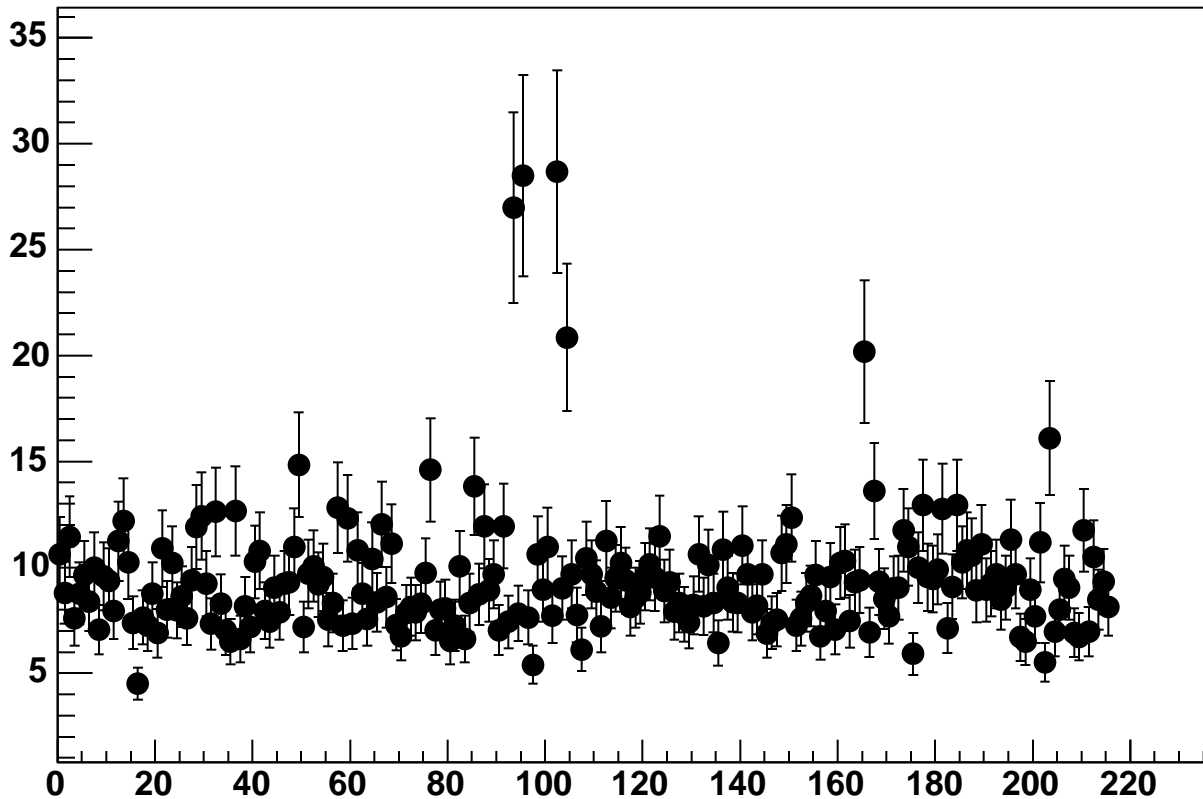
Enable 4, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



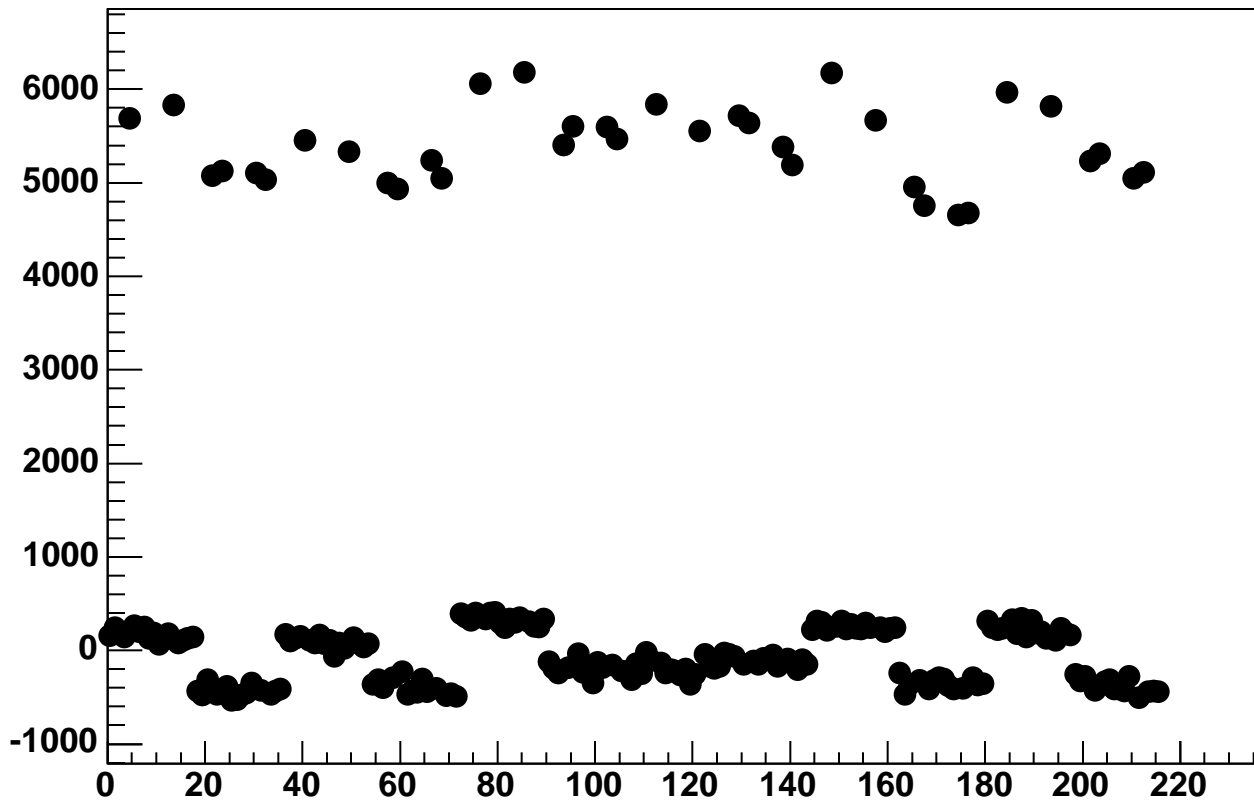
Enable 4, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



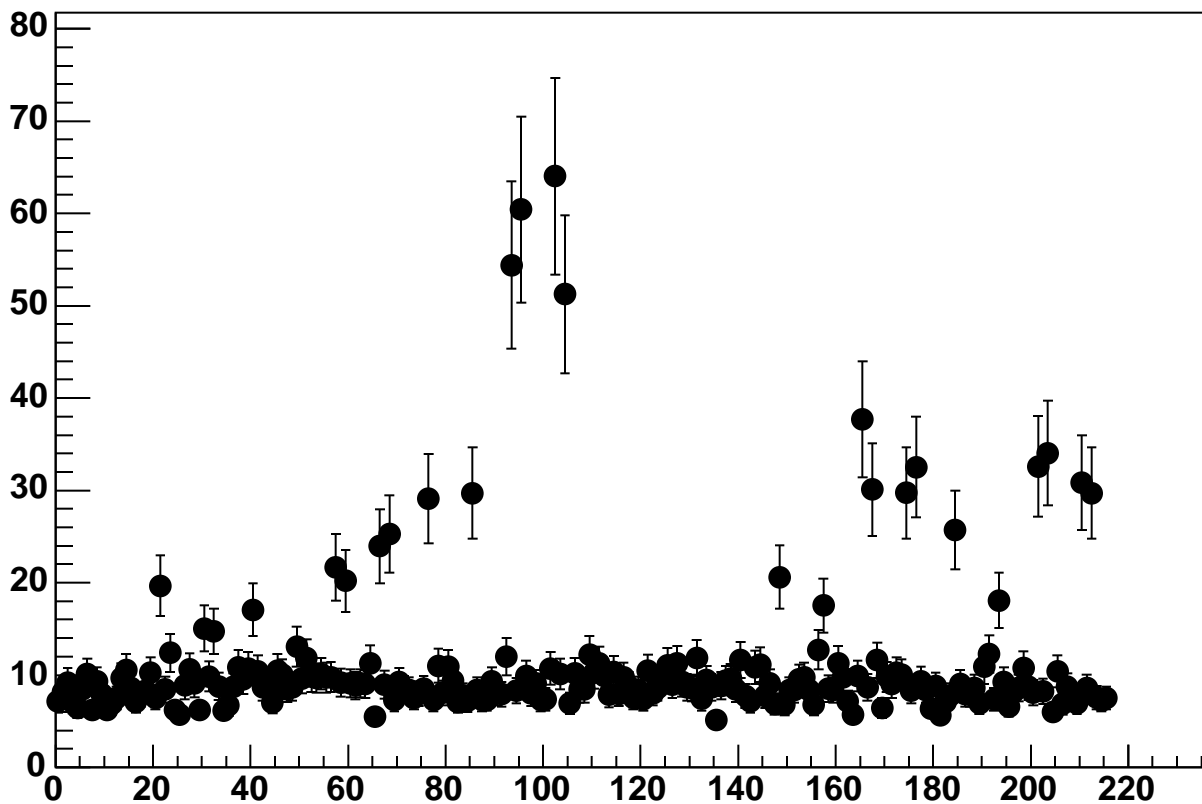
Enable 4, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



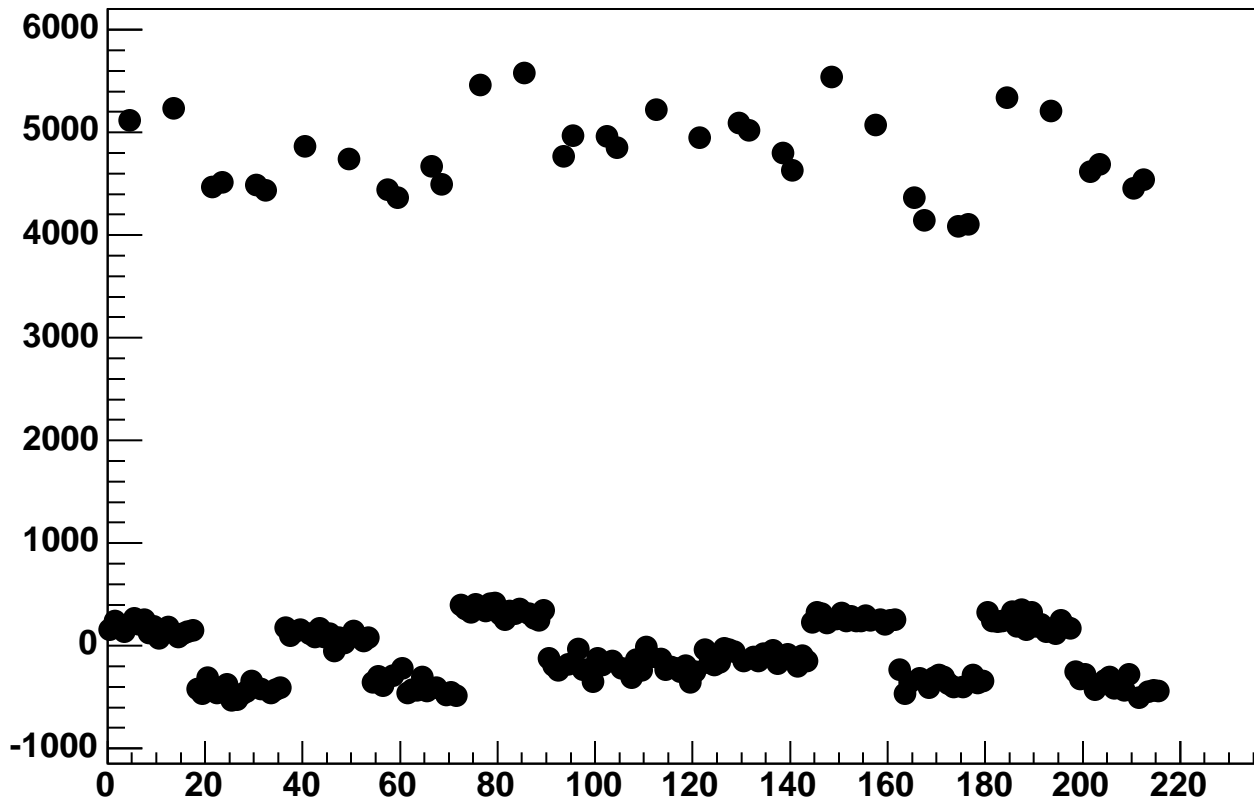
Enable 4, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



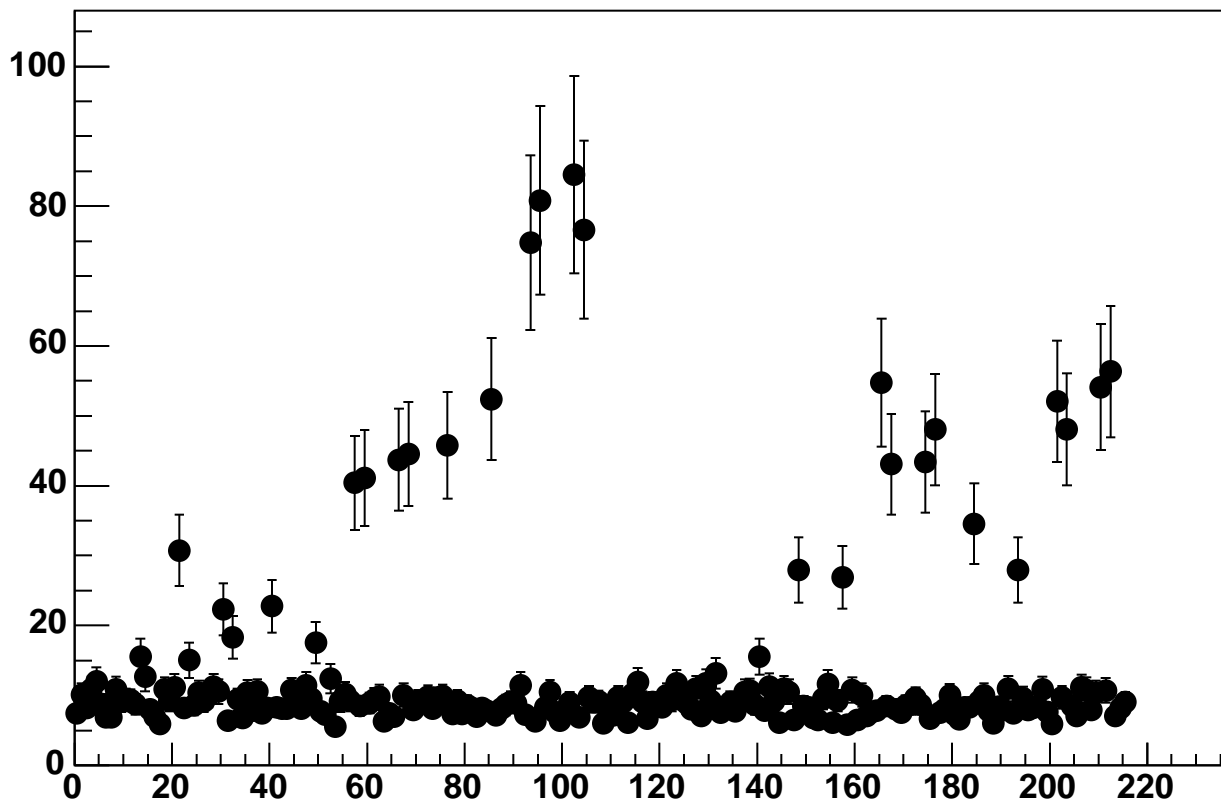
Enable 4, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



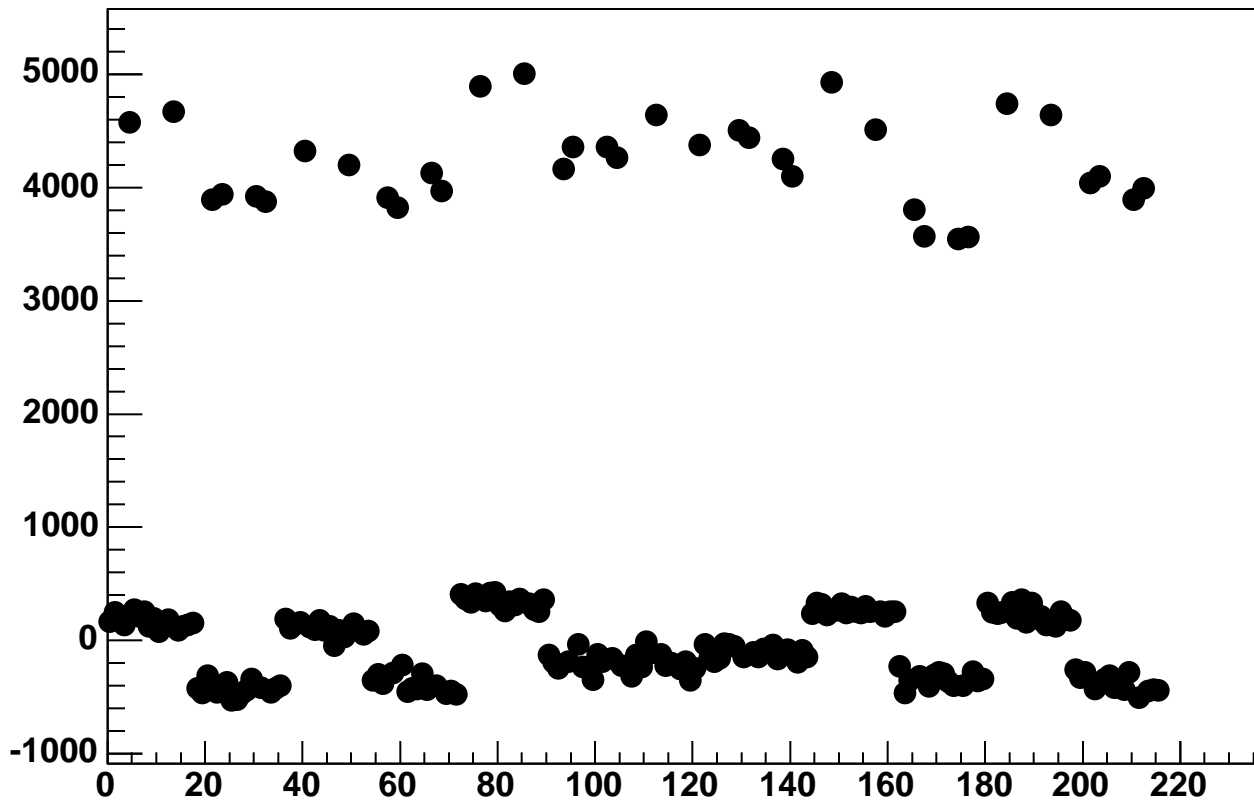
Enable 4, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



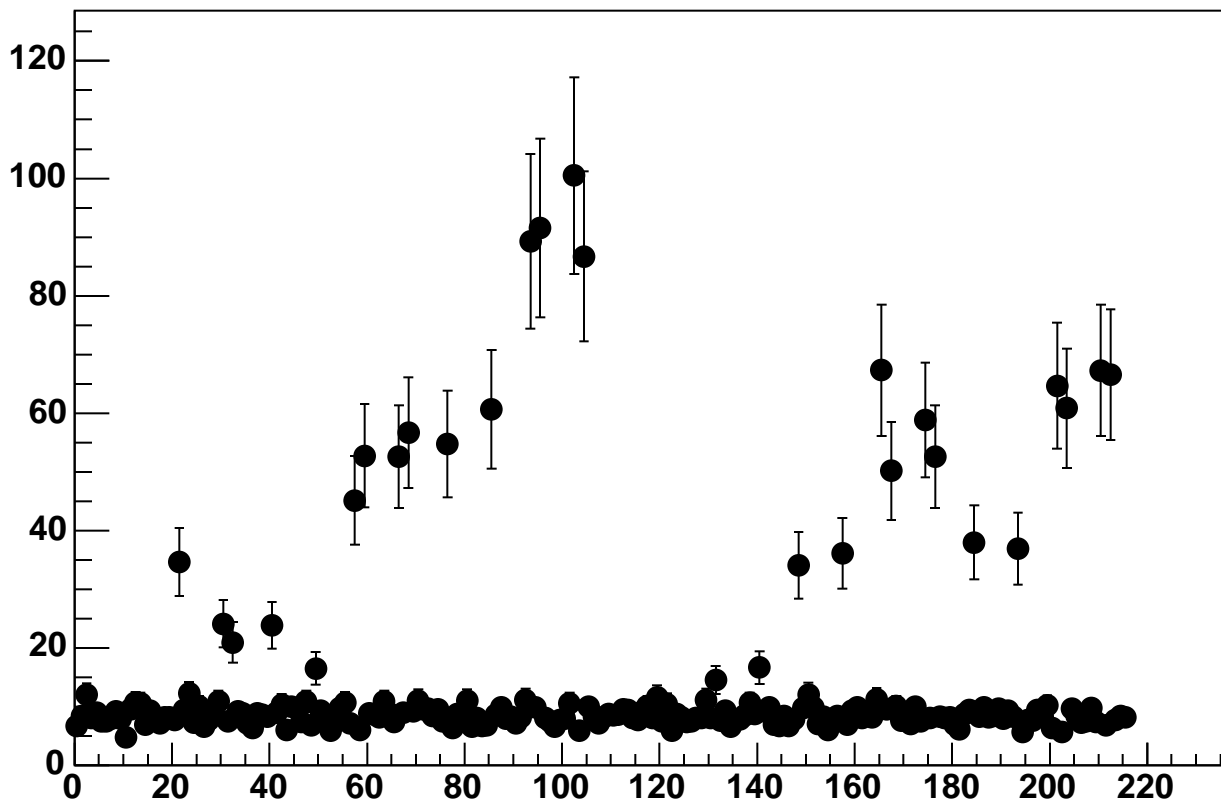
Enable 4, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



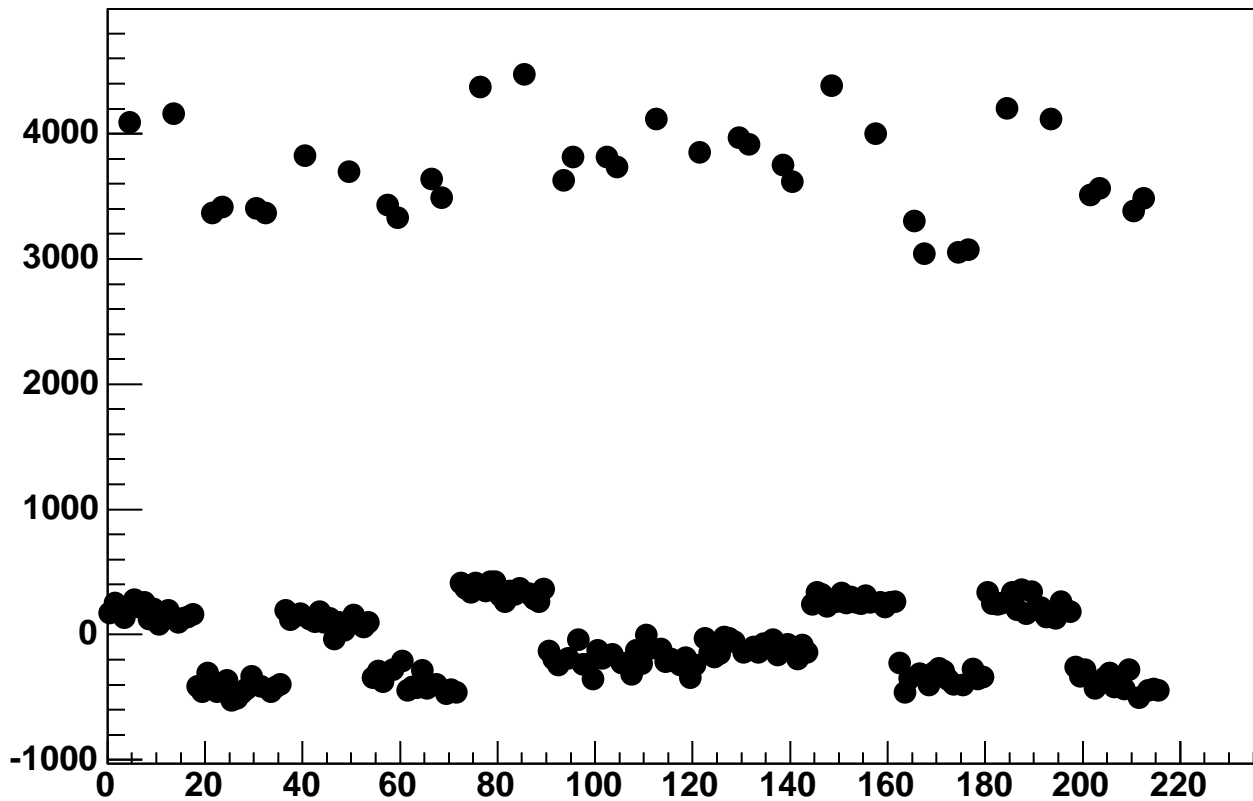
Enable 4, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



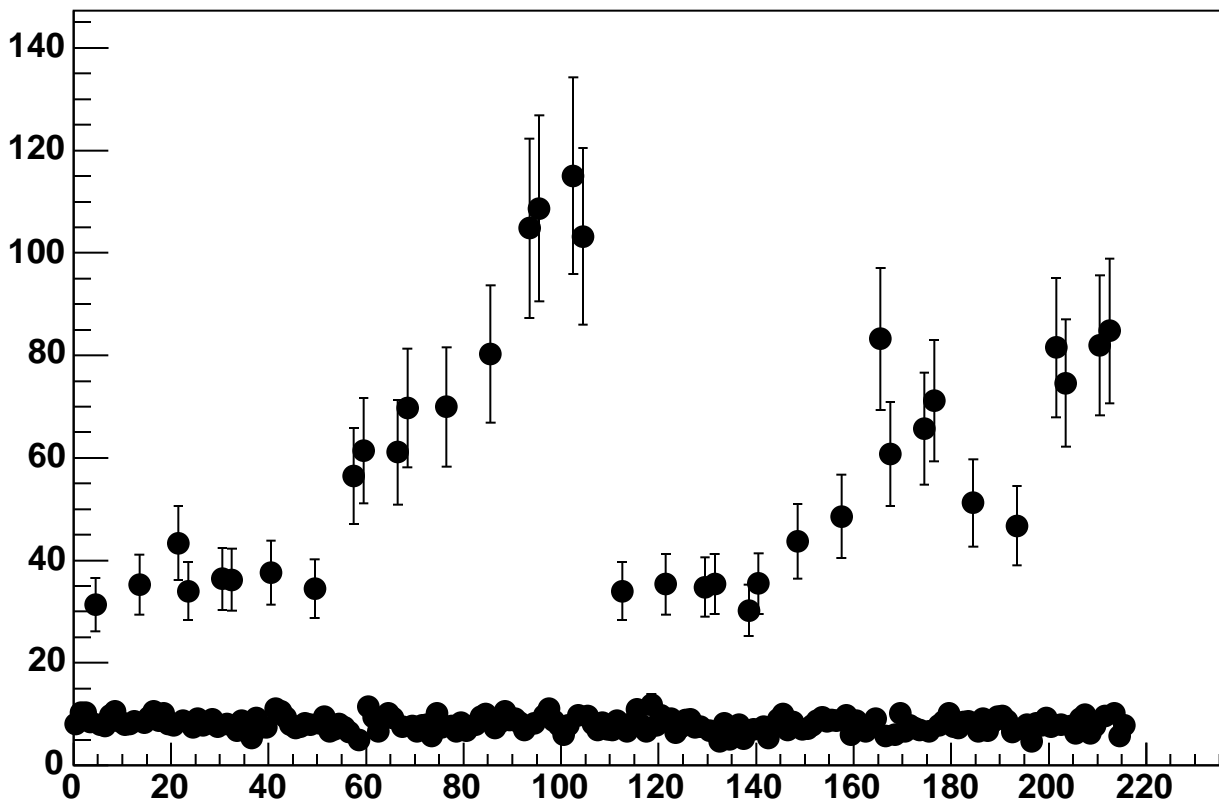
Enable 4, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



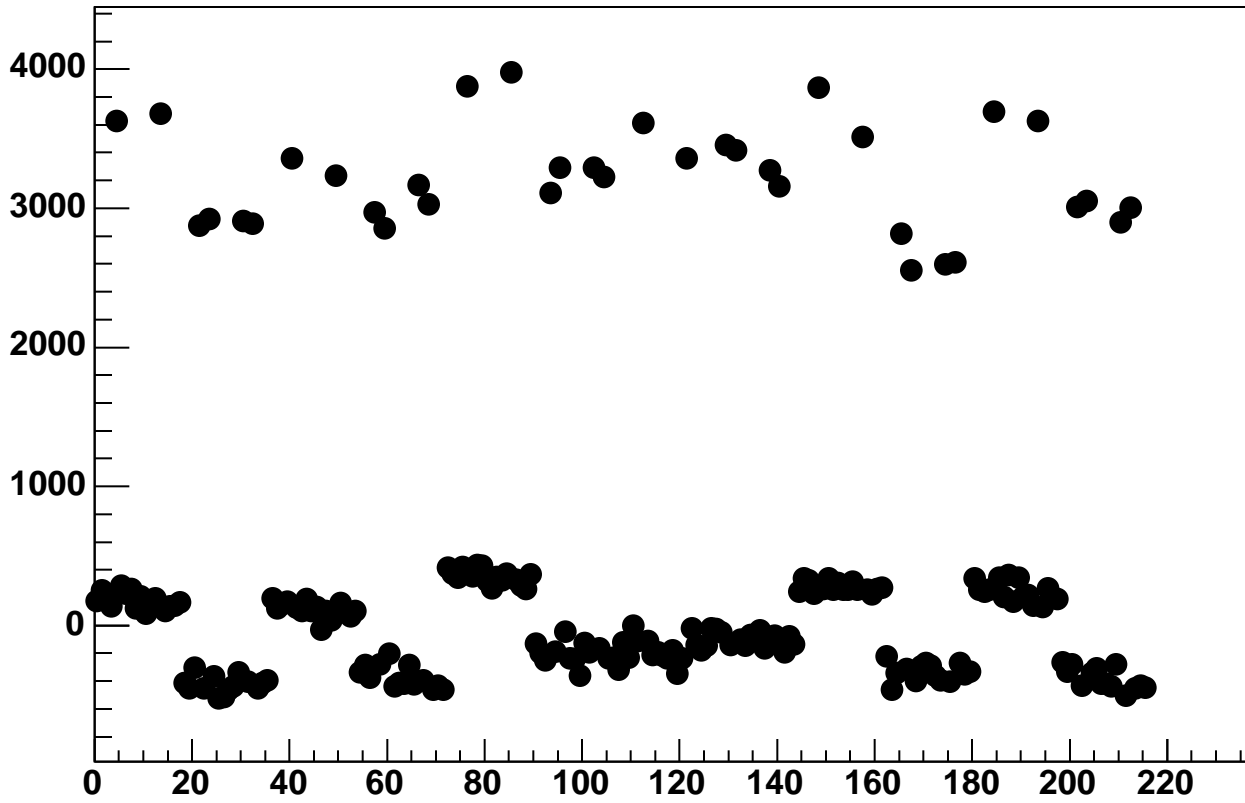
Enable 4, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



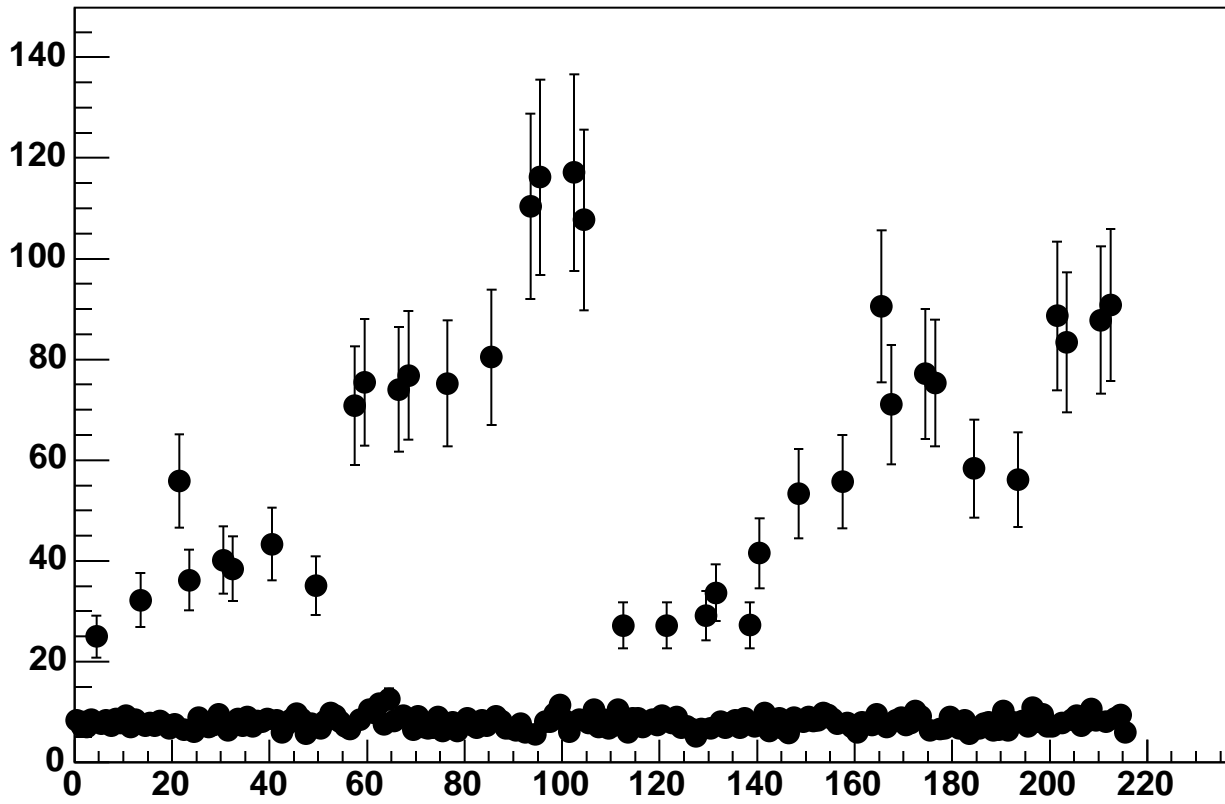
Enable 4, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



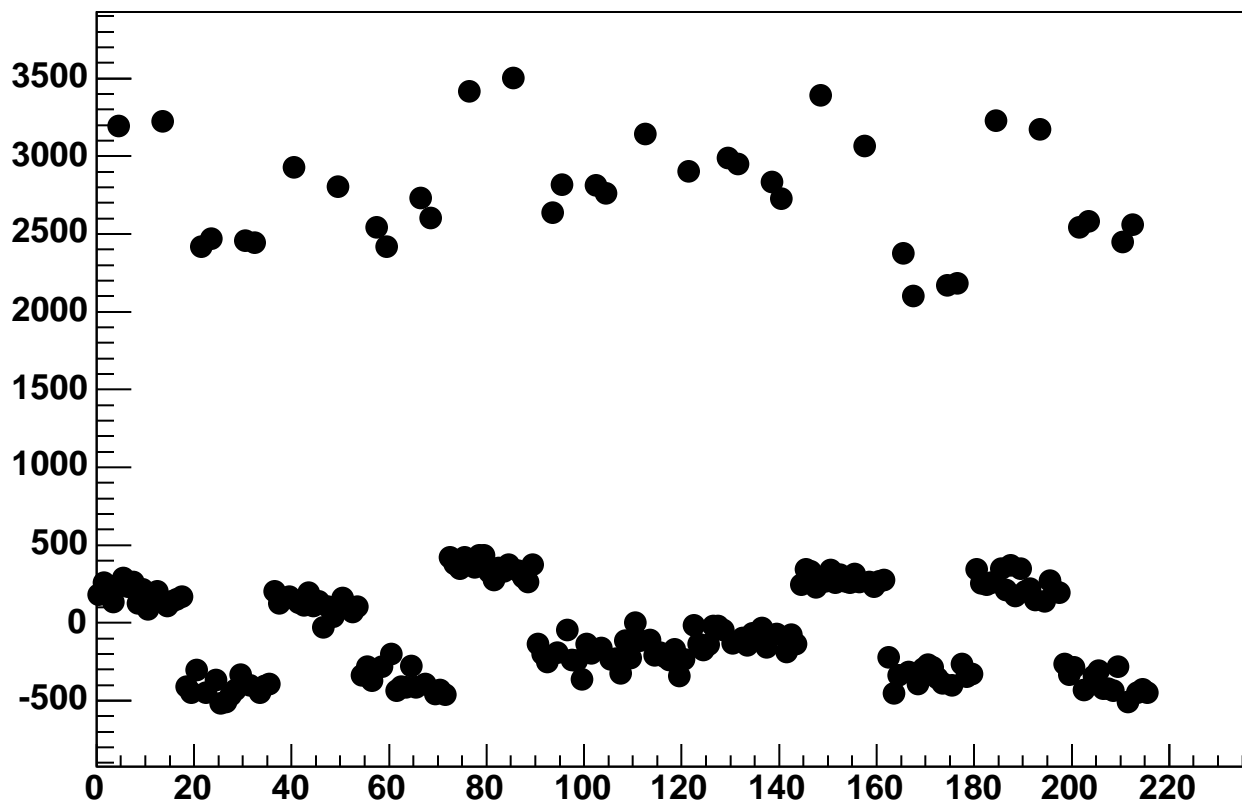
Enable 4, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



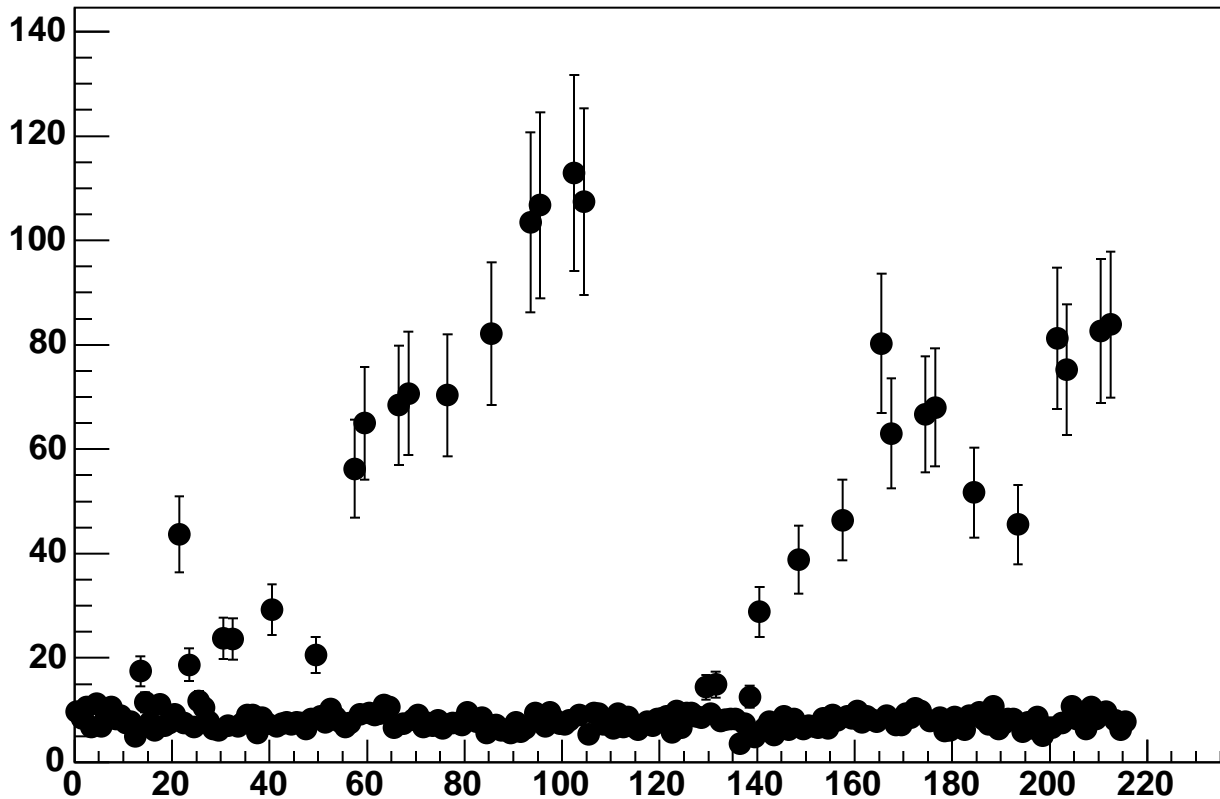
Enable 4, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



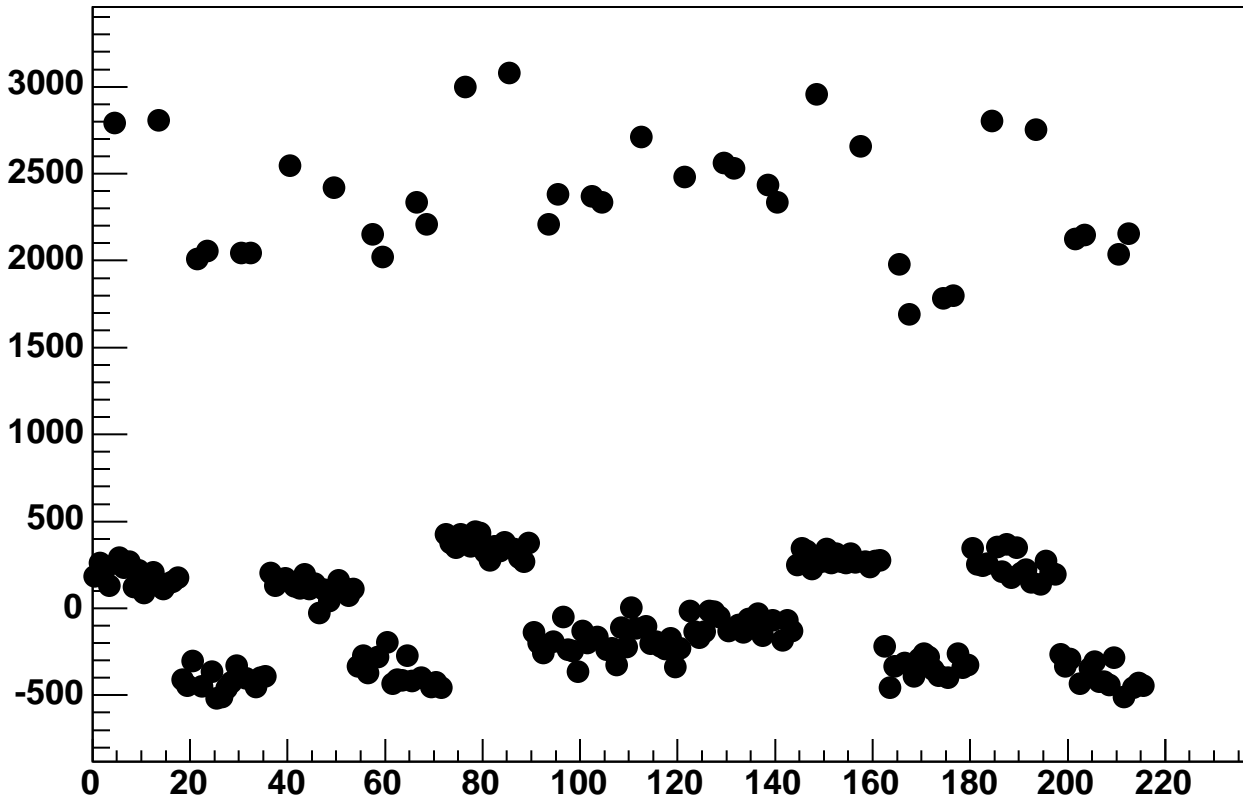
Enable 4, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



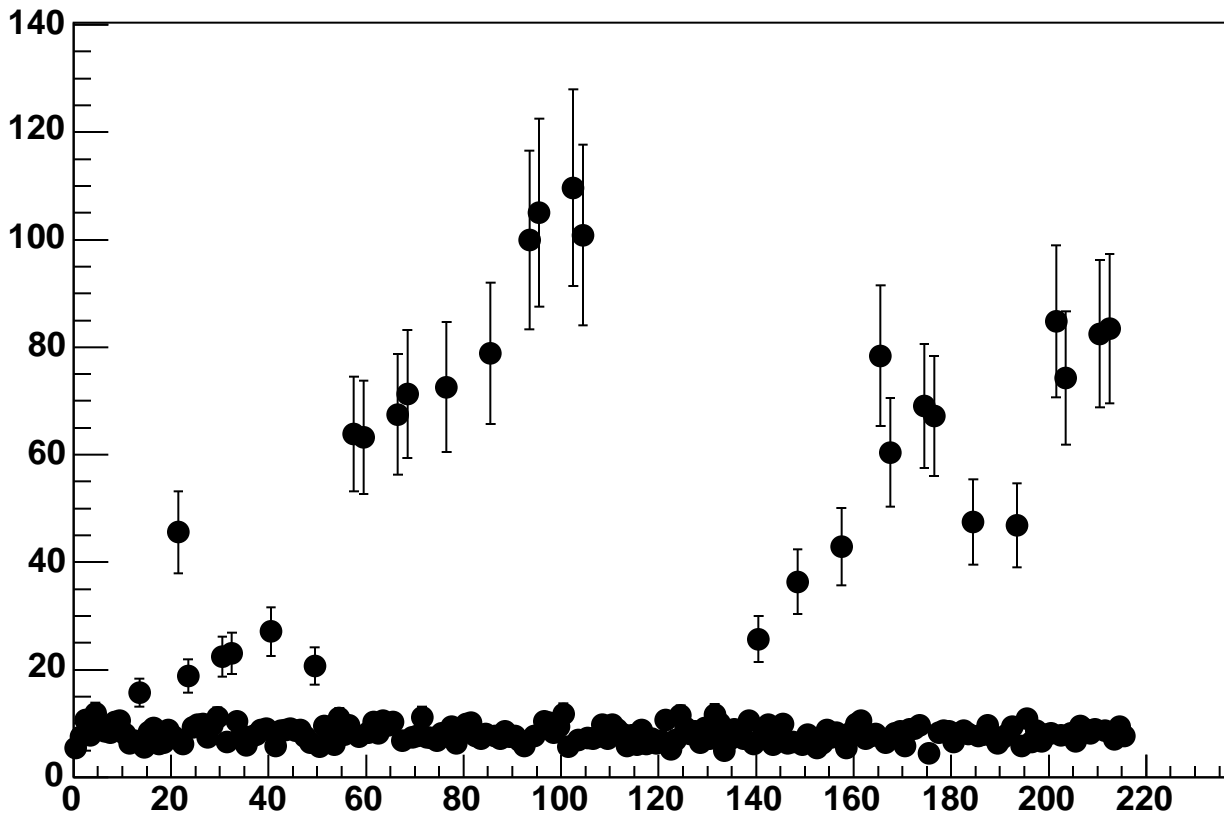
Enable 4, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



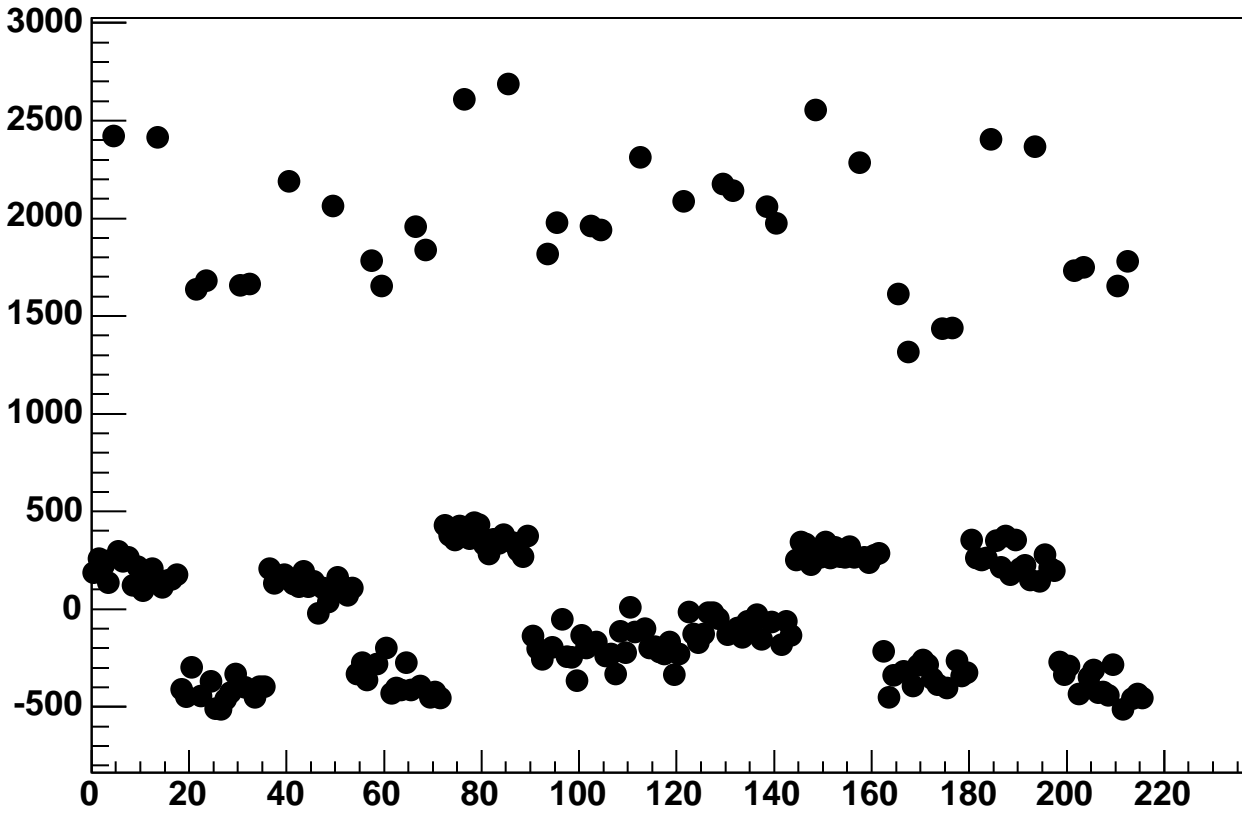
Enable 4, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



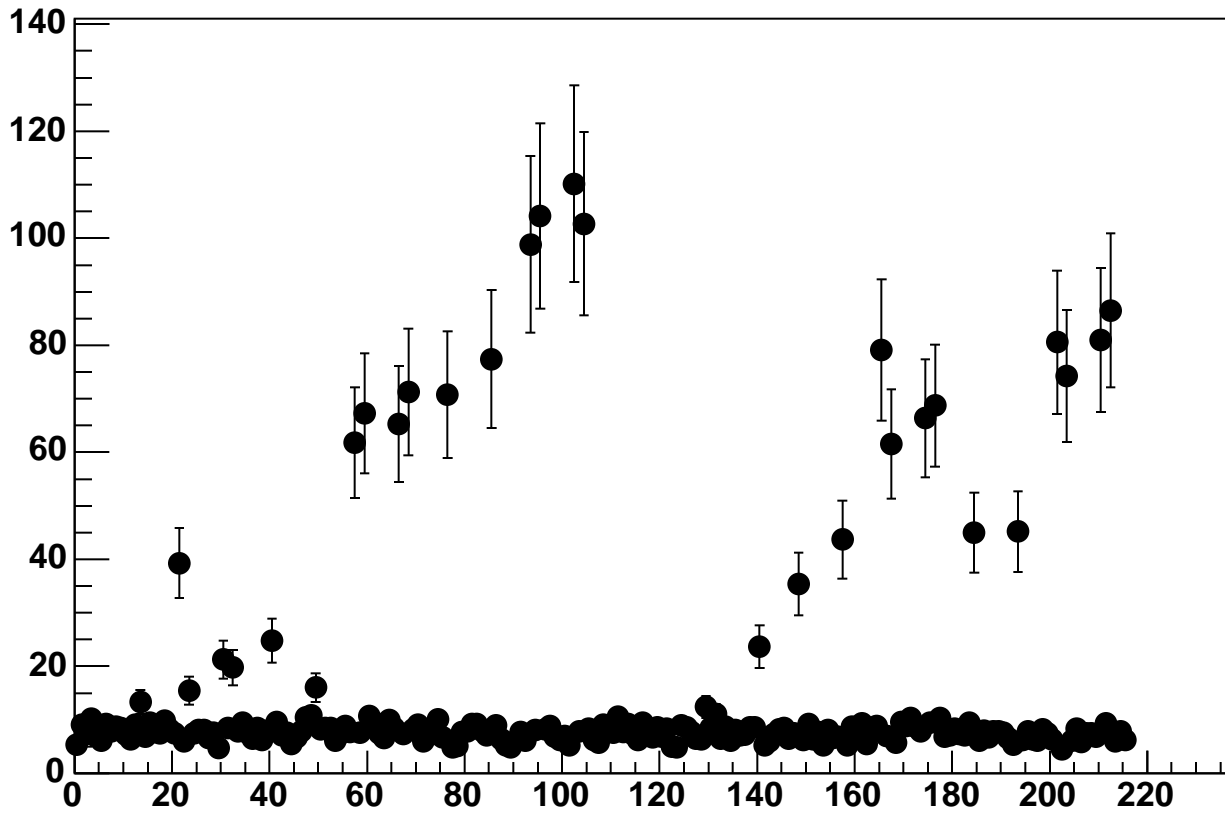
Enable 4, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



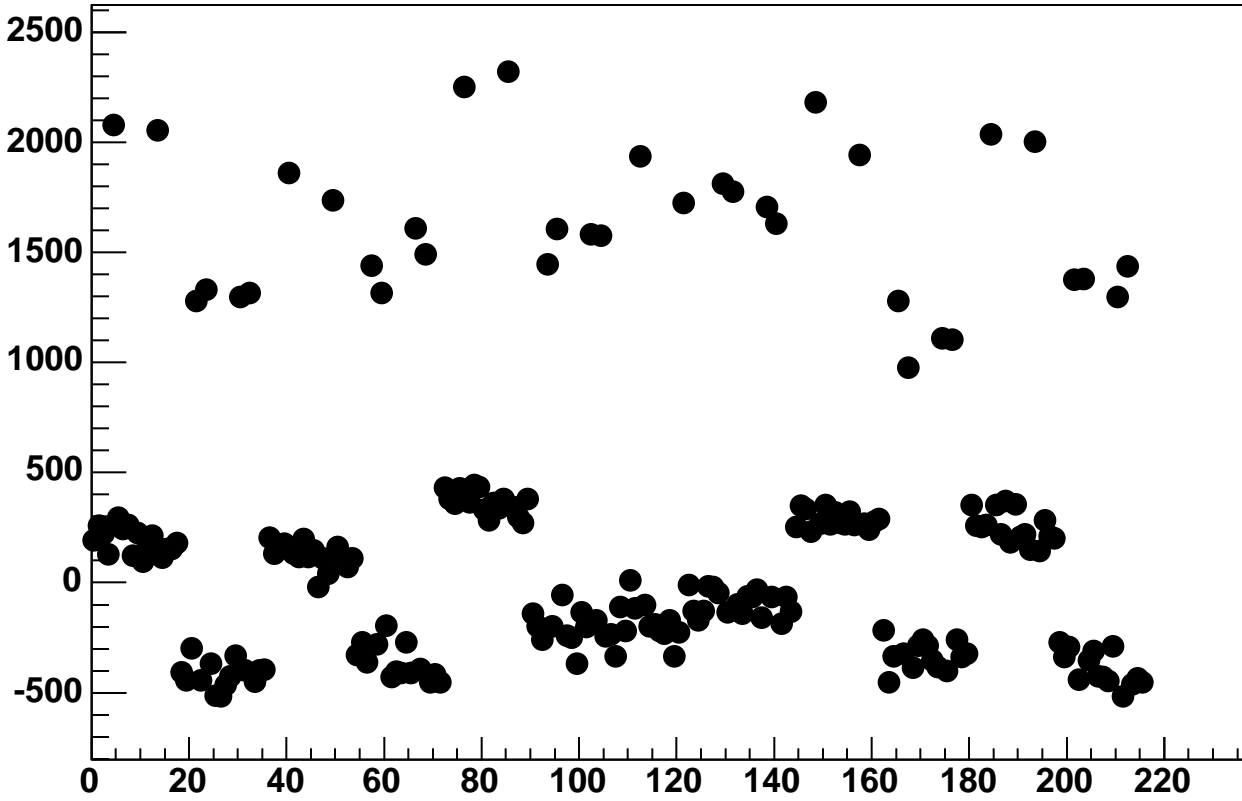
Enable 4, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



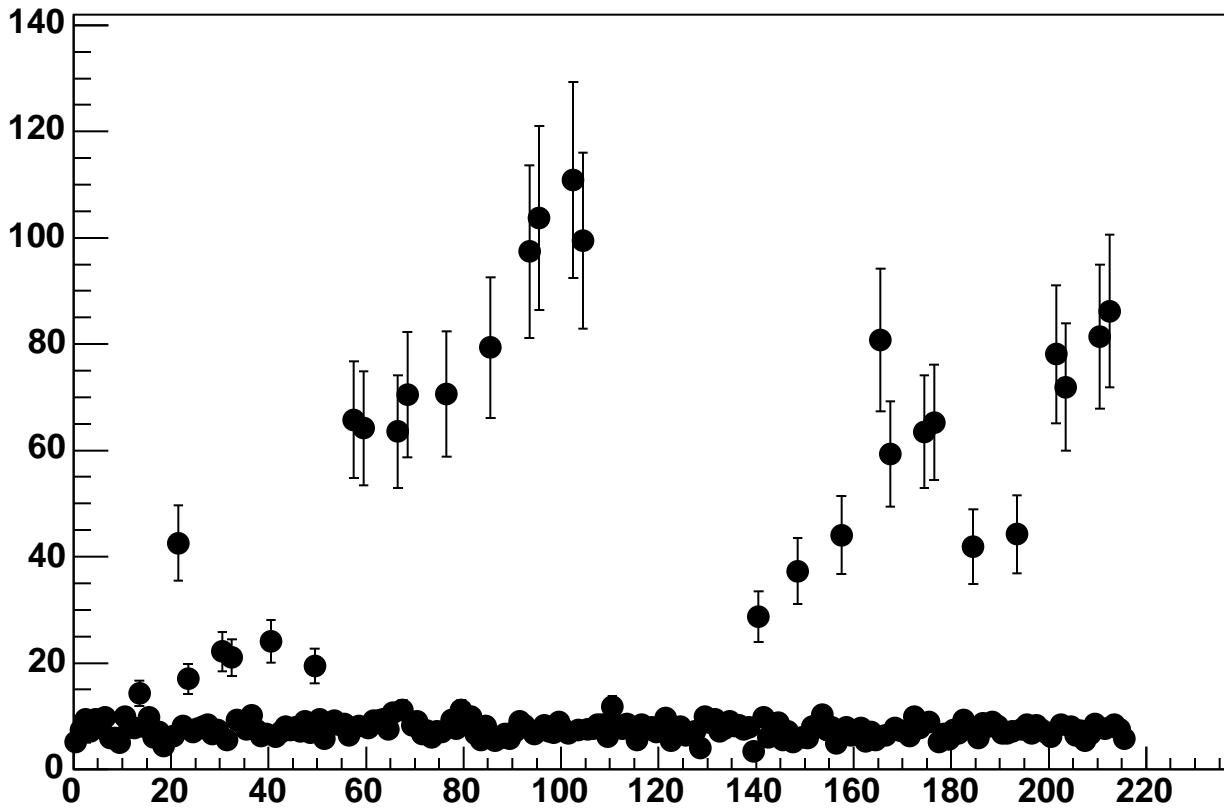
Enable 4, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



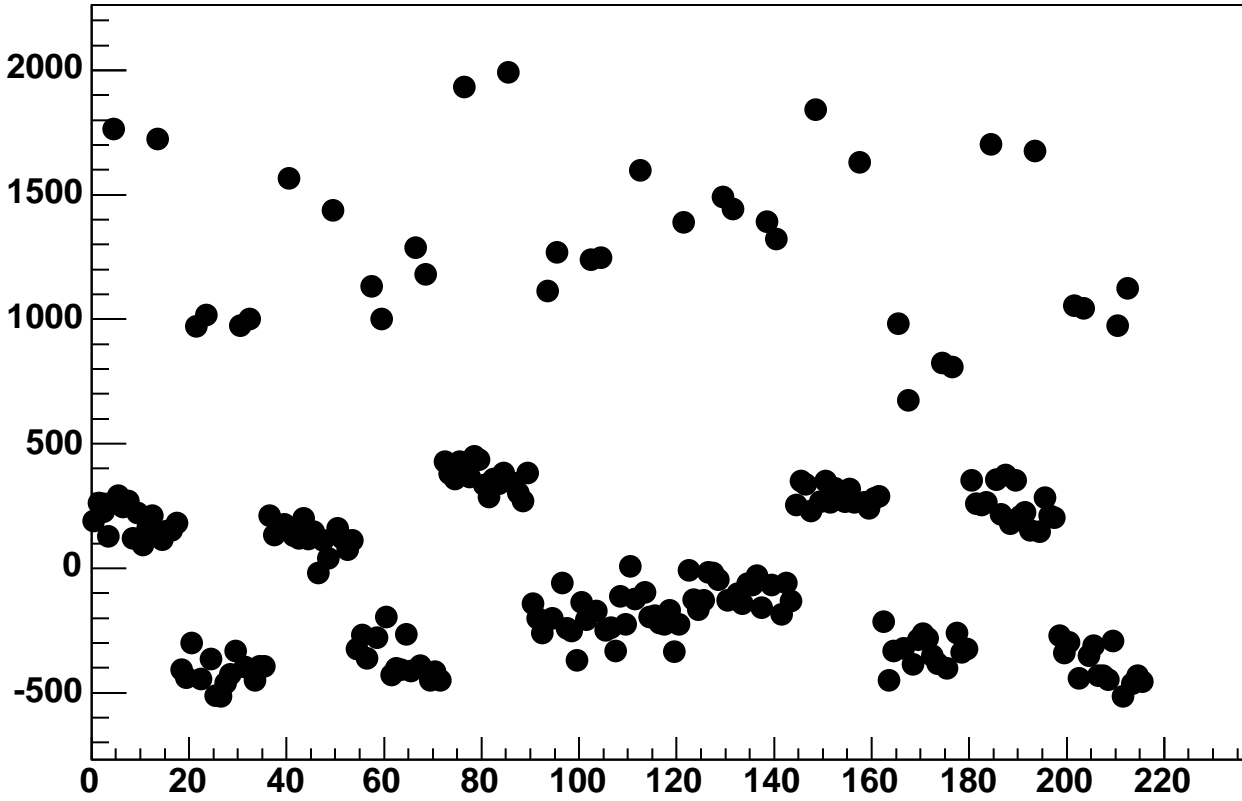
Enable 4, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



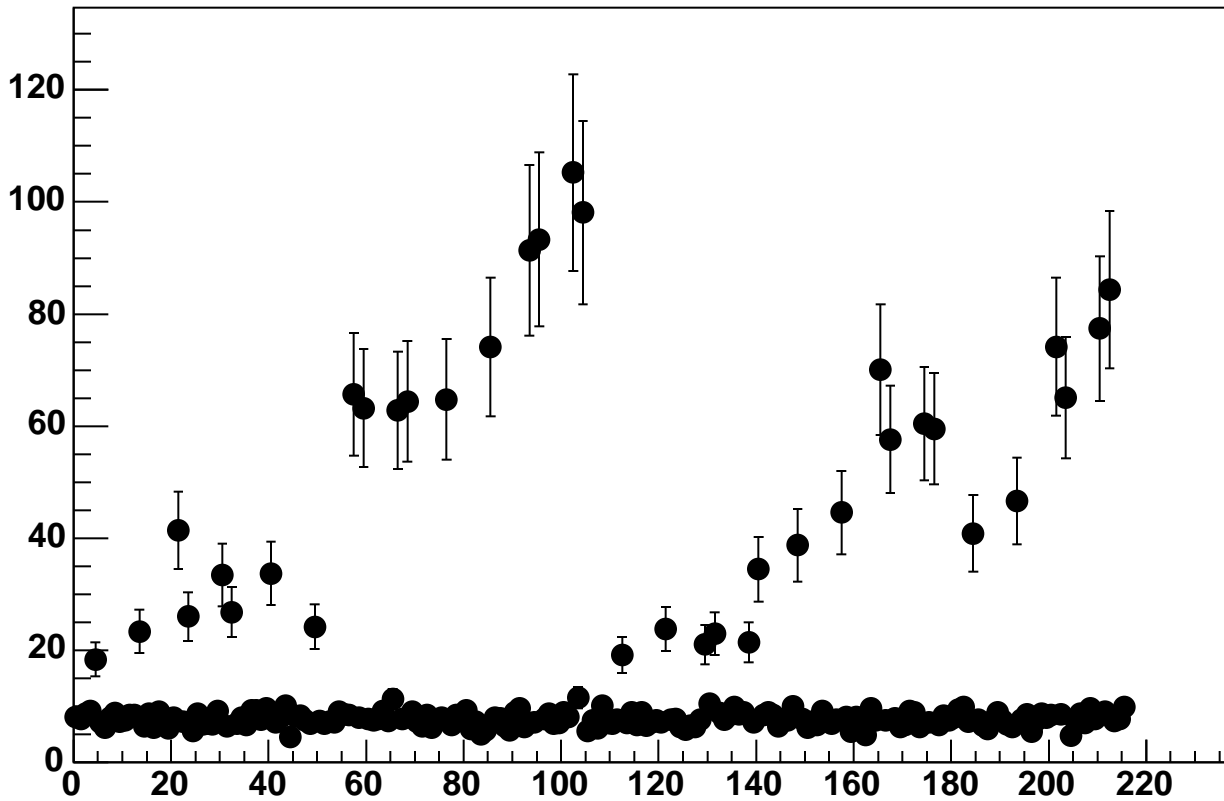
Enable 4, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



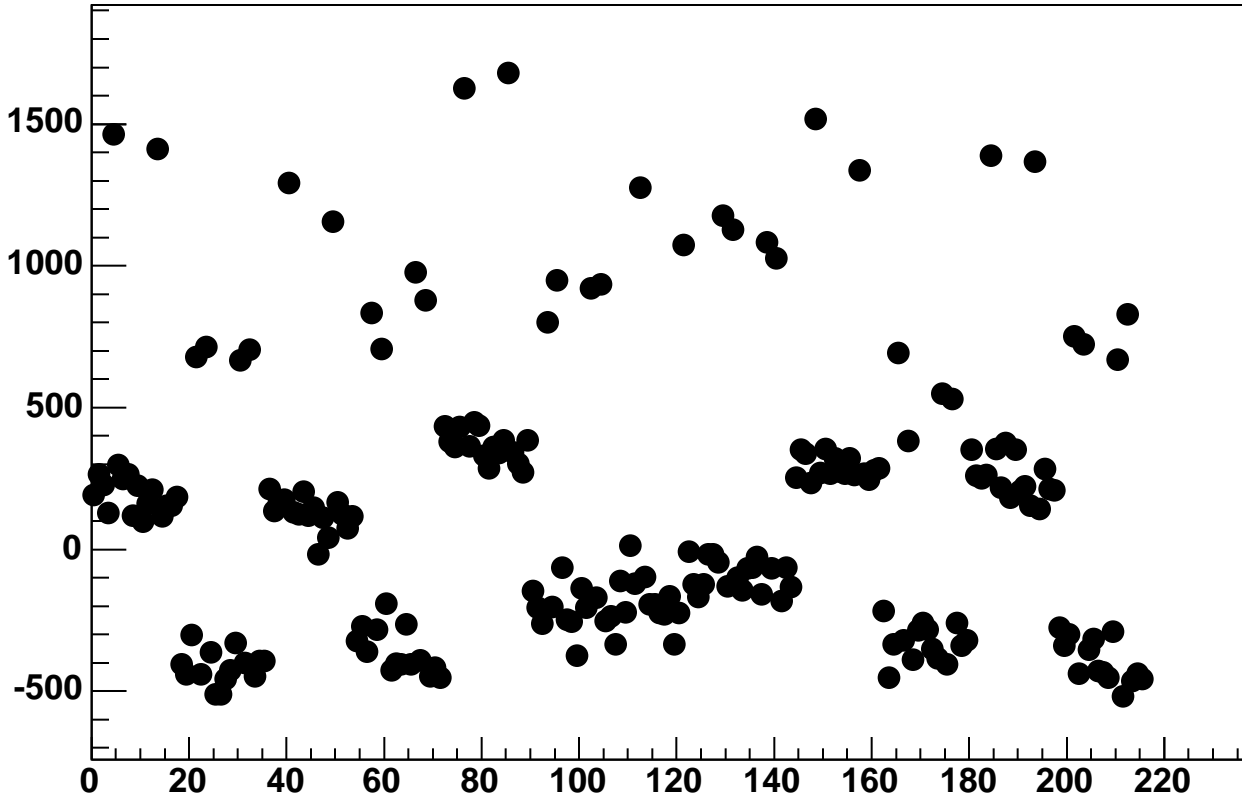
Enable 4, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



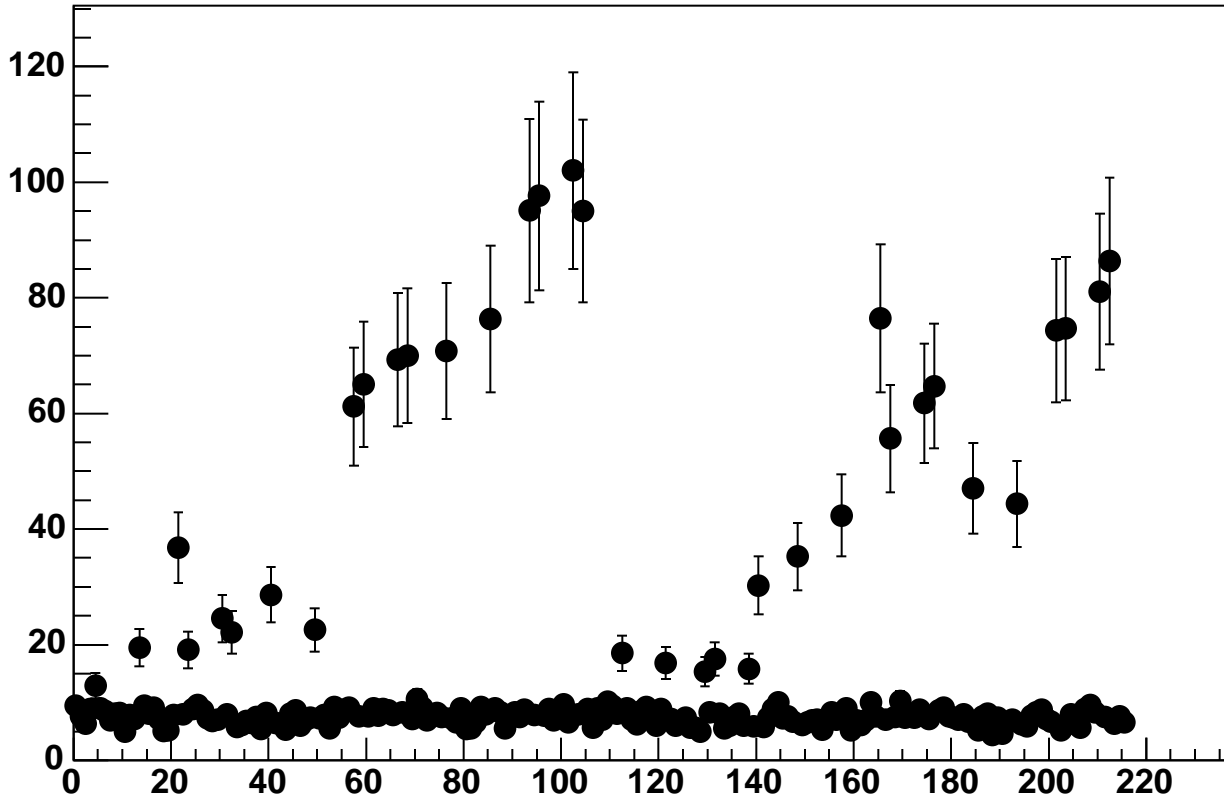
Enable 4, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



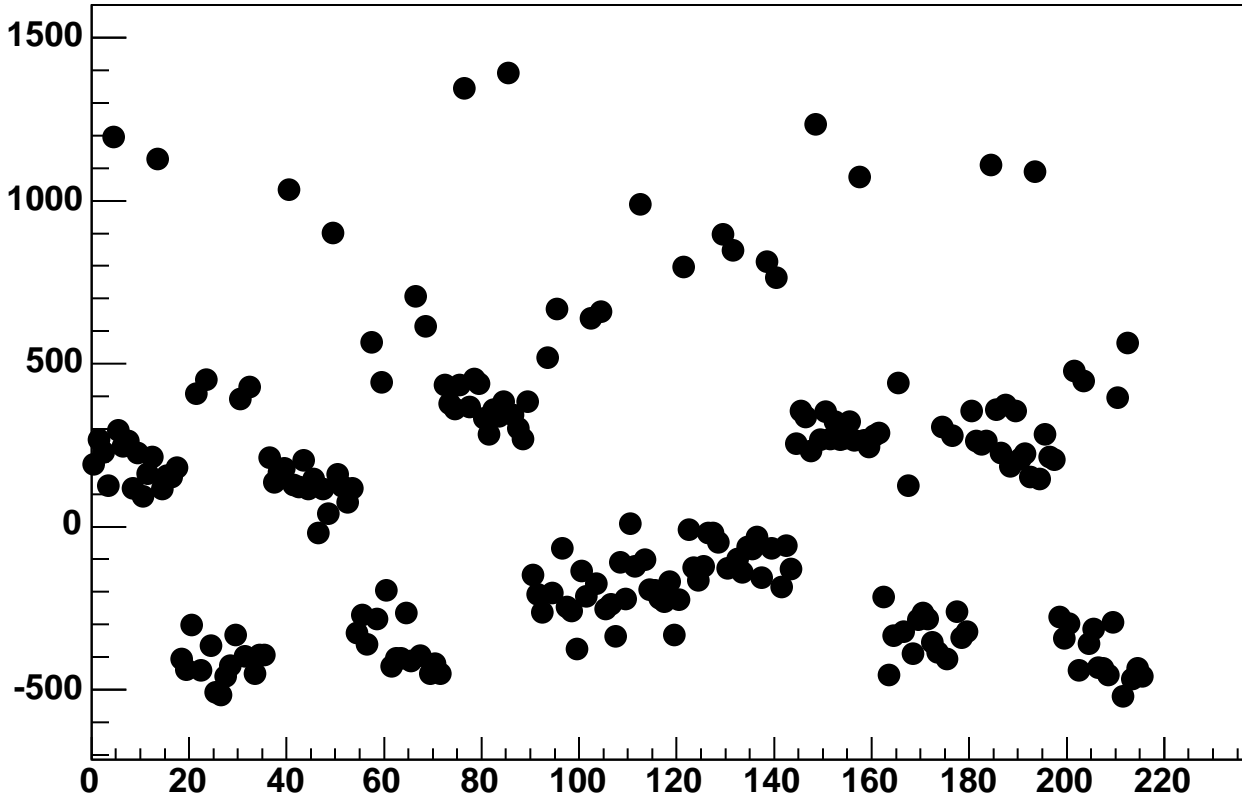
Enable 4, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



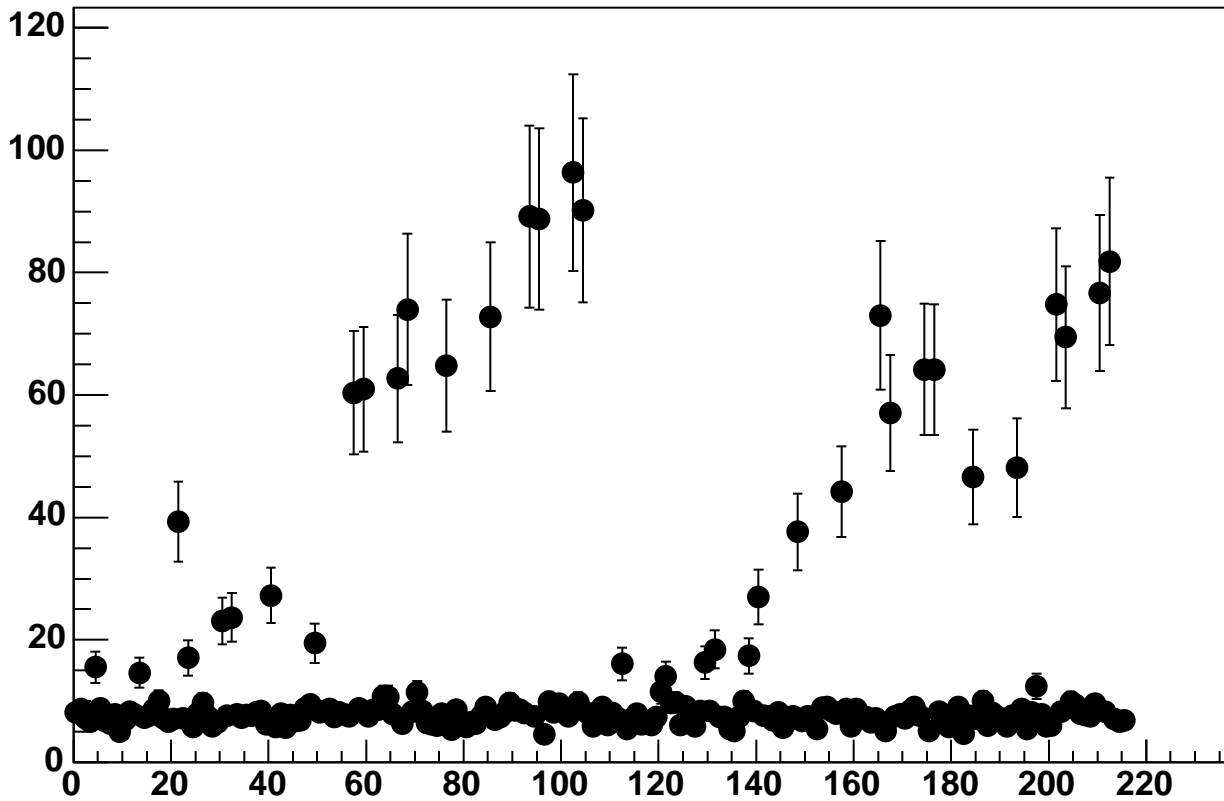
Enable 4, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



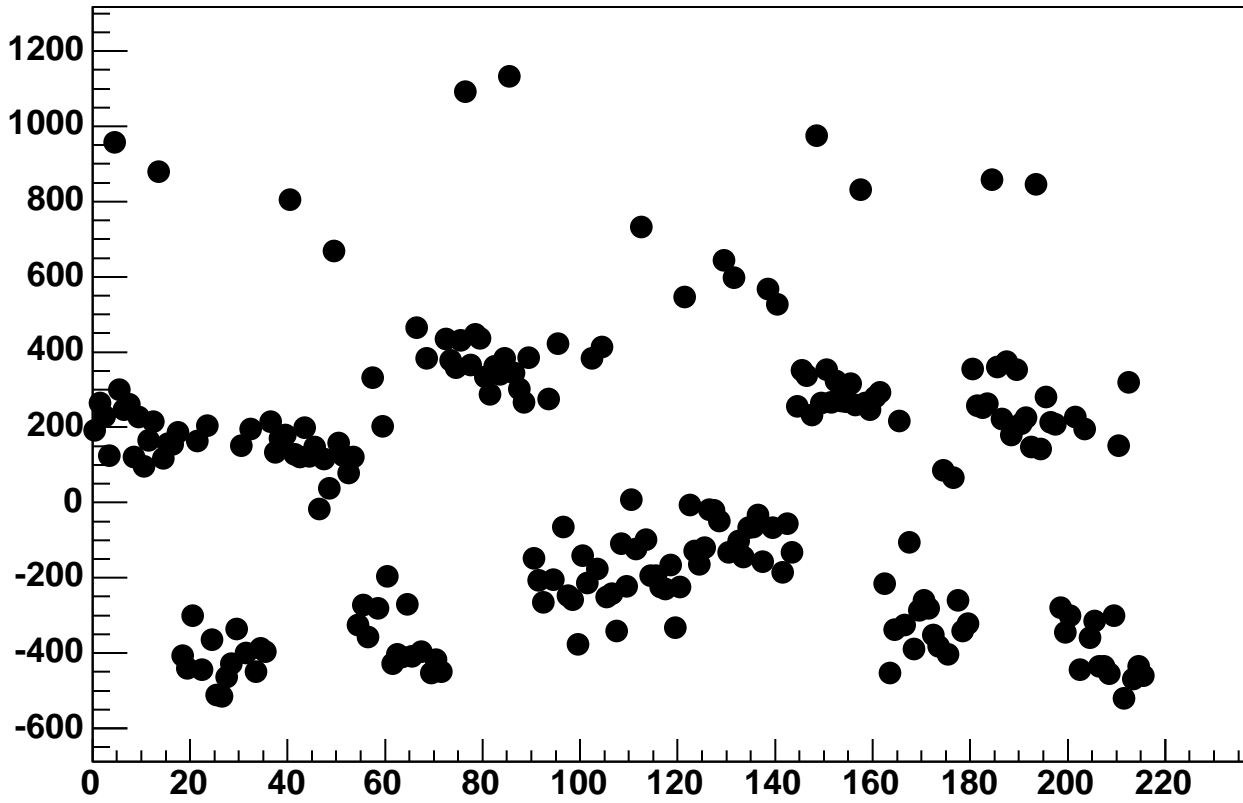
Enable 4, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



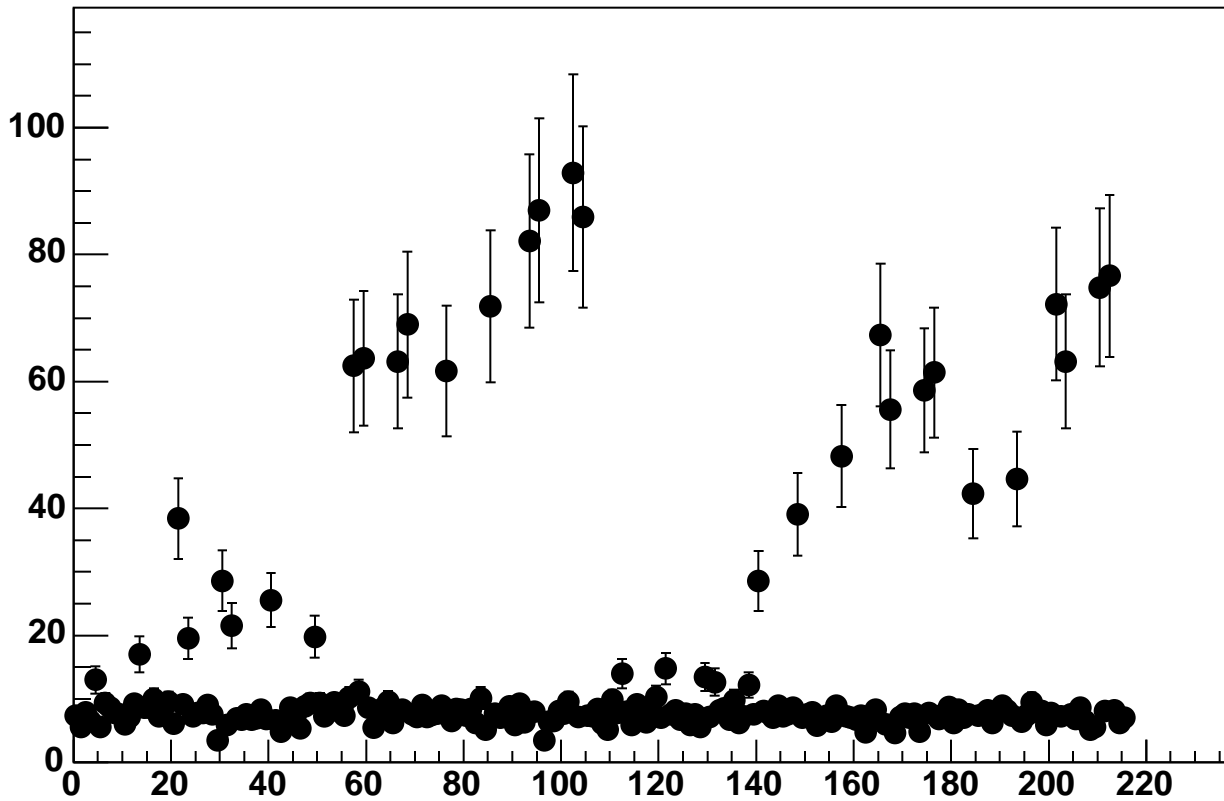
Enable 4, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



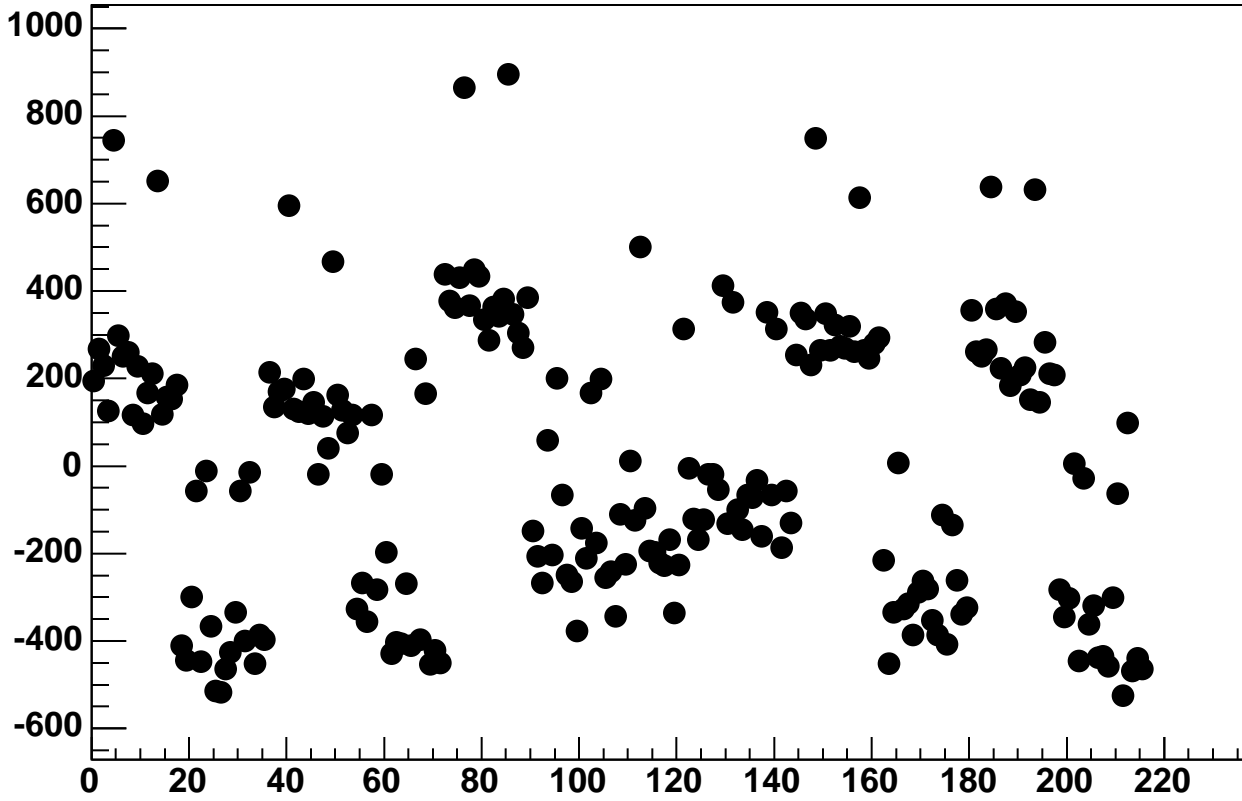
Enable 4, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



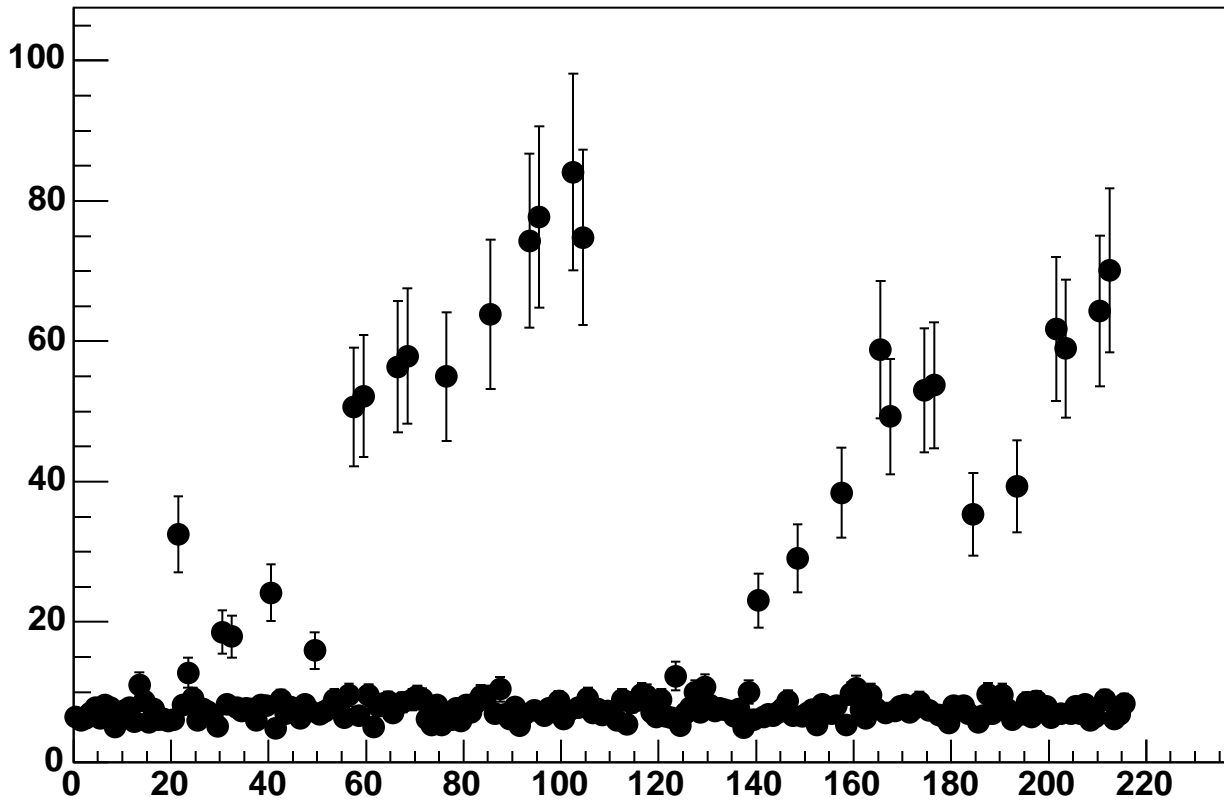
Enable 4, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



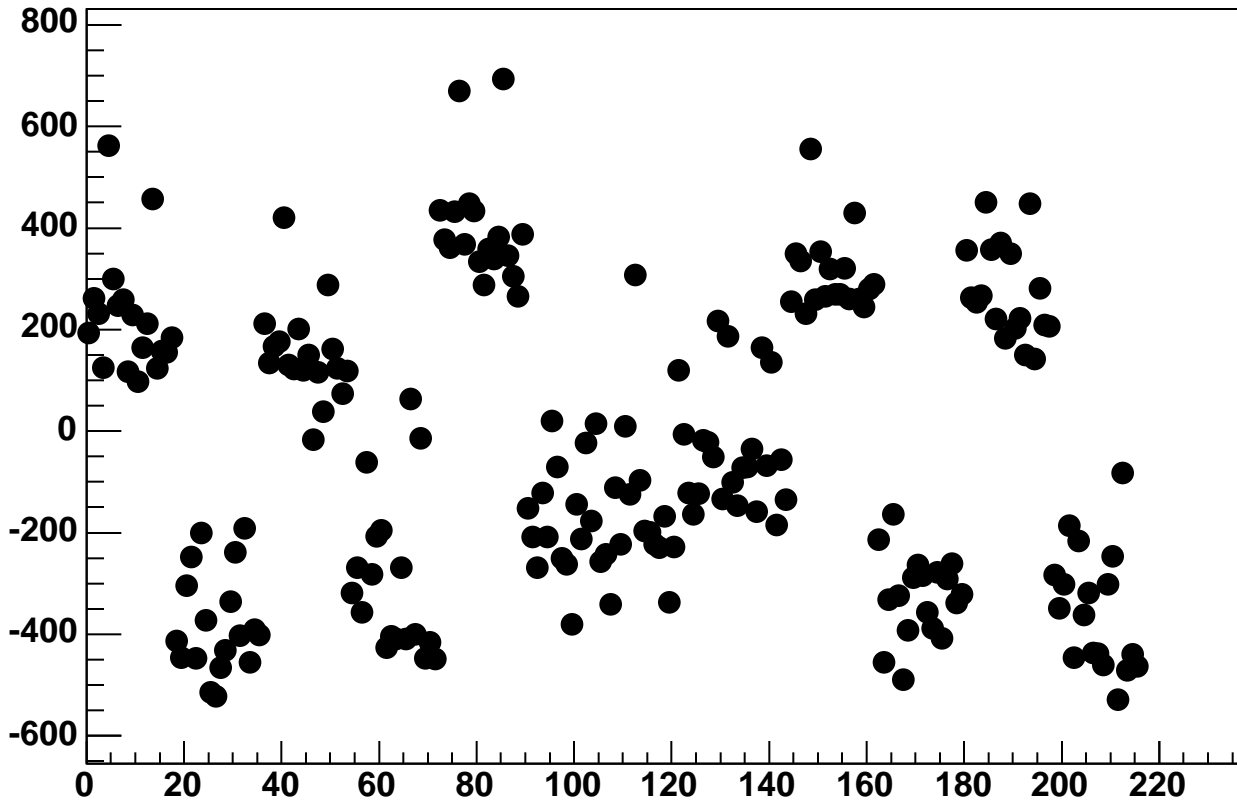
Enable 4, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



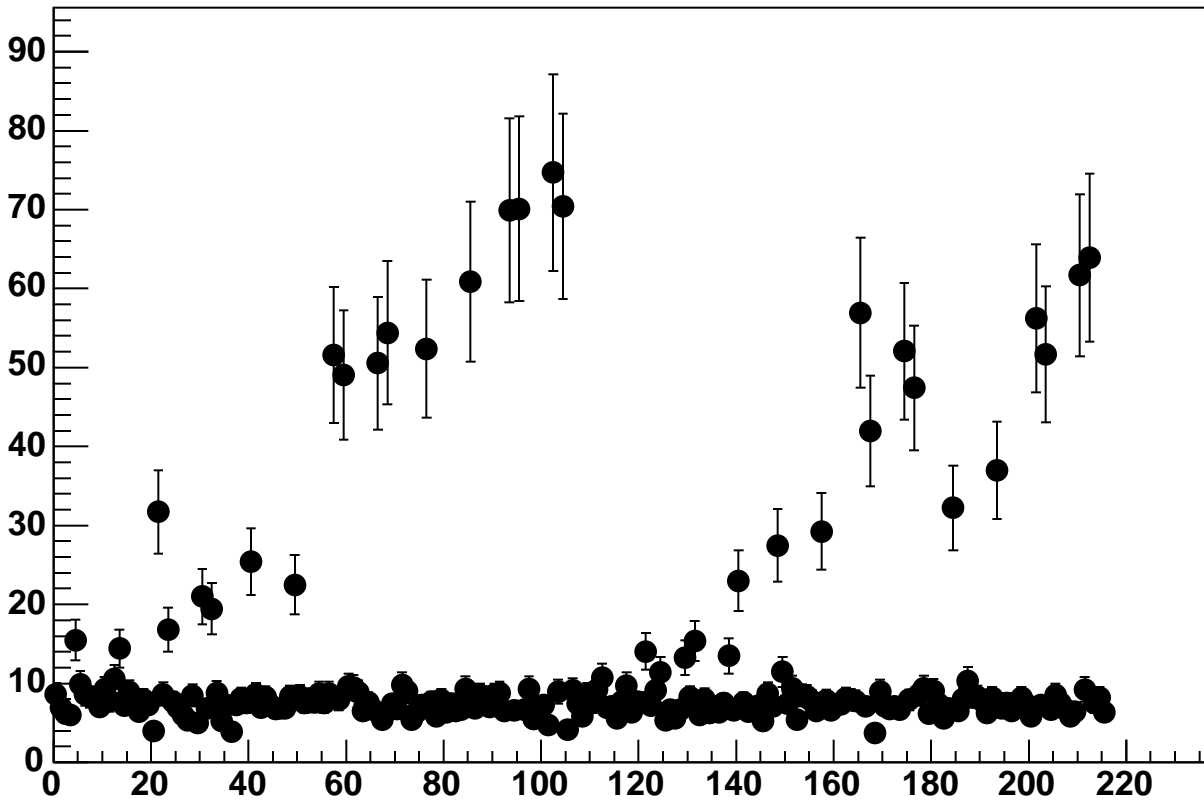
Enable 4, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



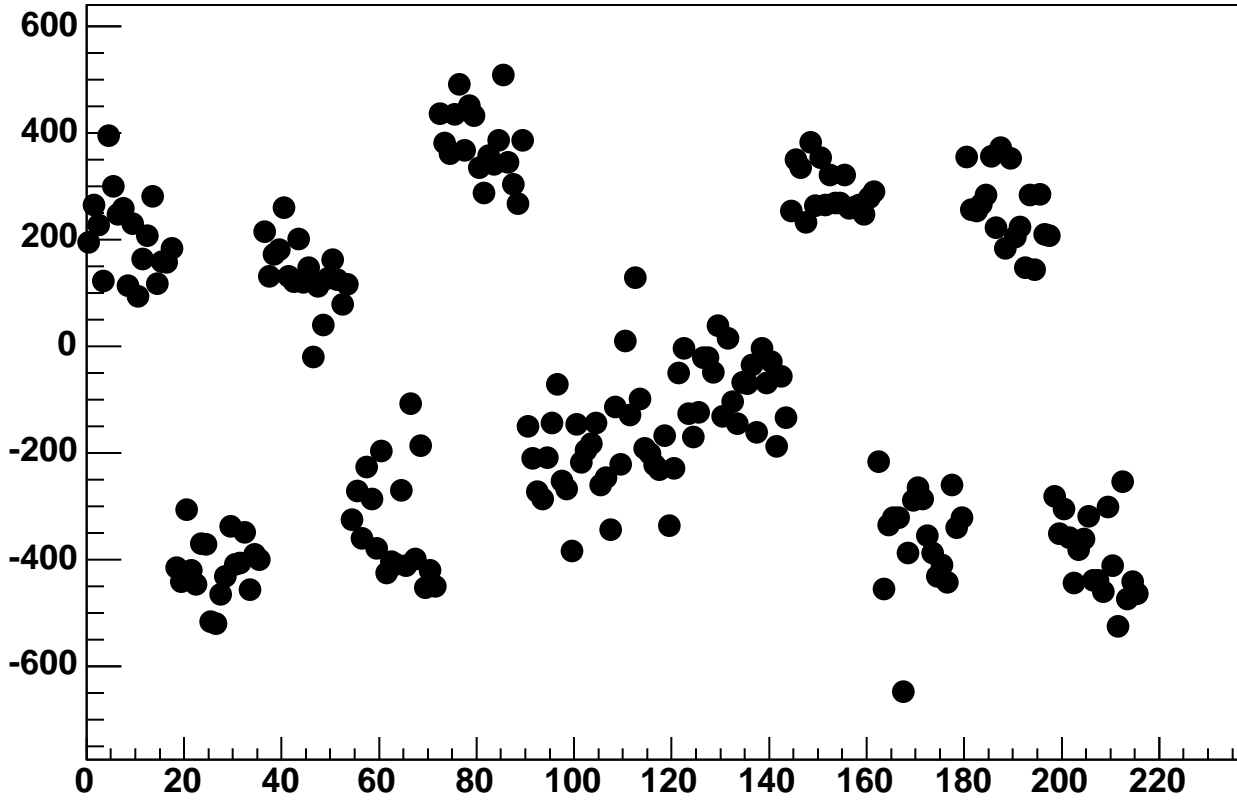
Enable 4, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



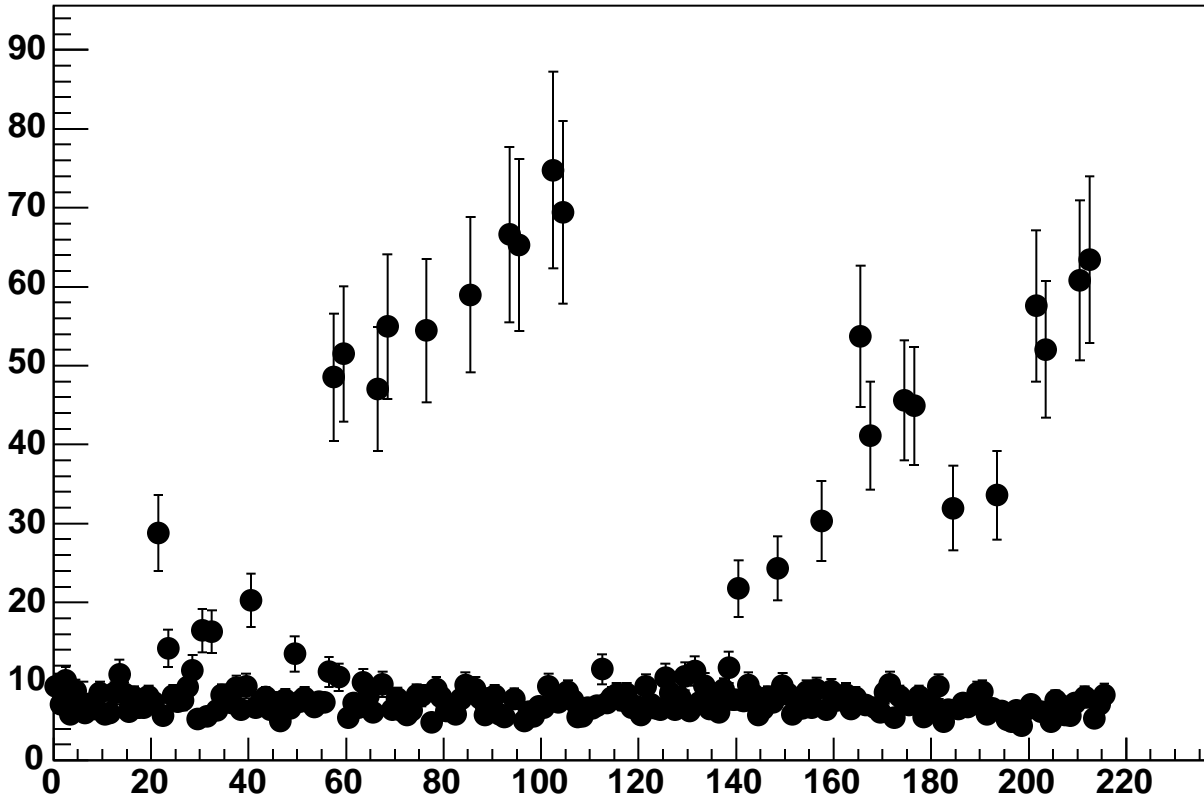
Enable 4, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



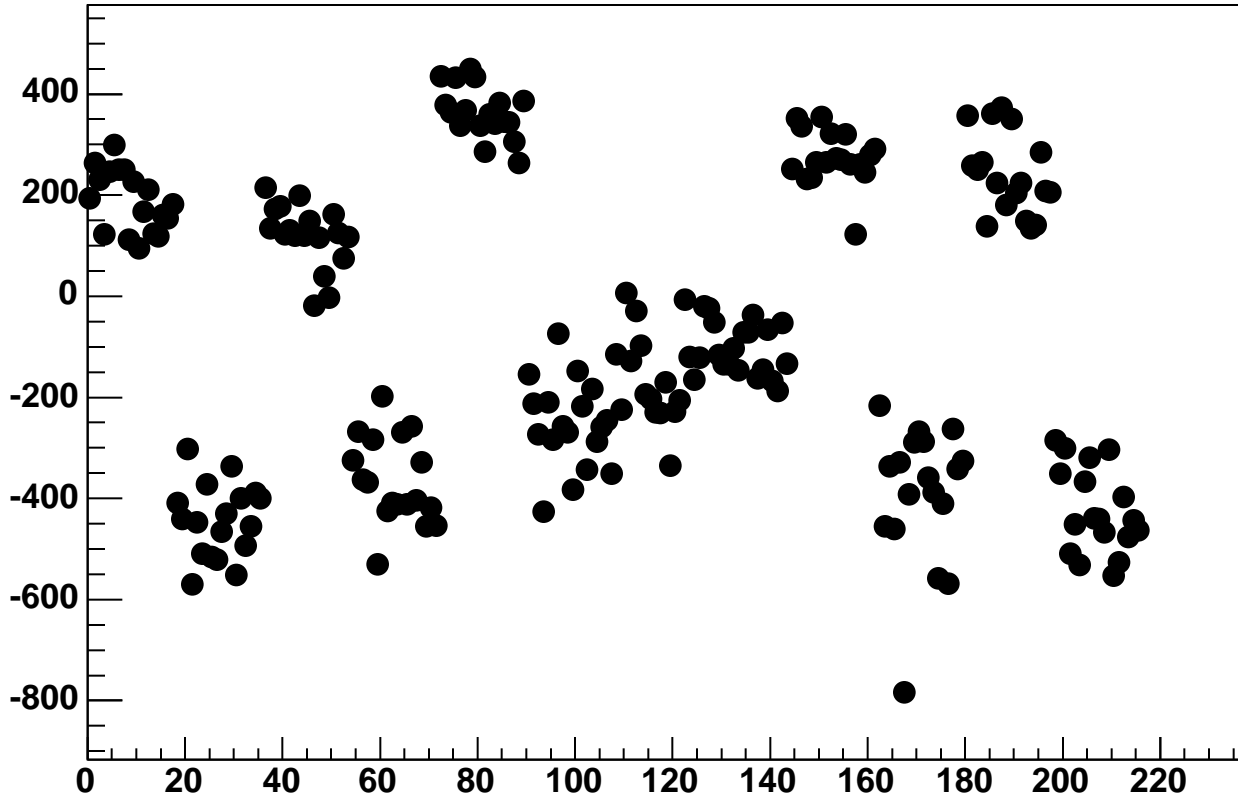
Enable 4, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



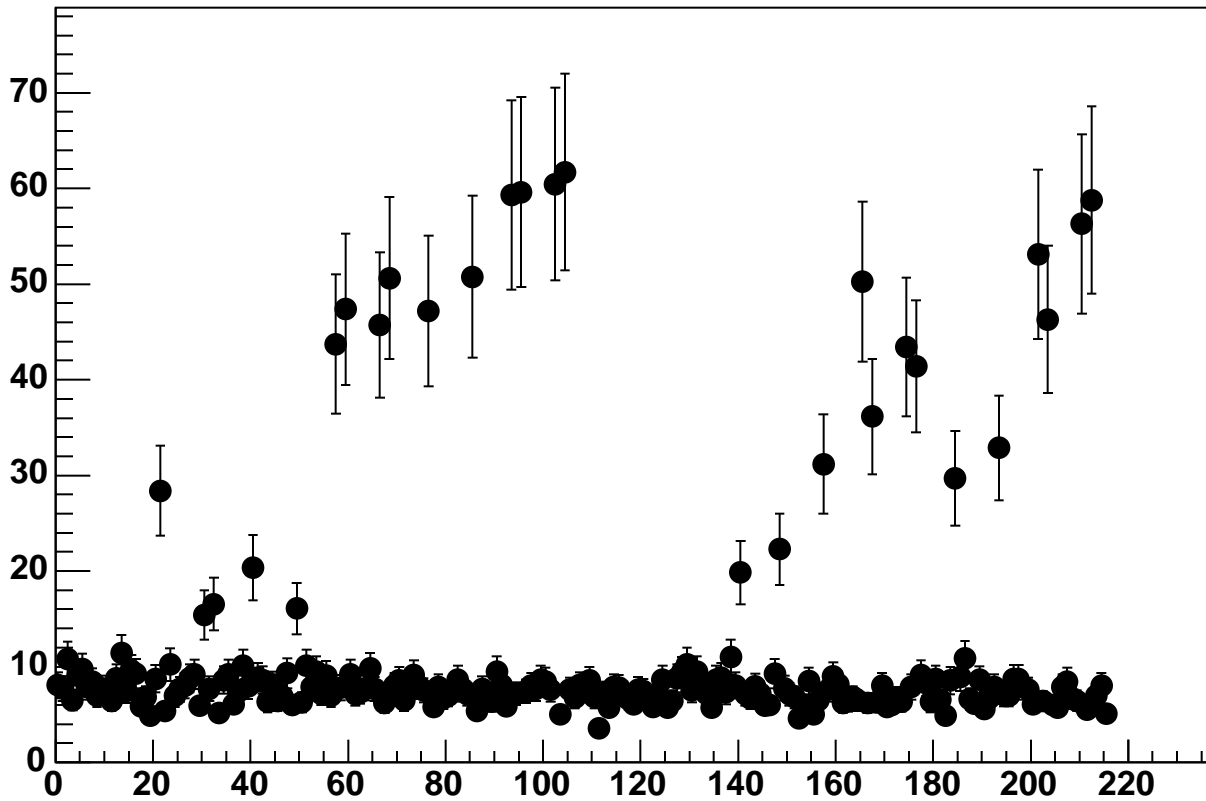
Enable 4, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



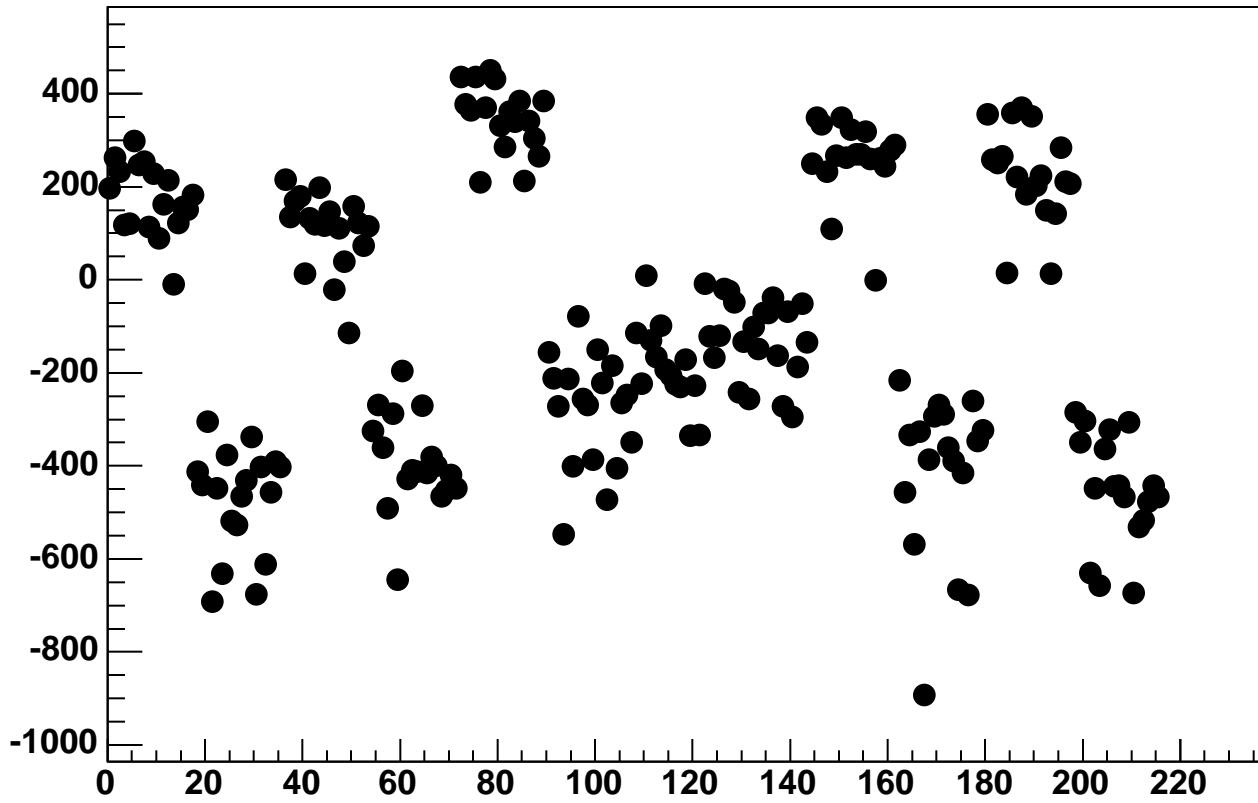
Enable 4, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



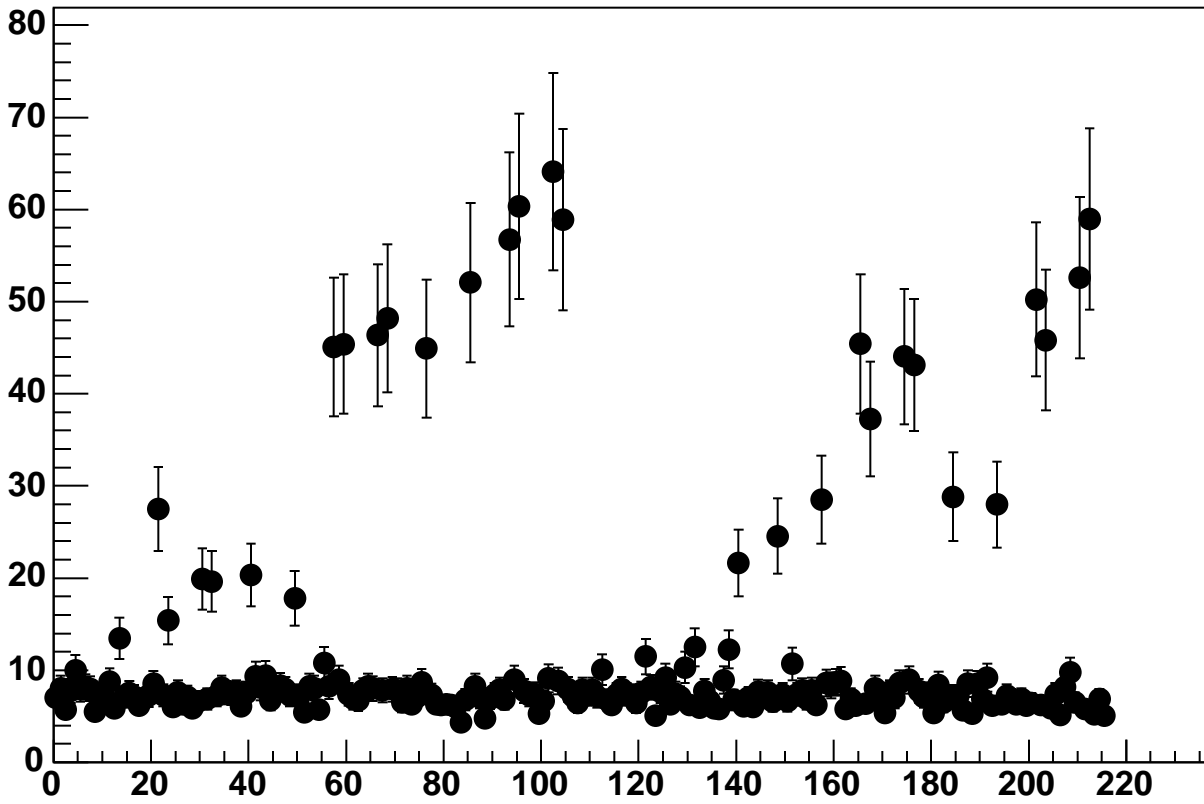
Enable 4, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



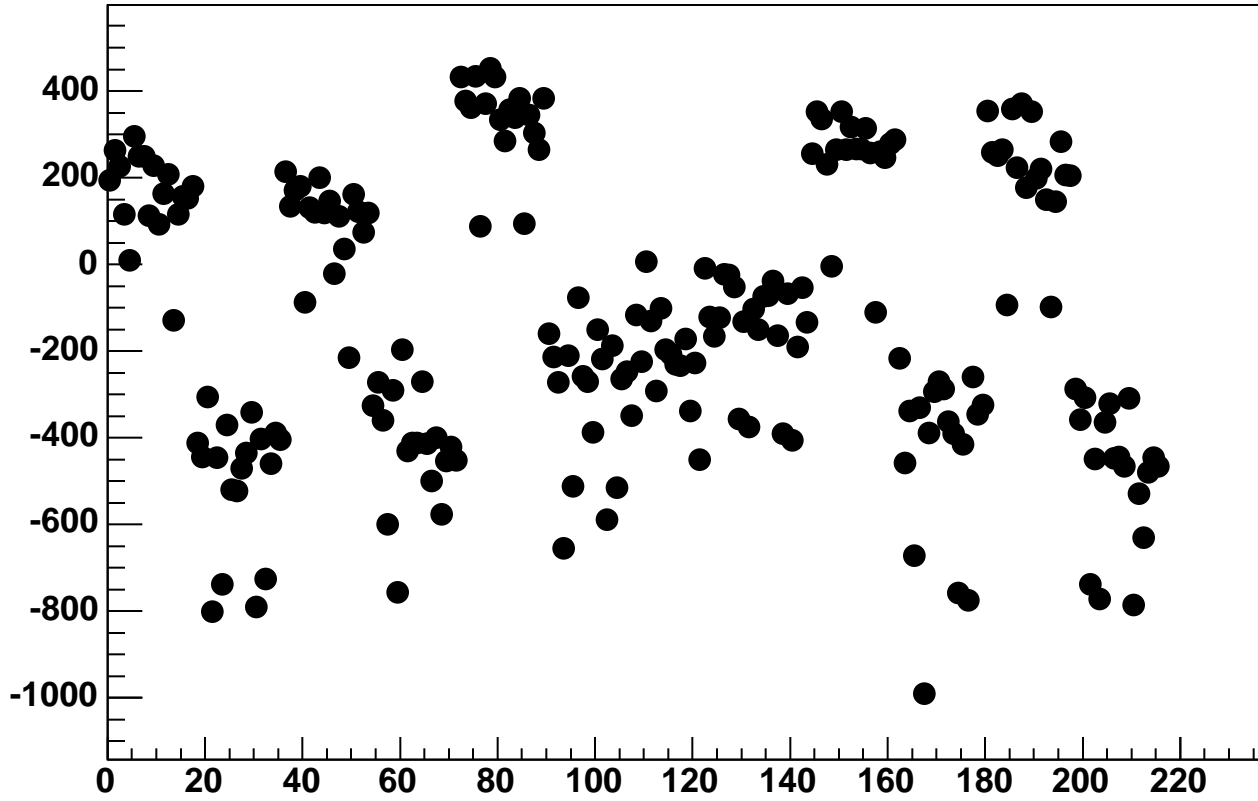
Enable 4, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



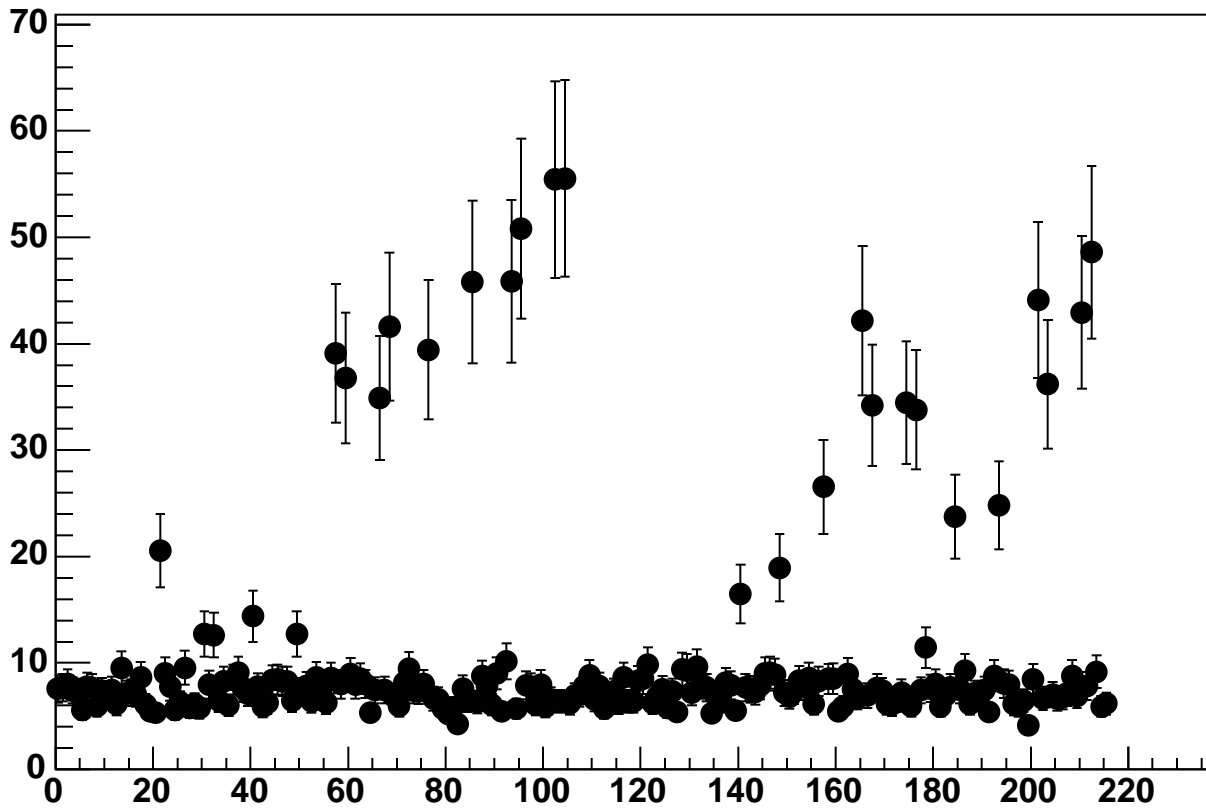
Enable 4, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



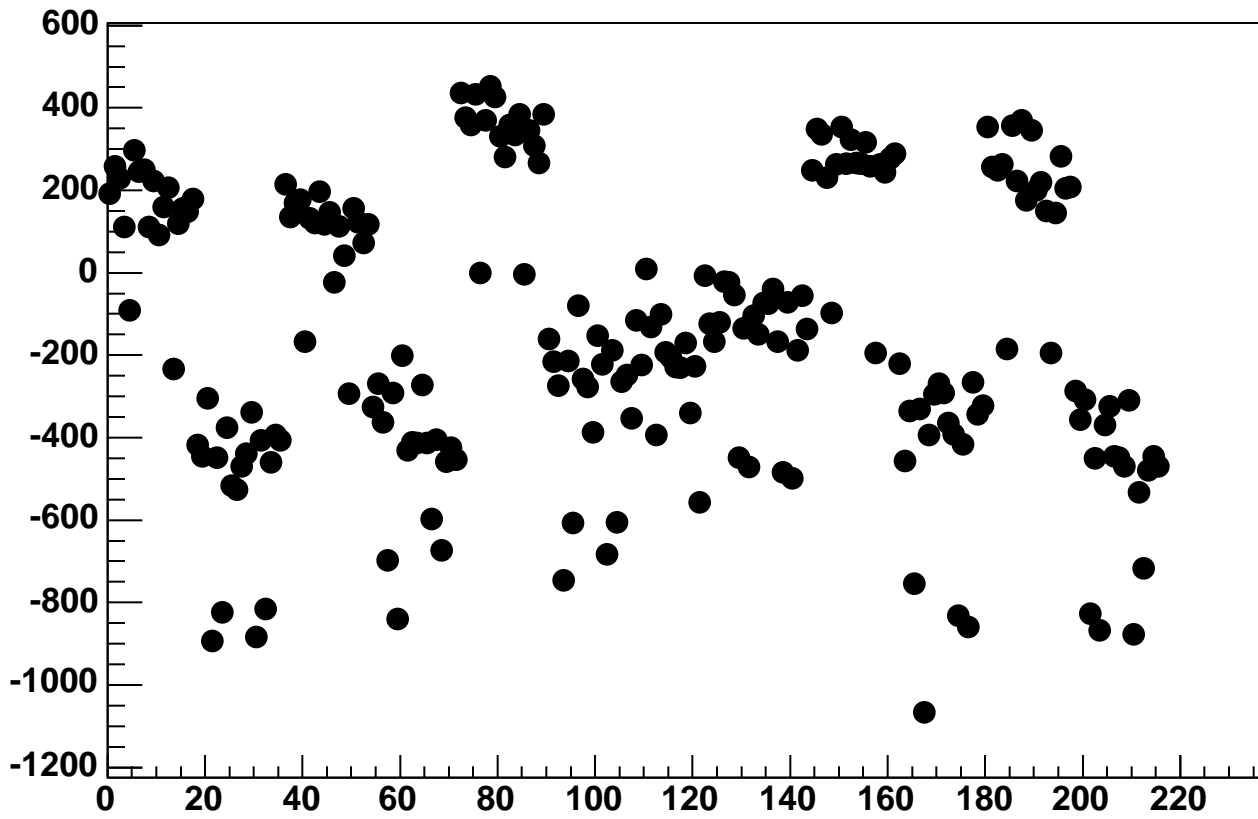
Enable 4, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



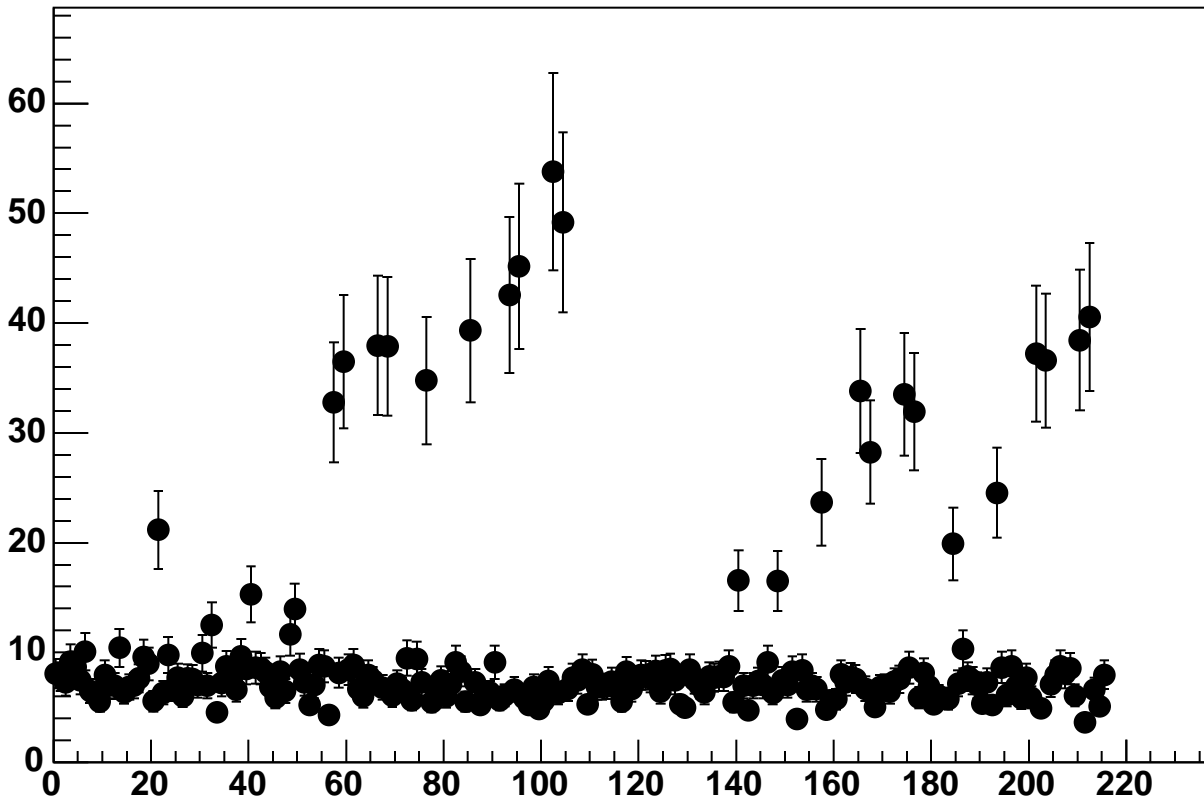
Enable 4, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



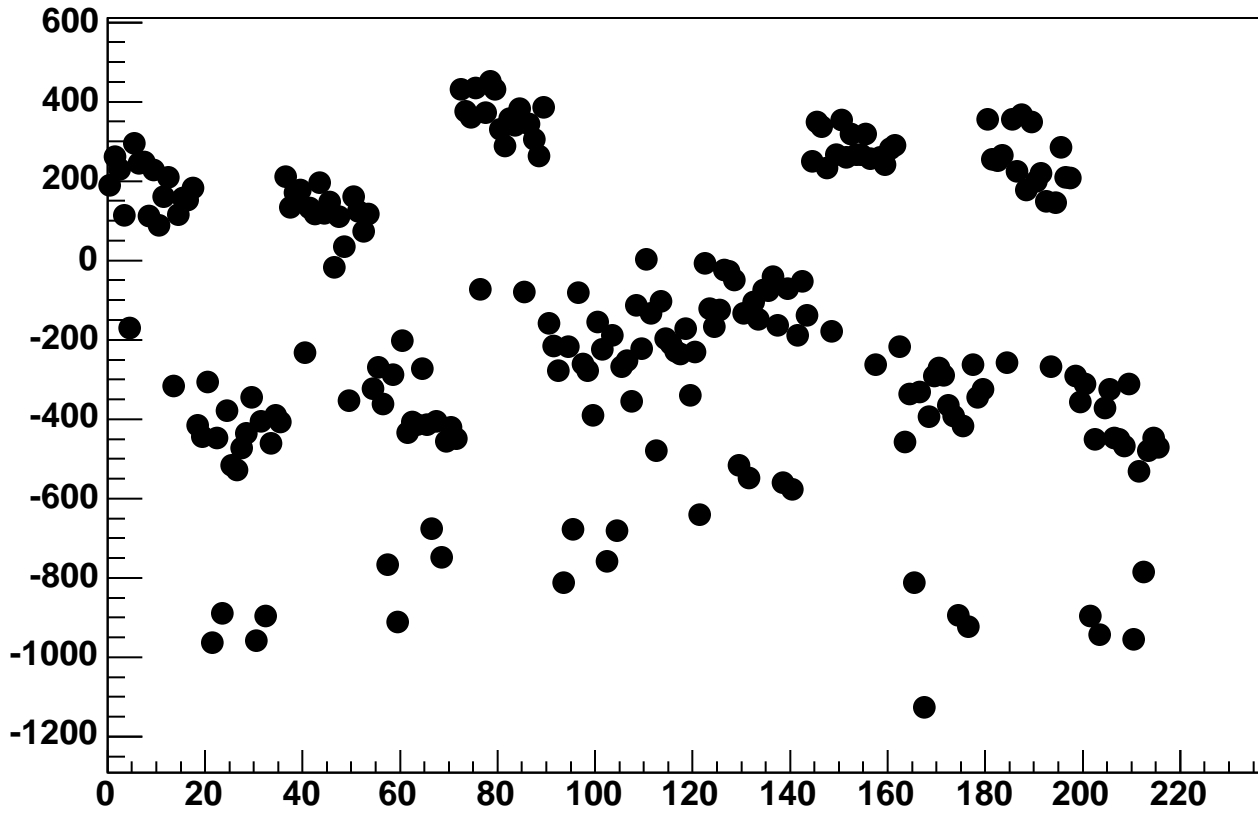
Enable 4, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



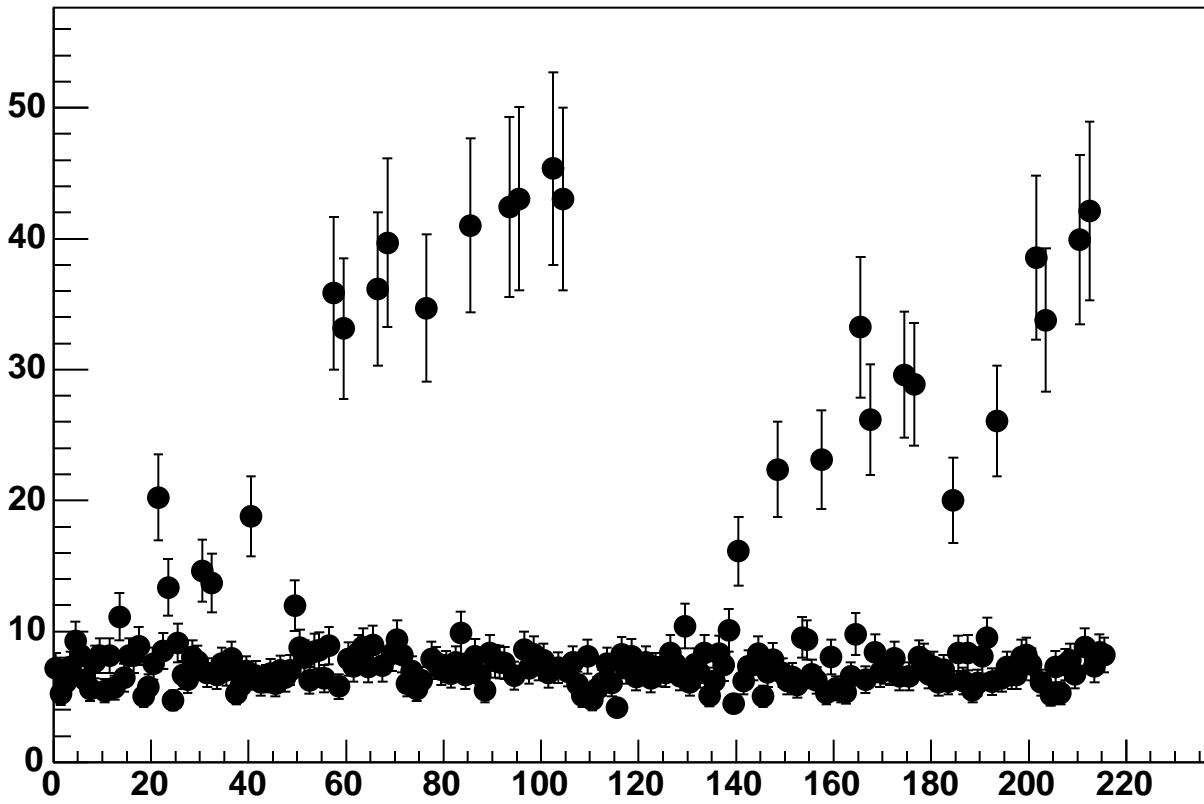
Enable 4, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



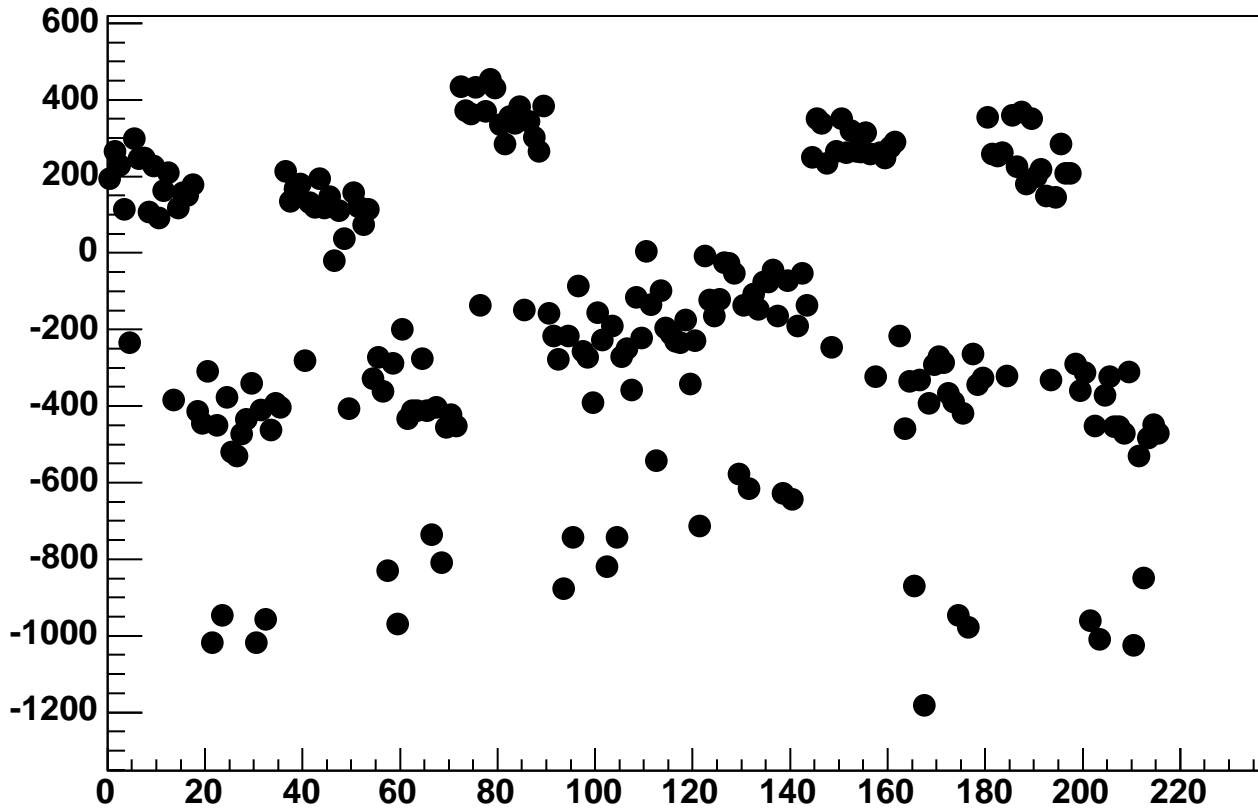
Enable 4, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



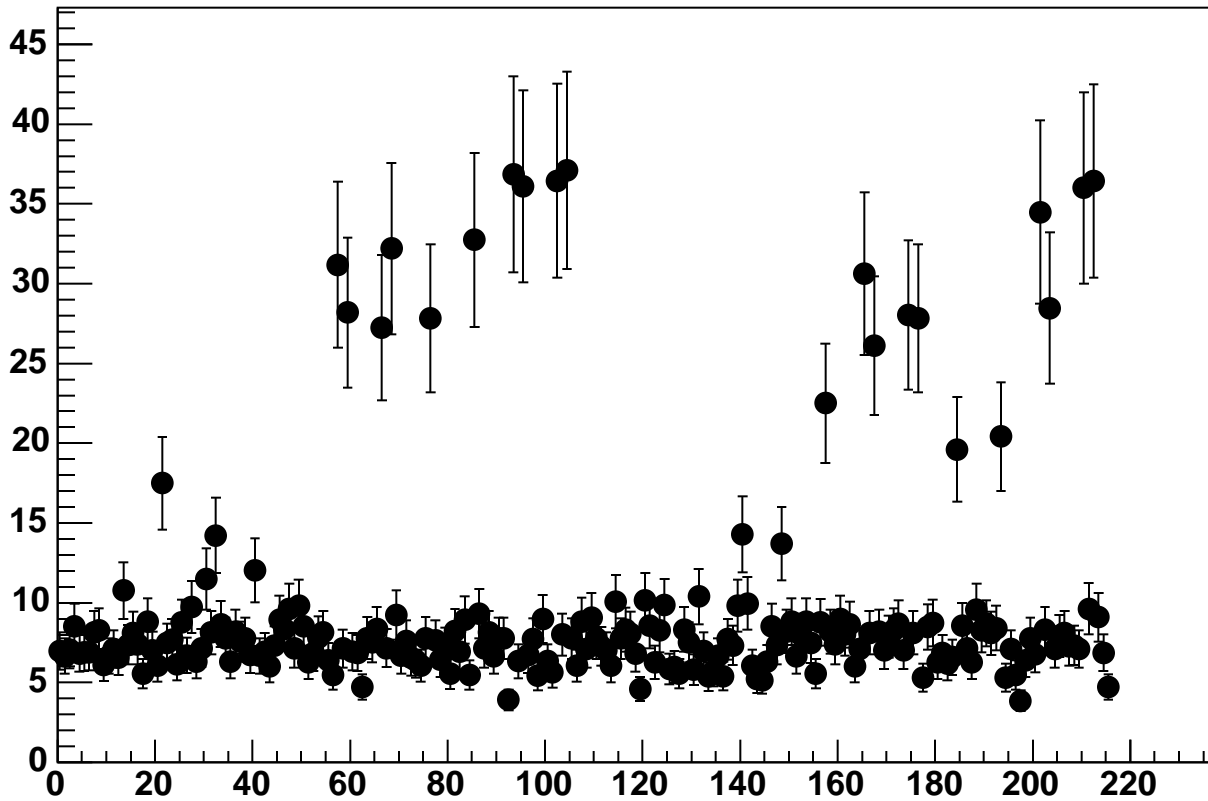
Enable 4, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



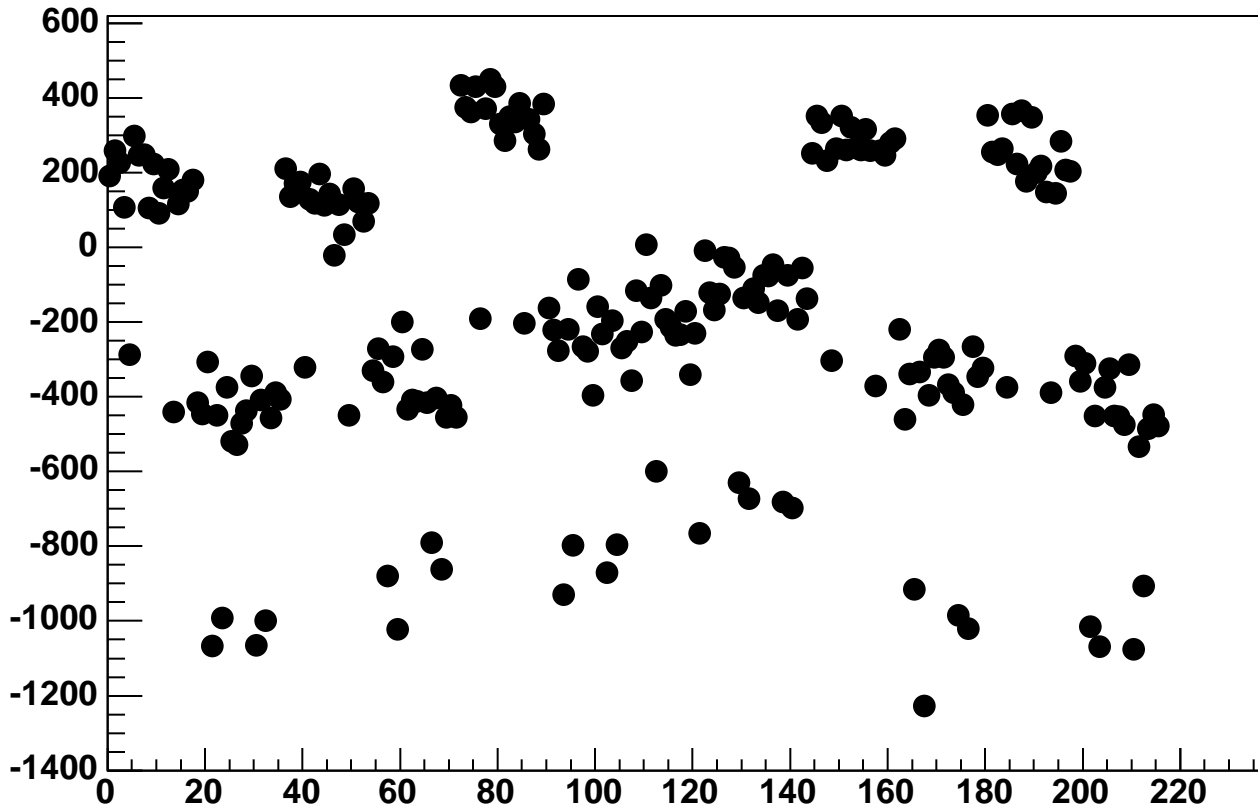
Enable 4, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



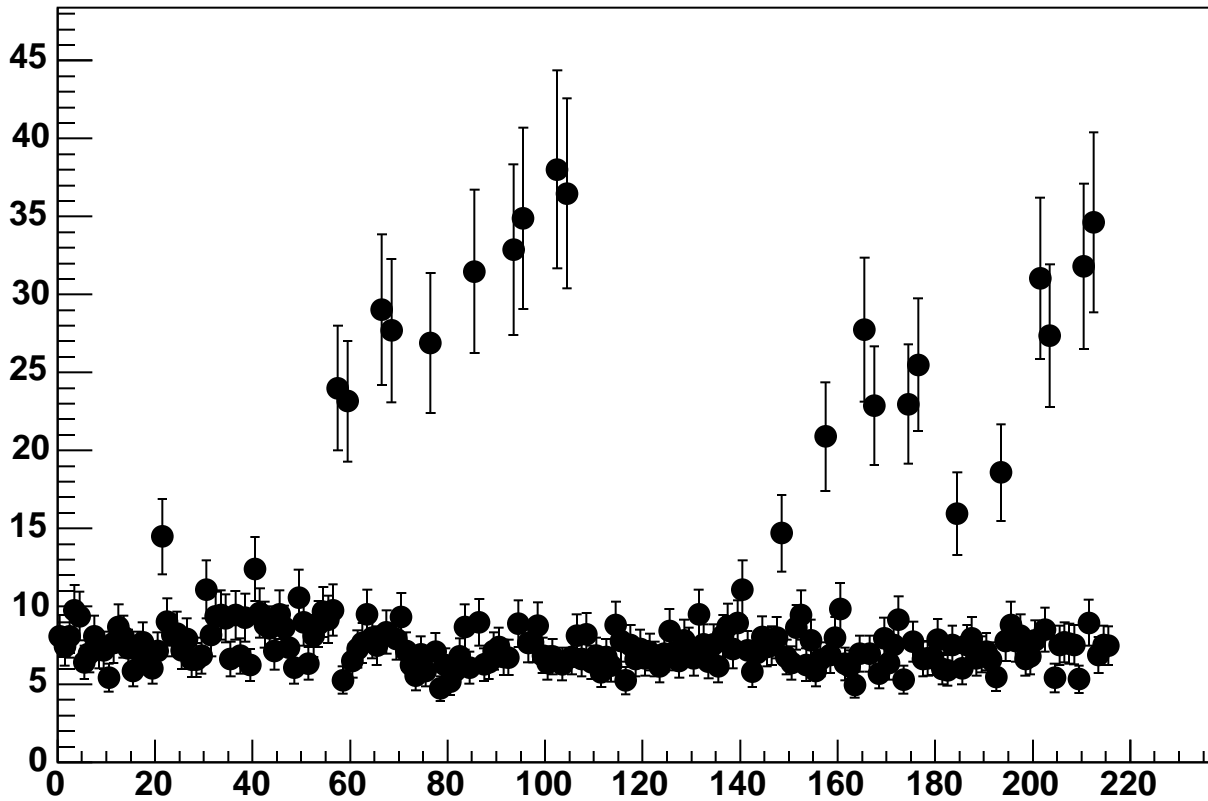
Enable 4, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



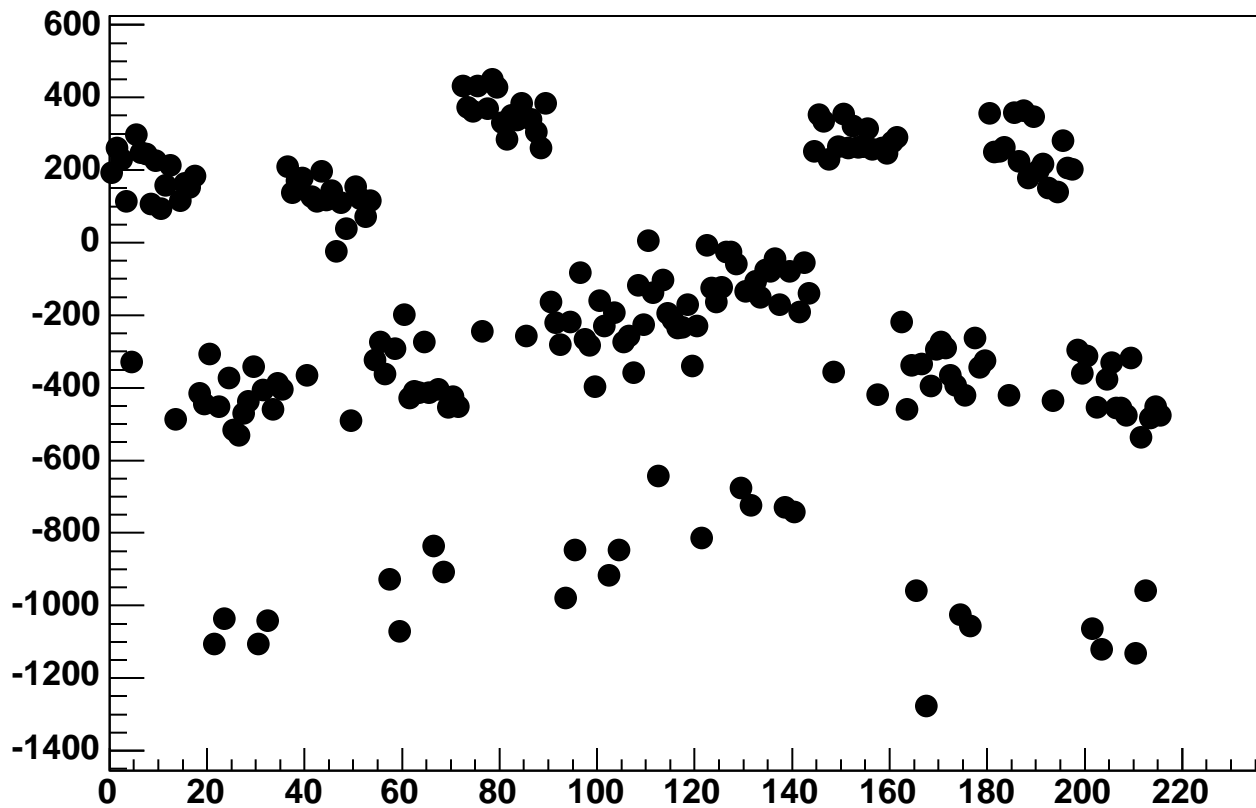
Enable 4, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



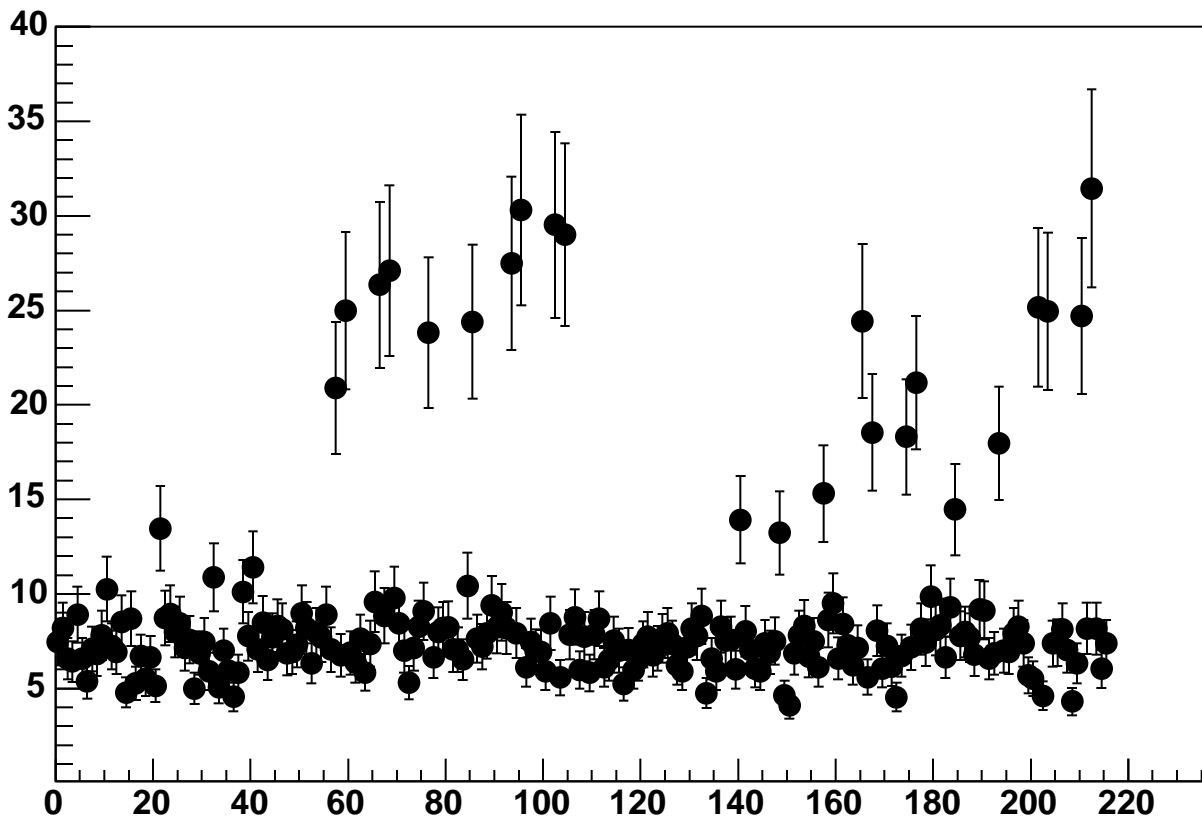
Enable 4, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



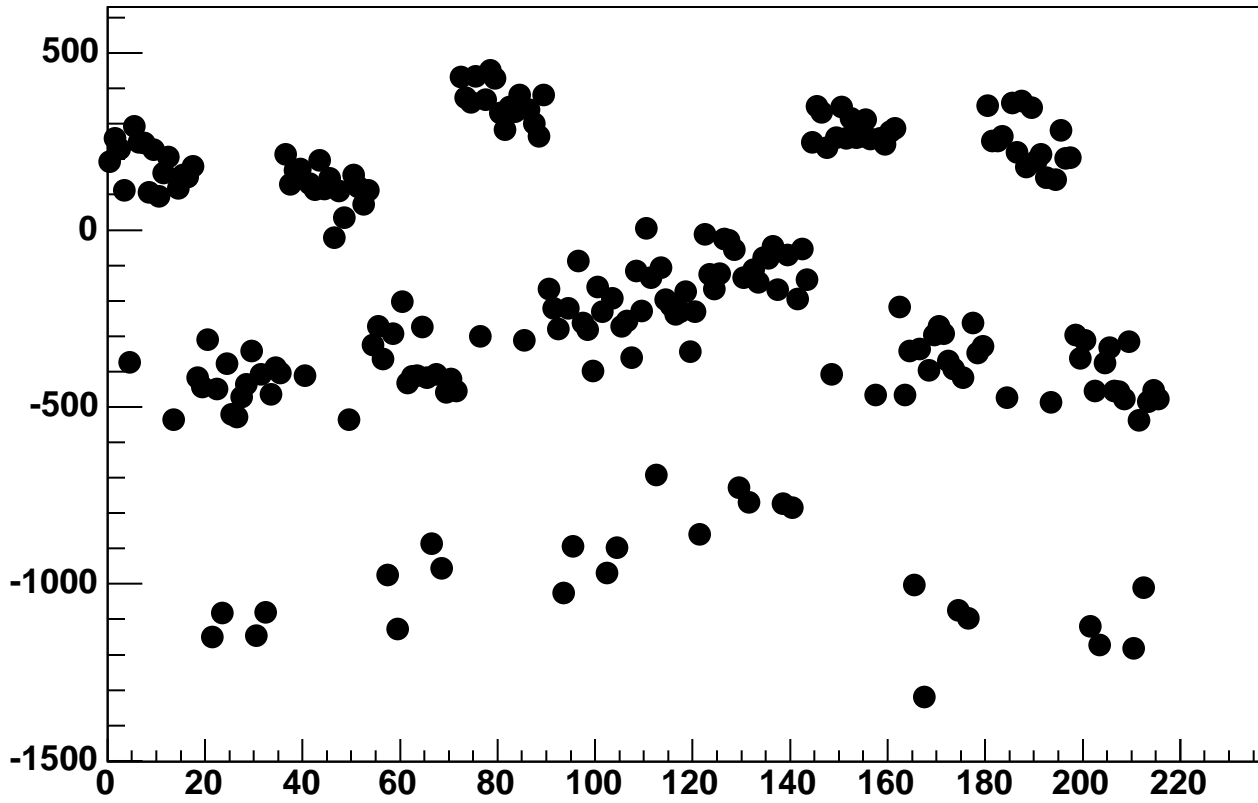
Enable 4, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



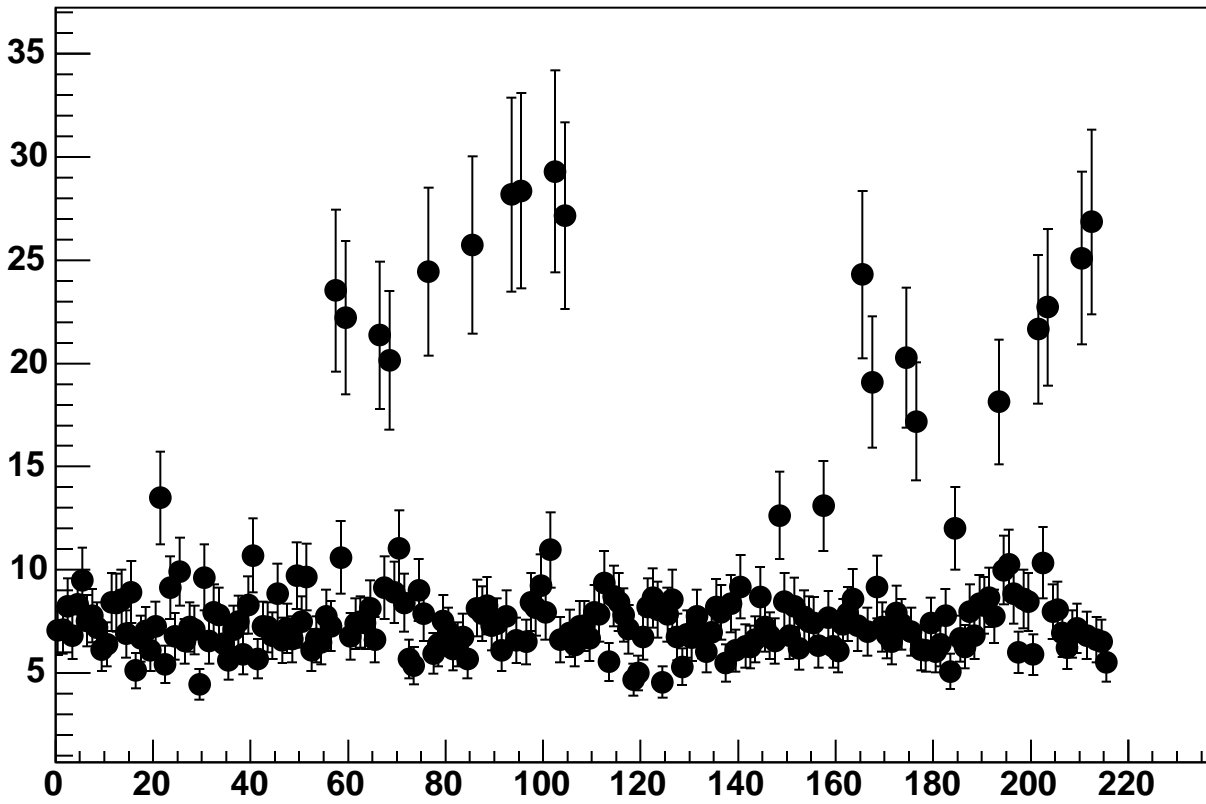
Enable 4, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



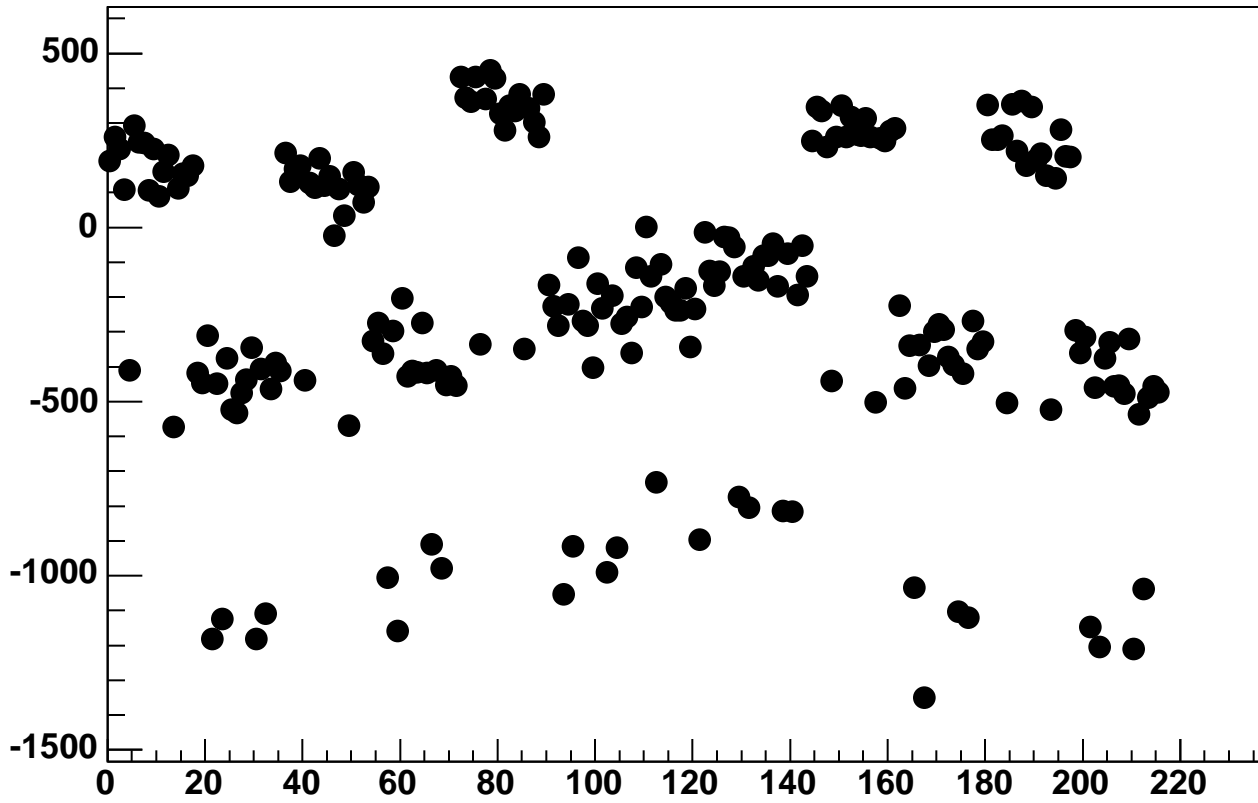
Enable 4, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



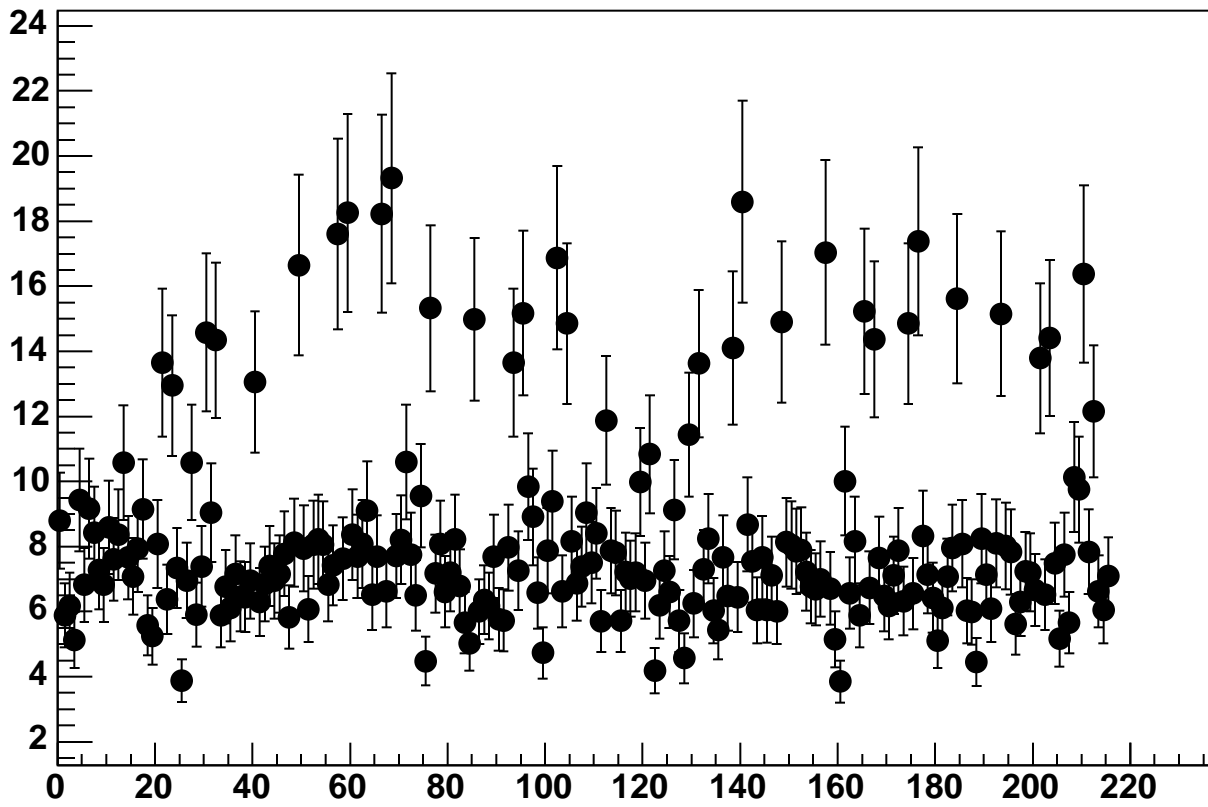
Enable 4, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



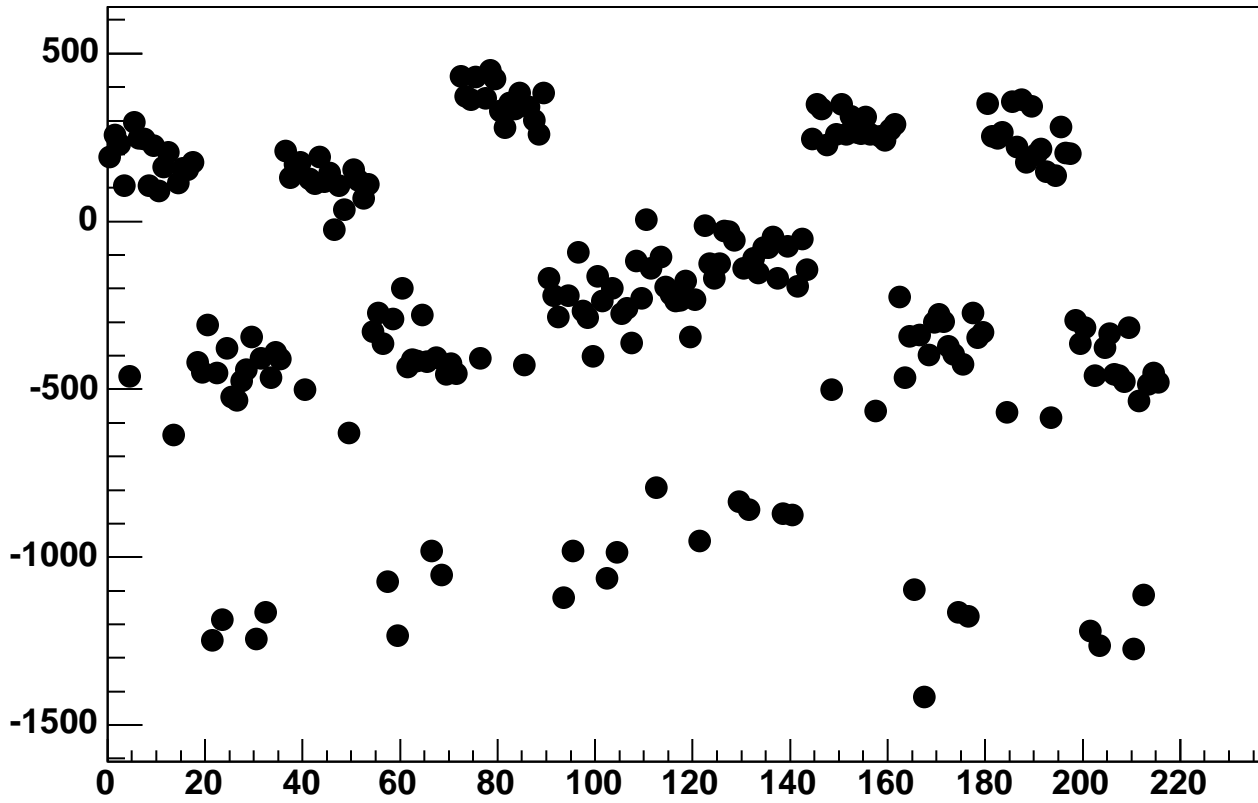
Enable 4, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



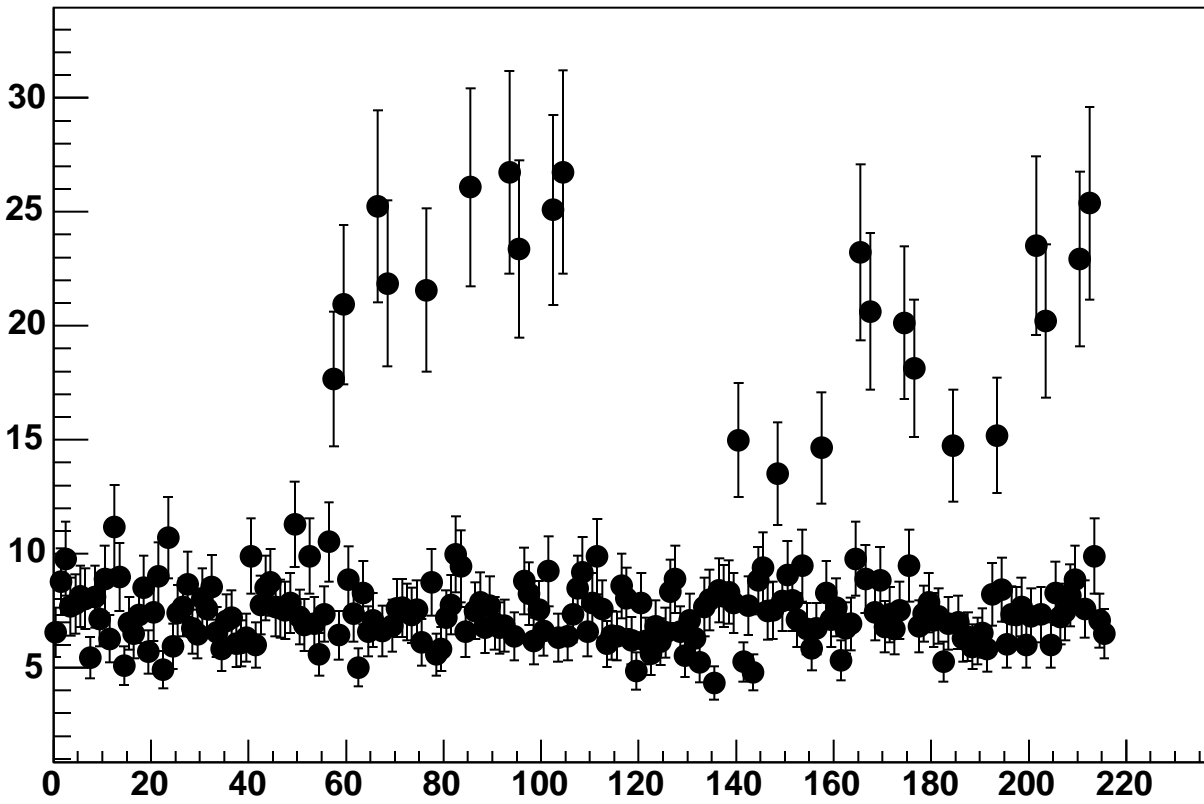
Enable 4, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



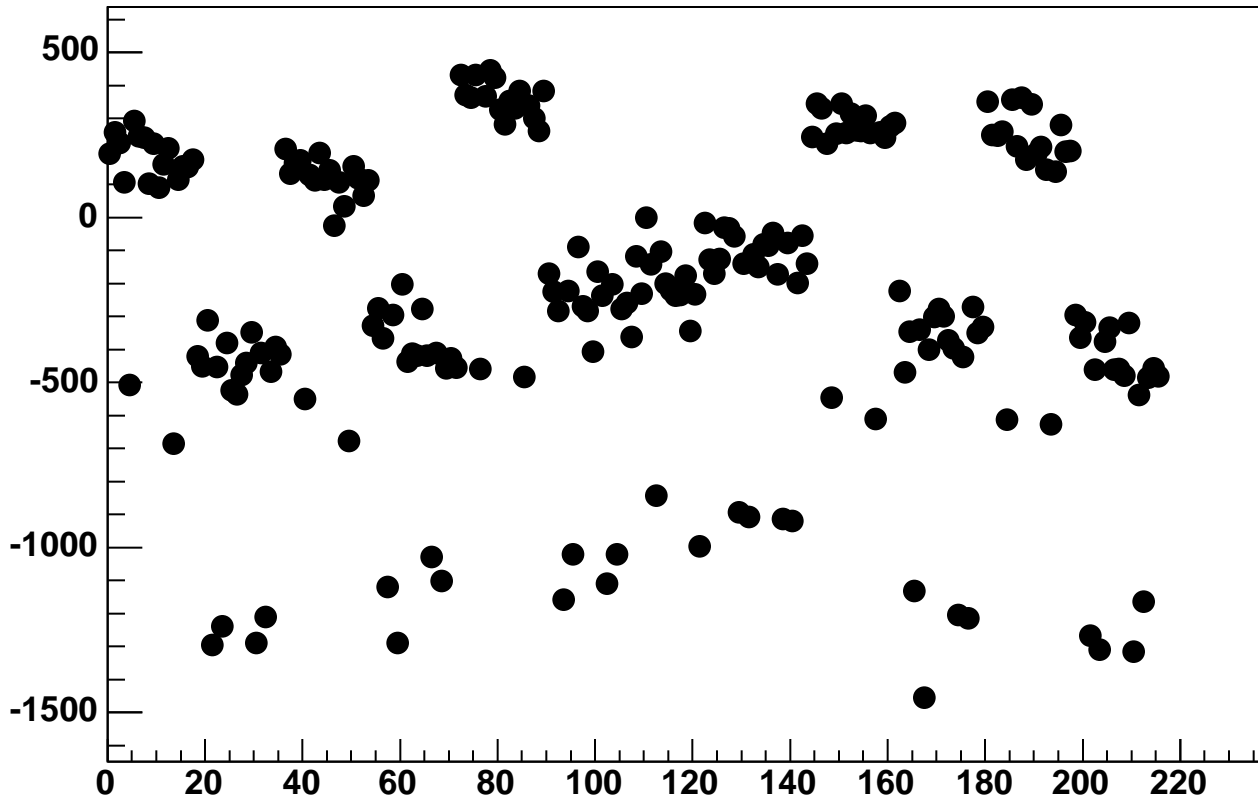
Enable 4, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



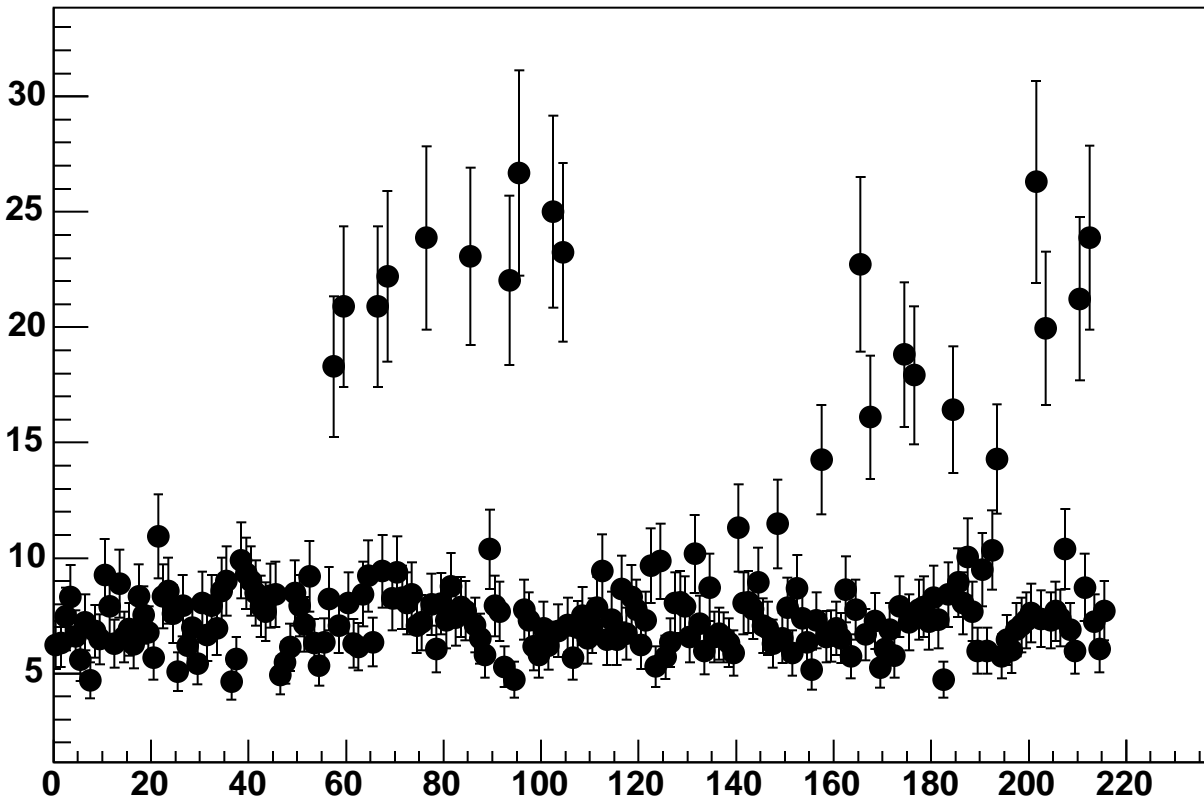
Enable 4, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



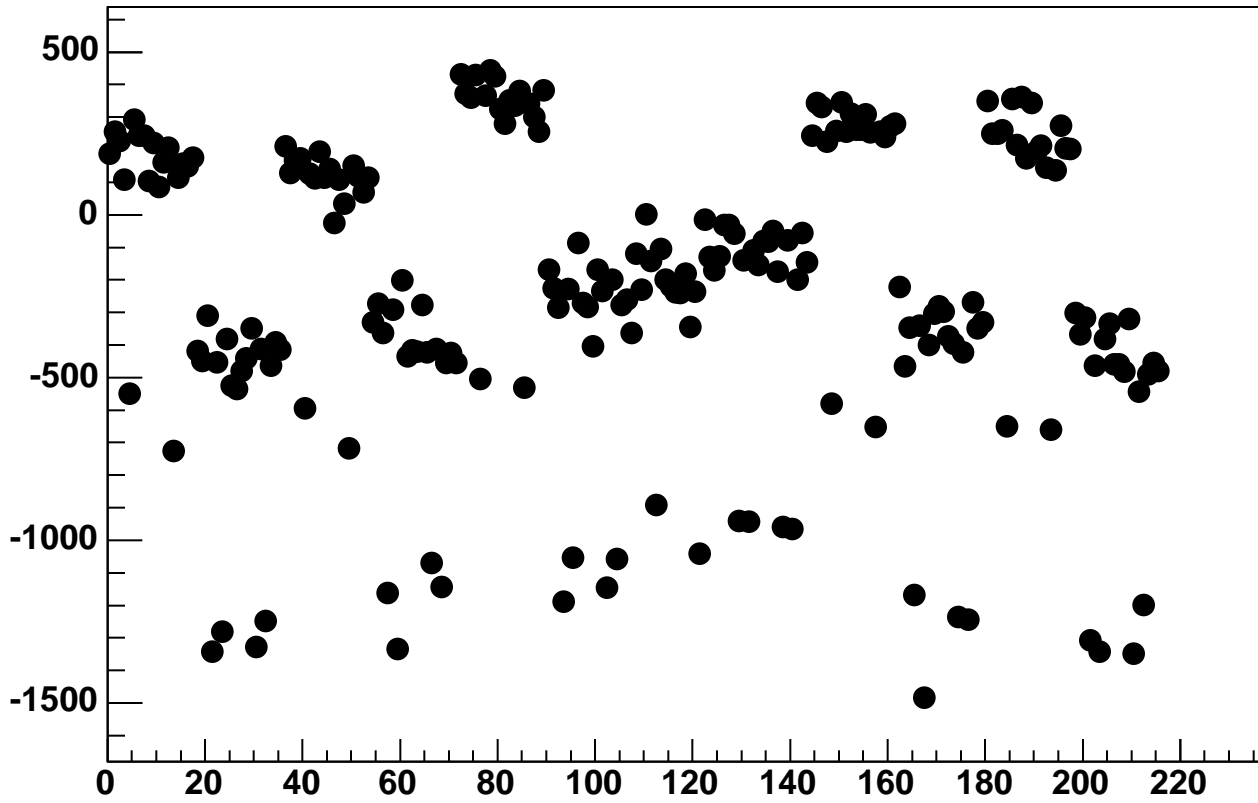
Enable 4, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



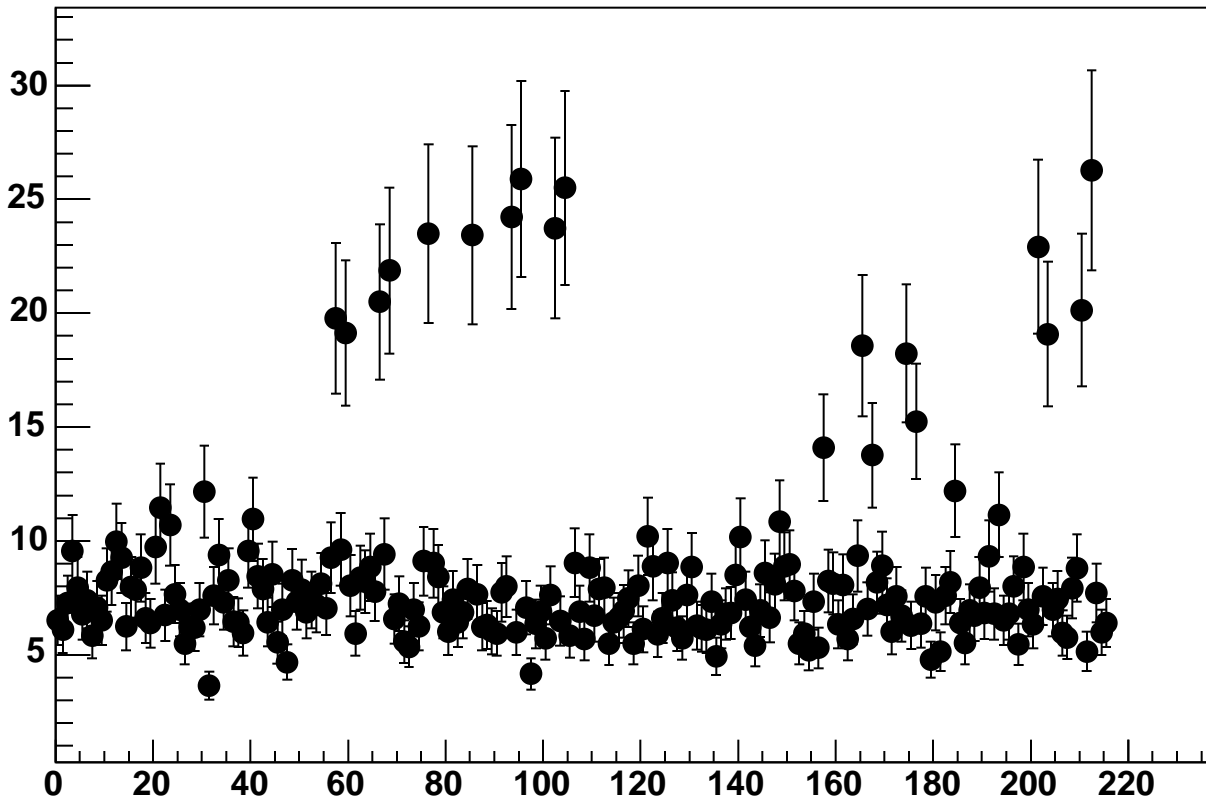
Enable 4, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



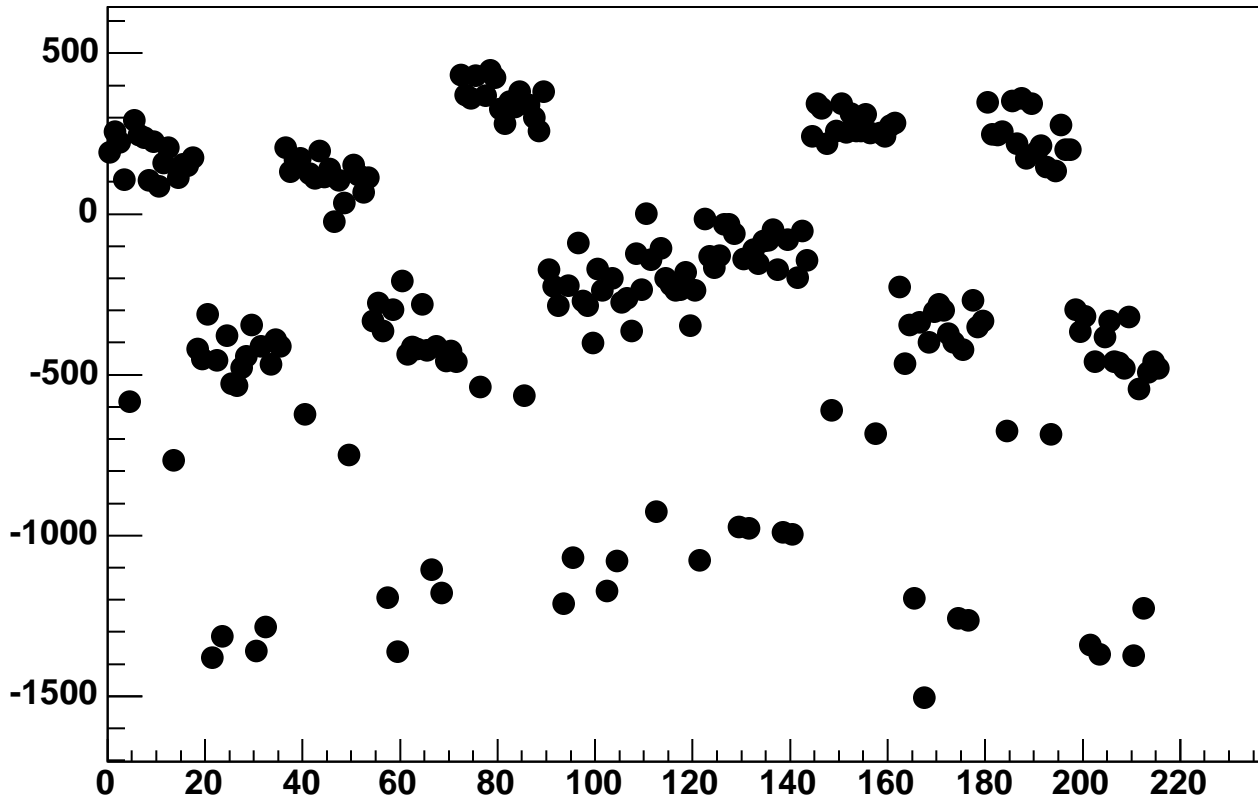
Enable 4, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



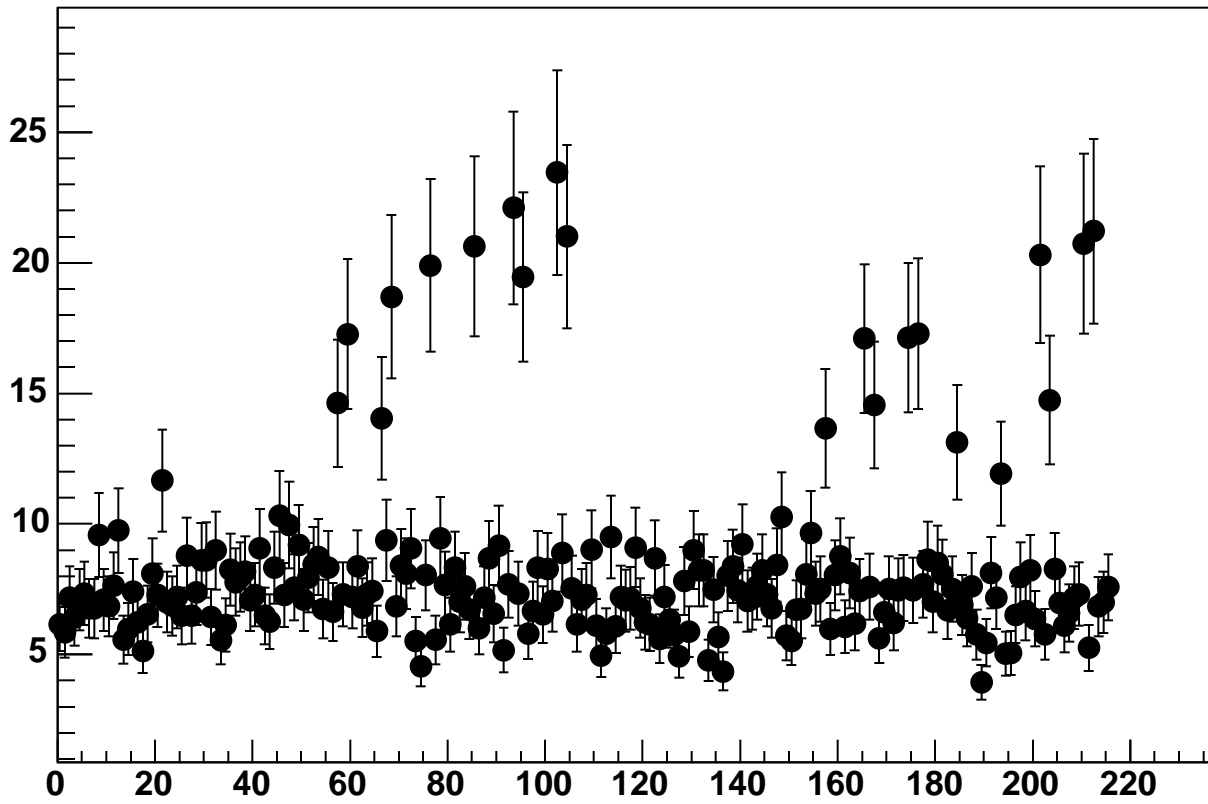
Enable 4, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



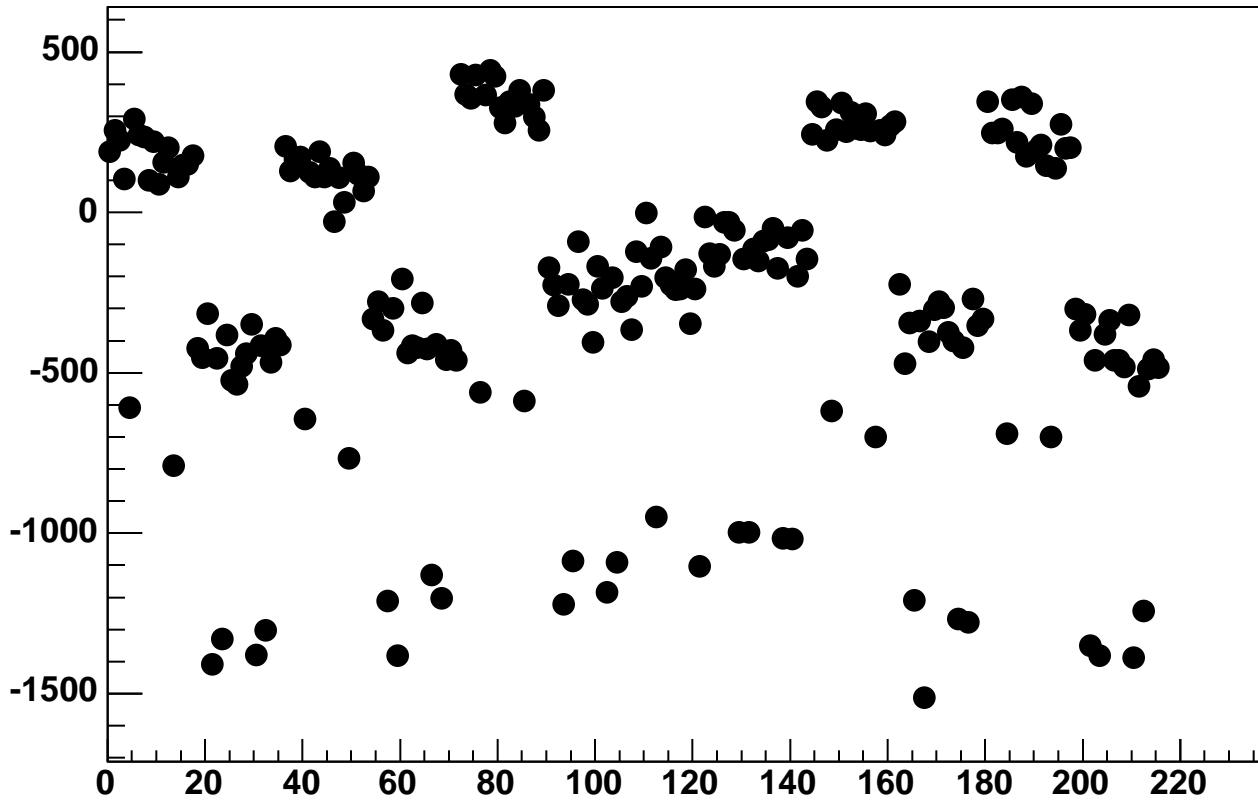
Enable 4, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



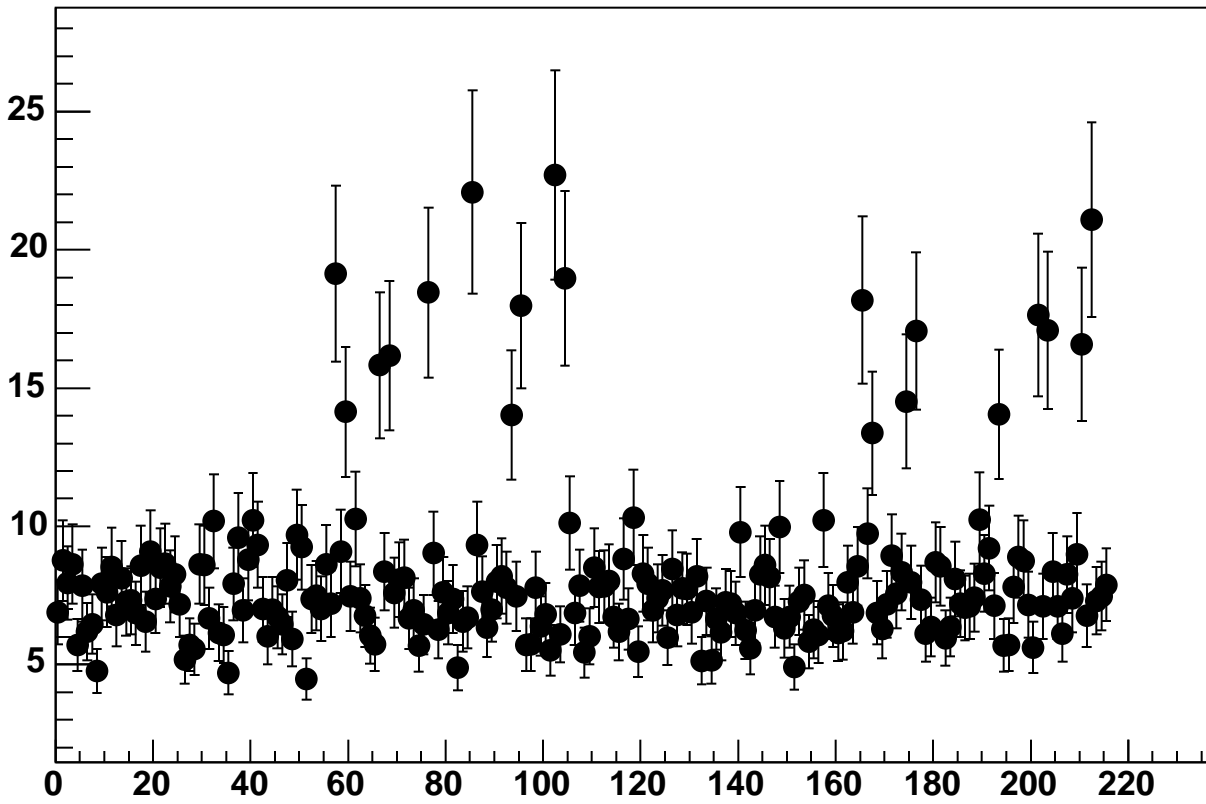
Enable 4, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



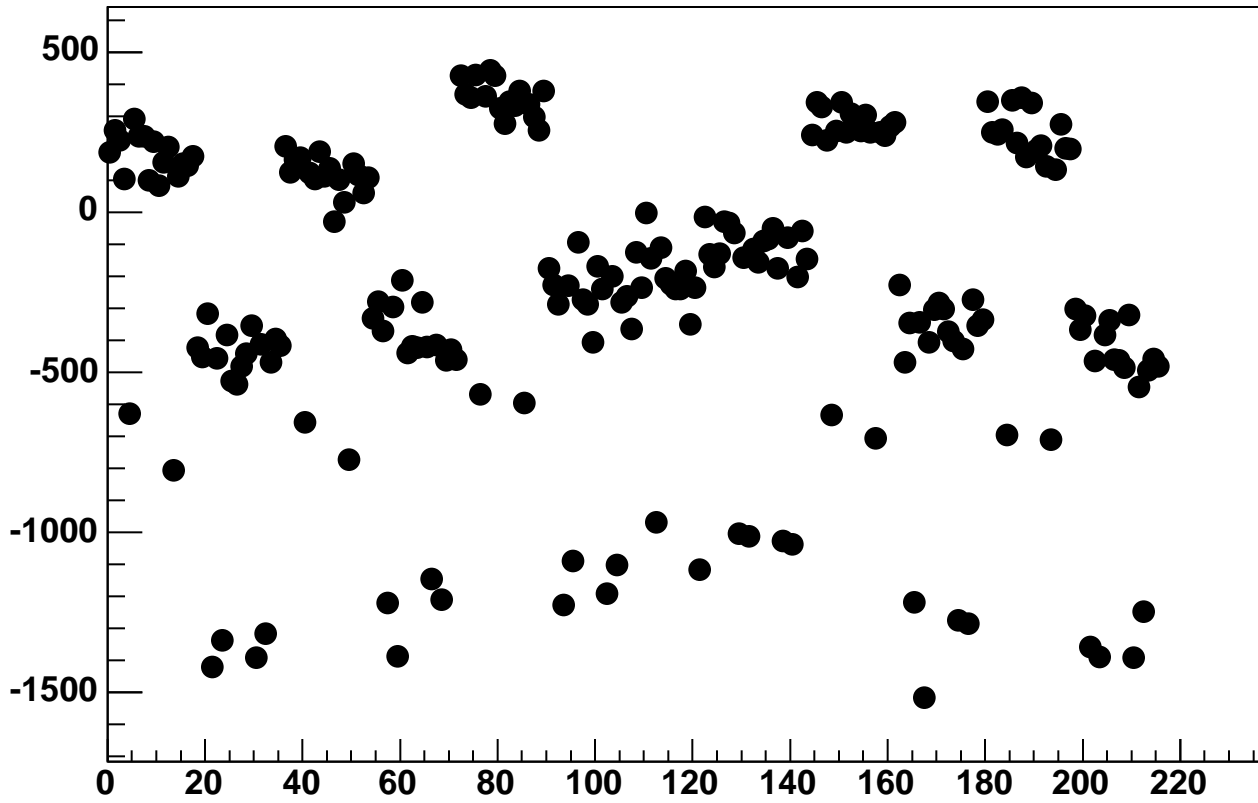
Enable 4, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



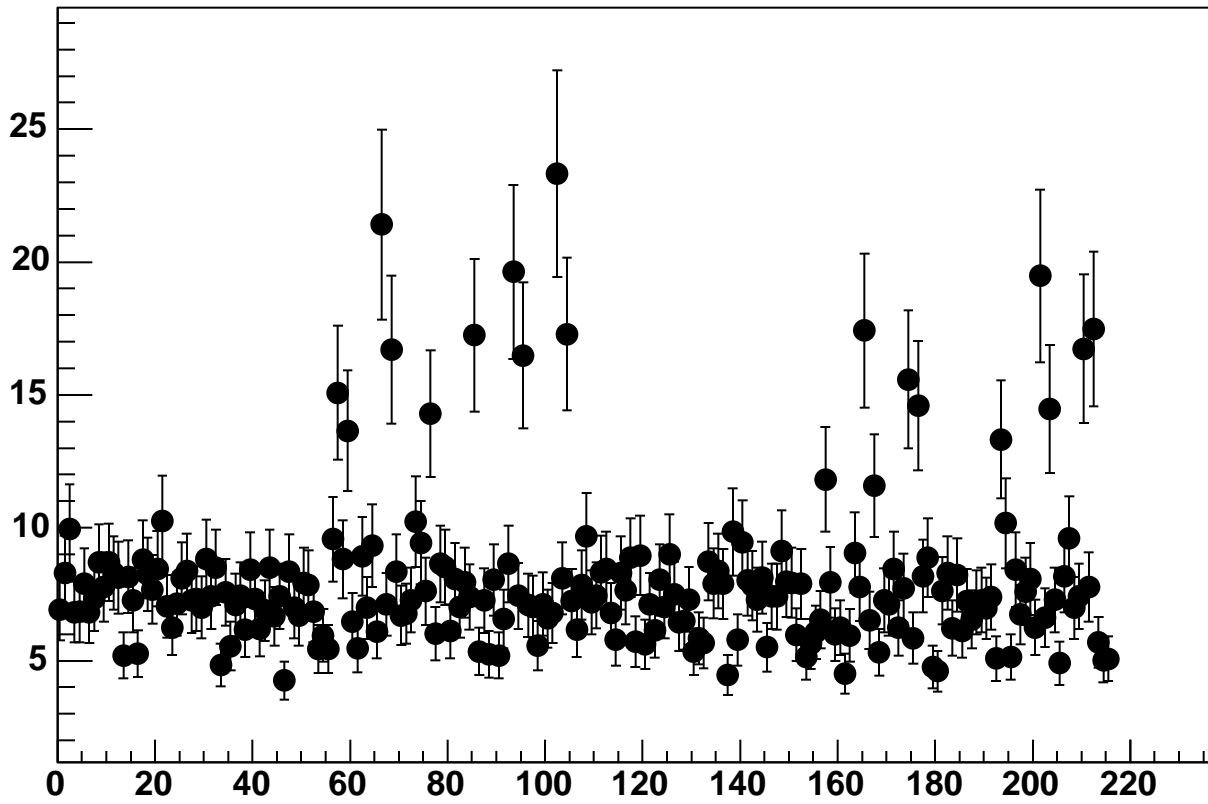
Enable 4, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



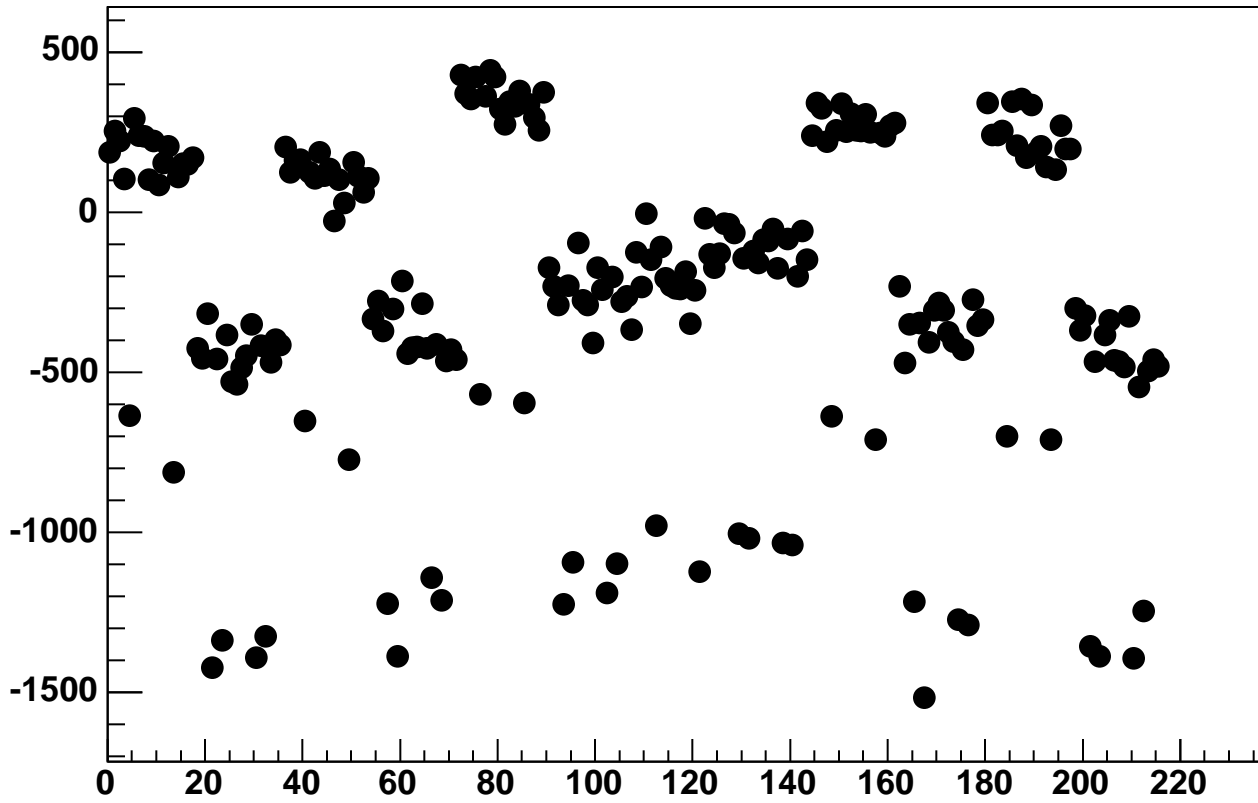
Enable 4, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



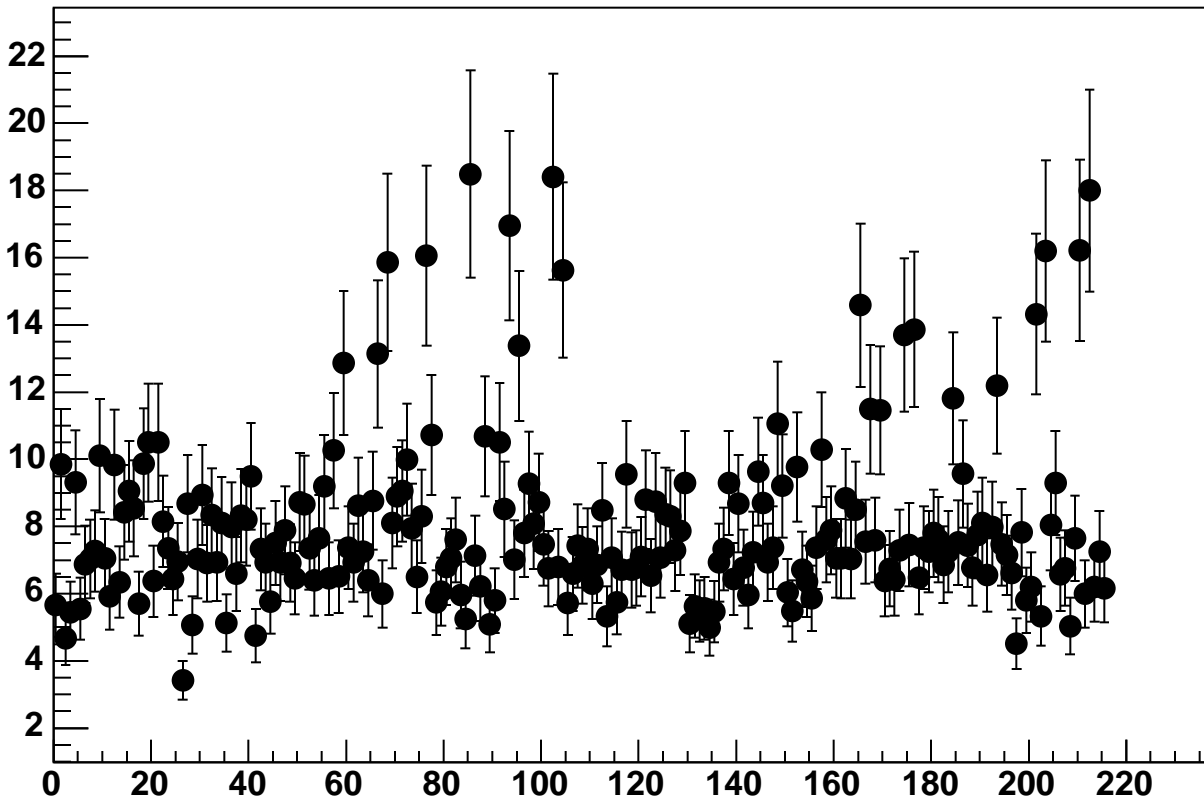
Enable 4, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



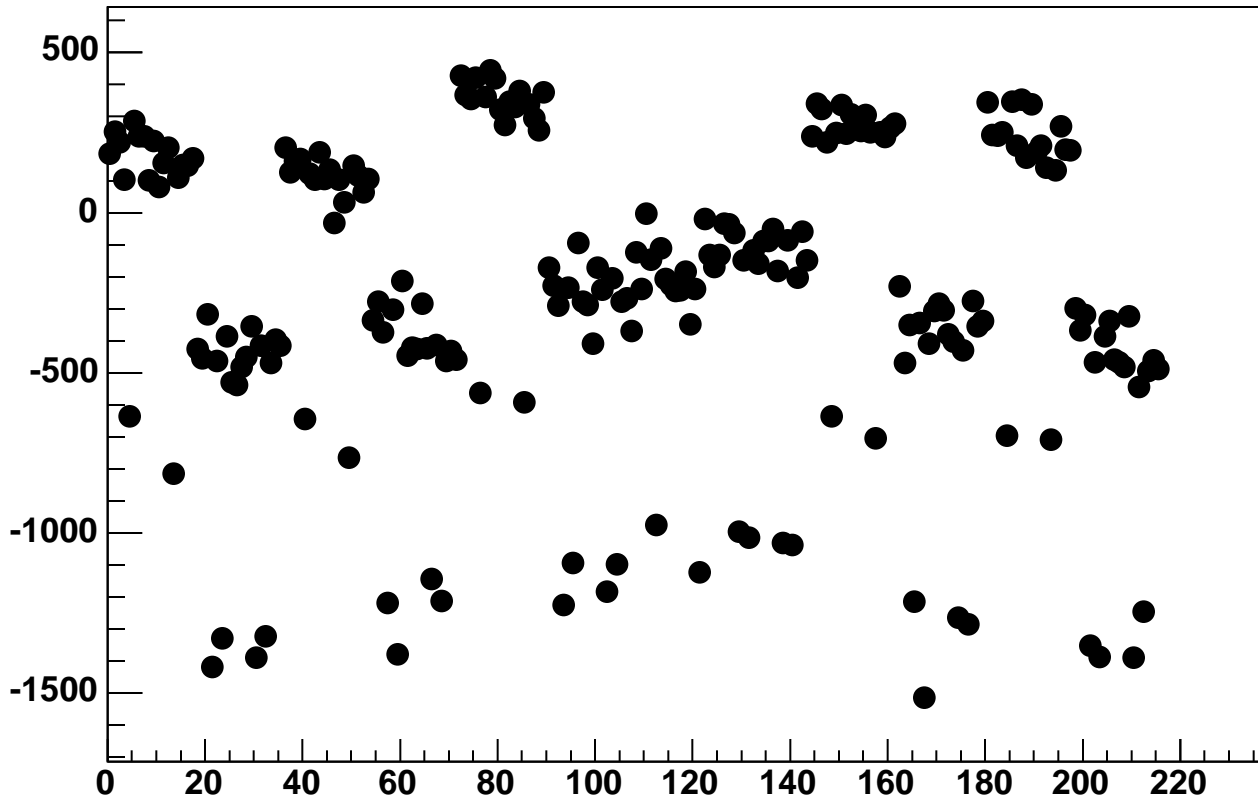
Enable 4, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



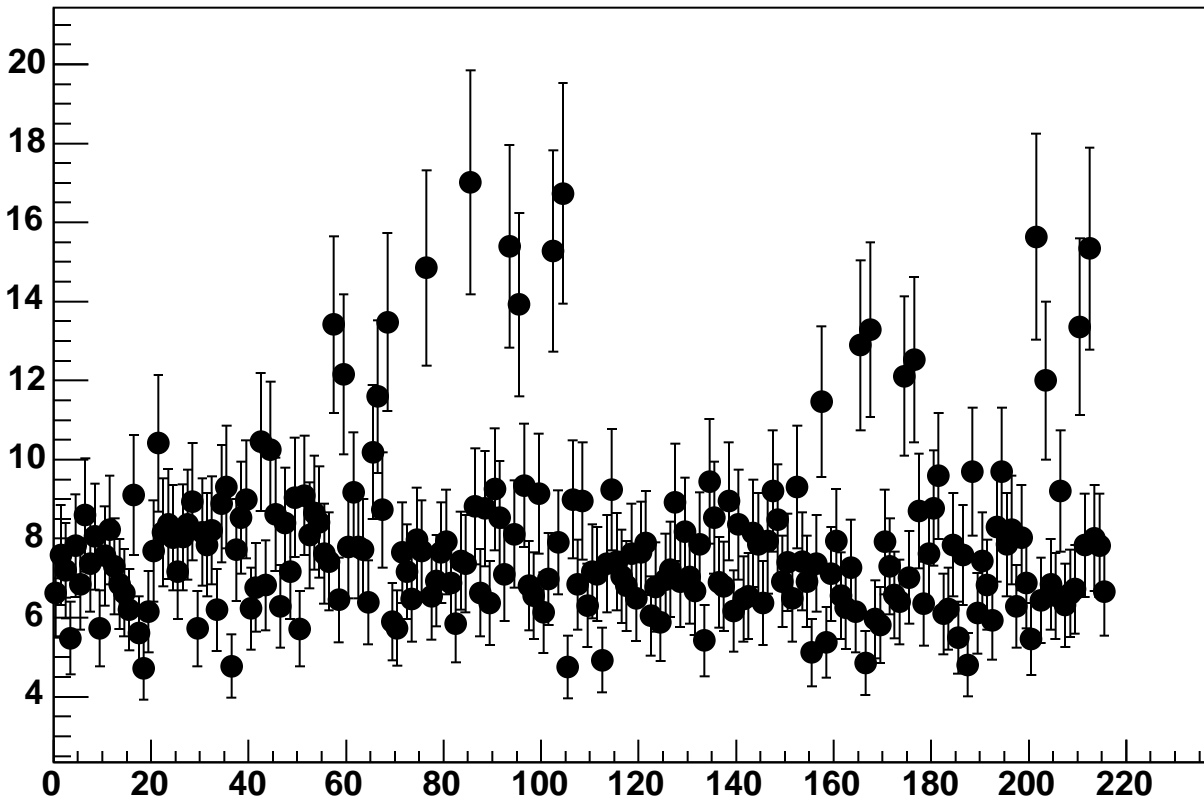
Enable 4, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



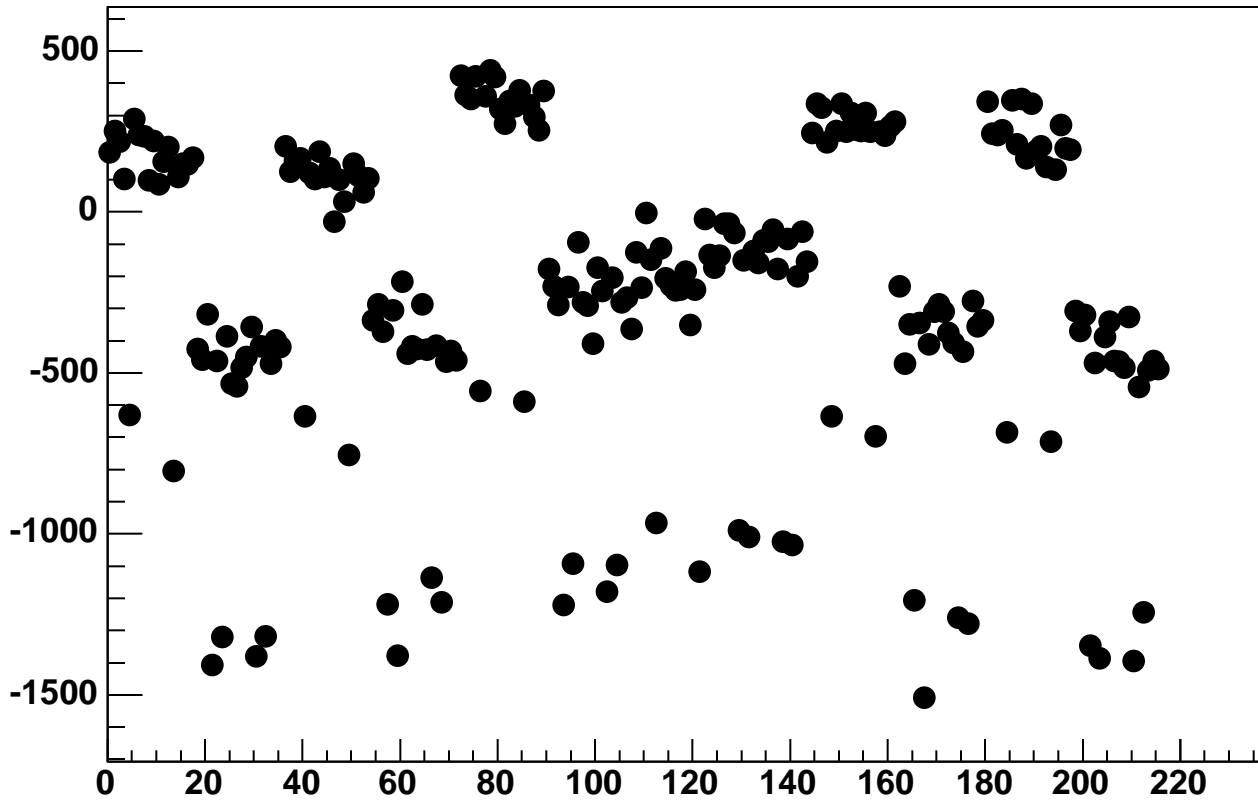
Enable 4, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



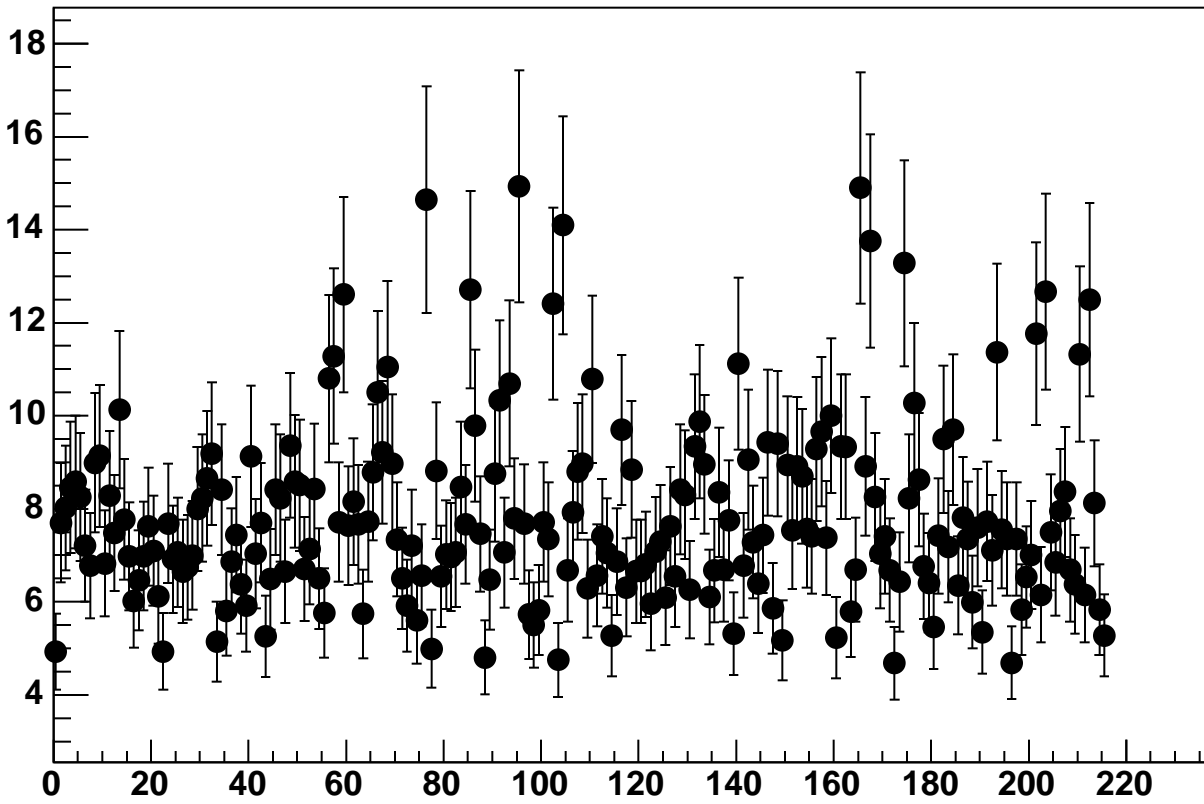
Enable 4, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



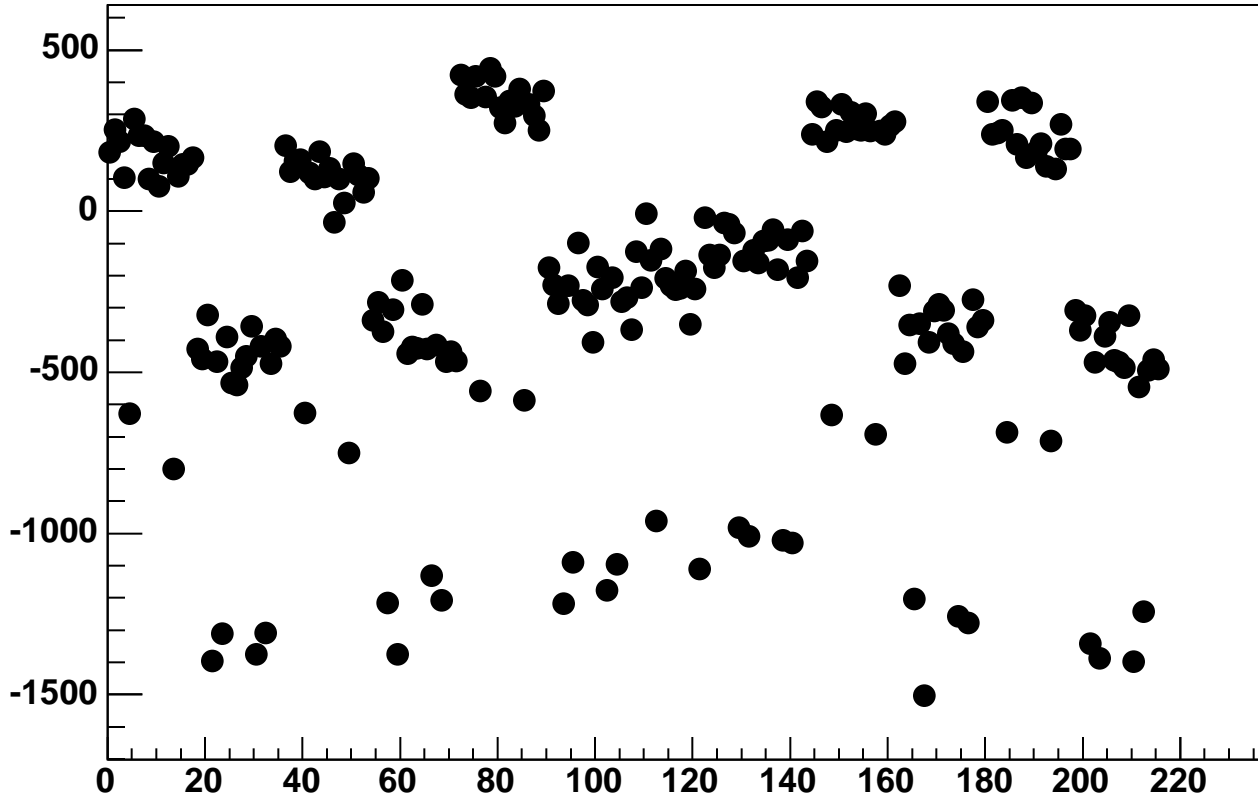
Enable 4, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



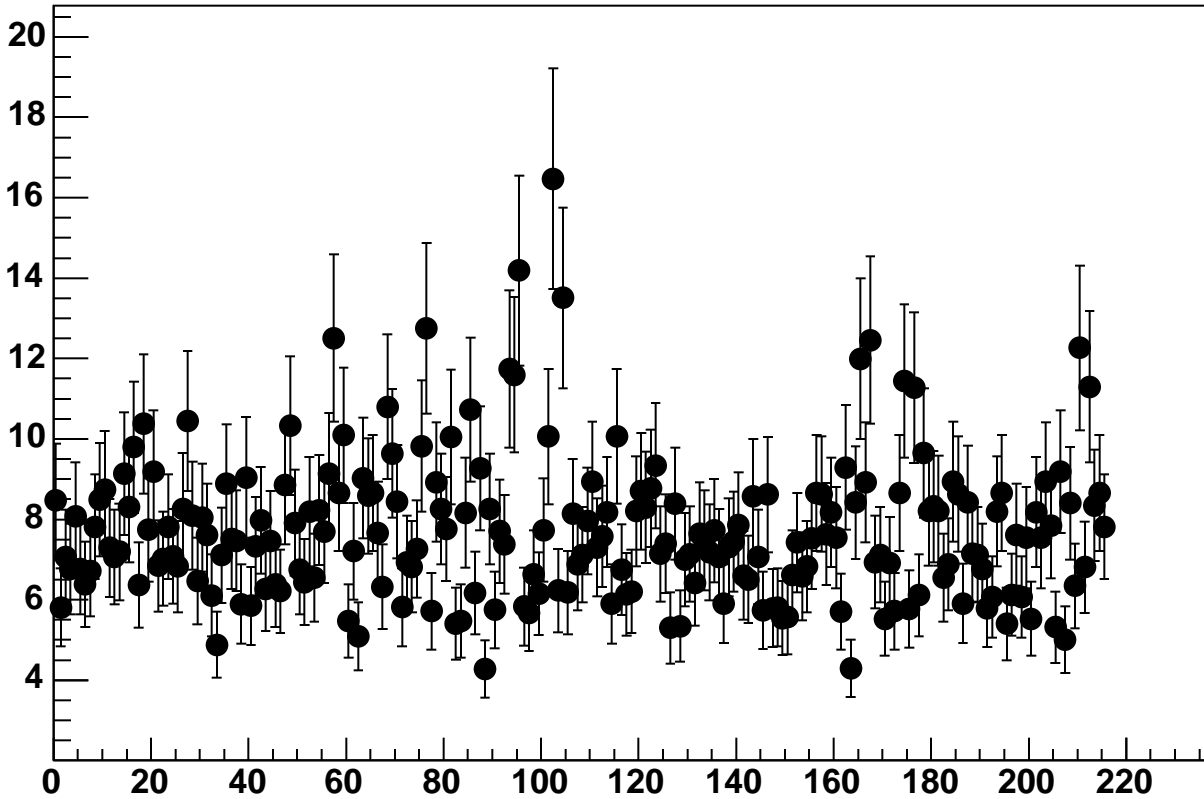
Enable 4, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan



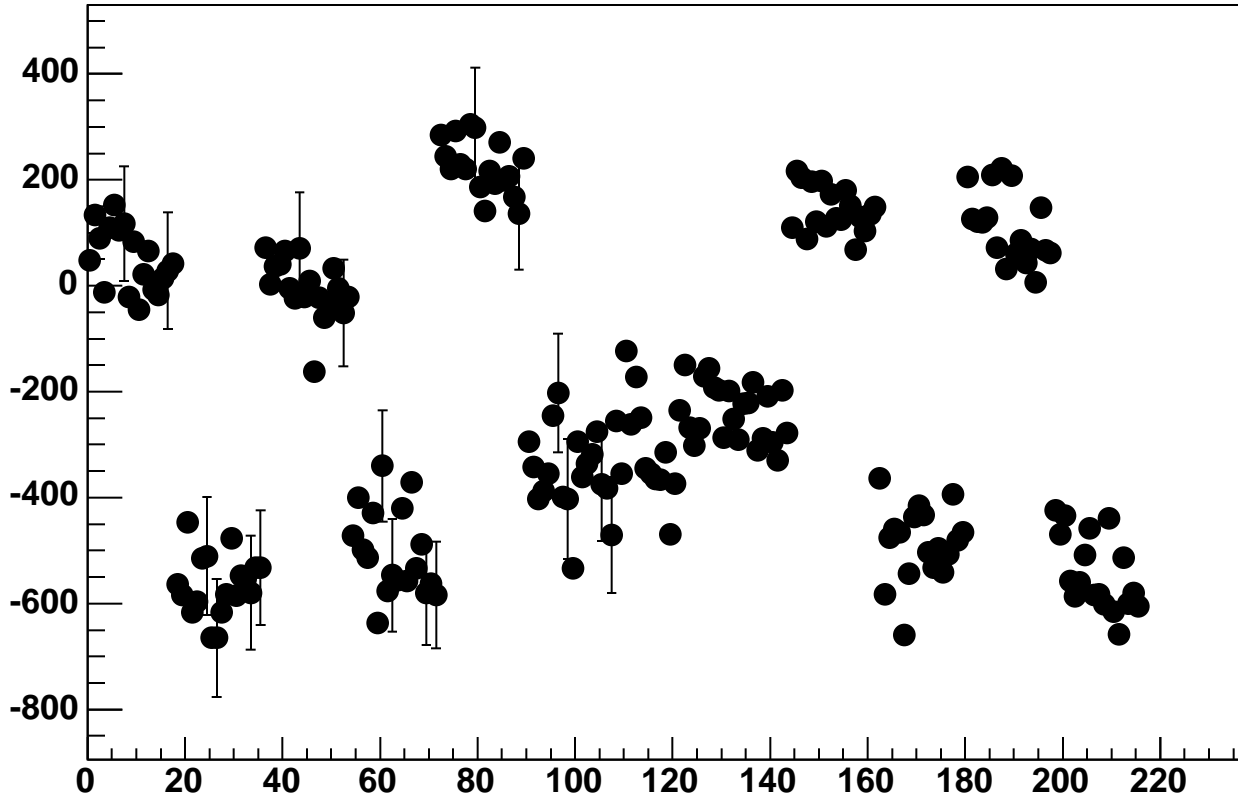
Enable 4, DAC=4000, Hold=245, ADC Mean vs 18*Chip+Chan



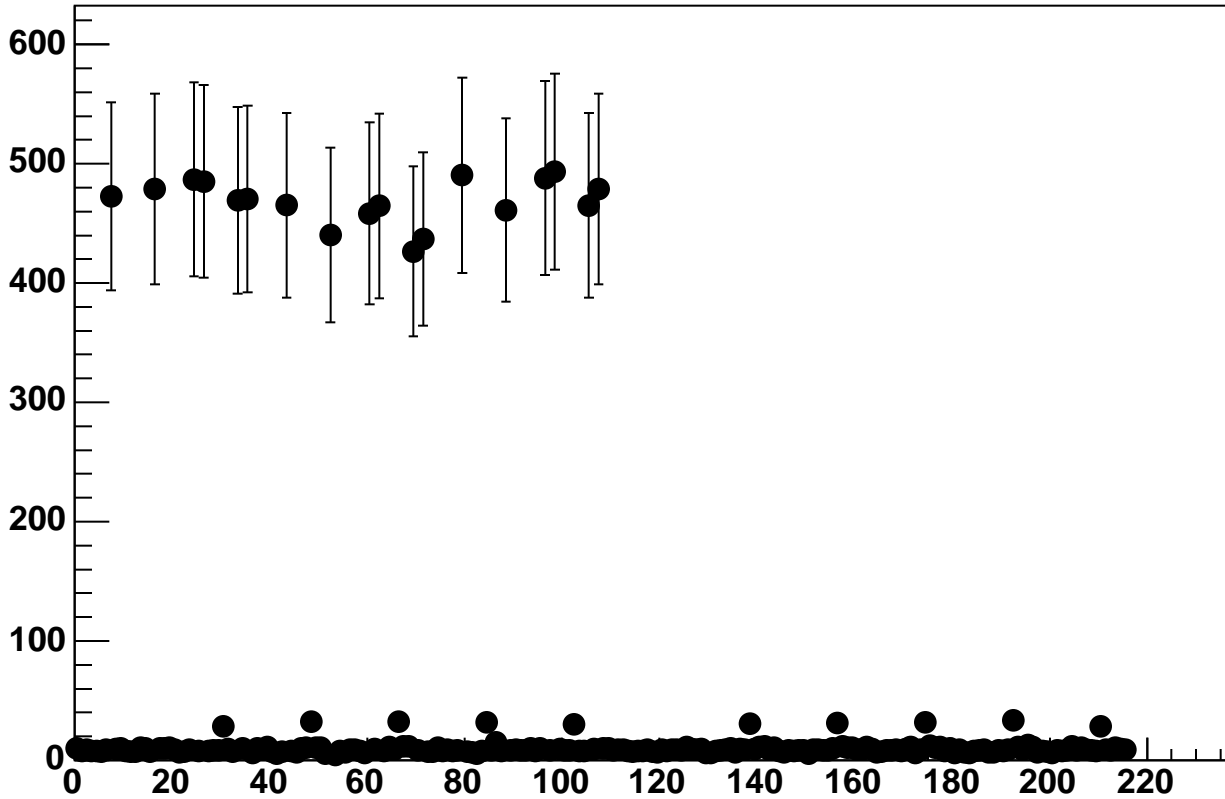
Enable 4, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



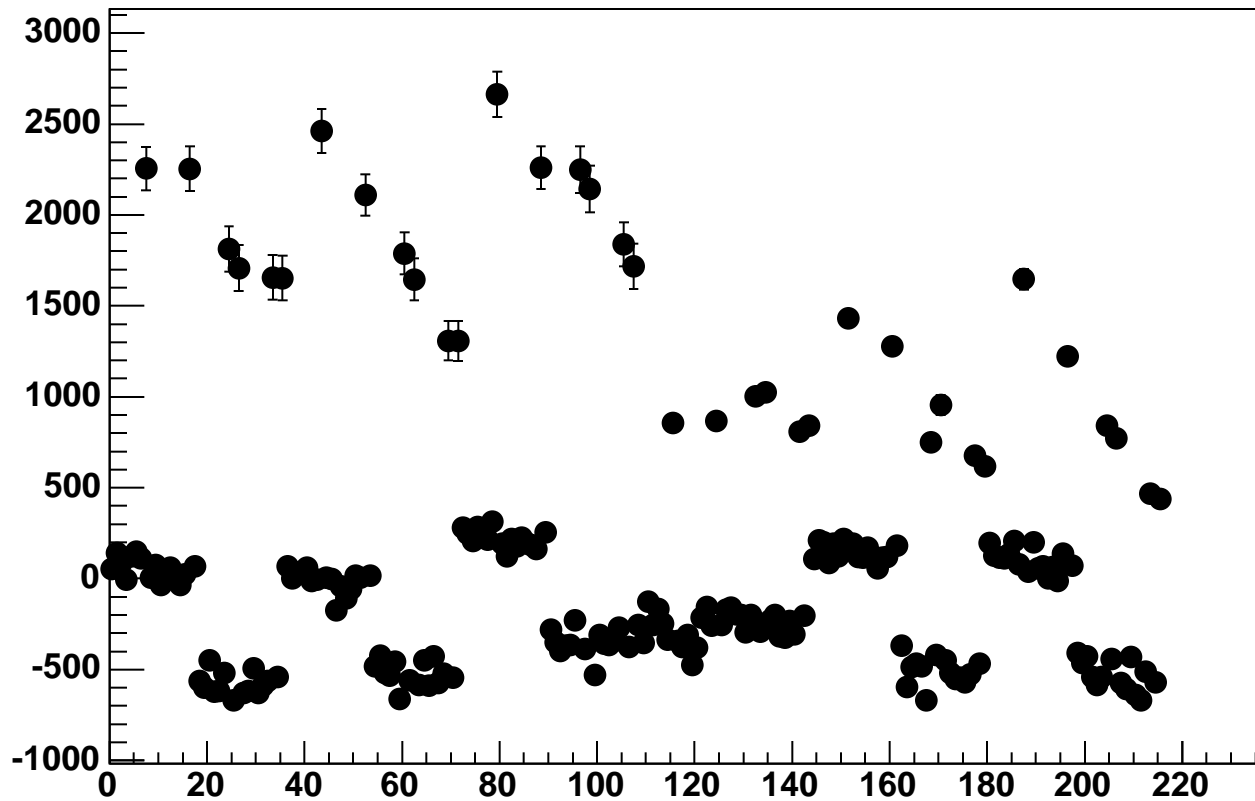
Enable 5, DAC=4000, Hold=0, ADC Mean vs 18*Chip+Chan



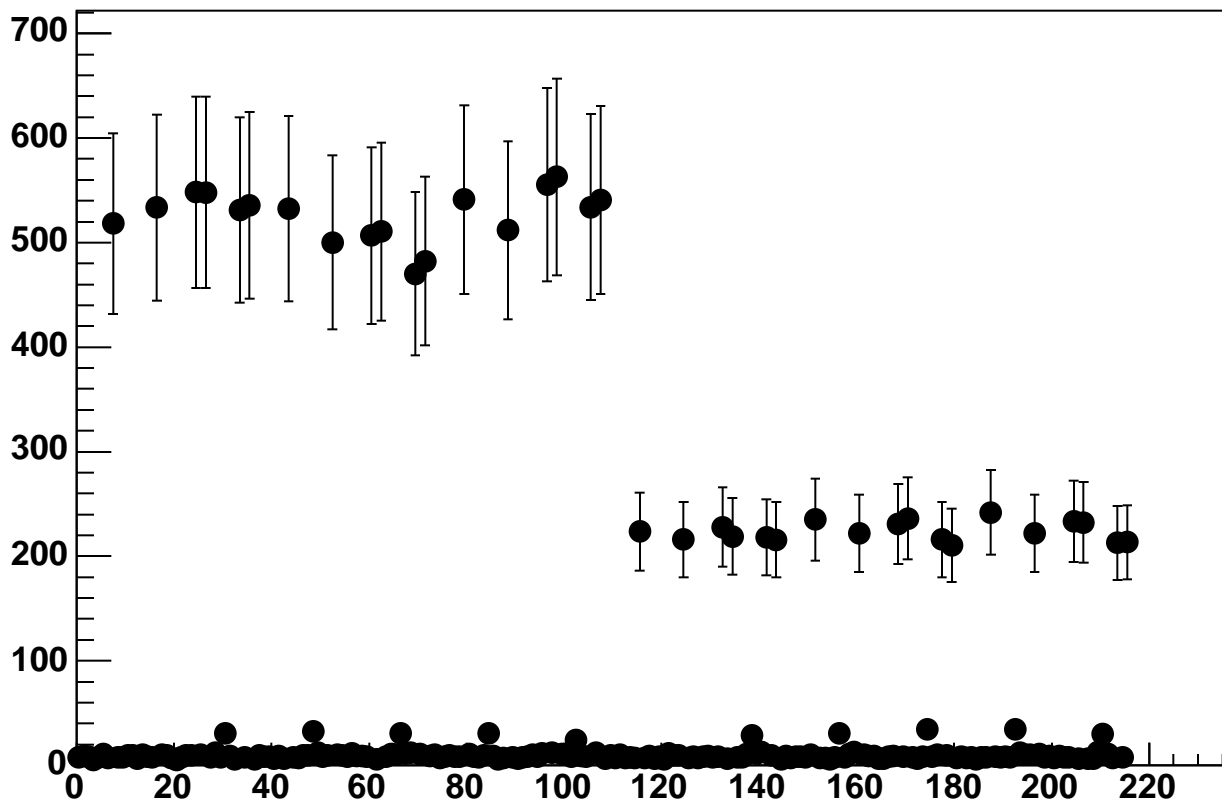
Enable 5, DAC=4000, Hold=0, ADC Noise vs 18*Chip+Chan



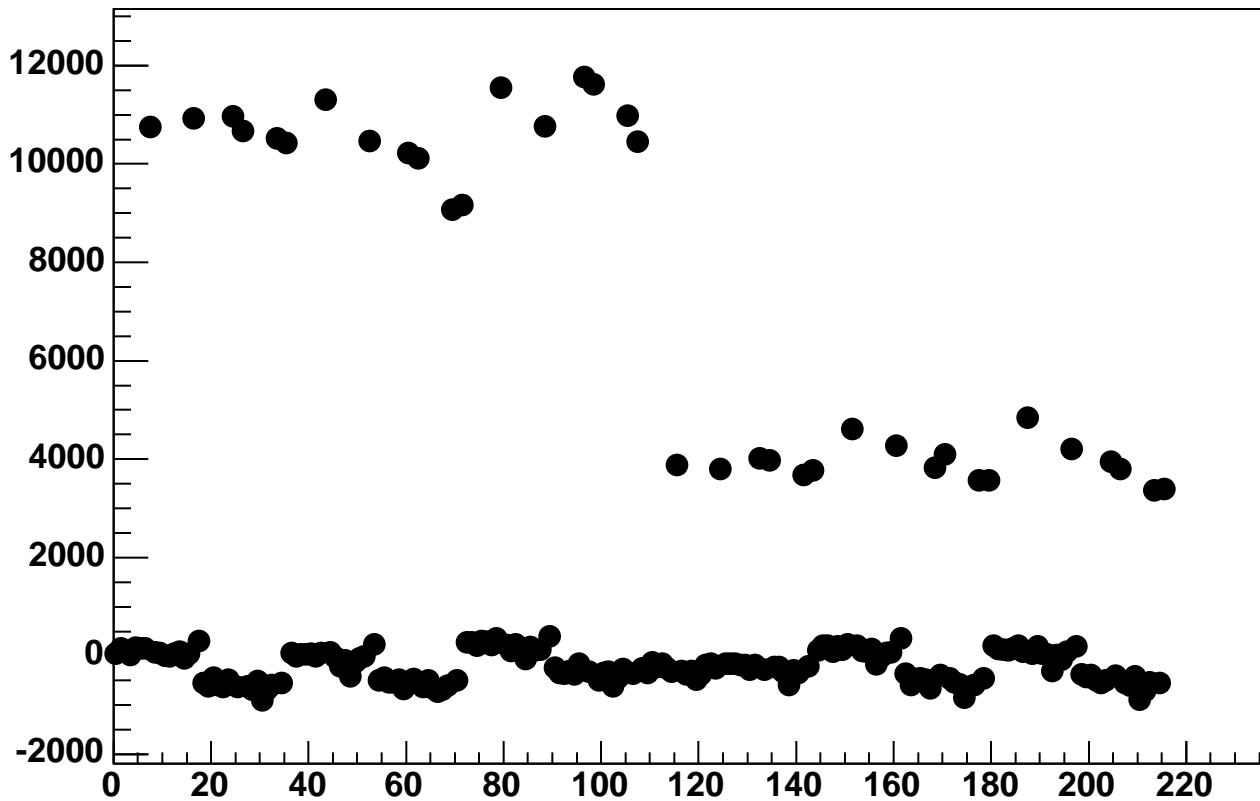
Enable 5, DAC=4000, Hold=5, ADC Mean vs 18*Chip+Chan



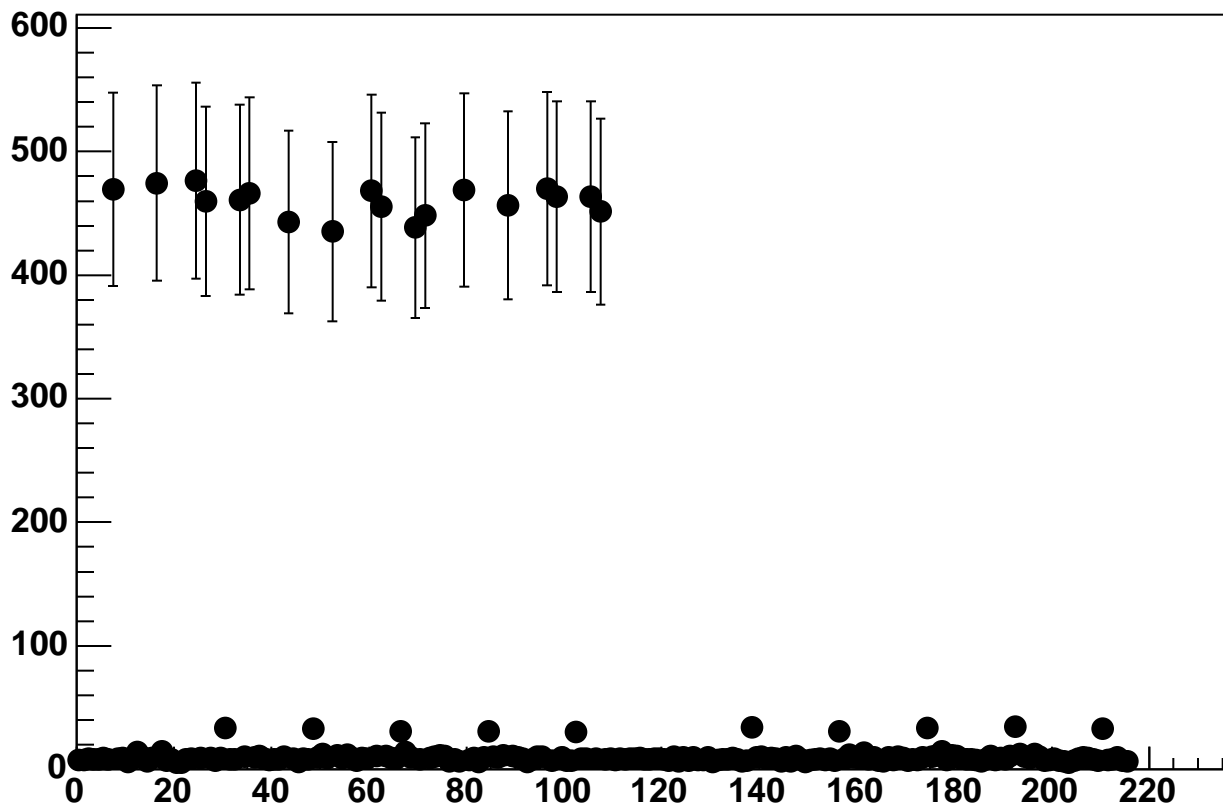
Enable 5, DAC=4000, Hold=5, ADC Noise vs 18*Chip+Chan



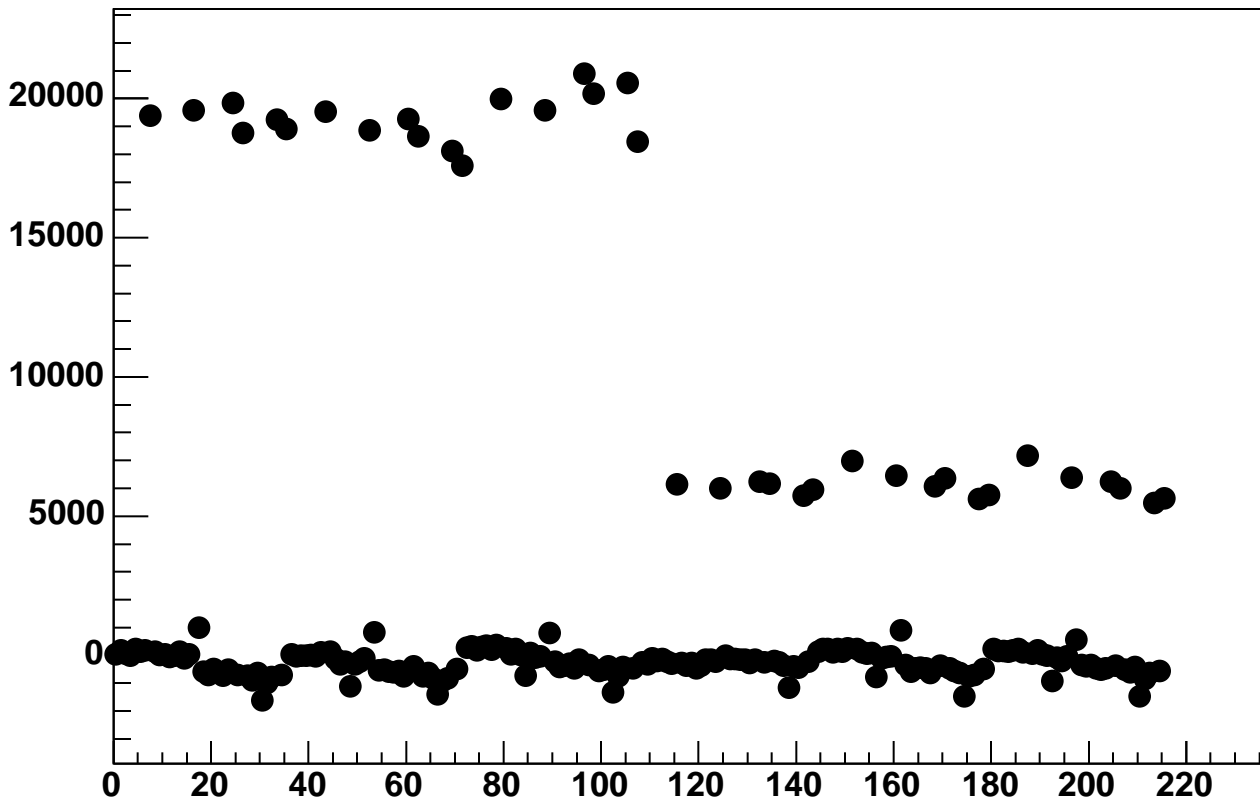
Enable 5, DAC=4000, Hold=10, ADC Mean vs 18*Chip+Chan



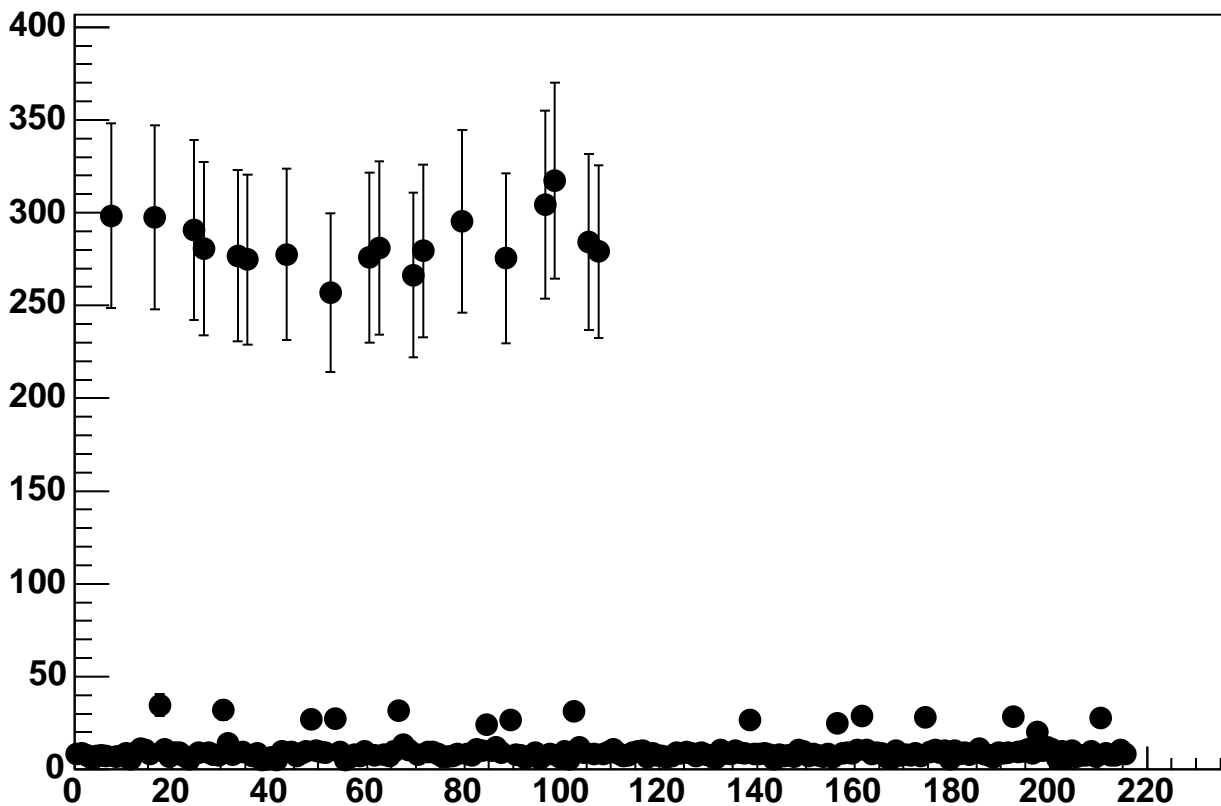
Enable 5, DAC=4000, Hold=10, ADC Noise vs 18*Chip+Chan



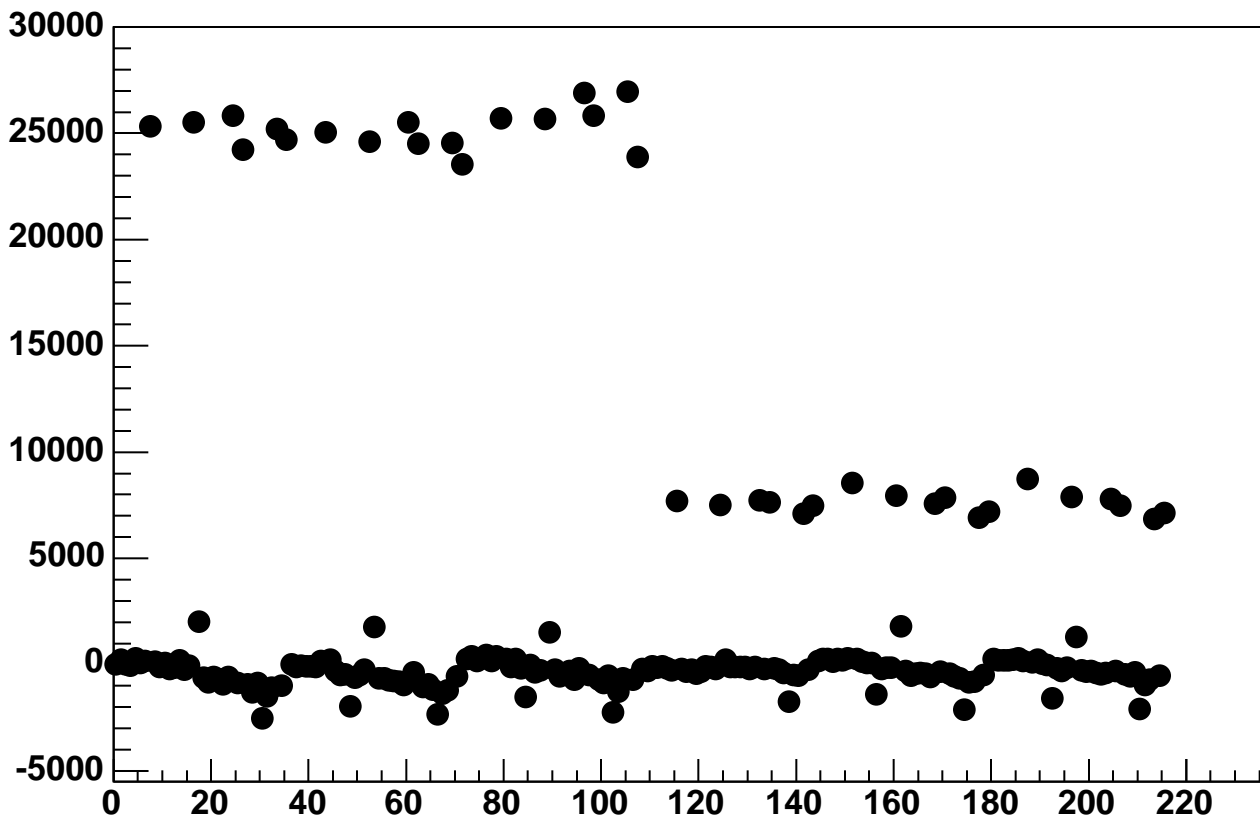
Enable 5, DAC=4000, Hold=15, ADC Mean vs 18*Chip+Chan



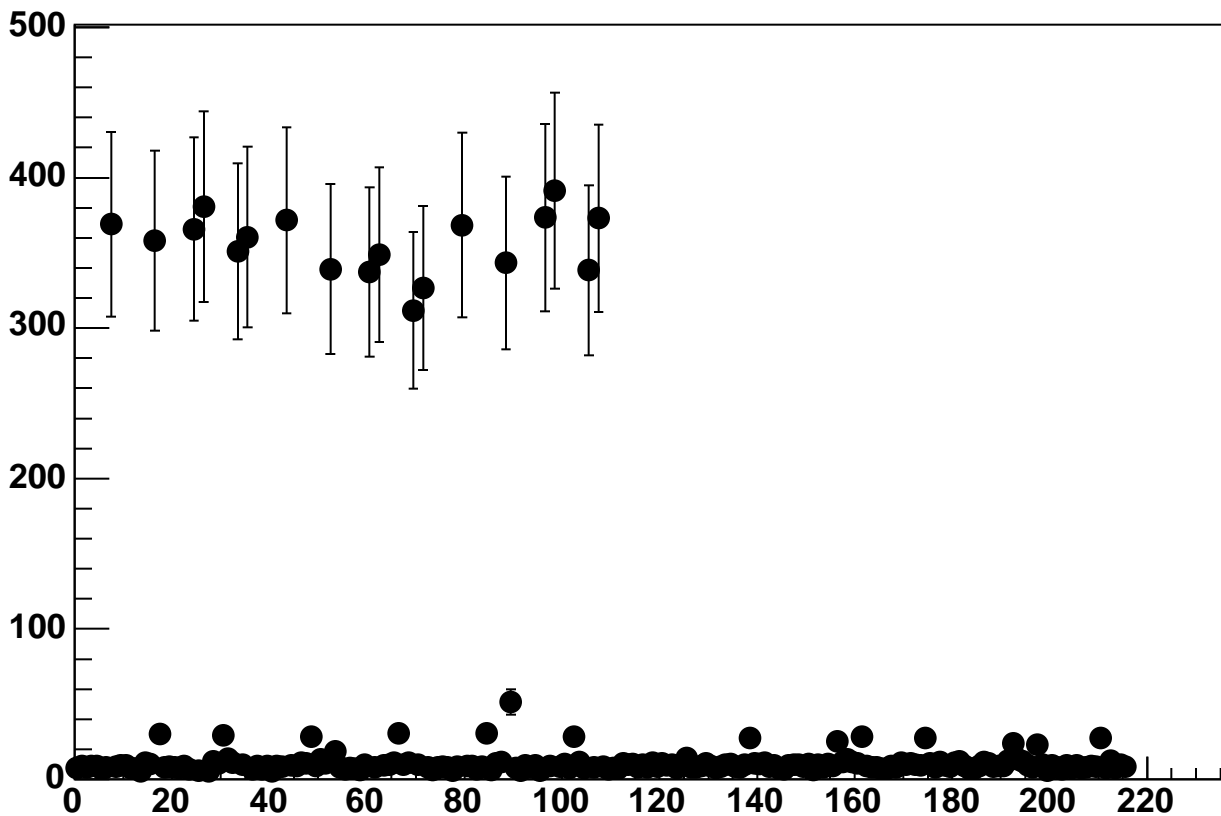
Enable 5, DAC=4000, Hold=15, ADC Noise vs 18*Chip+Chan



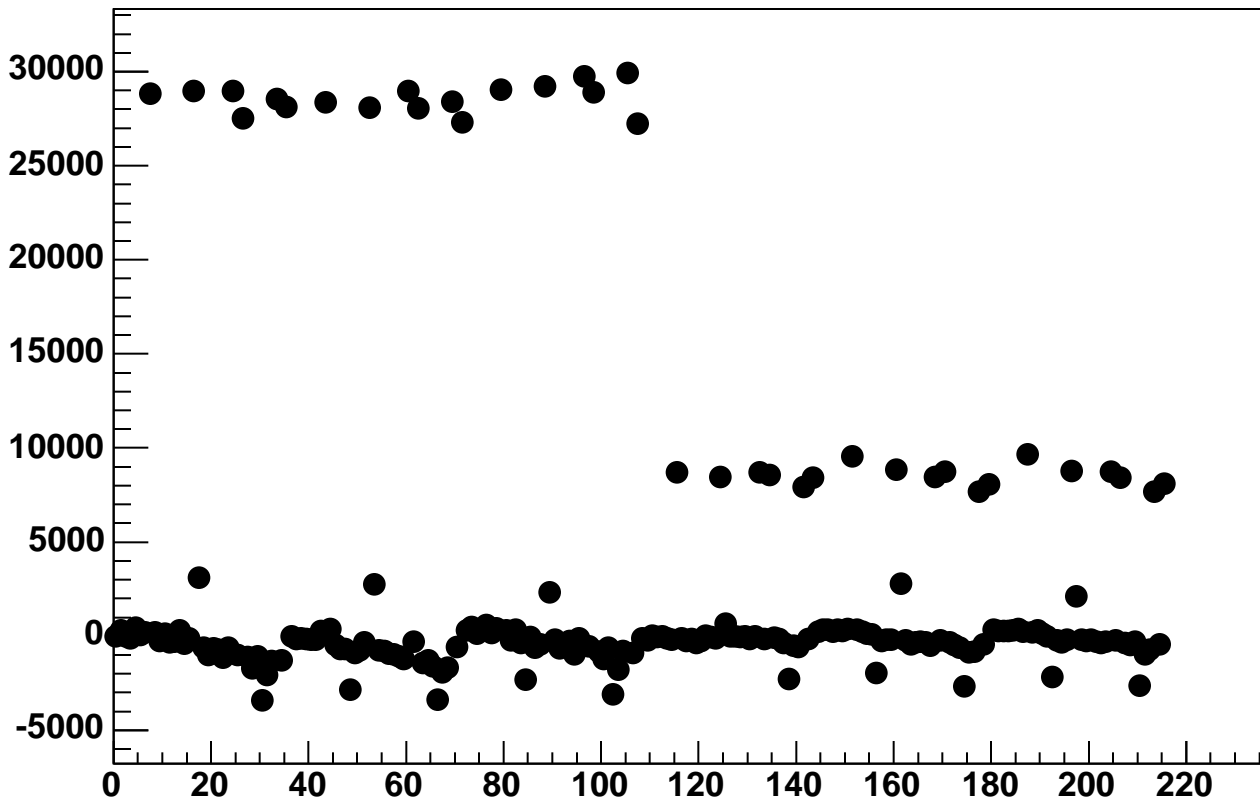
Enable 5, DAC=4000, Hold=20, ADC Mean vs 18*Chip+Chan



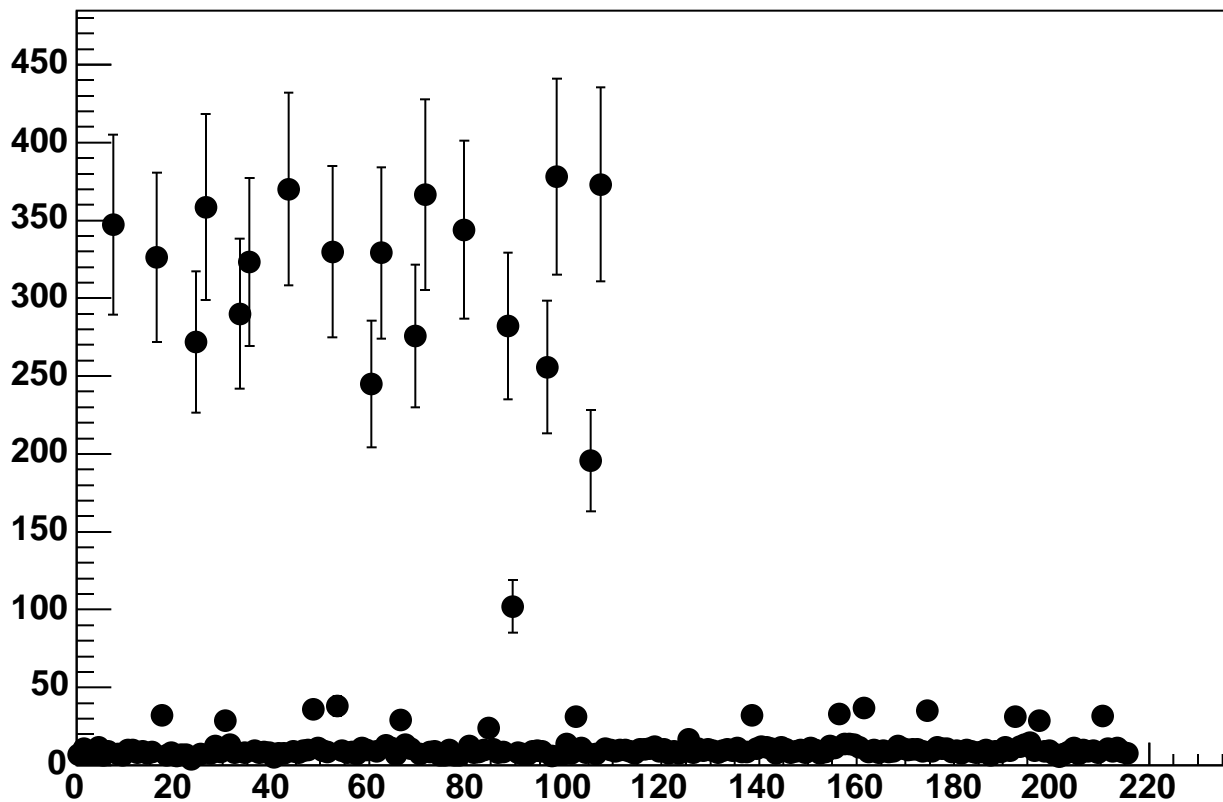
Enable 5, DAC=4000, Hold=20, ADC Noise vs 18*Chip+Chan



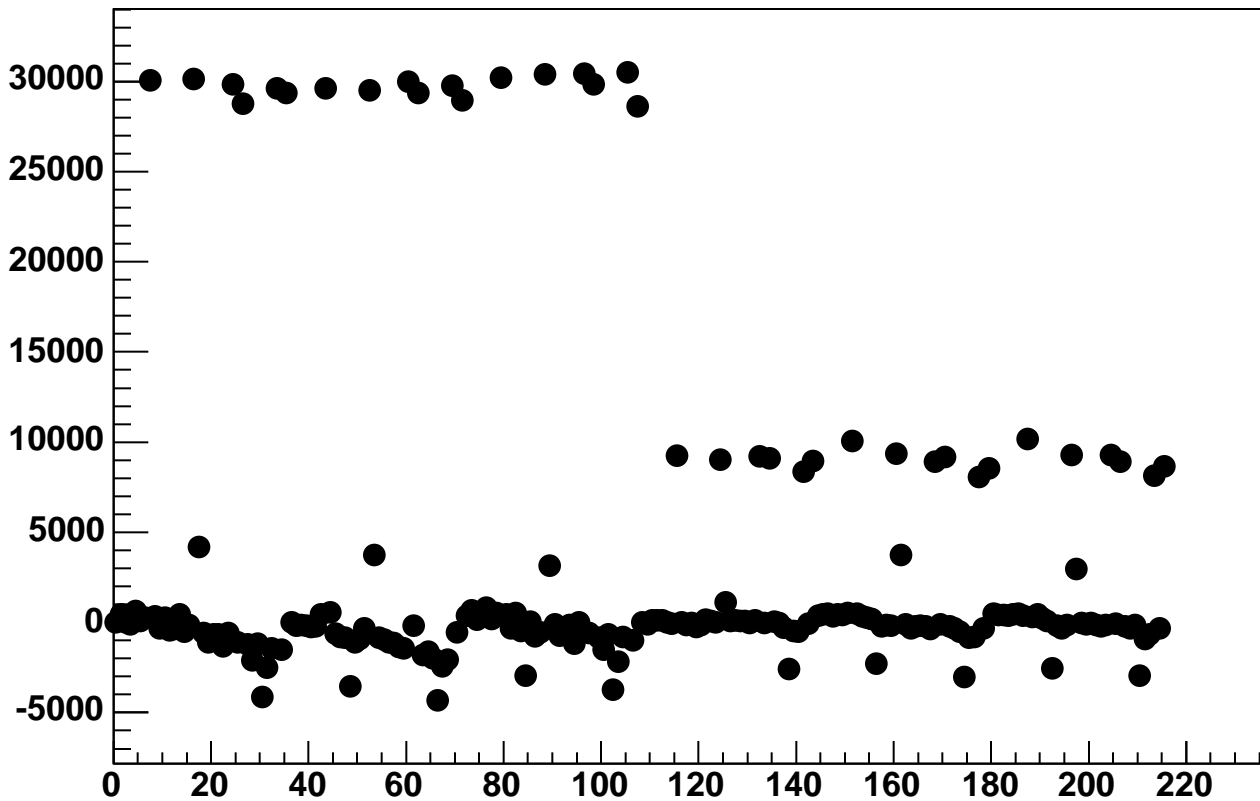
Enable 5, DAC=4000, Hold=25, ADC Mean vs 18*Chip+Chan



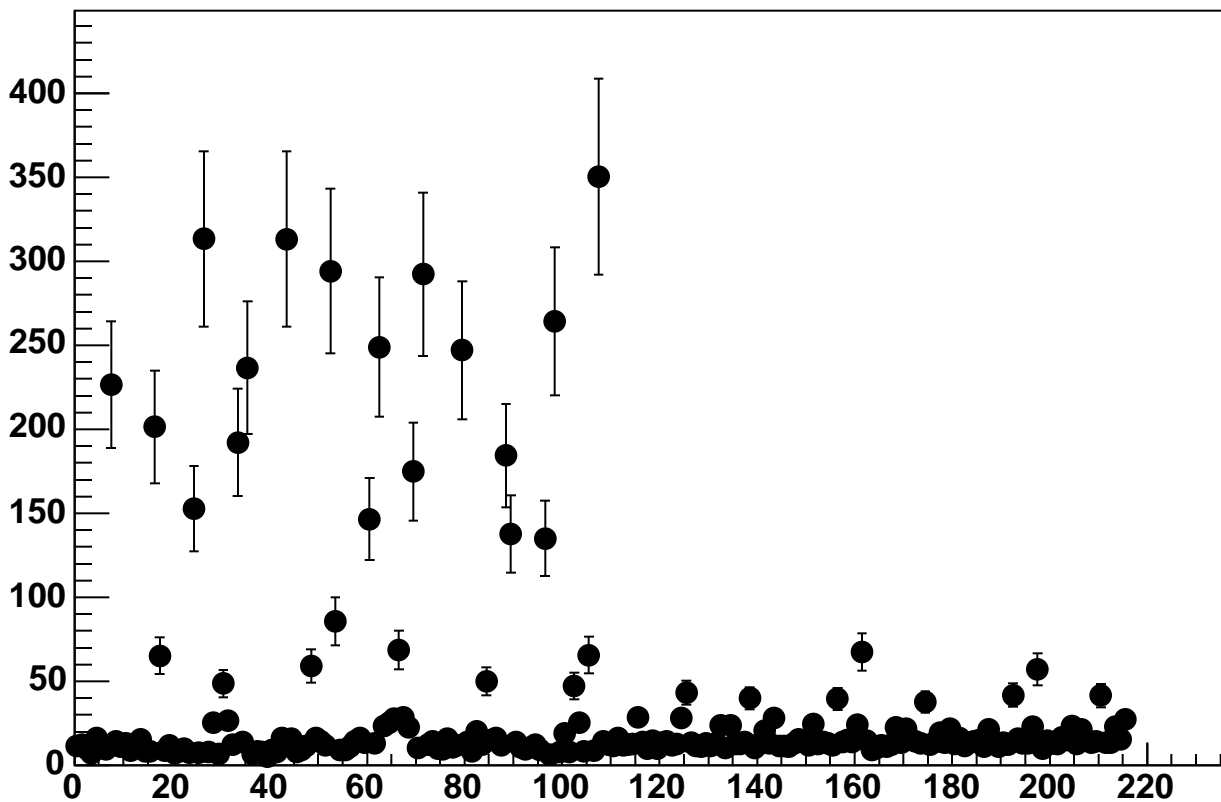
Enable 5, DAC=4000, Hold=25, ADC Noise vs 18*Chip+Chan



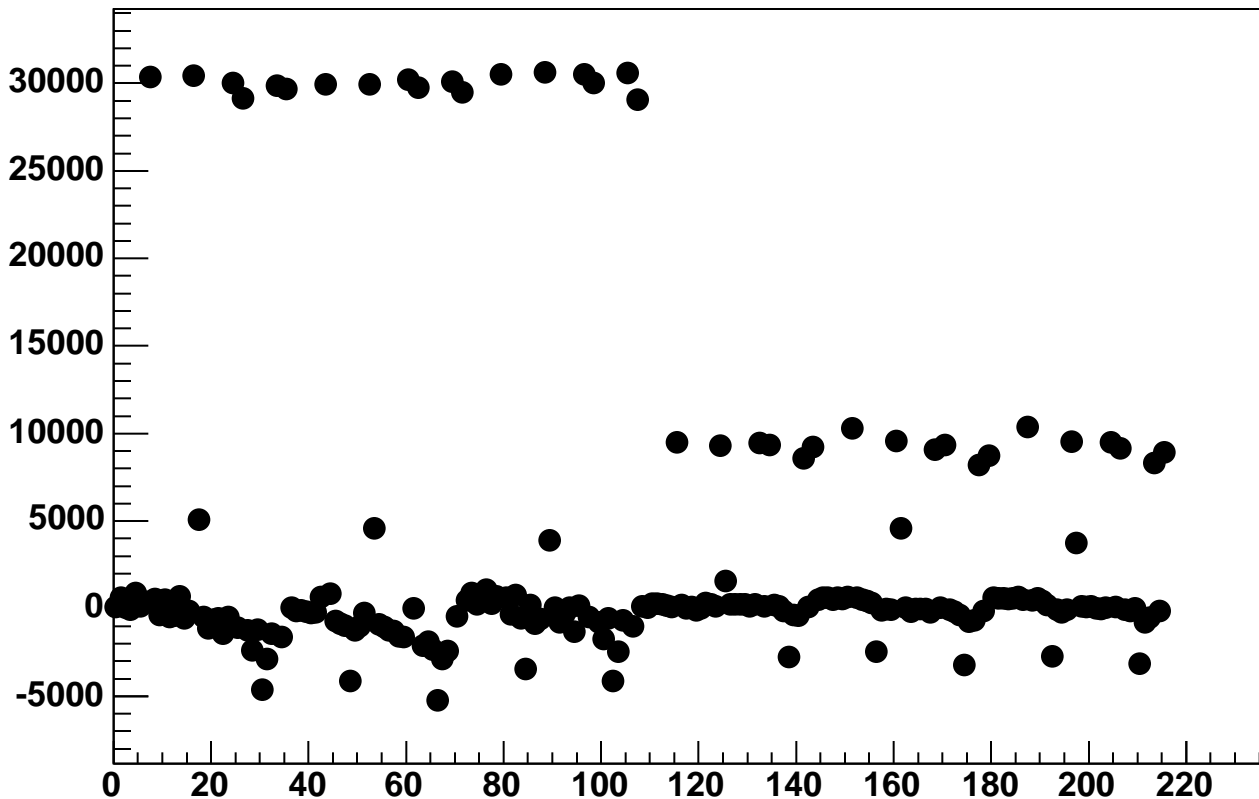
Enable 5, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



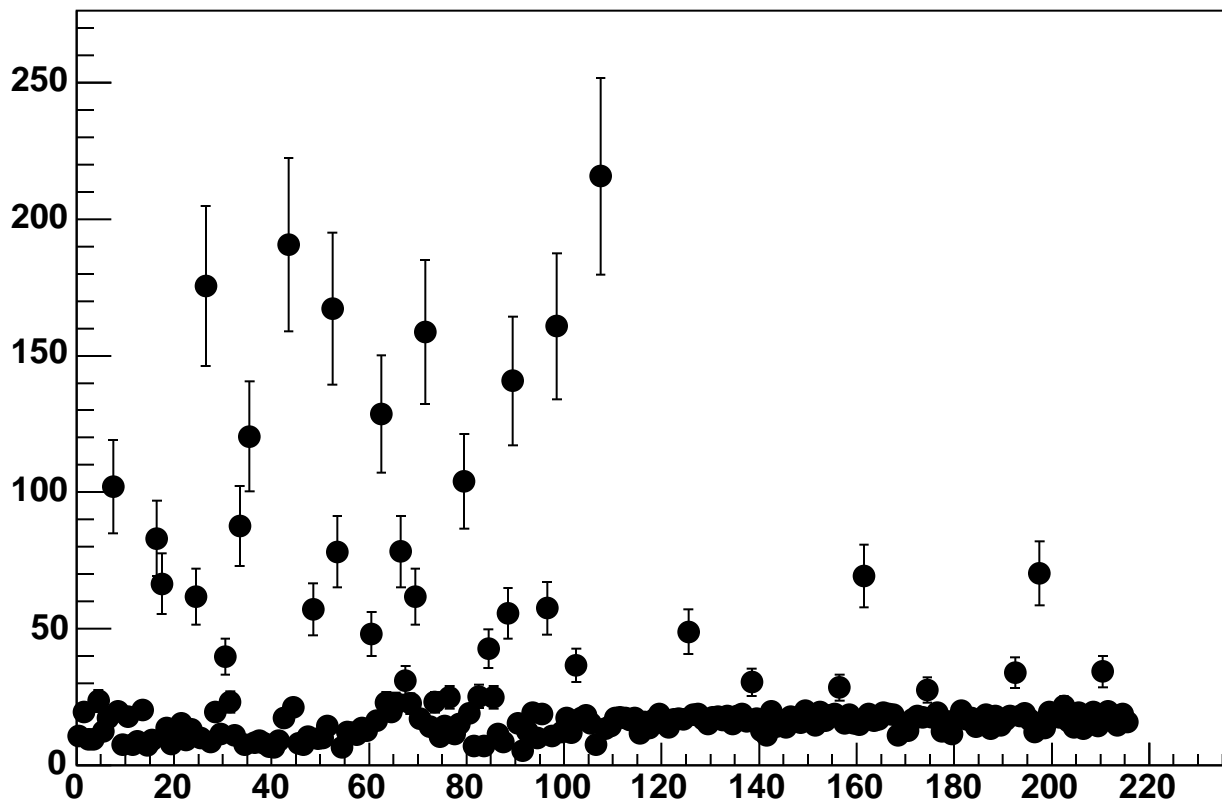
Enable 5, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



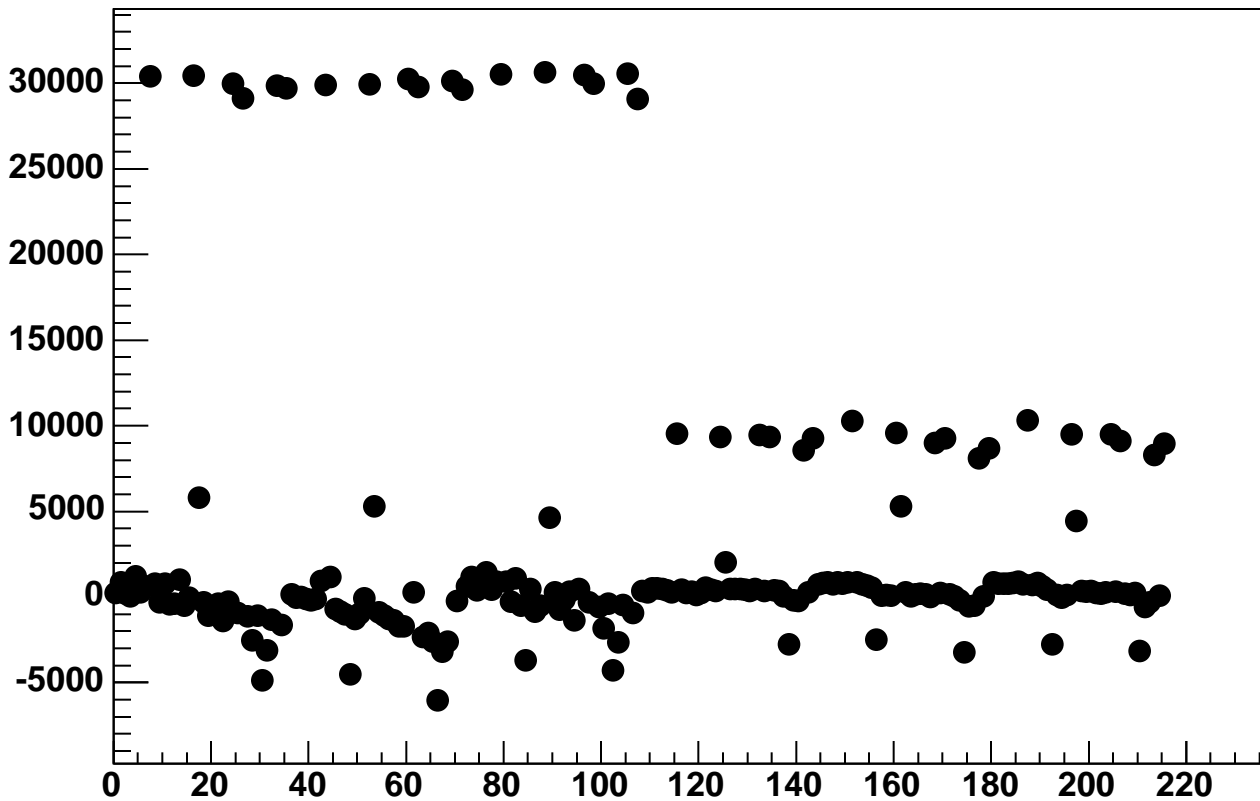
Enable 5, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



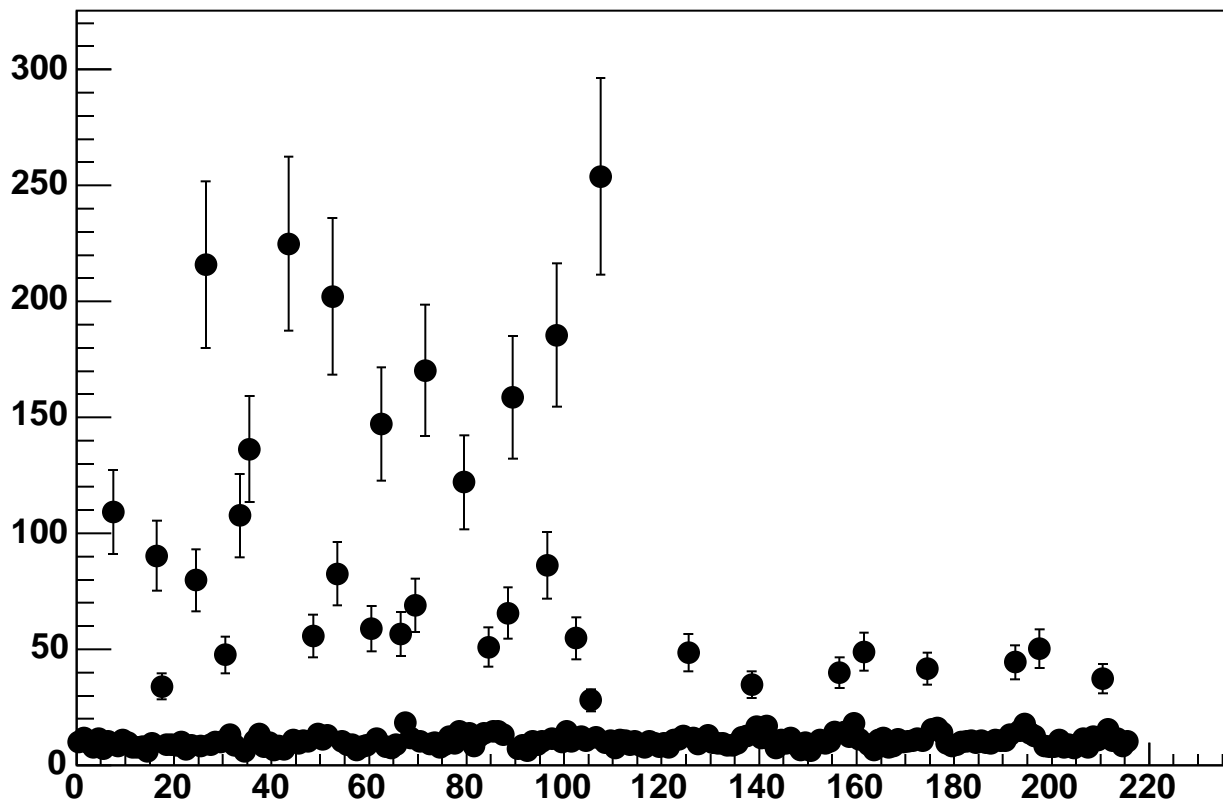
Enable 5, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



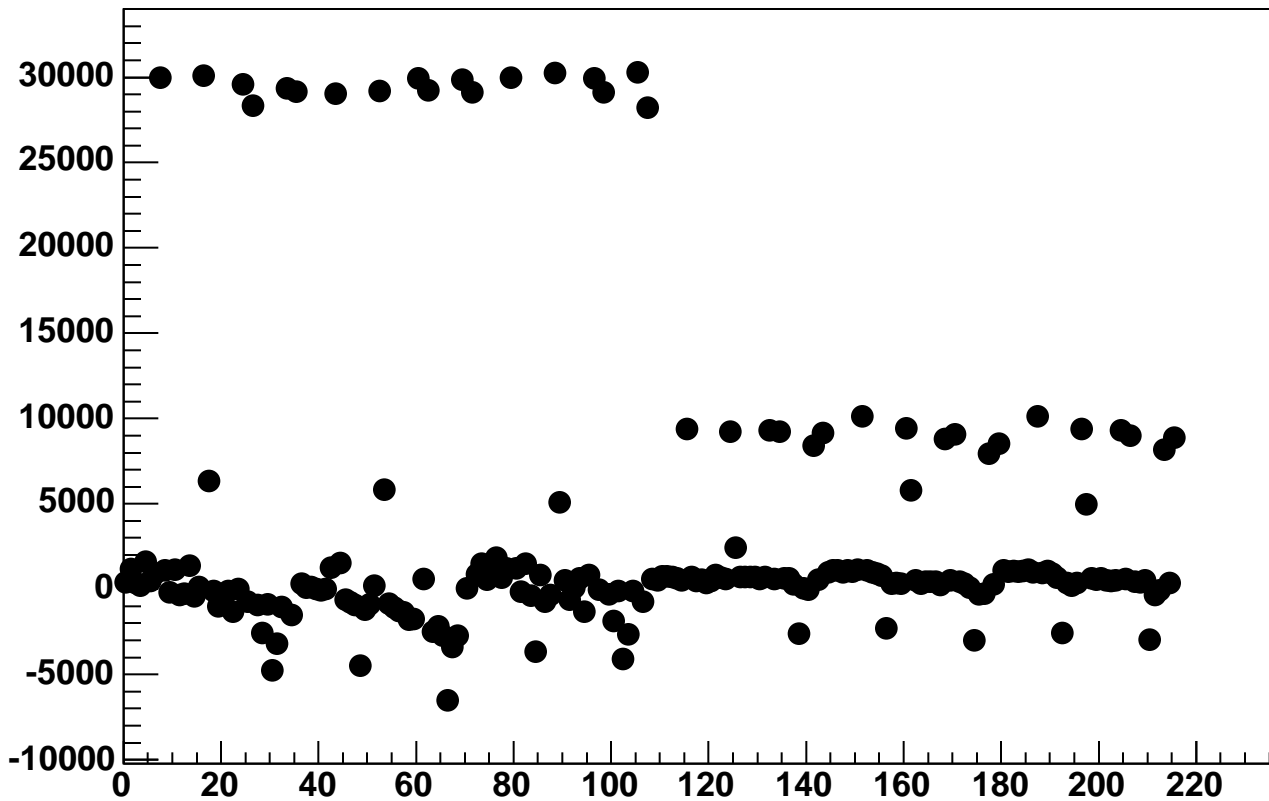
Enable 5, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



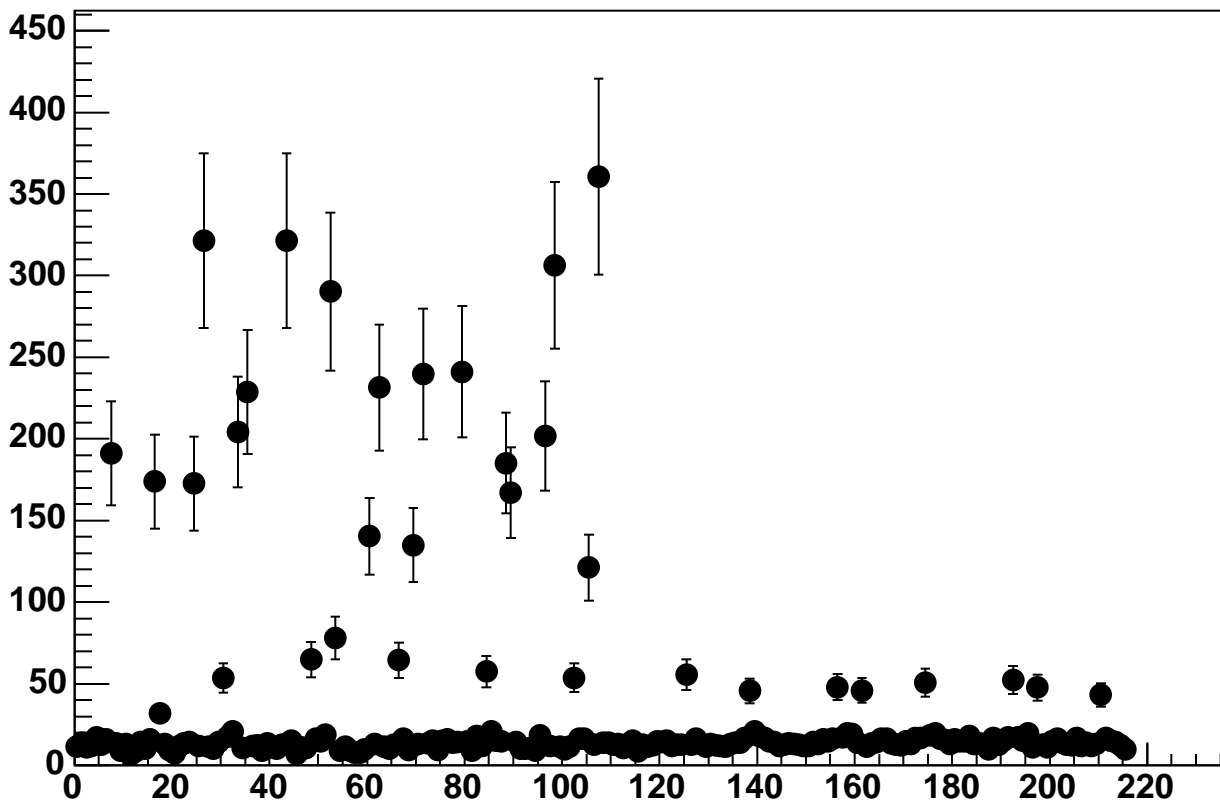
Enable 5, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



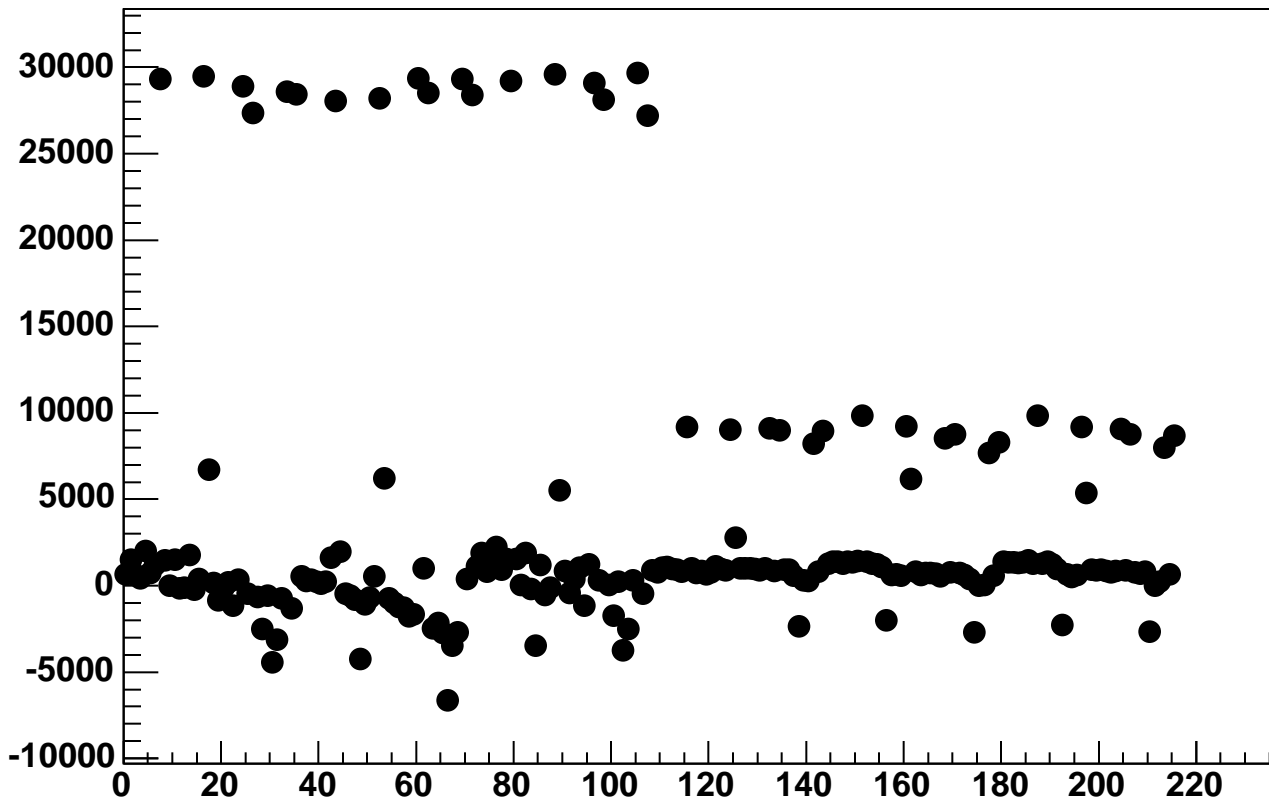
Enable 5, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



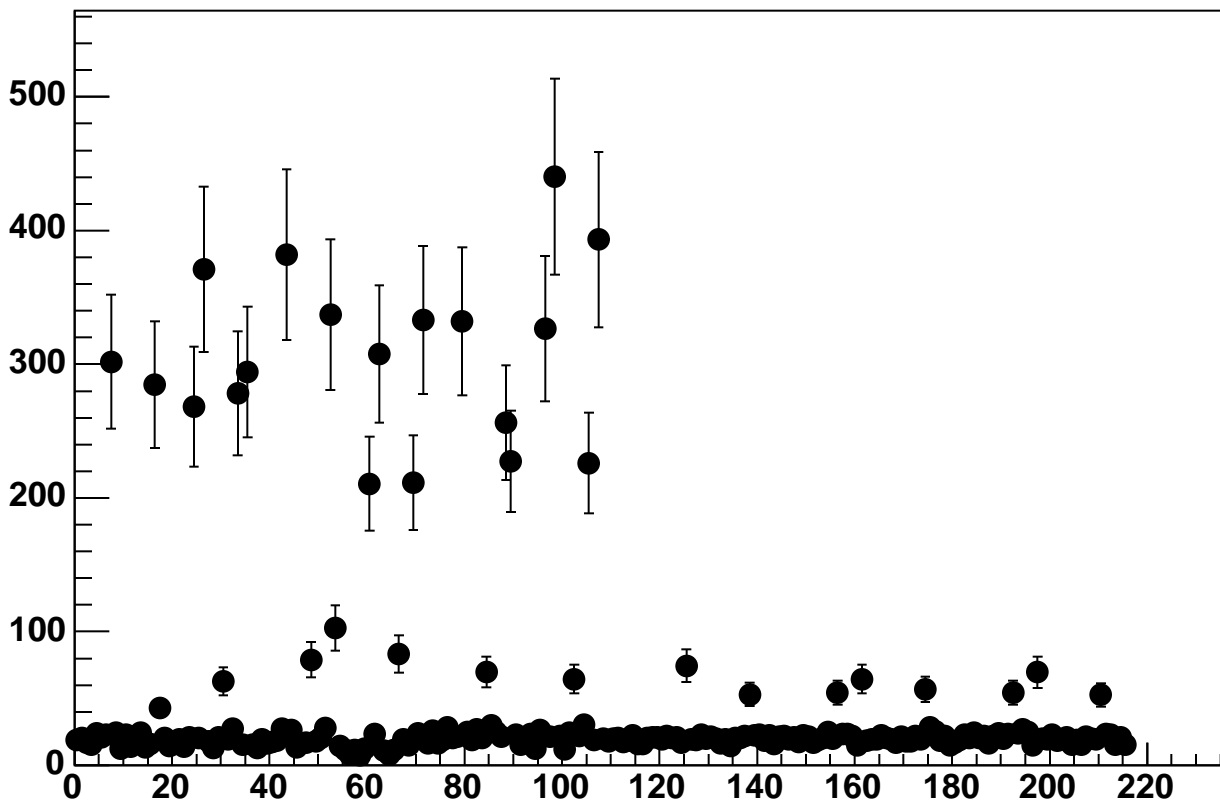
Enable 5, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



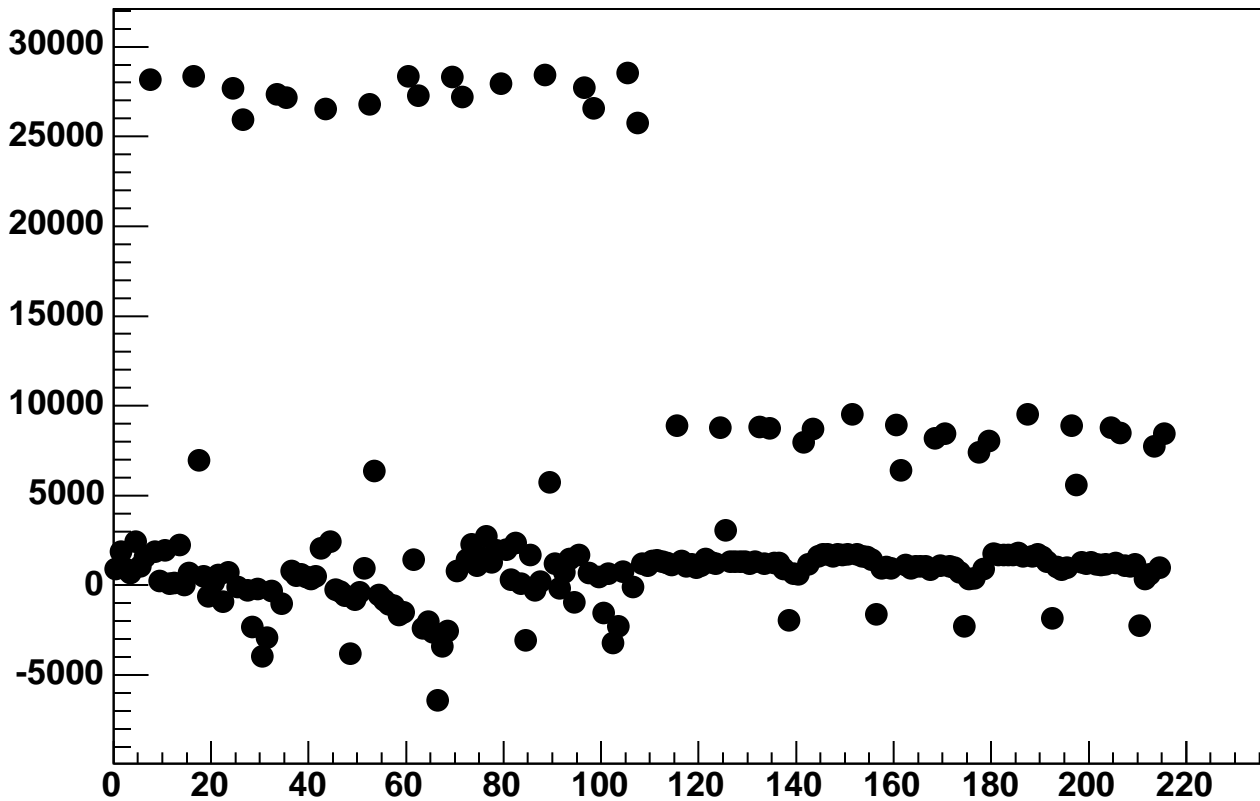
Enable 5, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



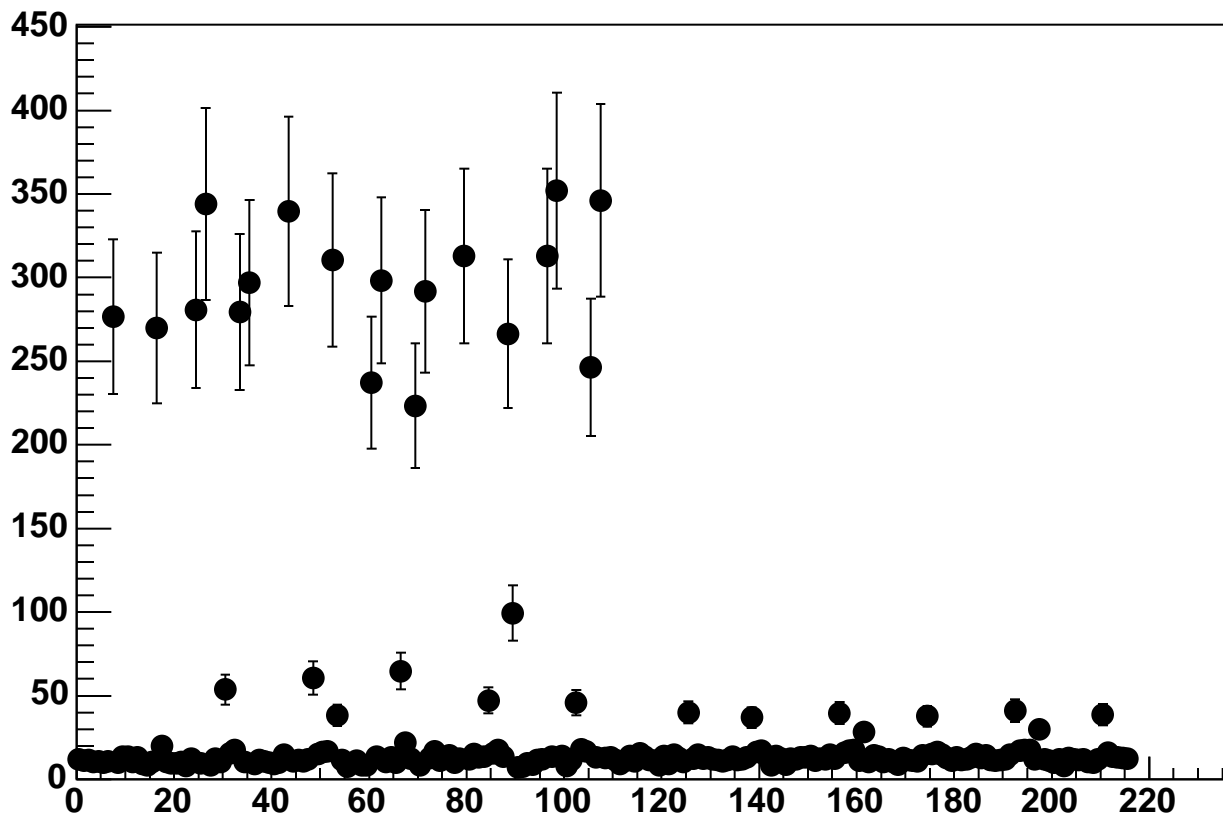
Enable 5, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



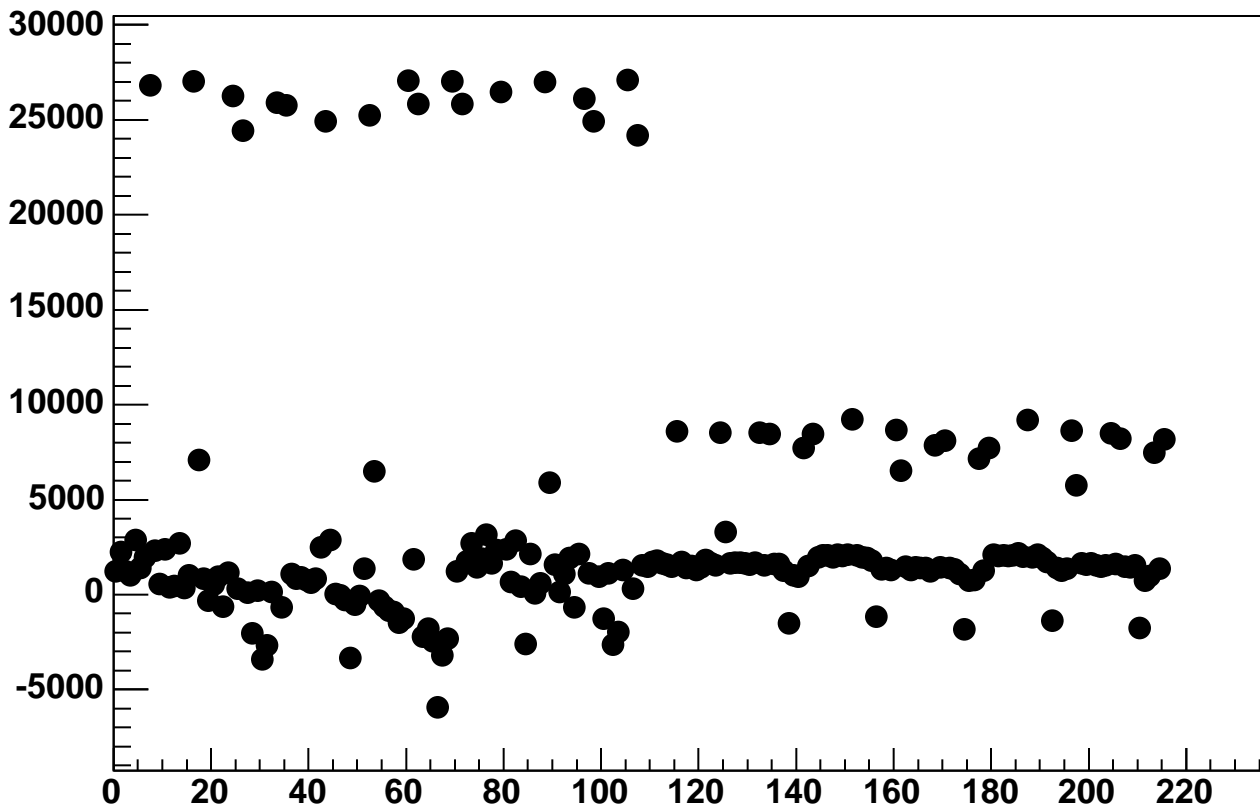
Enable 5, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



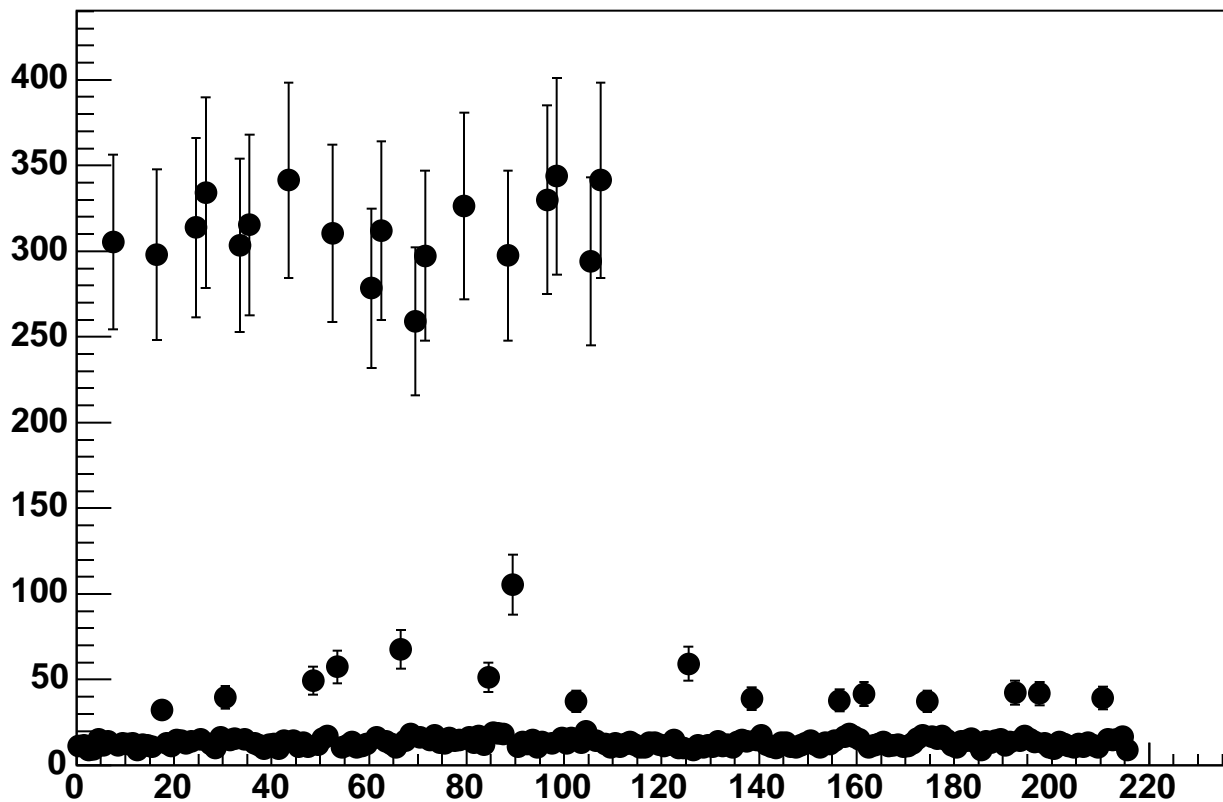
Enable 5, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



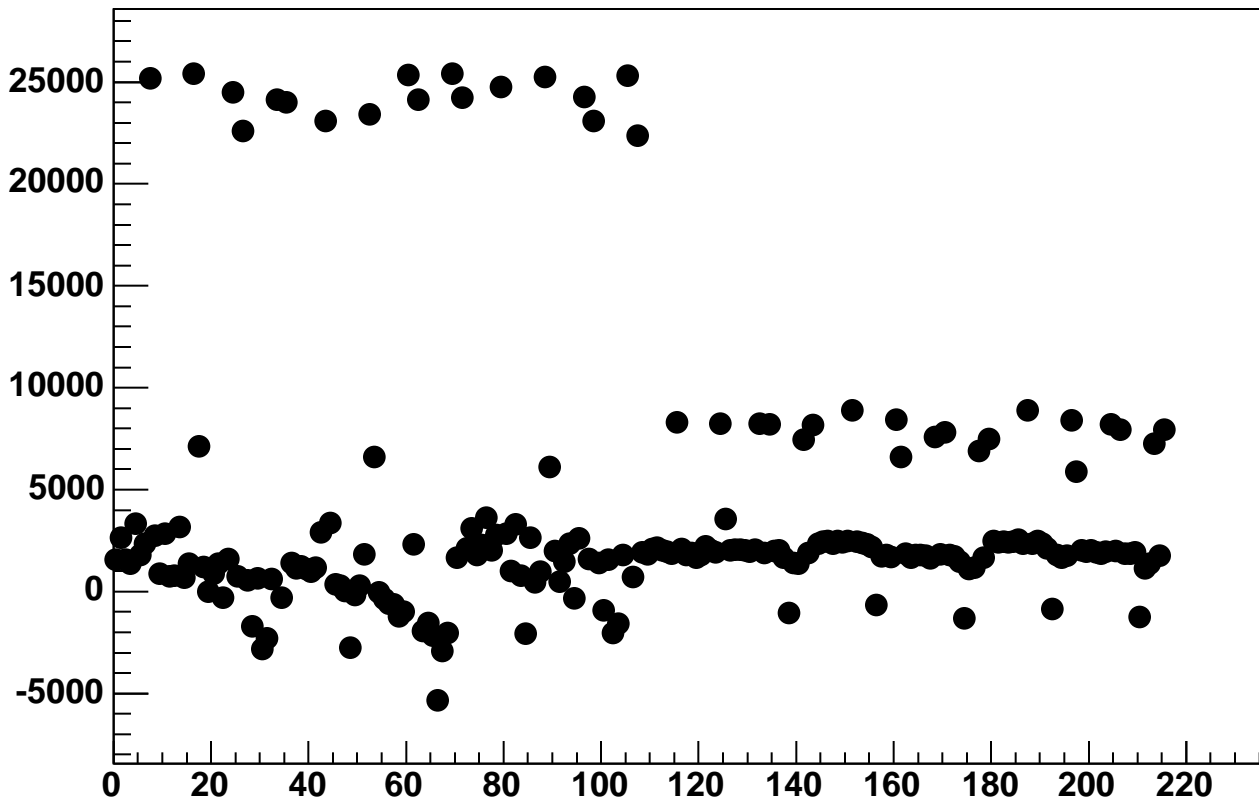
Enable 5, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



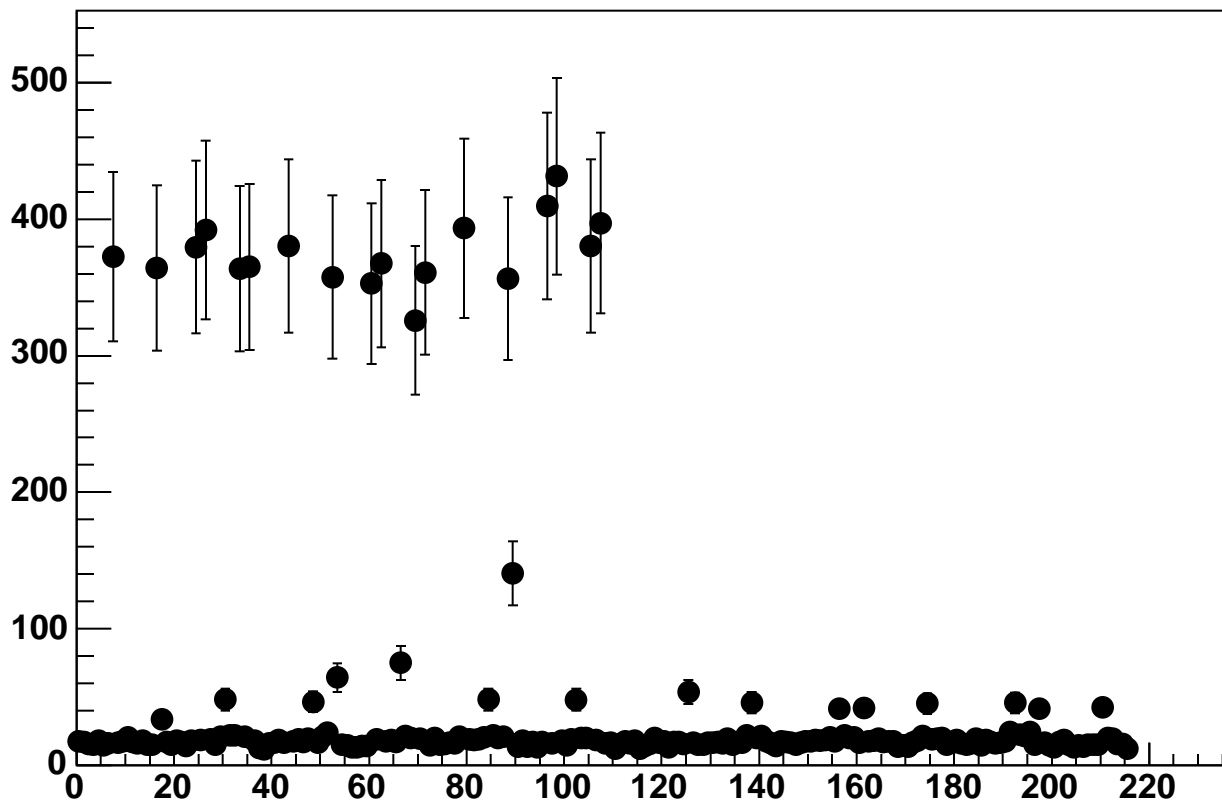
Enable 5, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



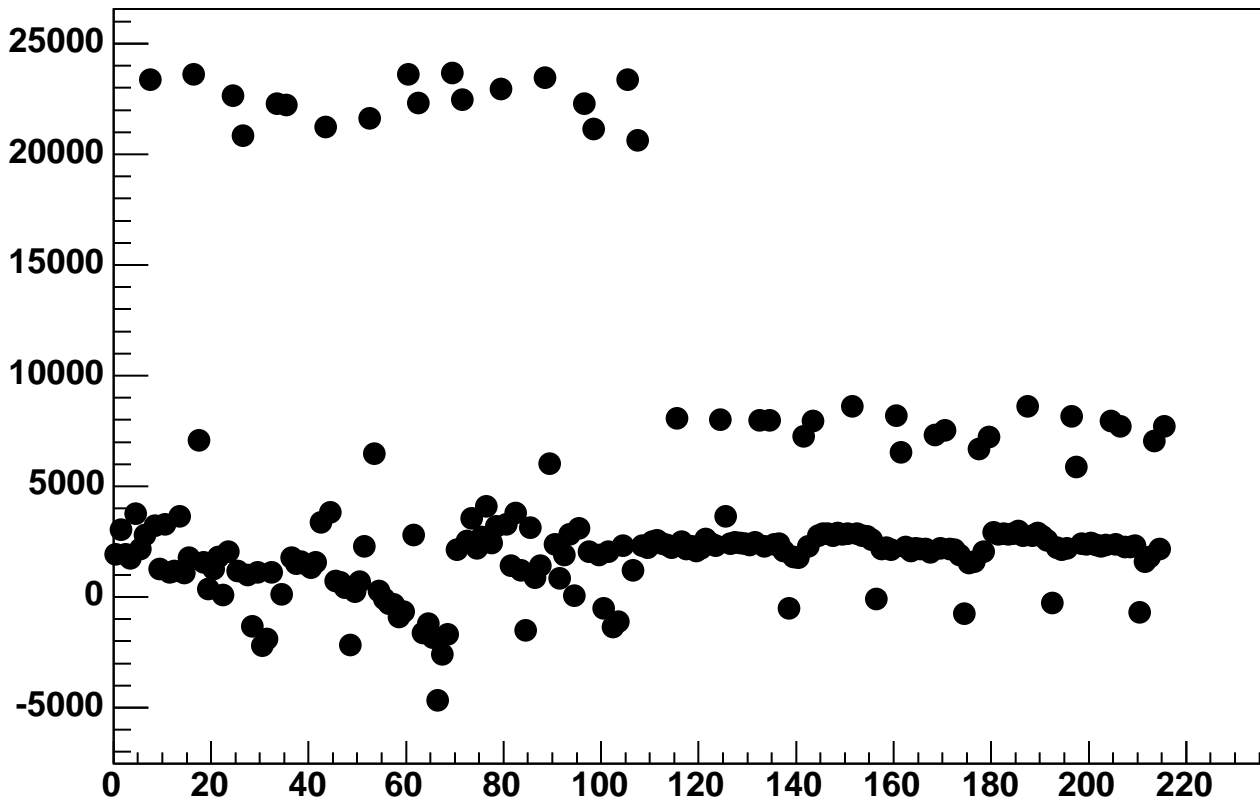
Enable 5, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



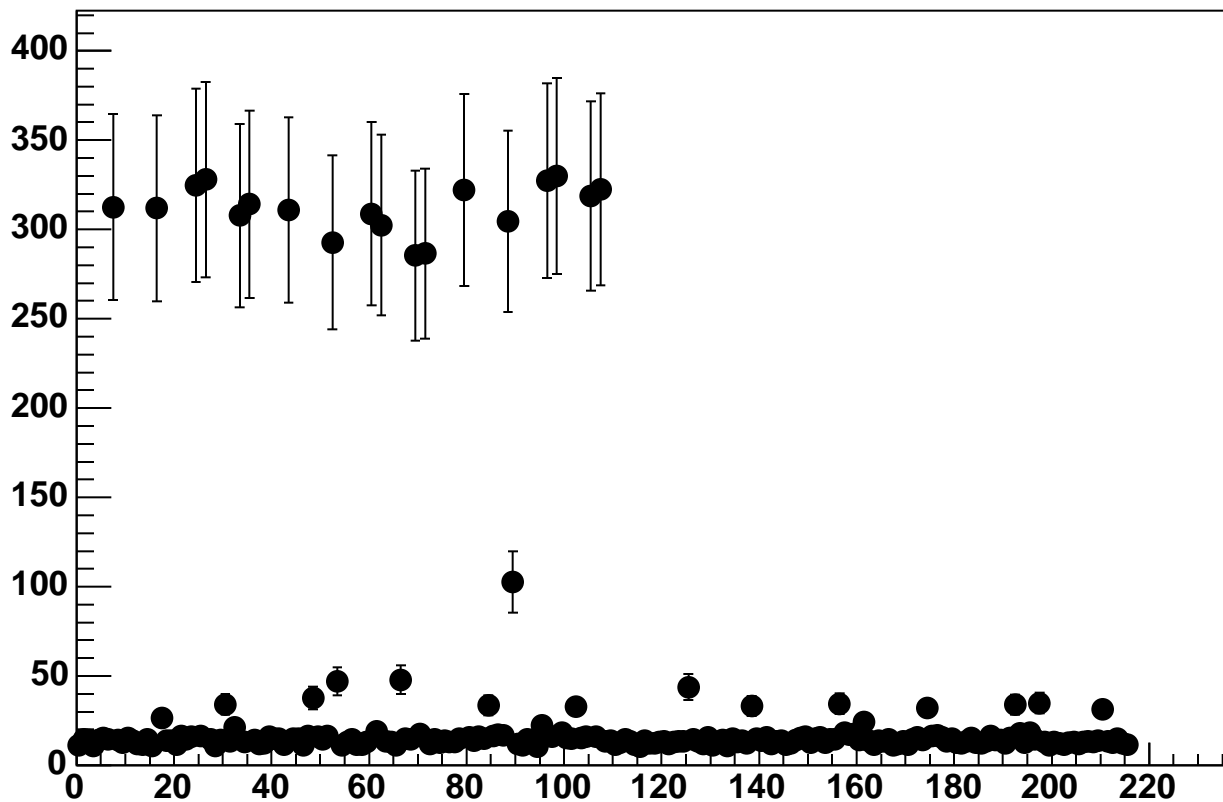
Enable 5, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



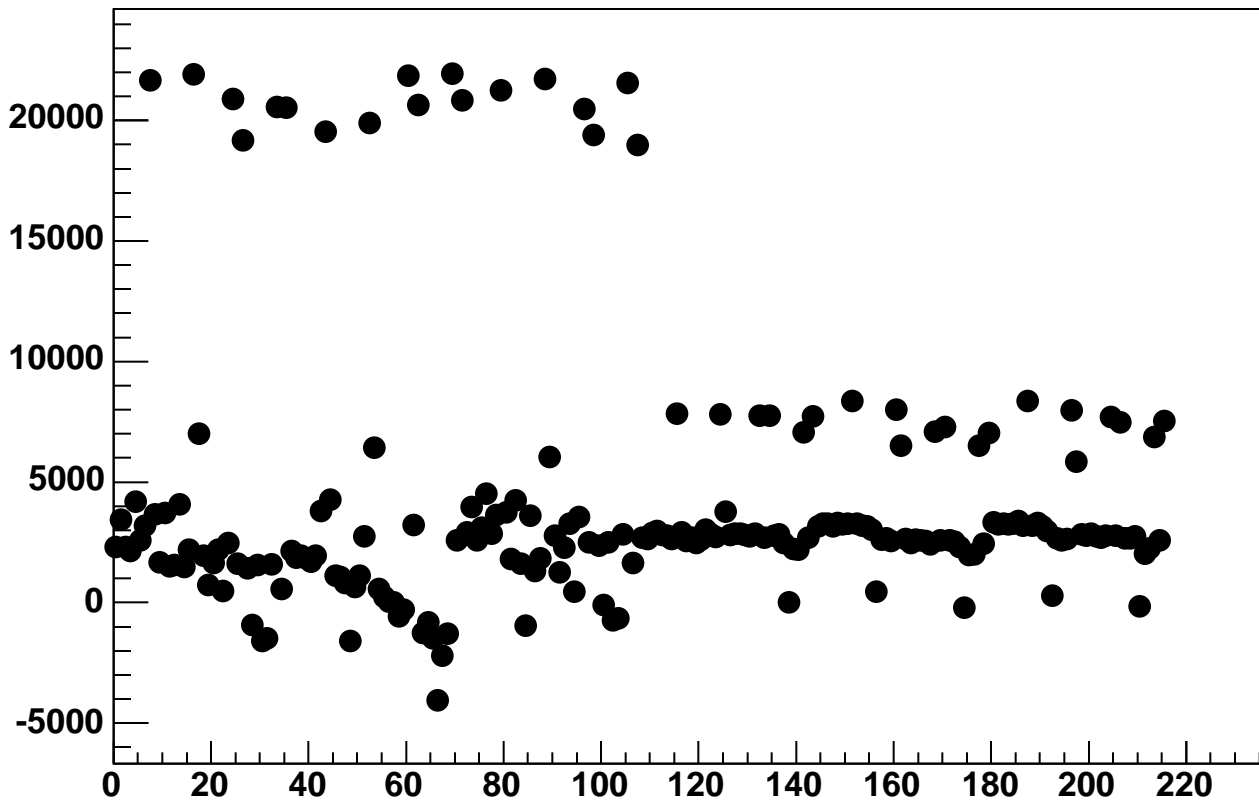
Enable 5, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



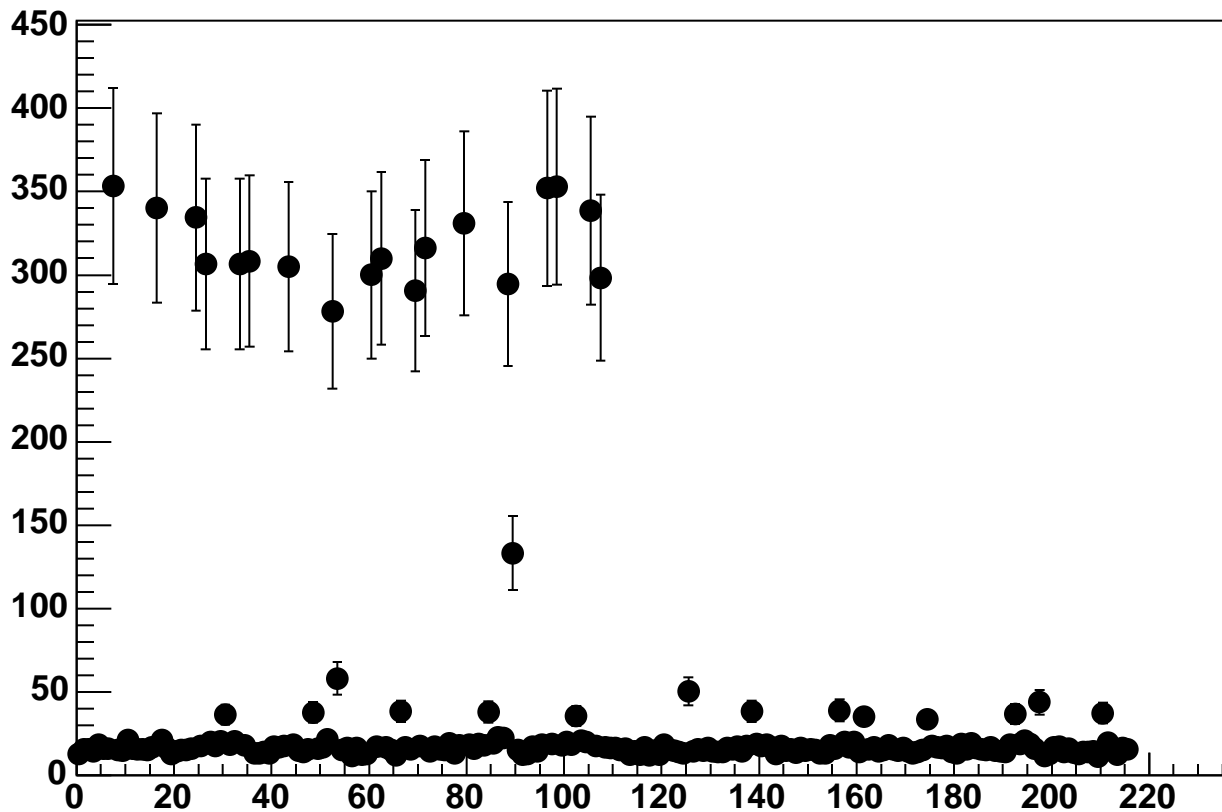
Enable 5, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



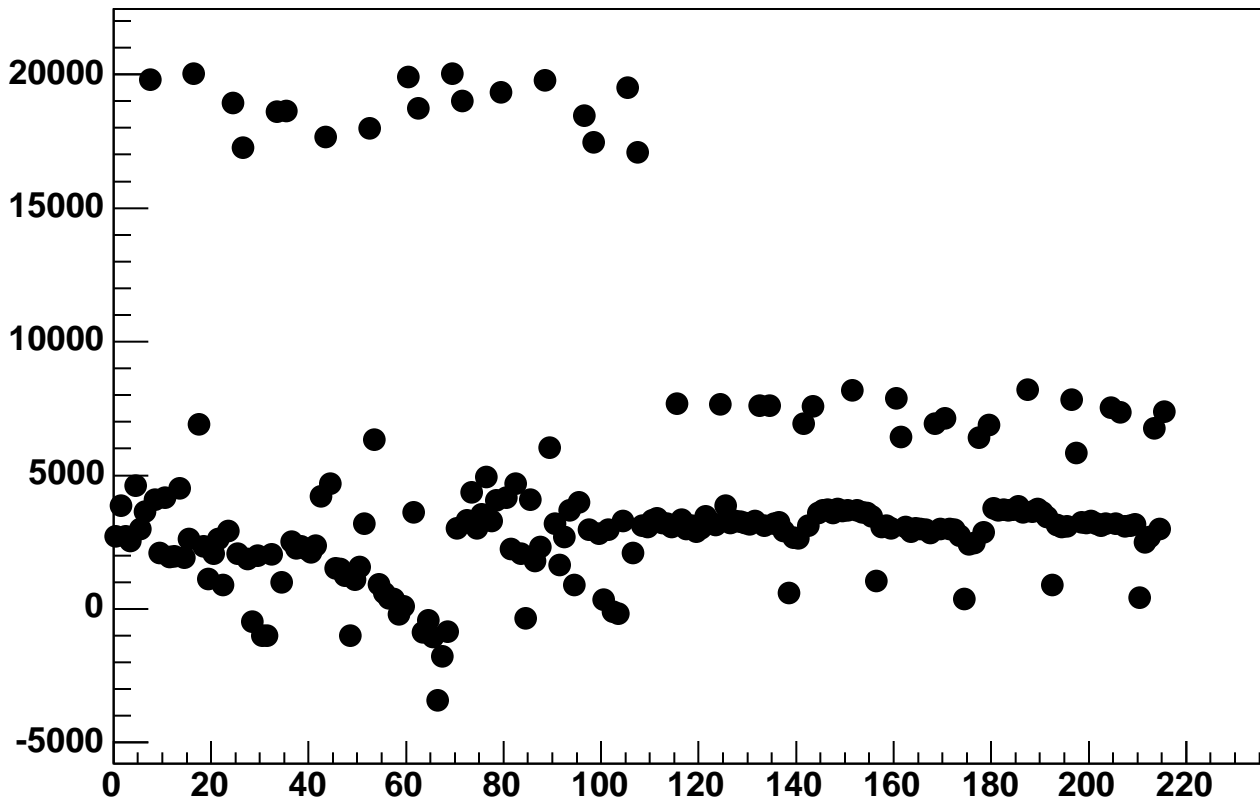
Enable 5, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



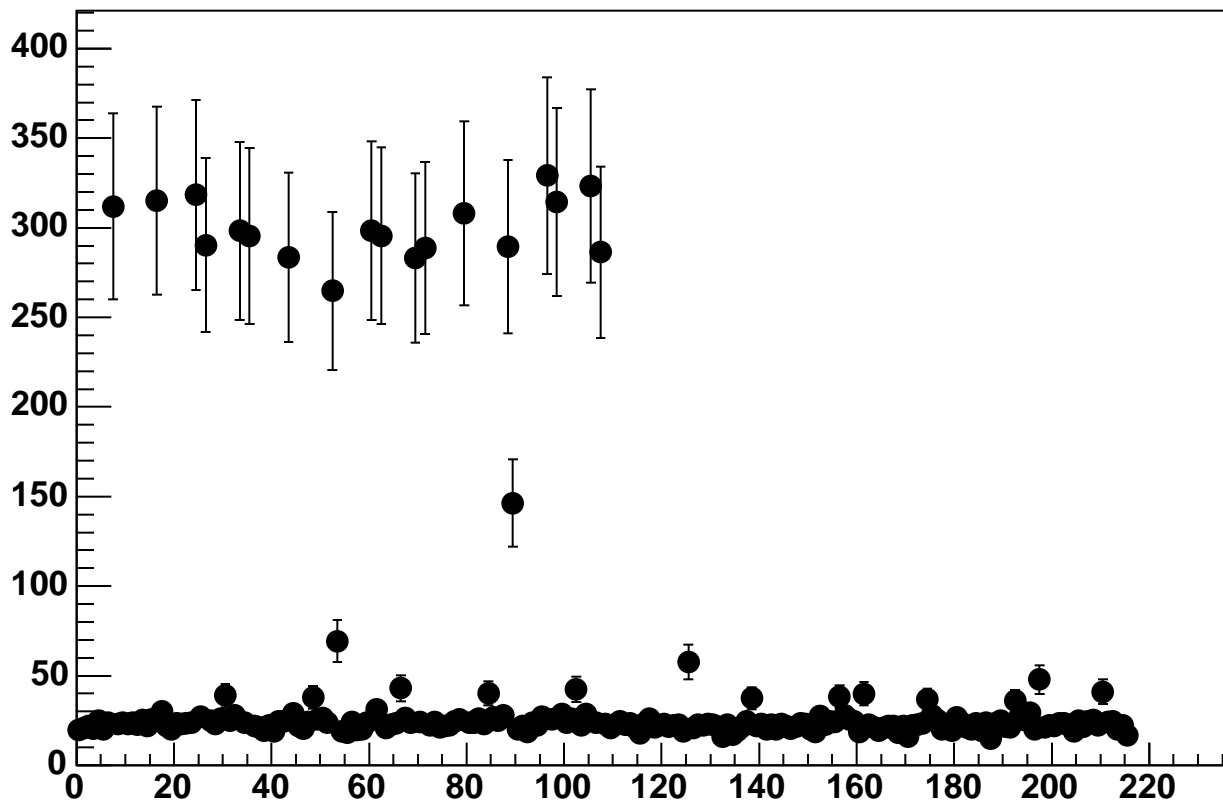
Enable 5, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



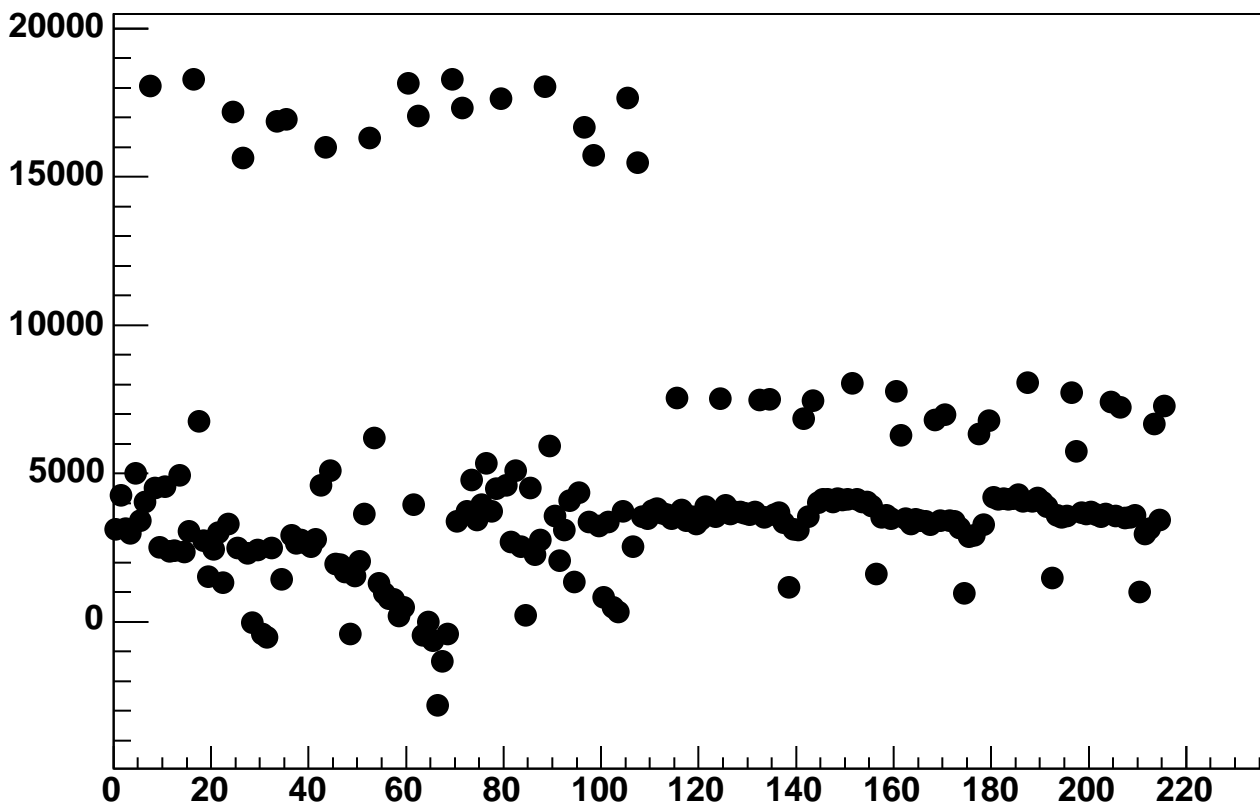
Enable 5, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



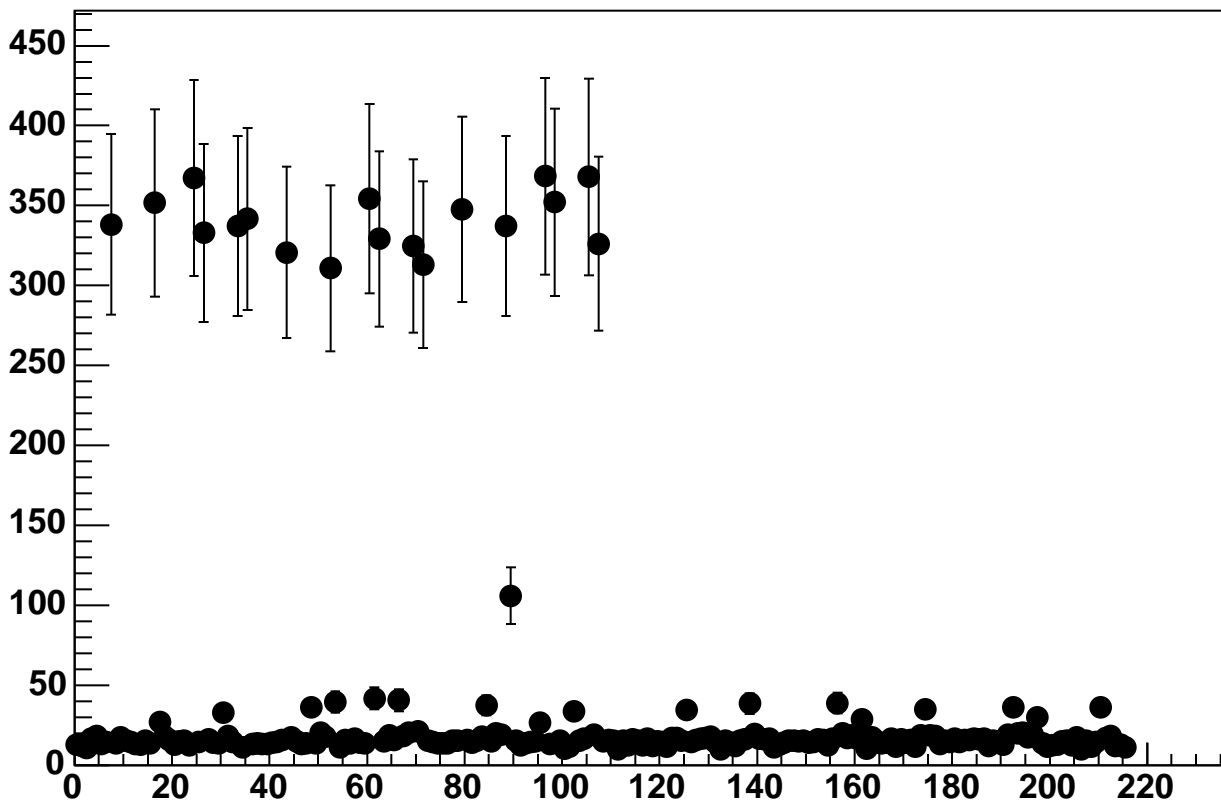
Enable 5, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



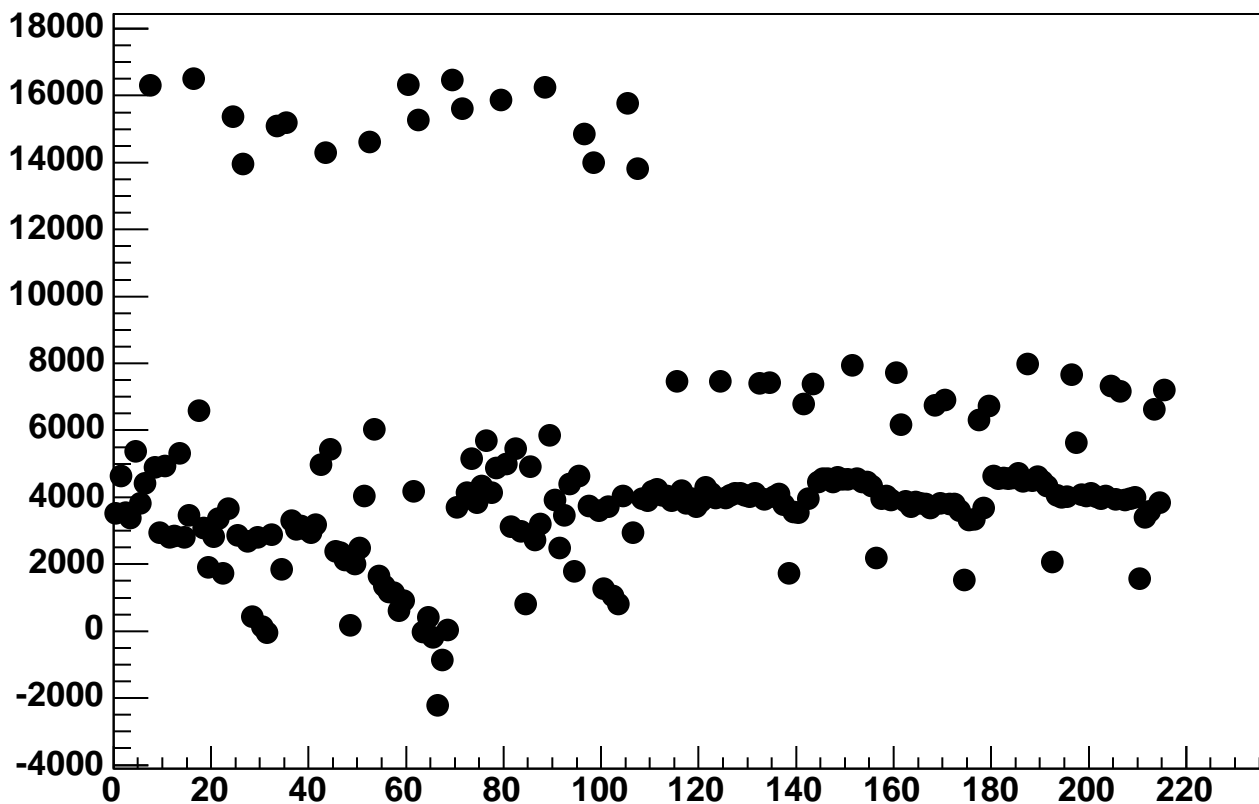
Enable 5, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



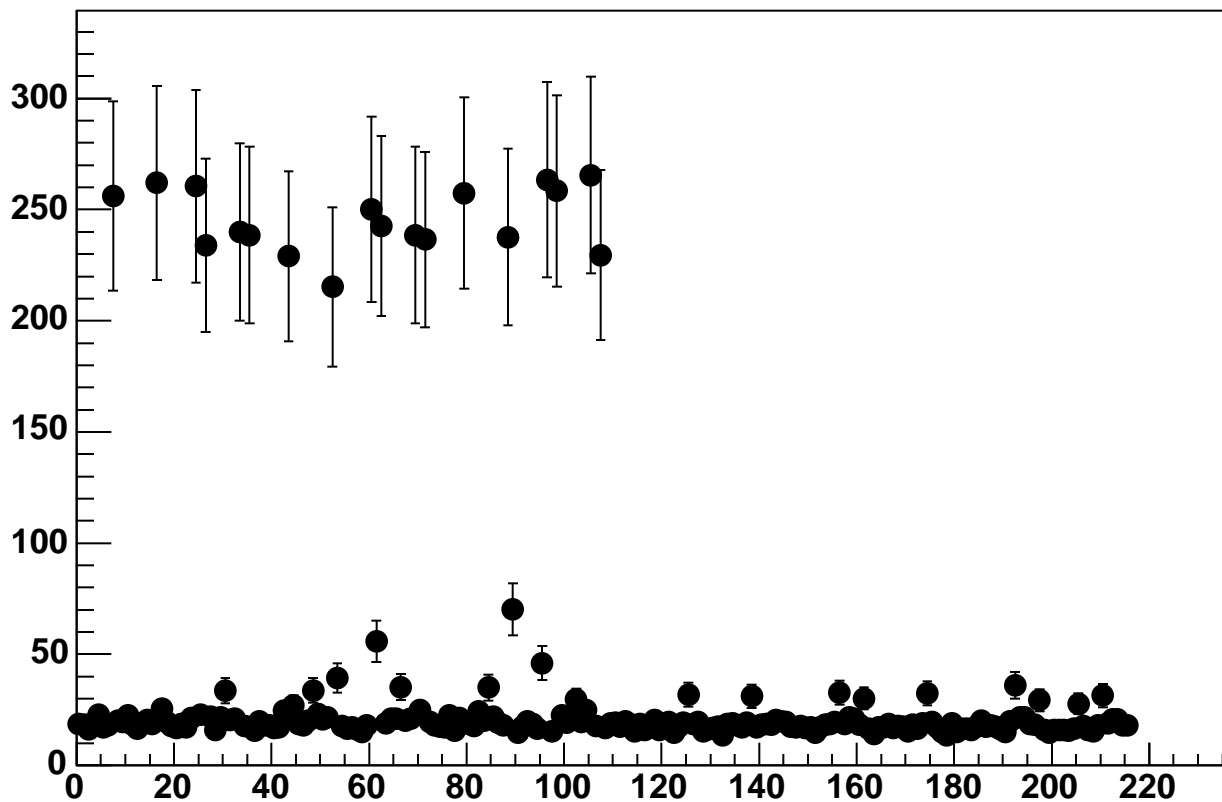
Enable 5, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



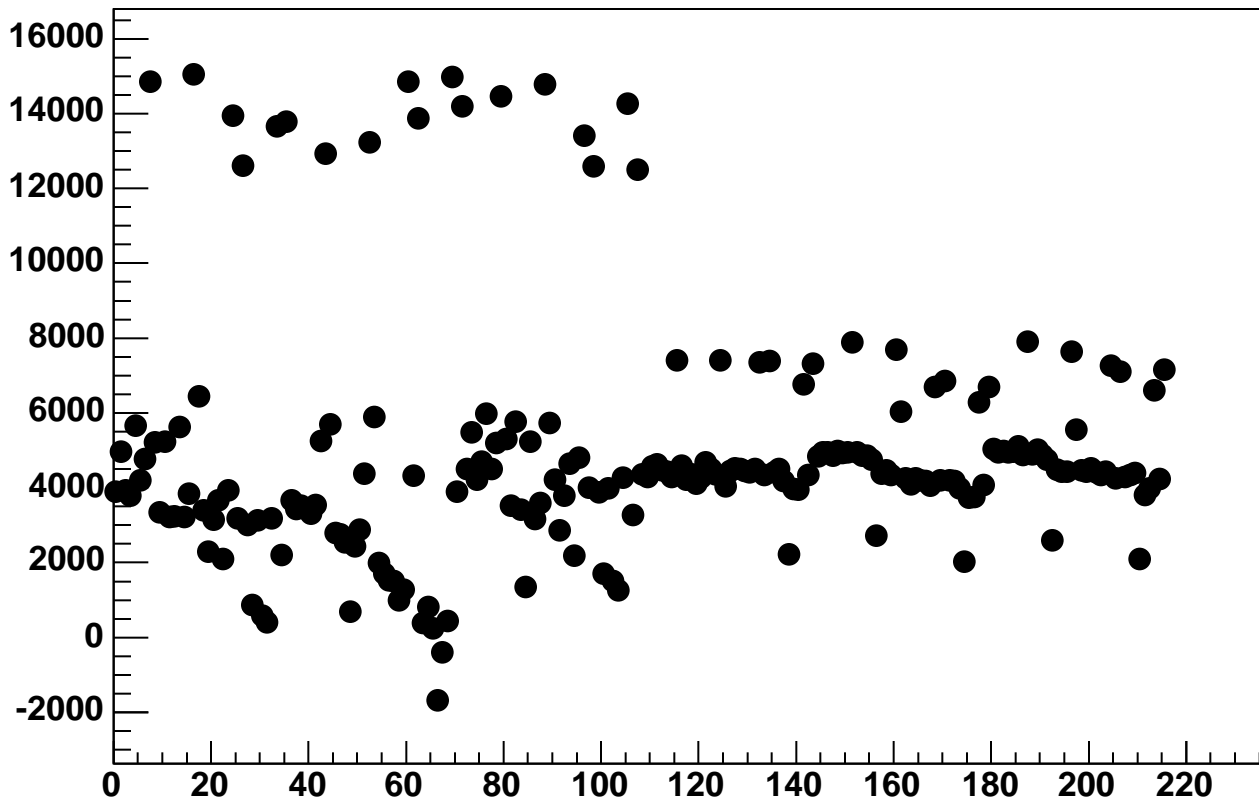
Enable 5, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



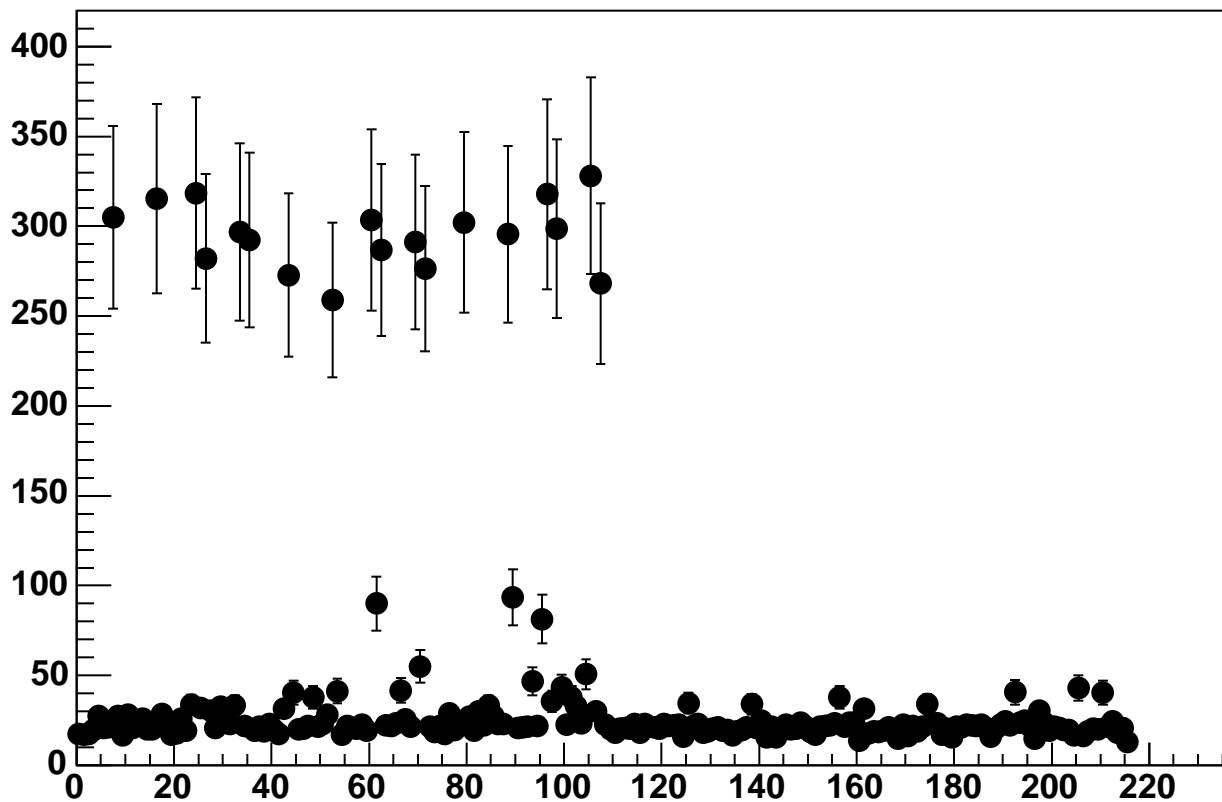
Enable 5, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



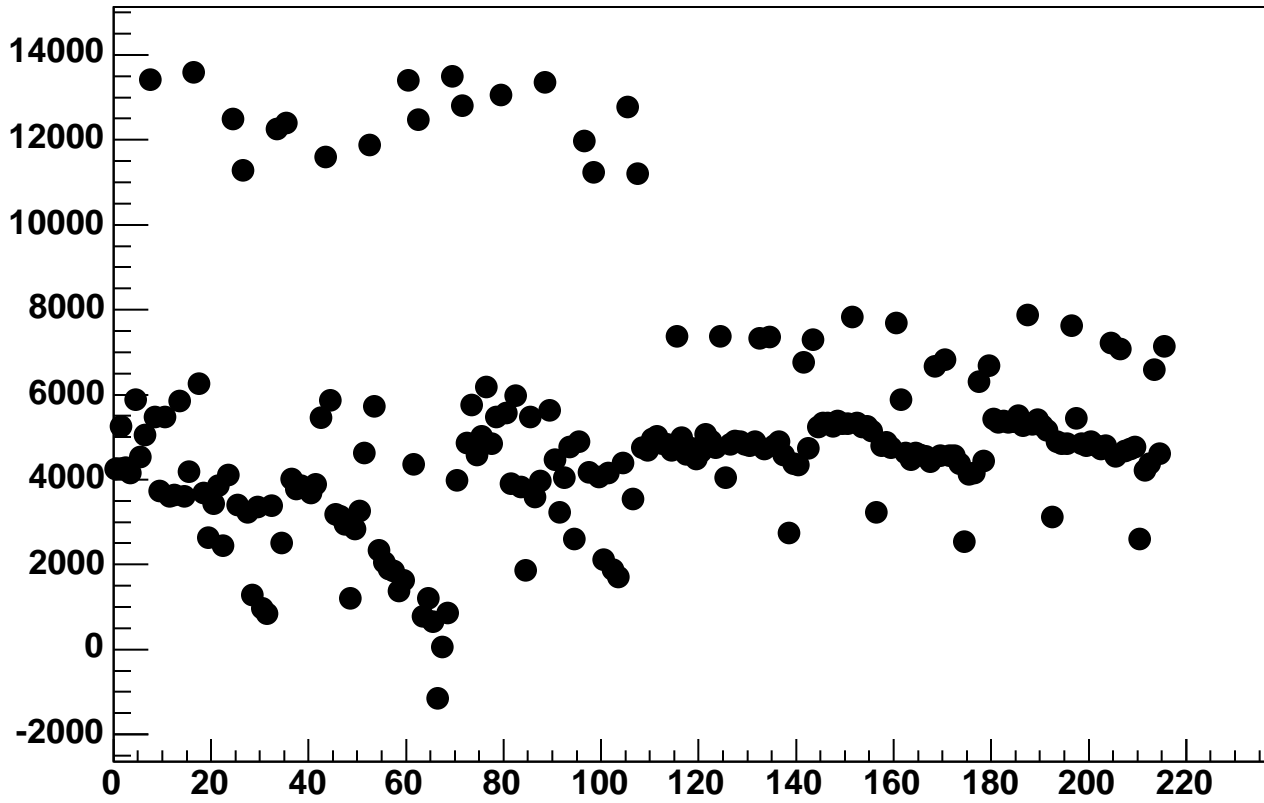
Enable 5, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



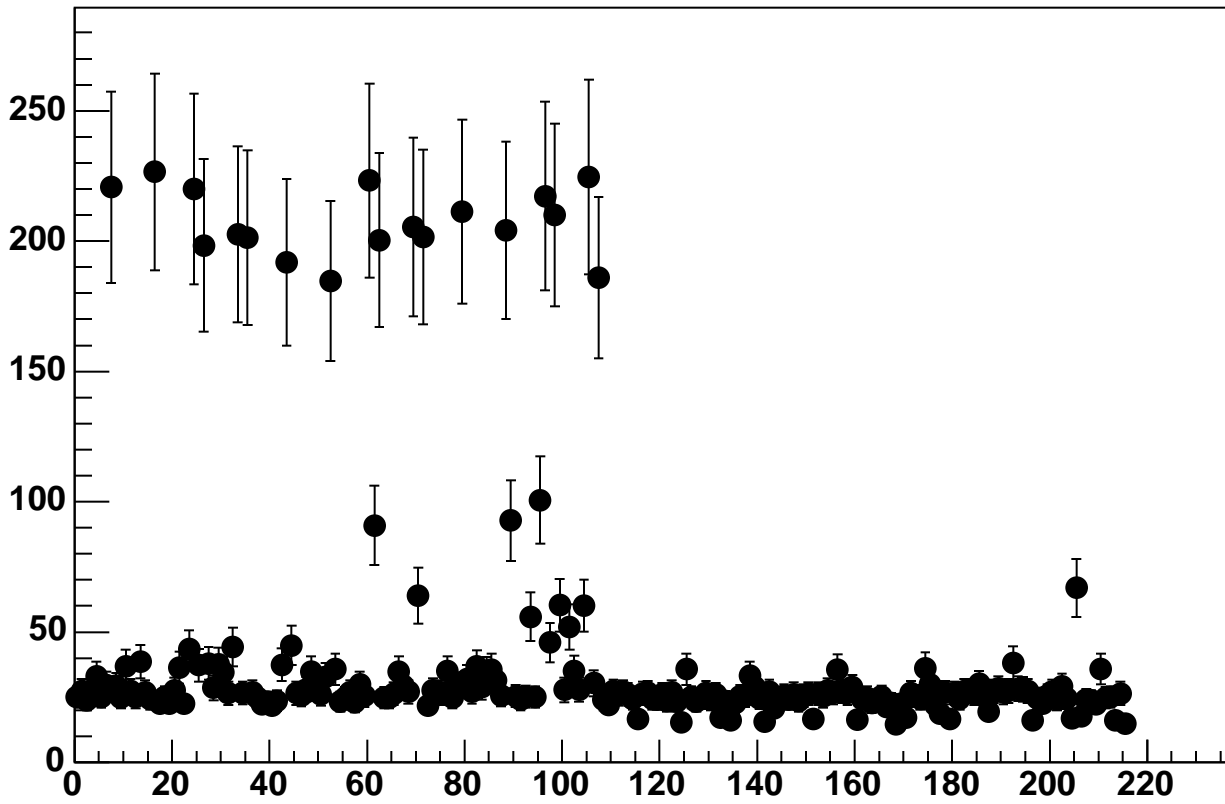
Enable 5, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



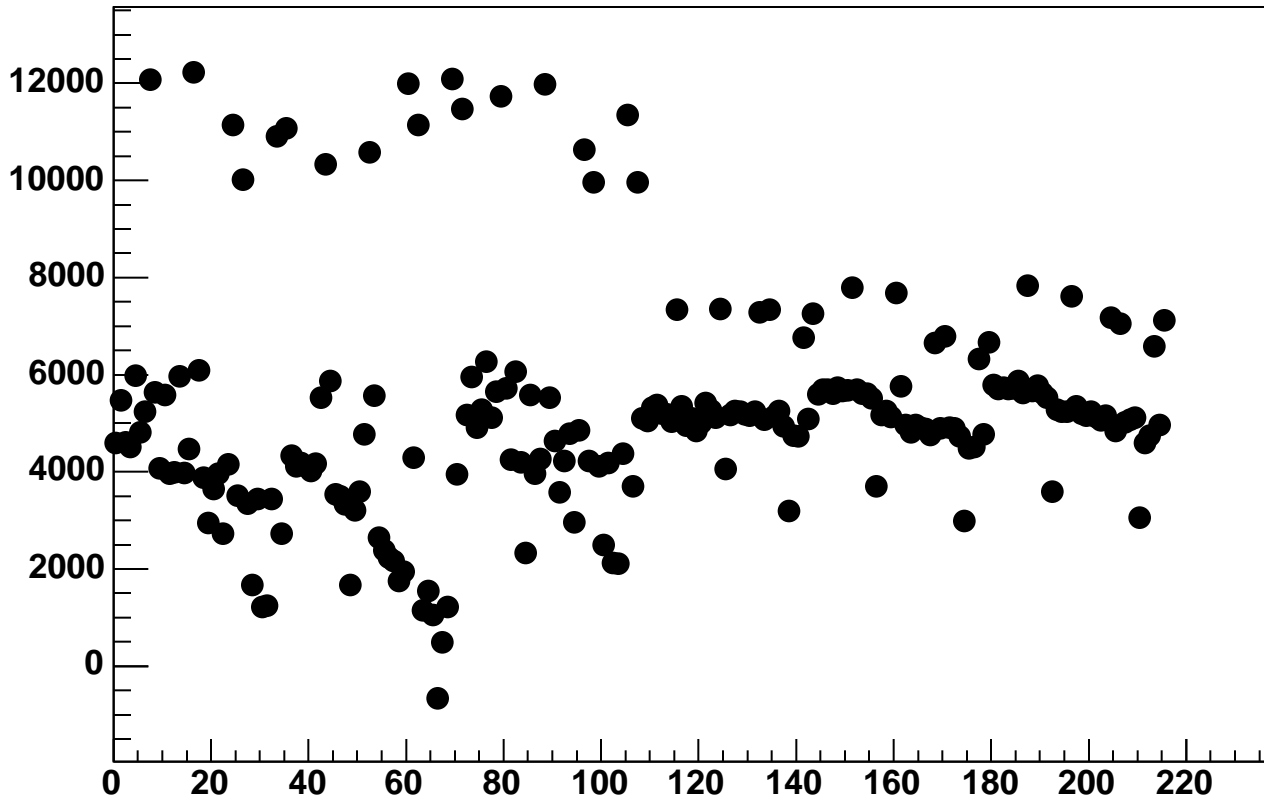
Enable 5, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



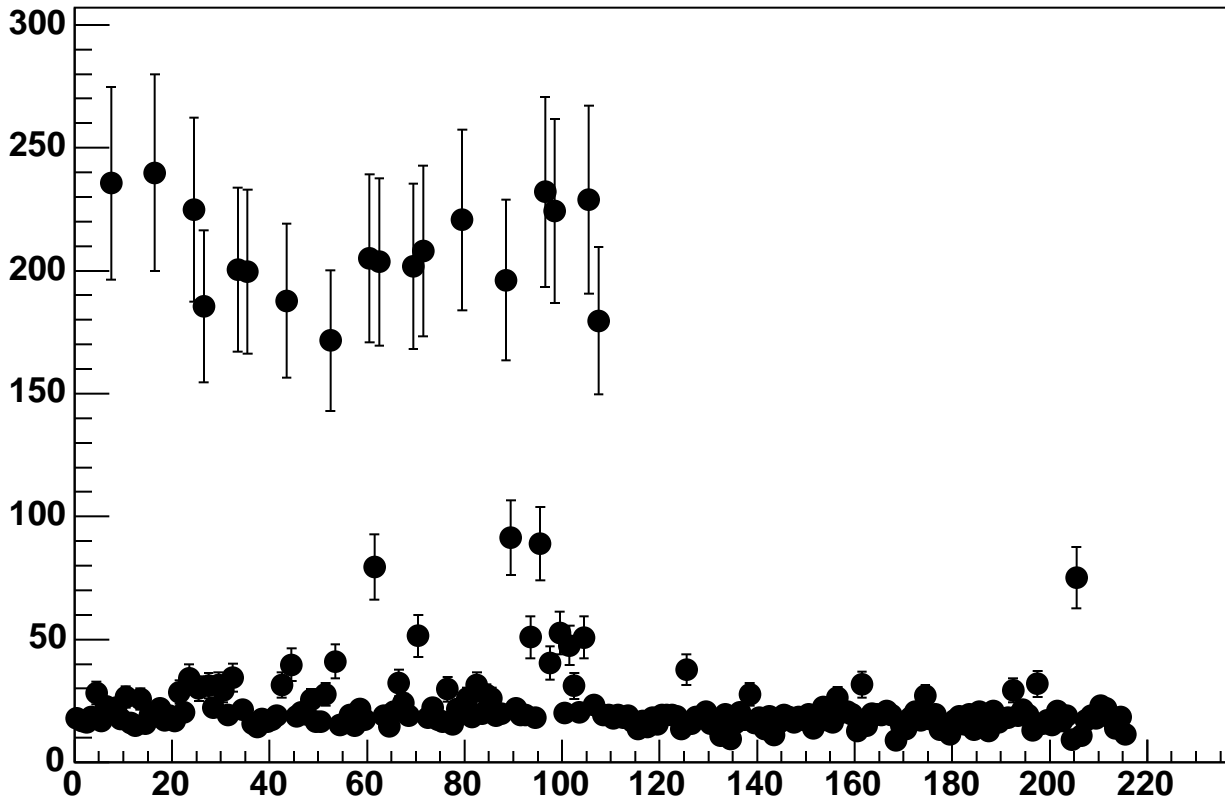
Enable 5, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



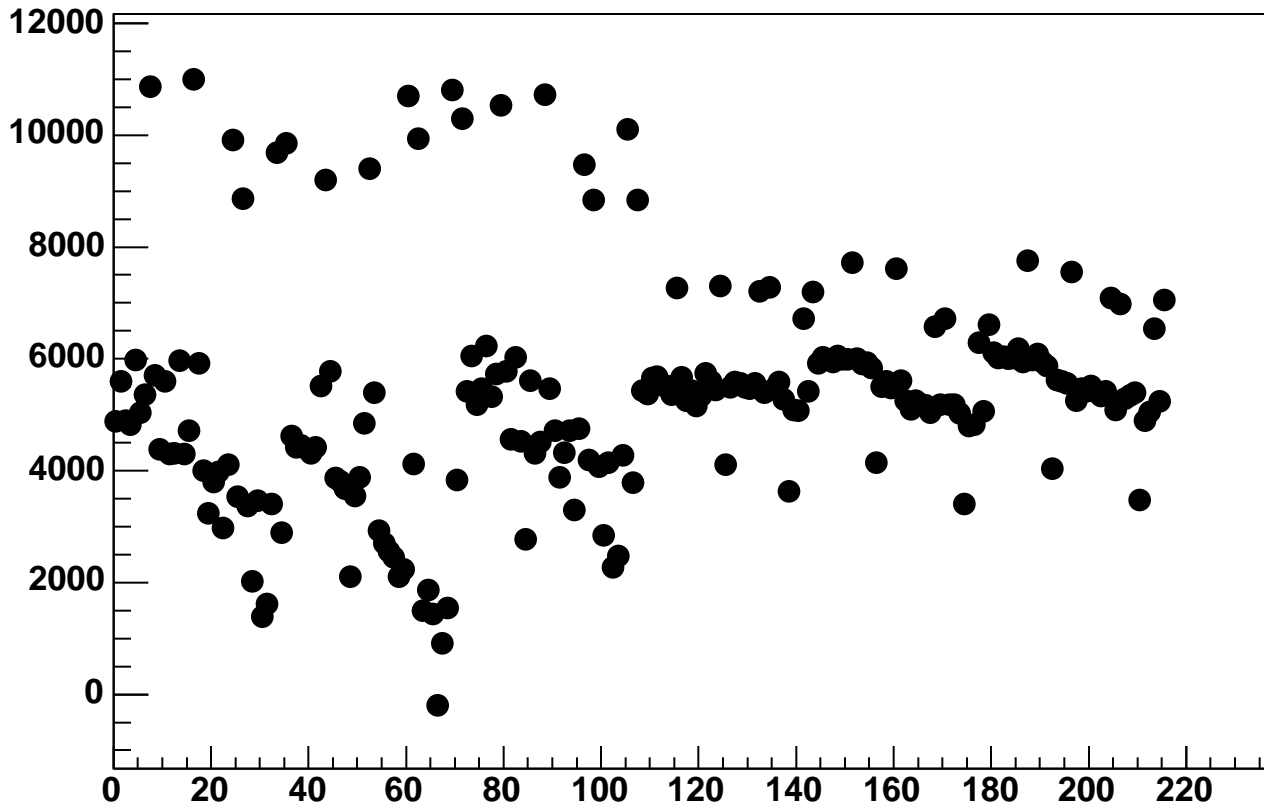
Enable 5, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



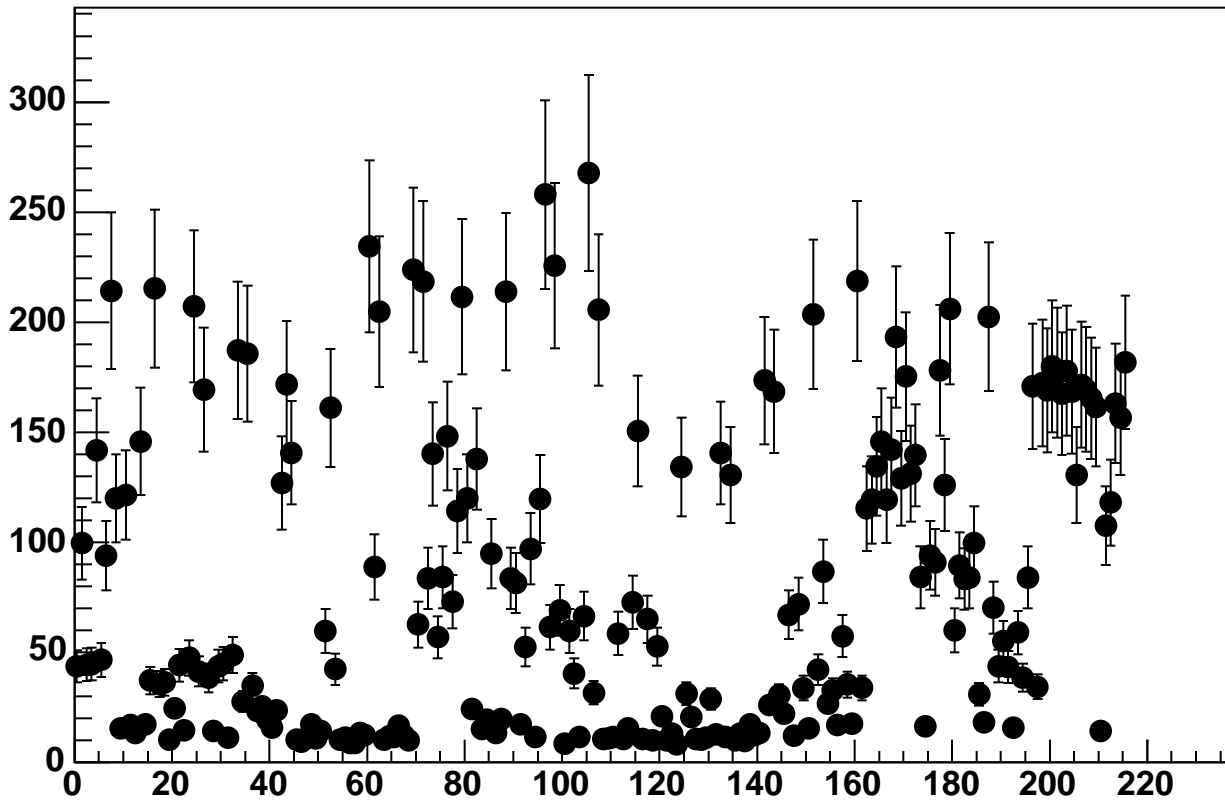
Enable 5, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



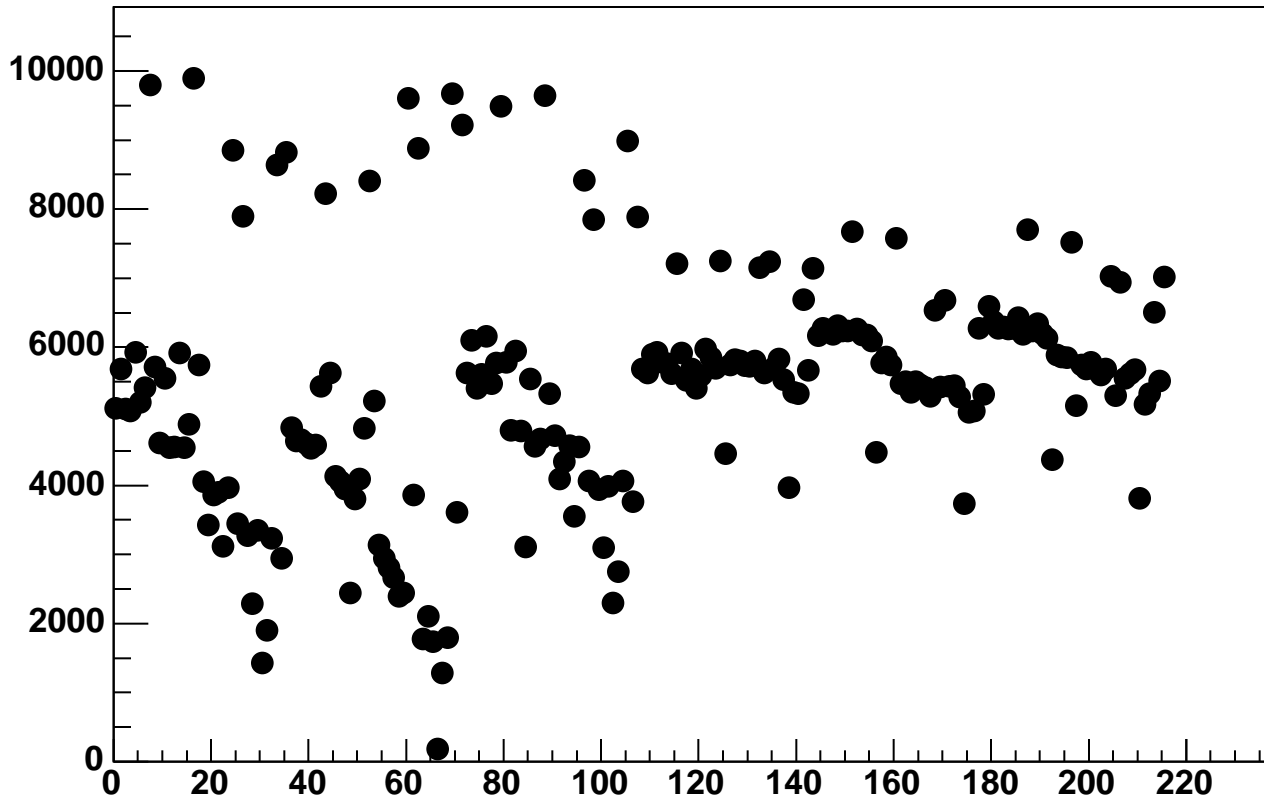
Enable 5, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



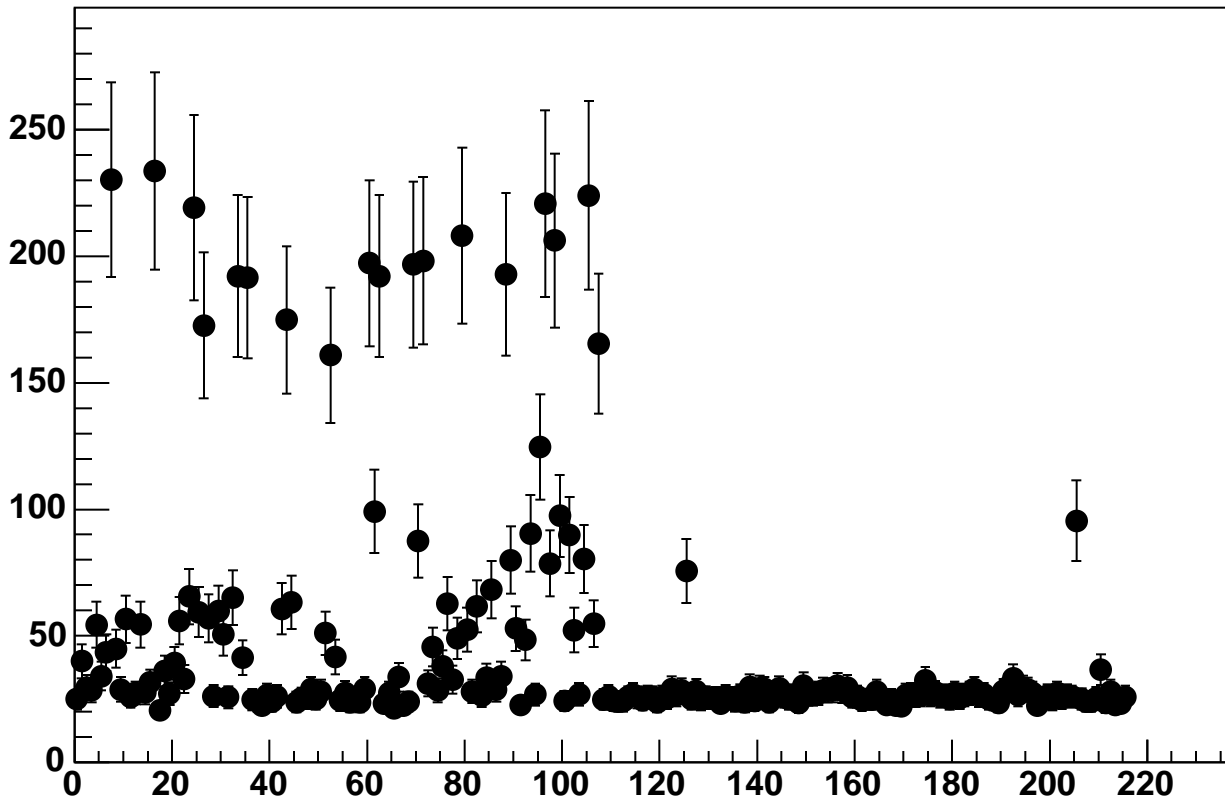
Enable 5, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



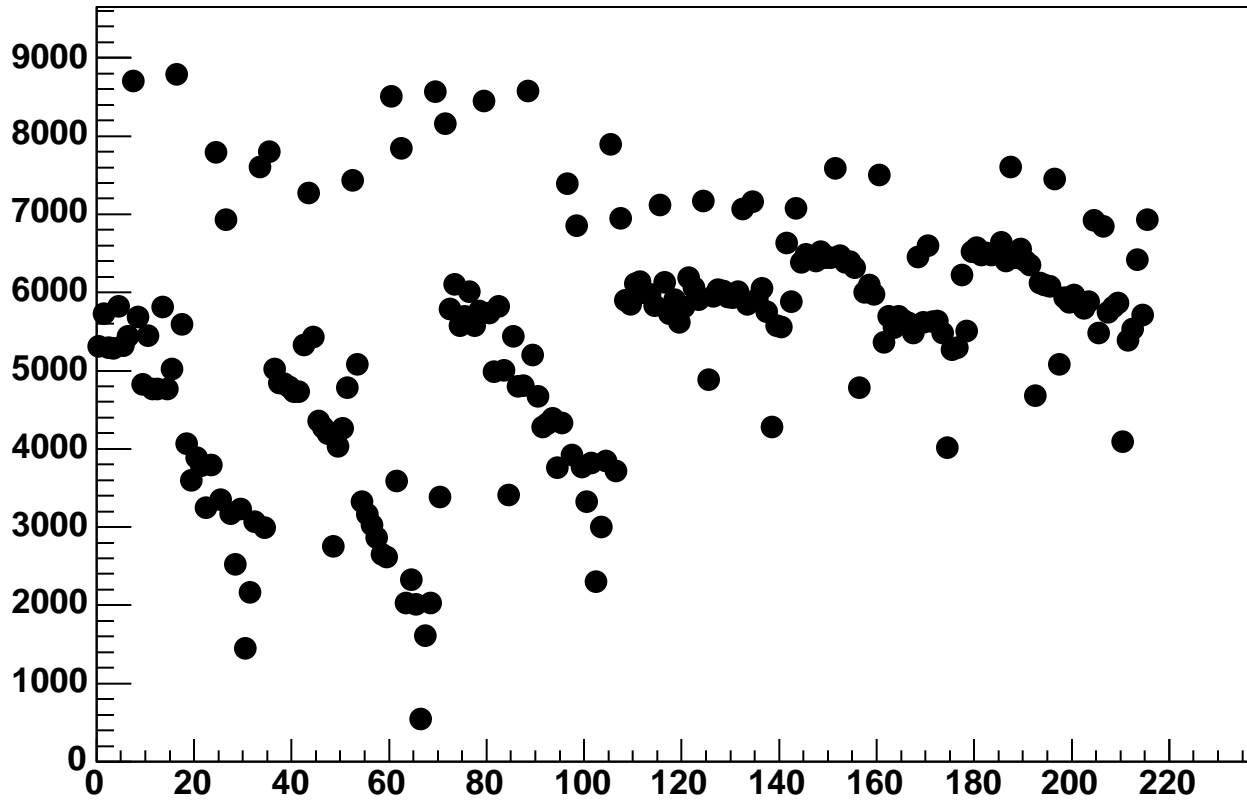
Enable 5, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



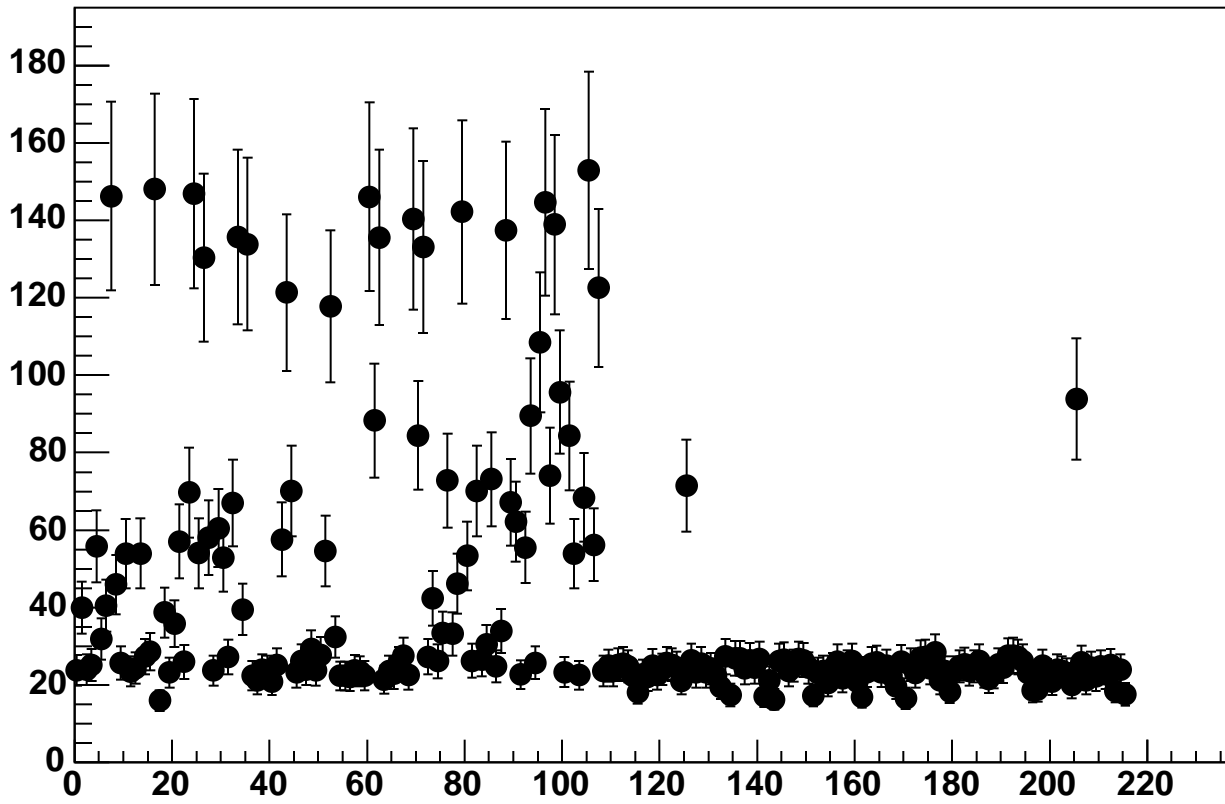
Enable 5, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



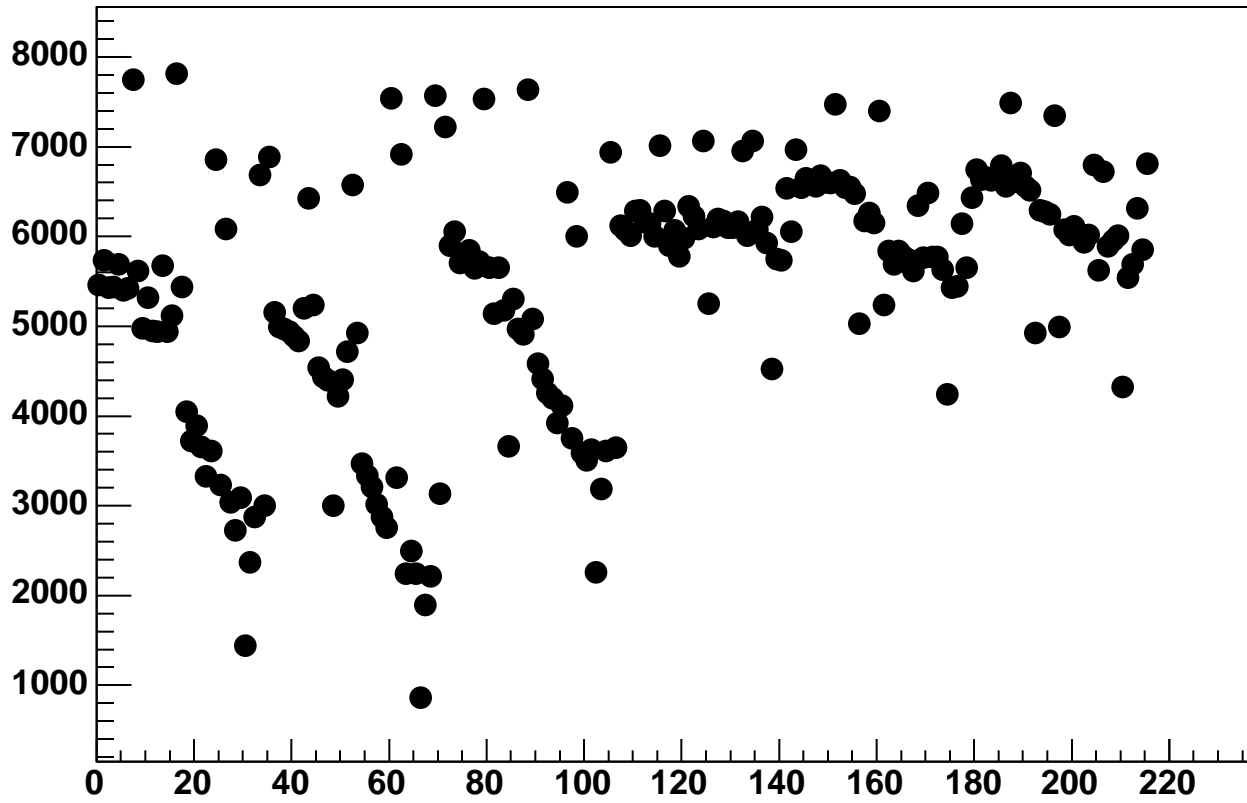
Enable 5, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



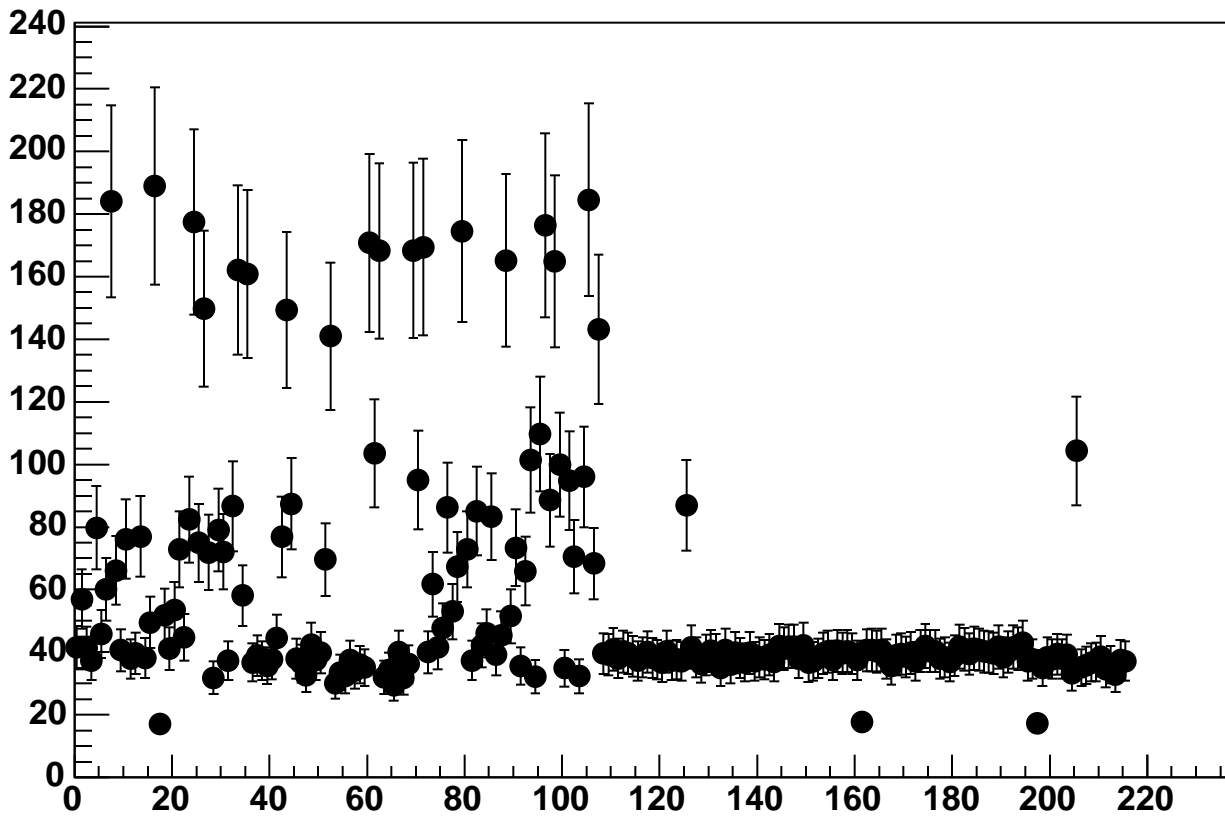
Enable 5, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



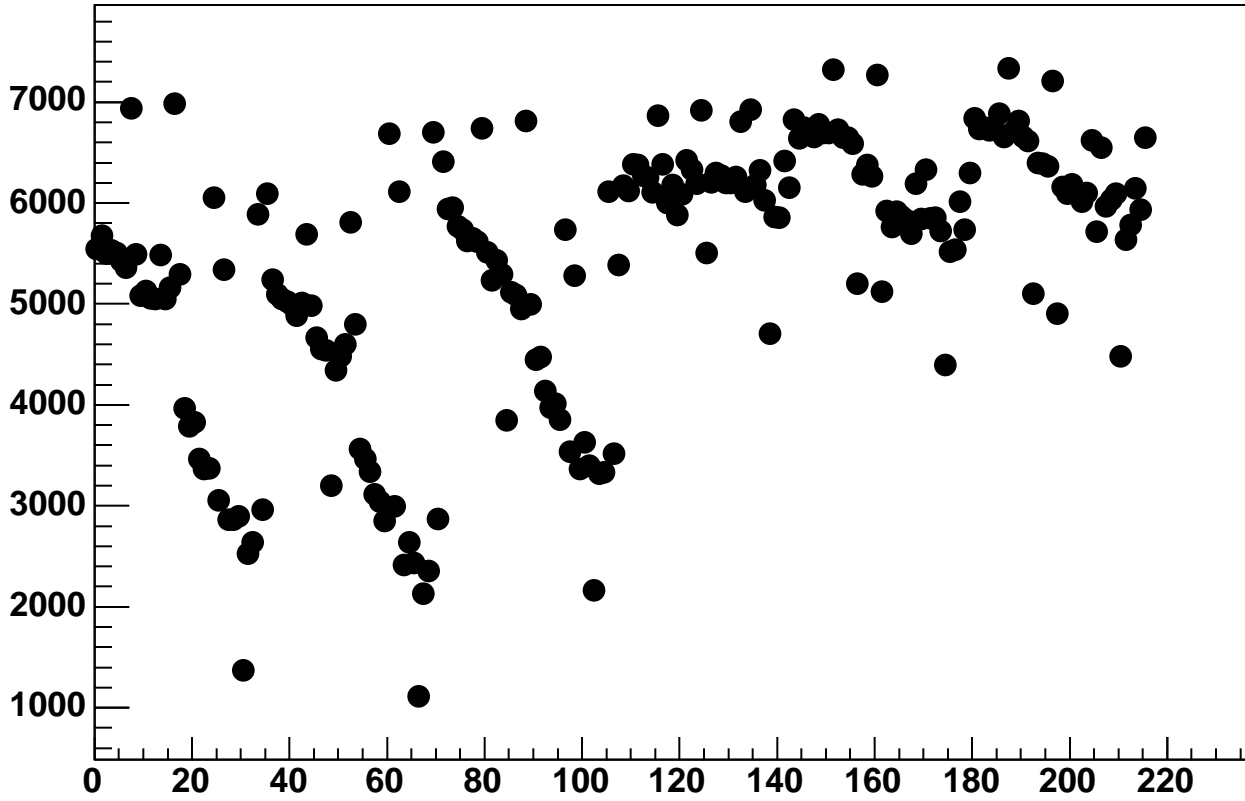
Enable 5, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



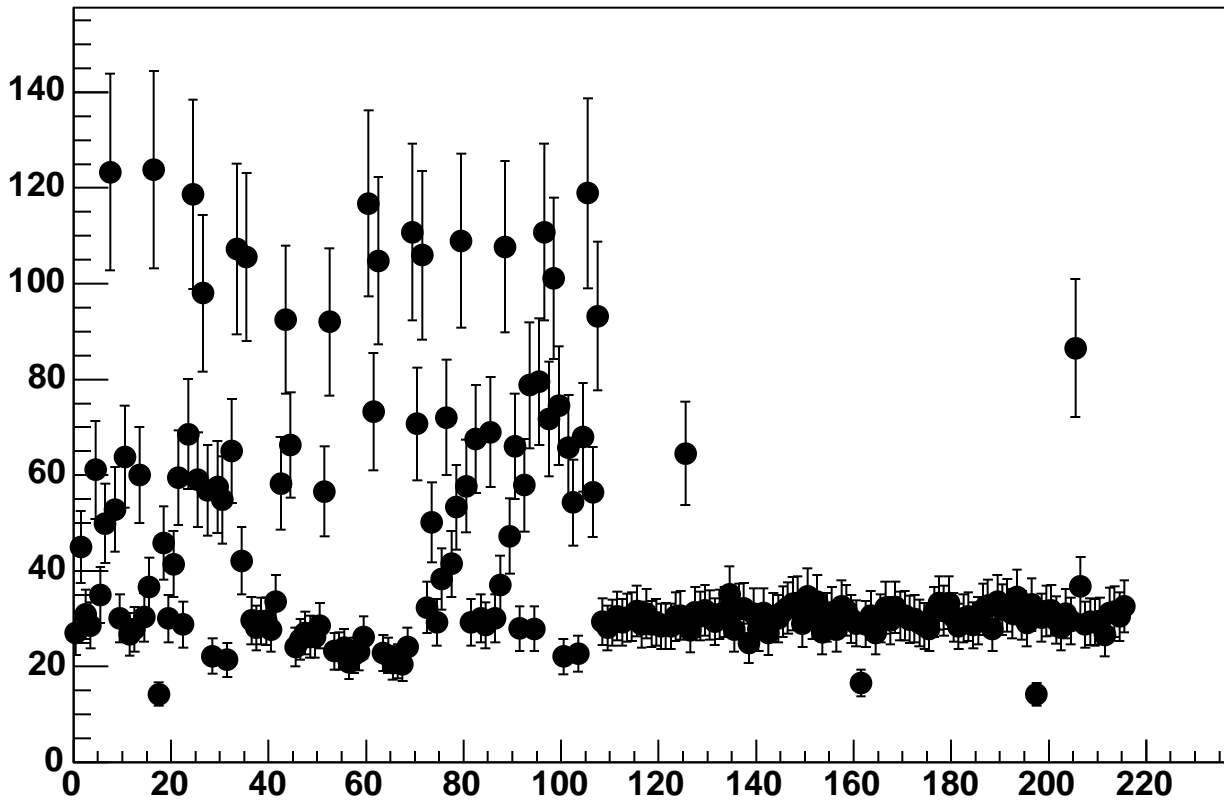
Enable 5, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



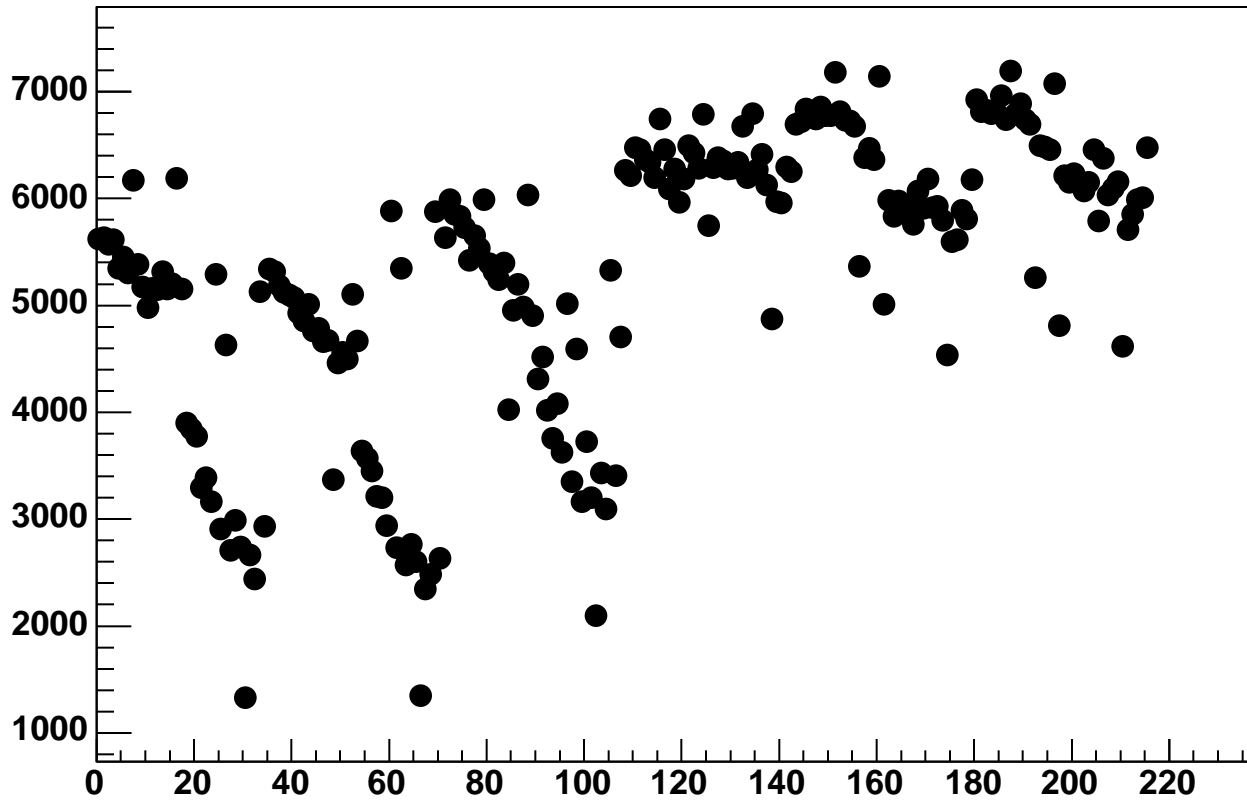
Enable 5, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



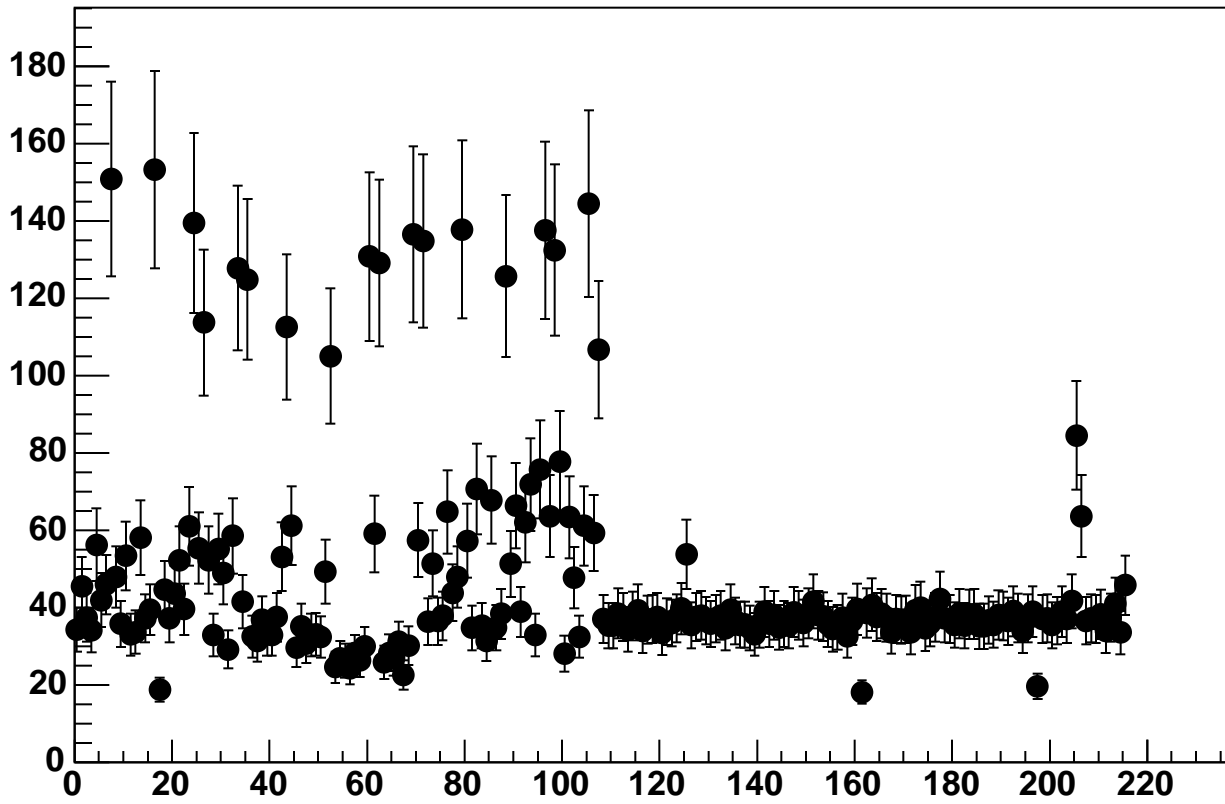
Enable 5, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



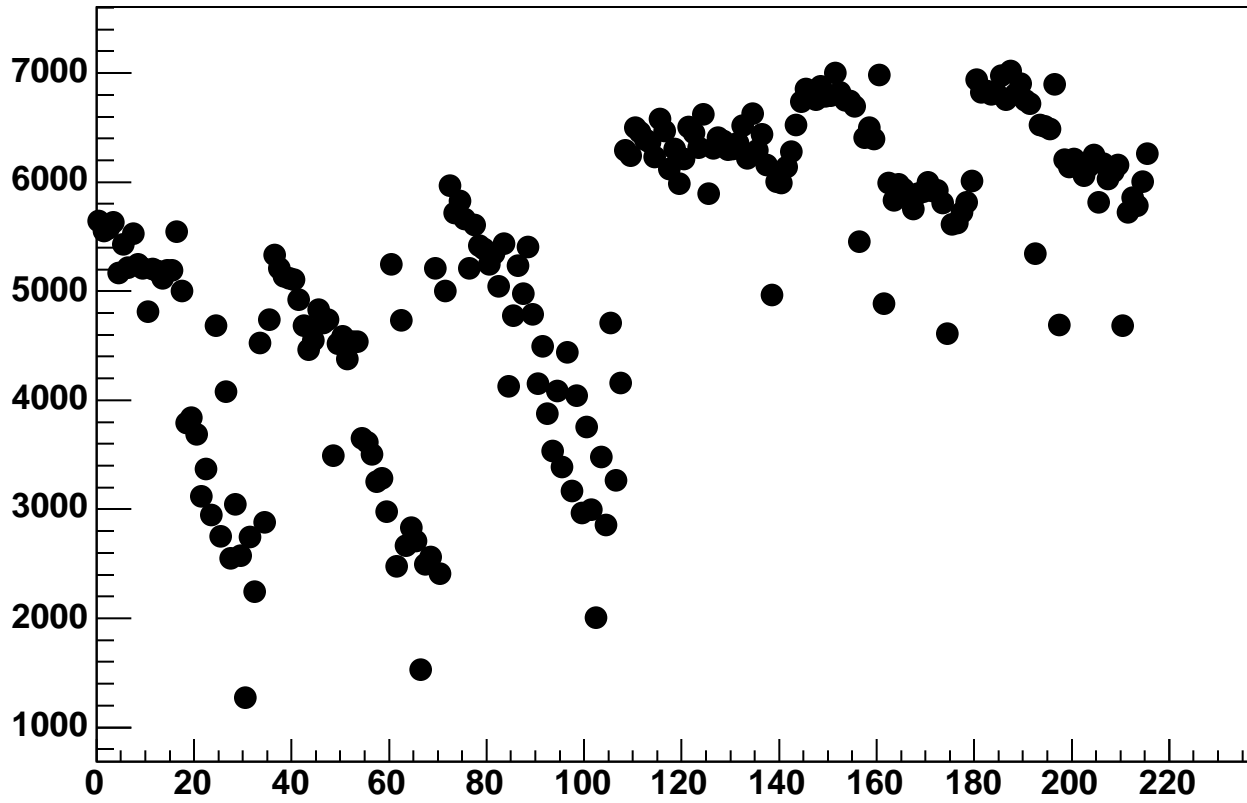
Enable 5, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



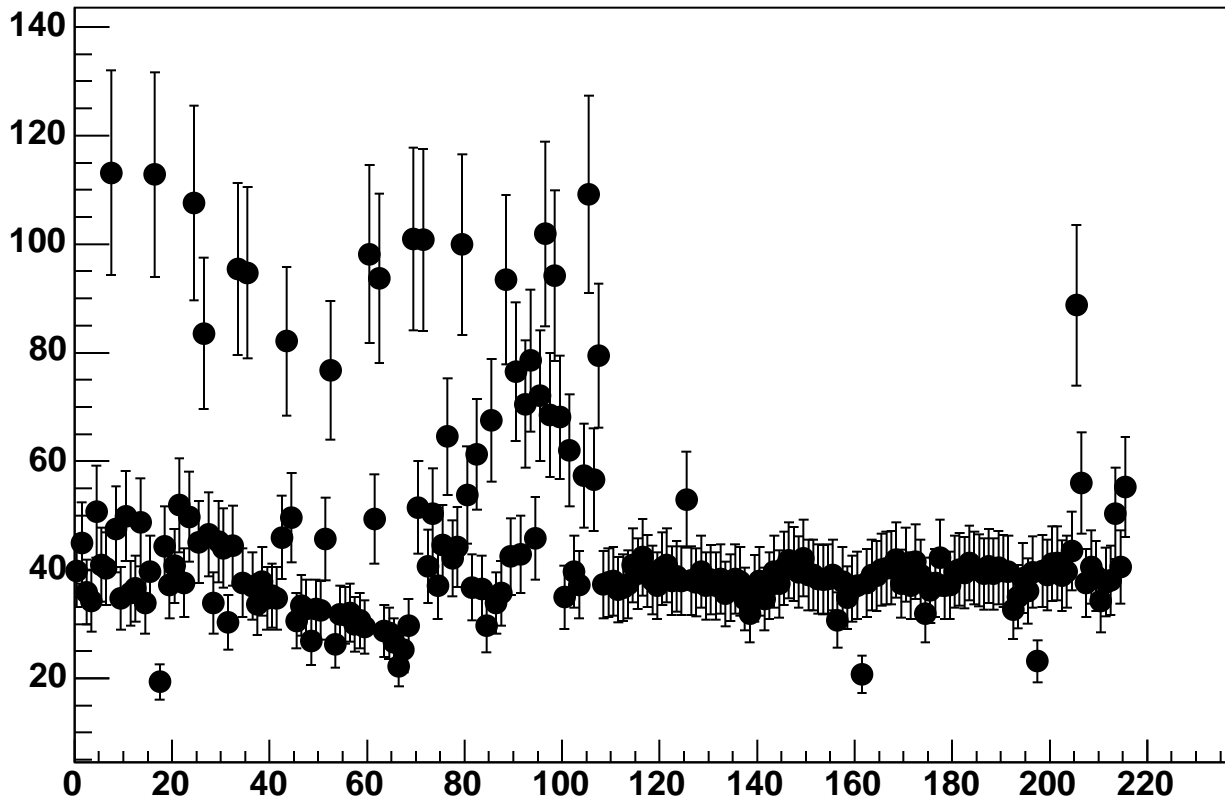
Enable 5, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



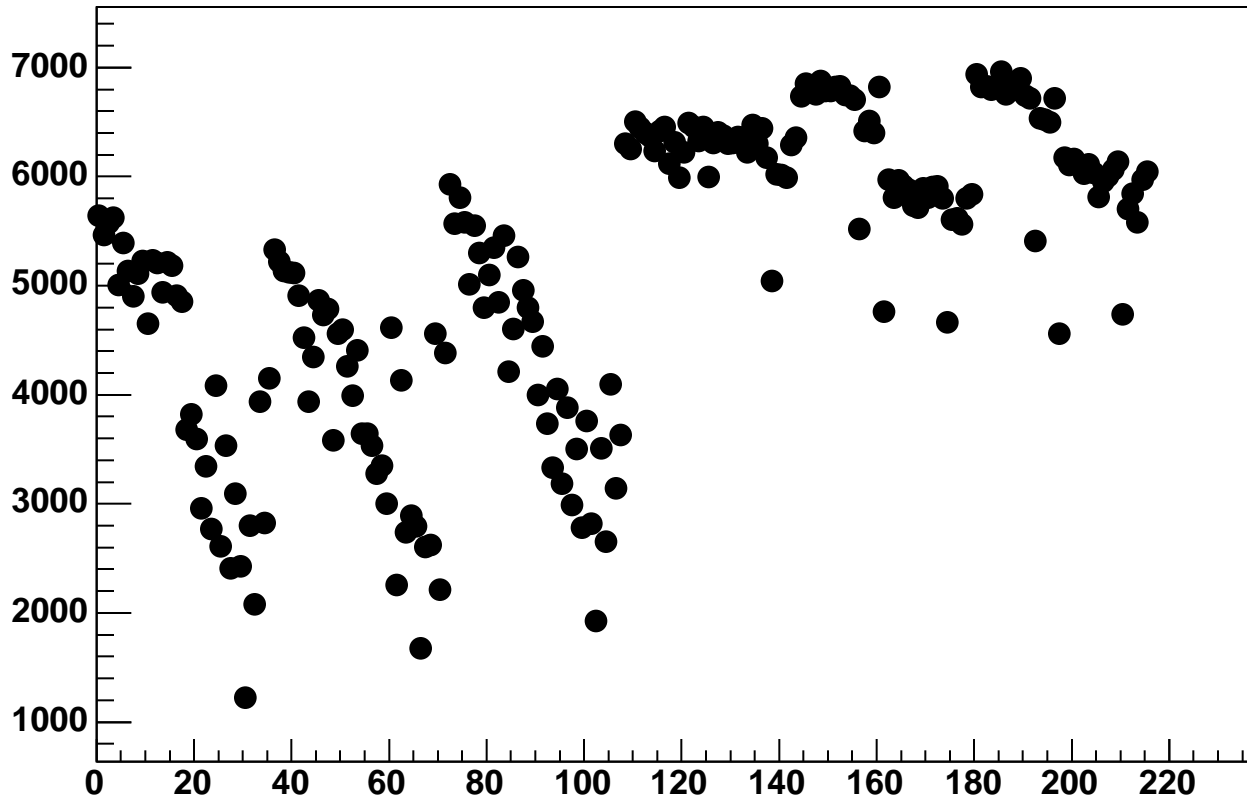
Enable 5, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



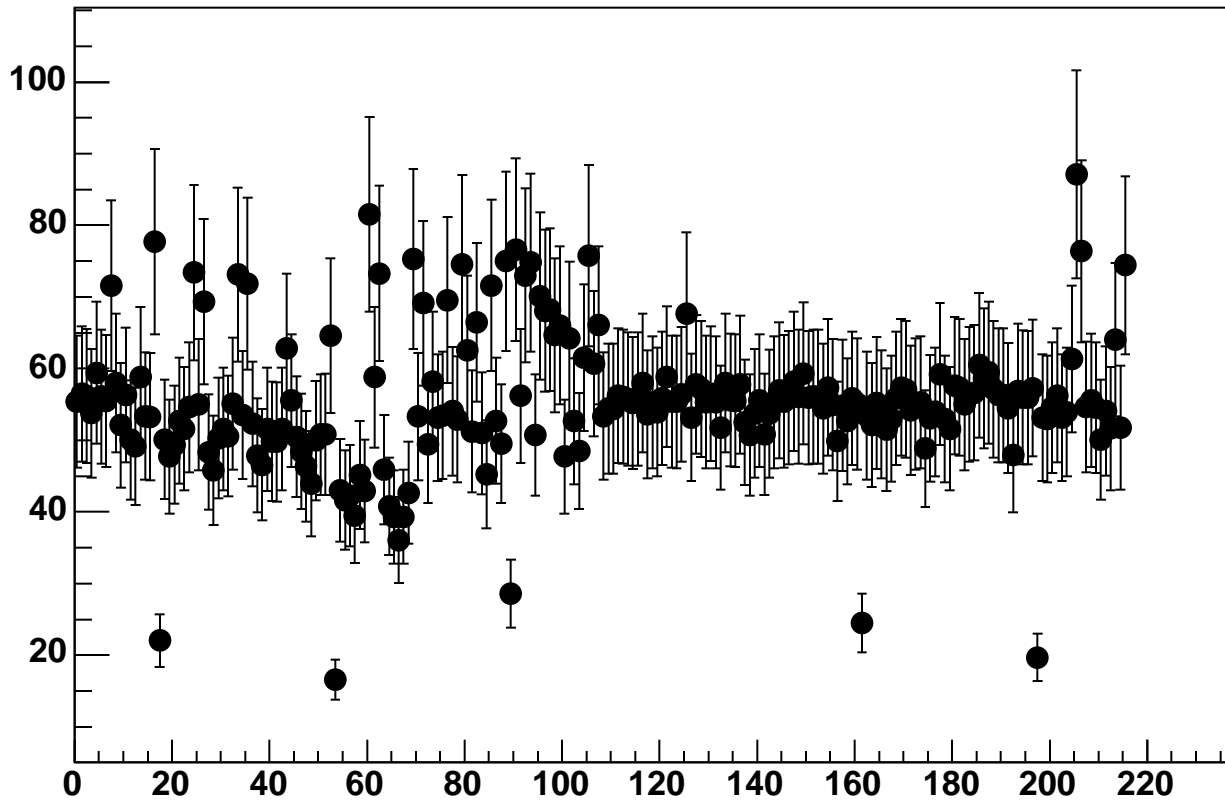
Enable 5, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



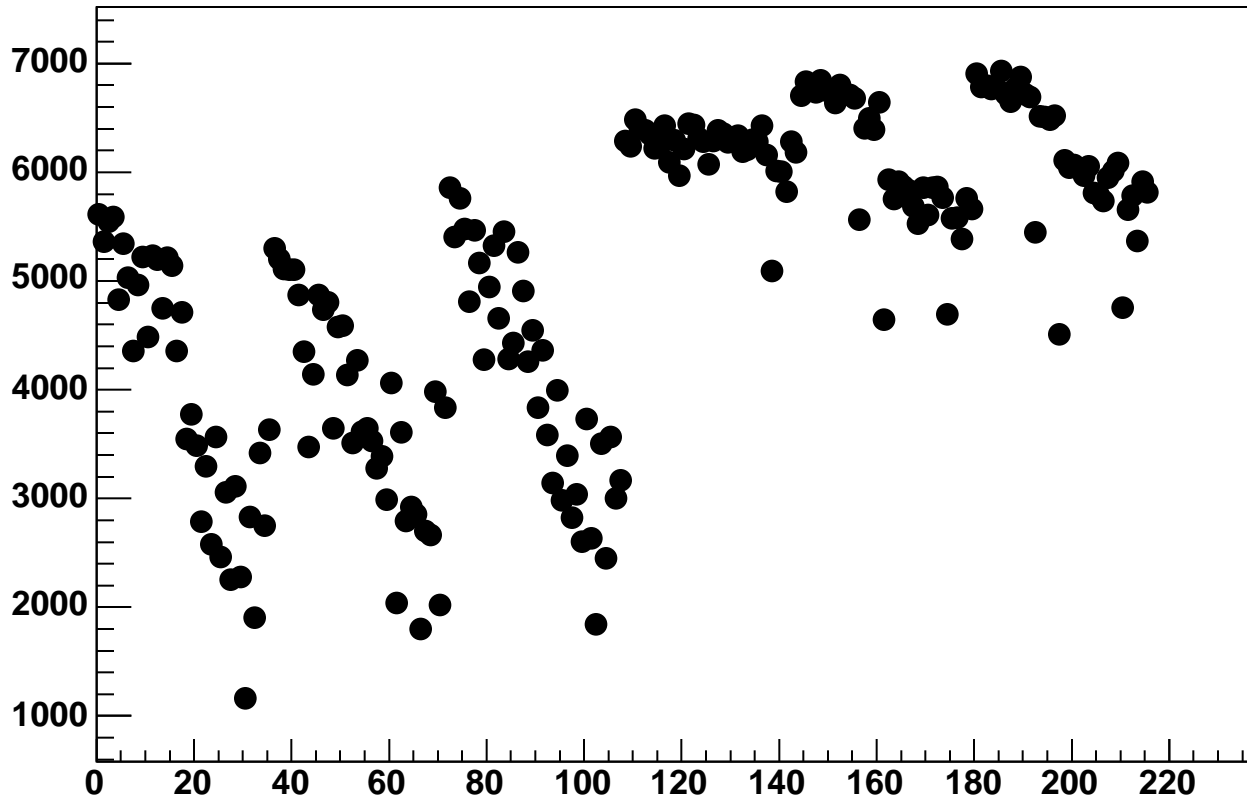
Enable 5, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



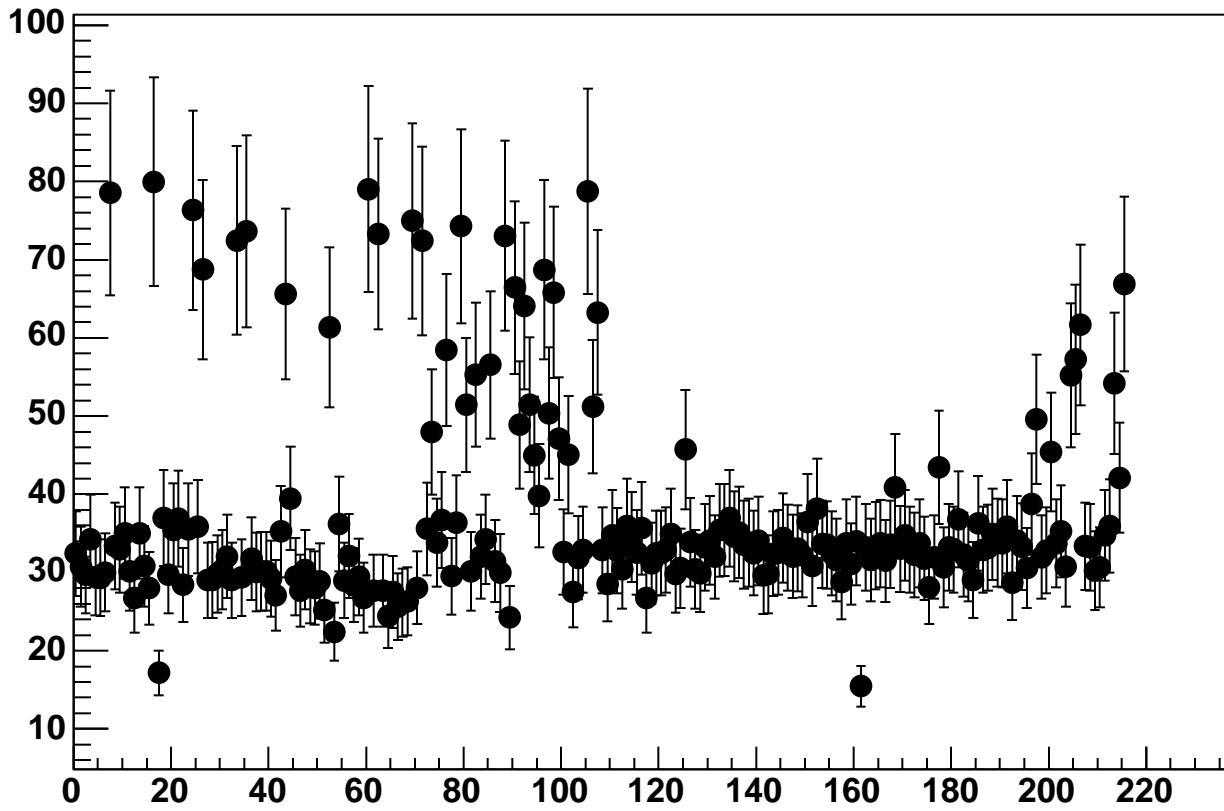
Enable 5, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



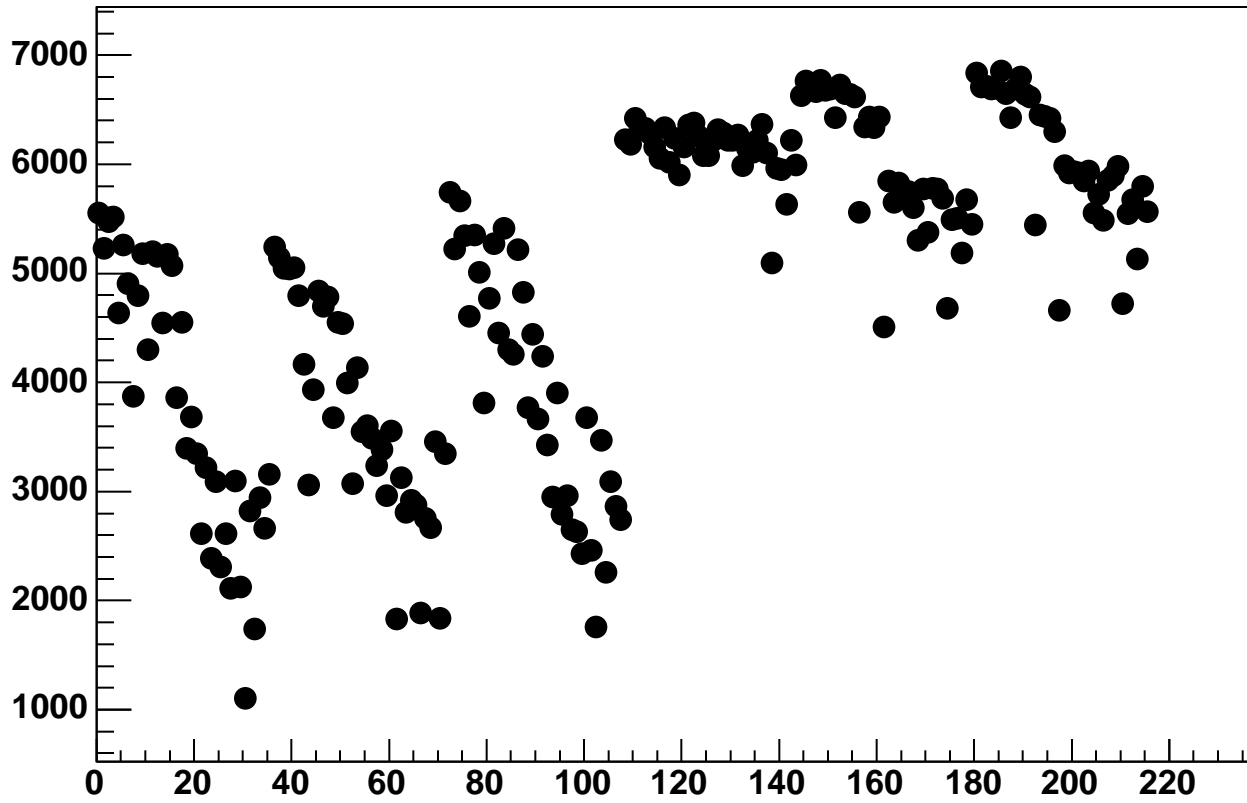
Enable 5, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



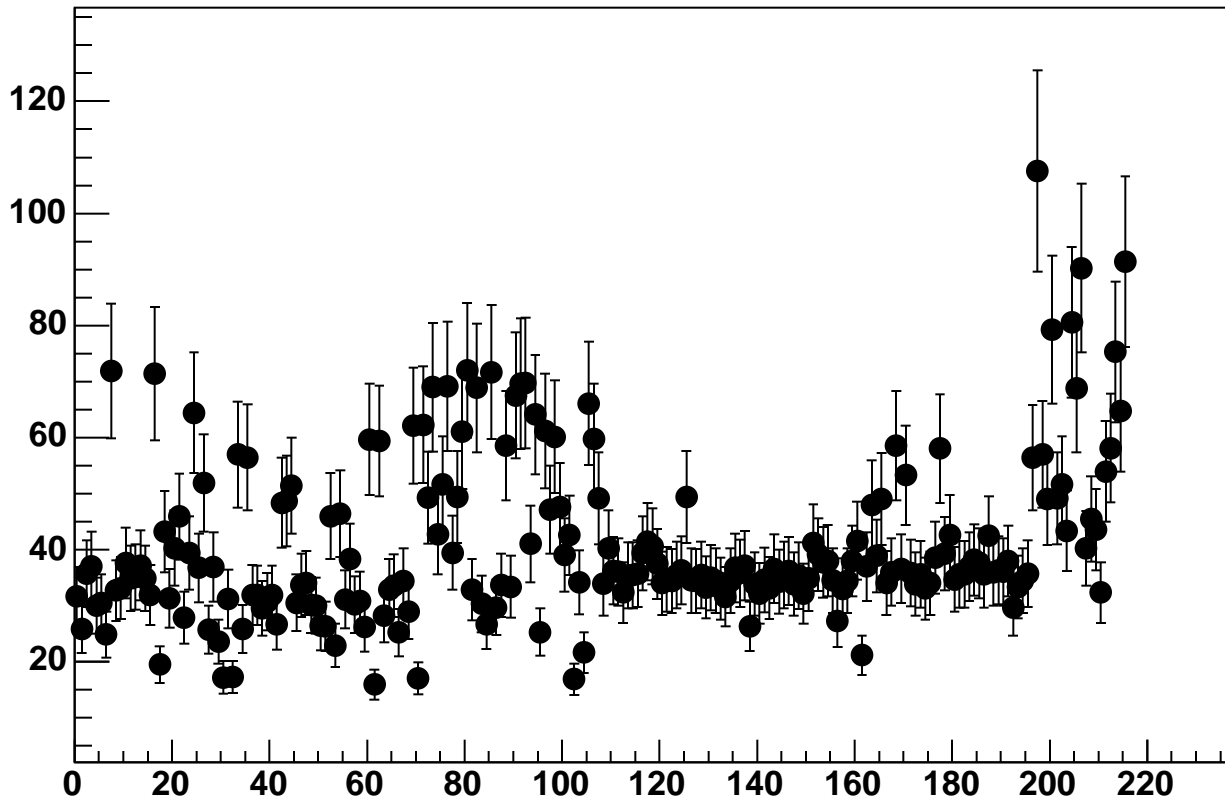
Enable 5, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



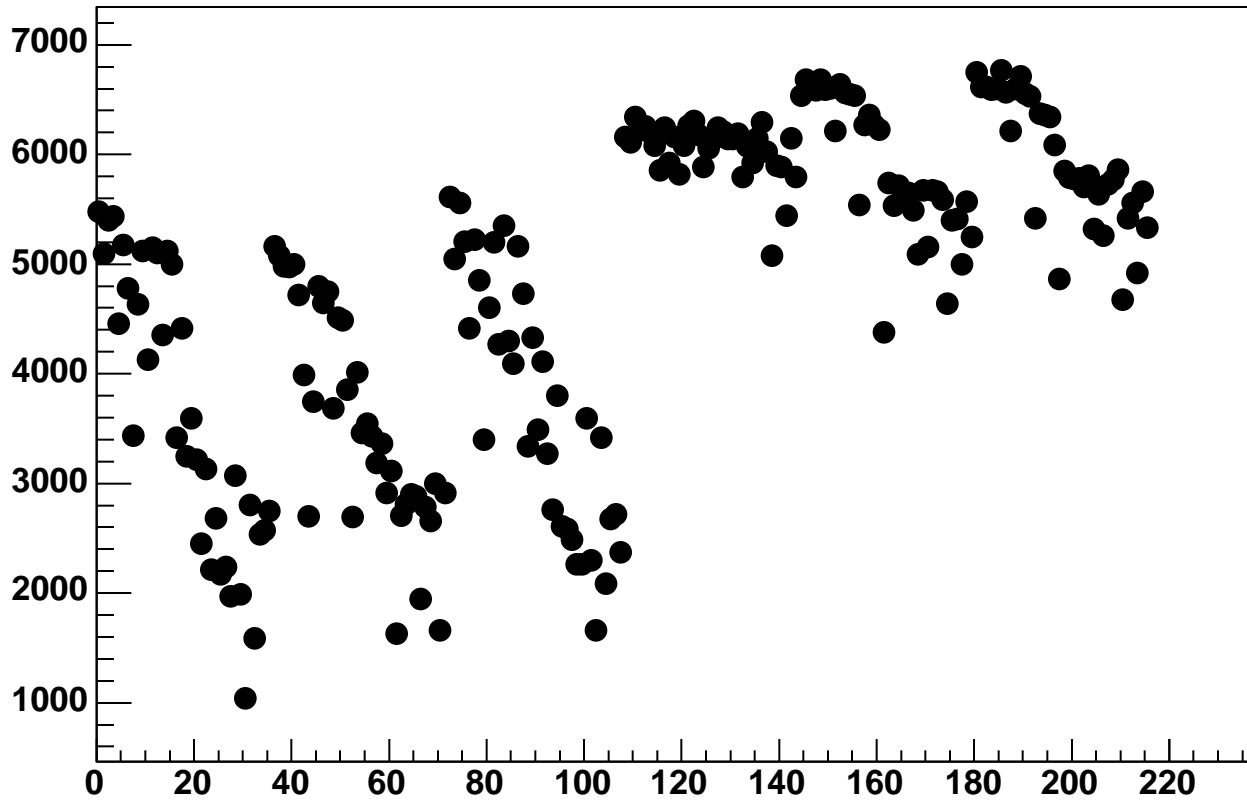
Enable 5, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



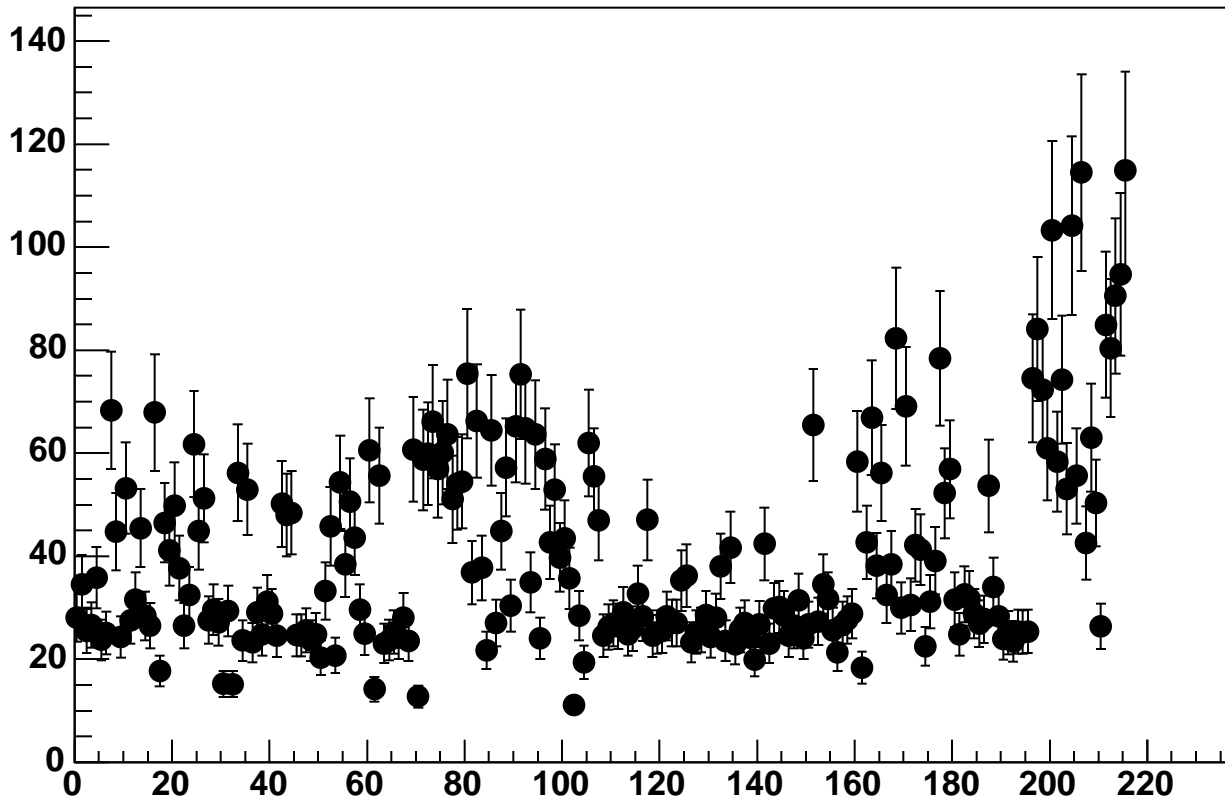
Enable 5, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



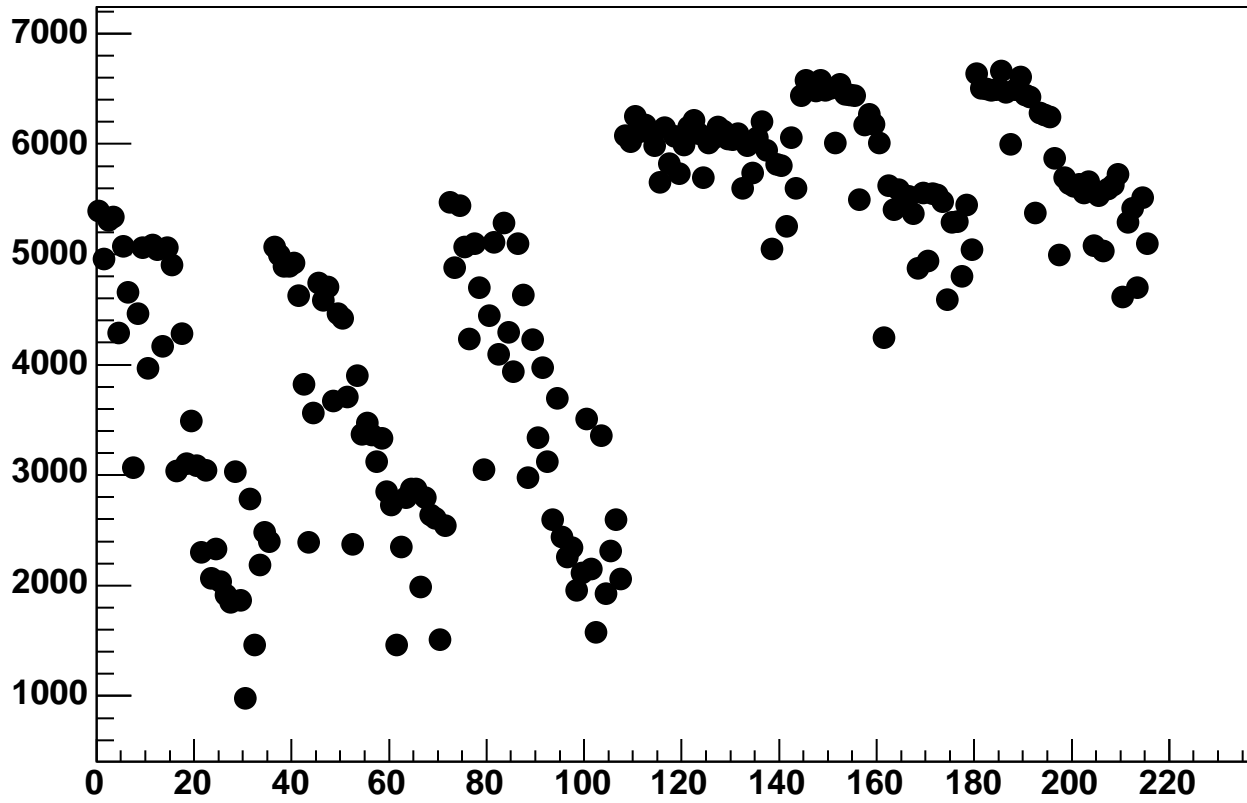
Enable 5, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



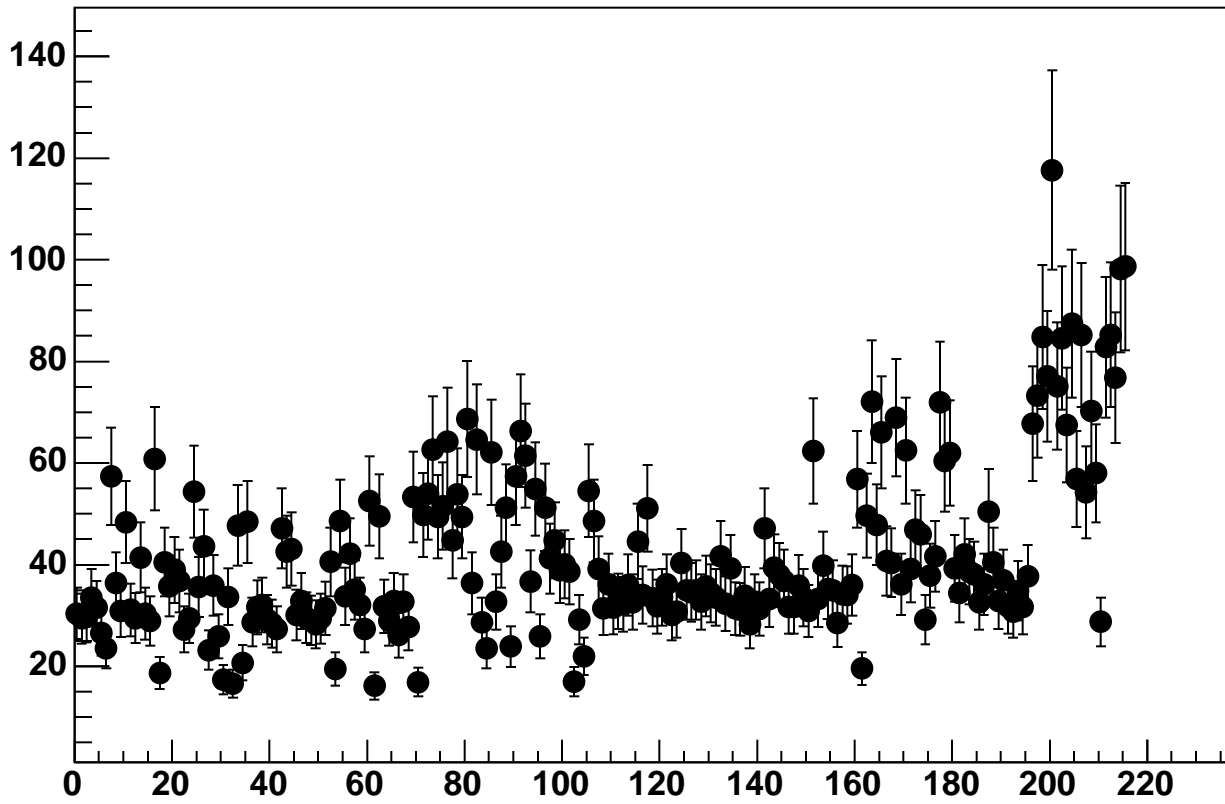
Enable 5, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



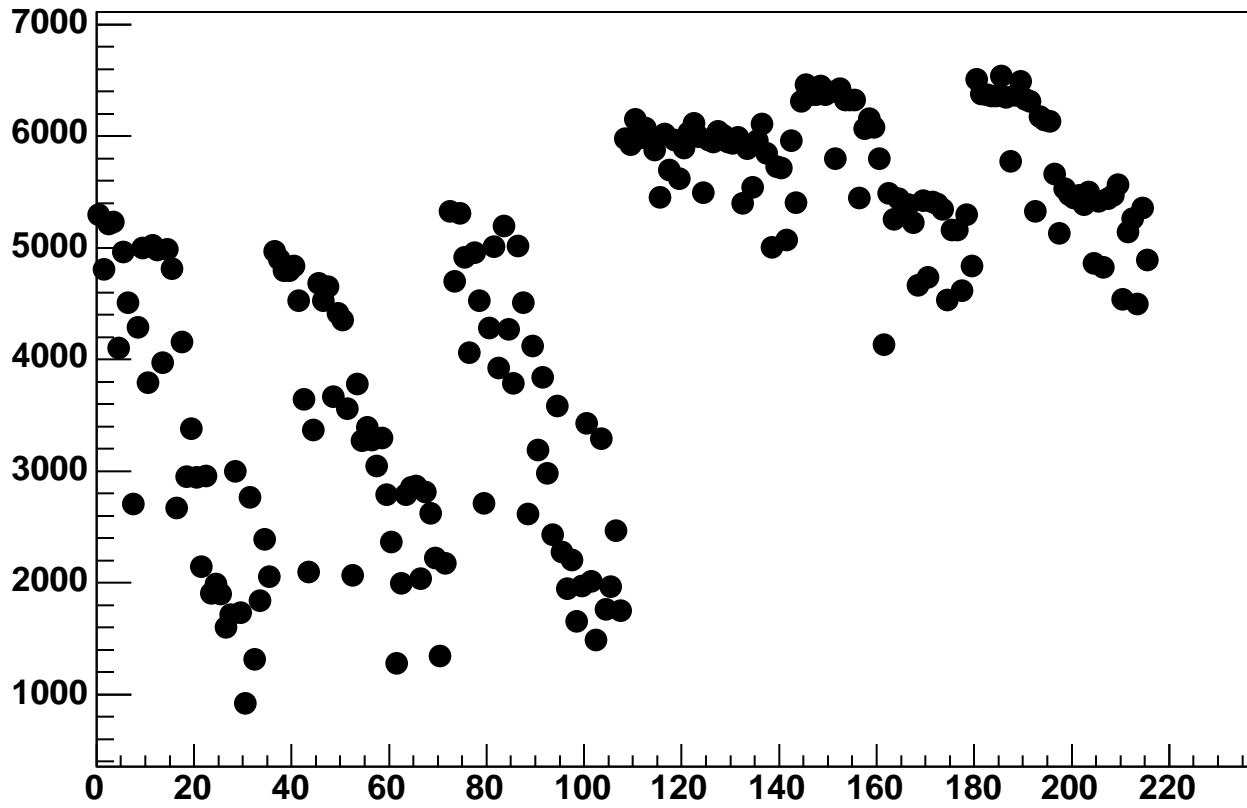
Enable 5, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



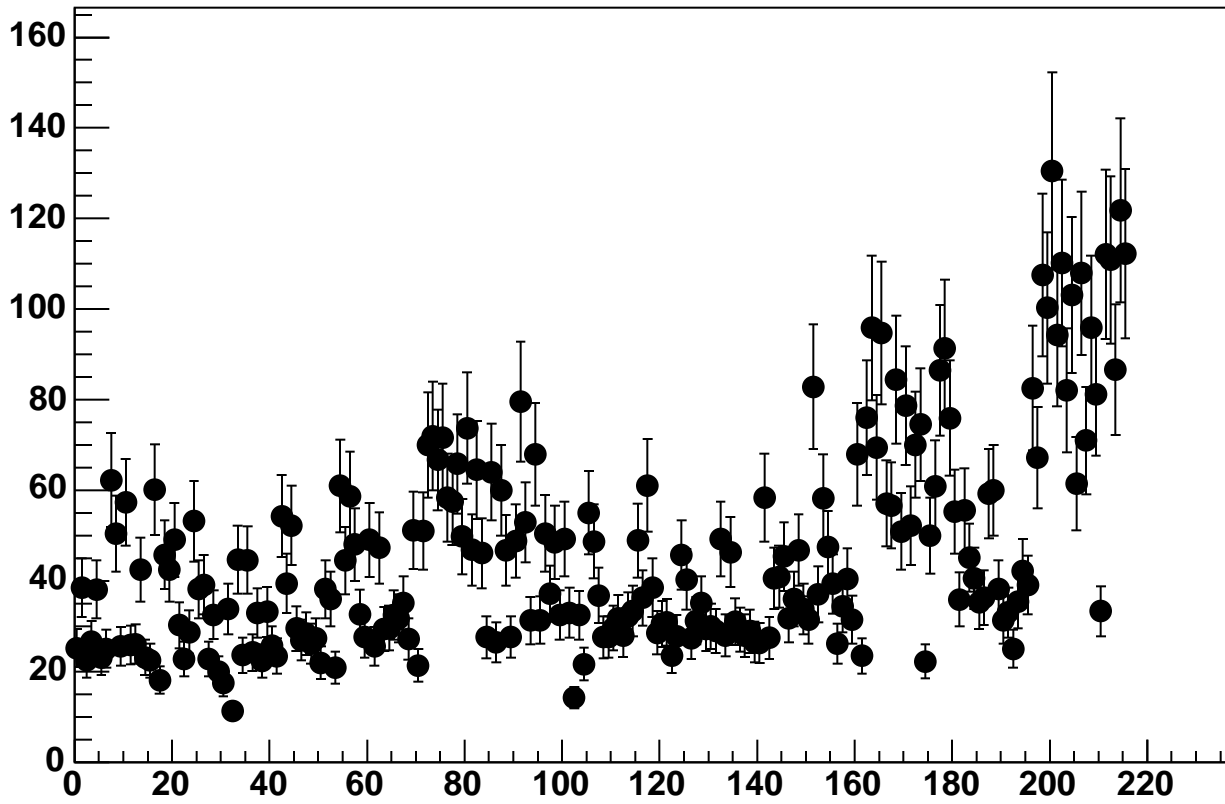
Enable 5, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



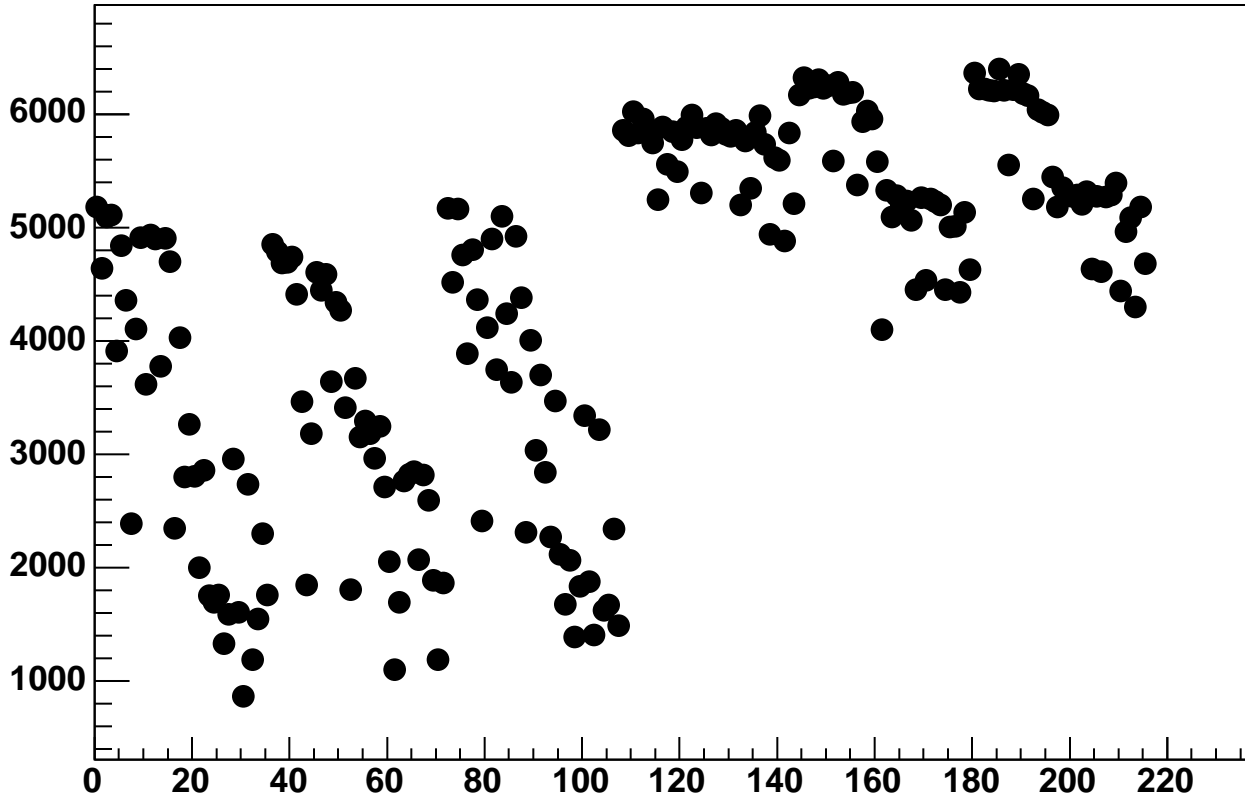
Enable 5, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



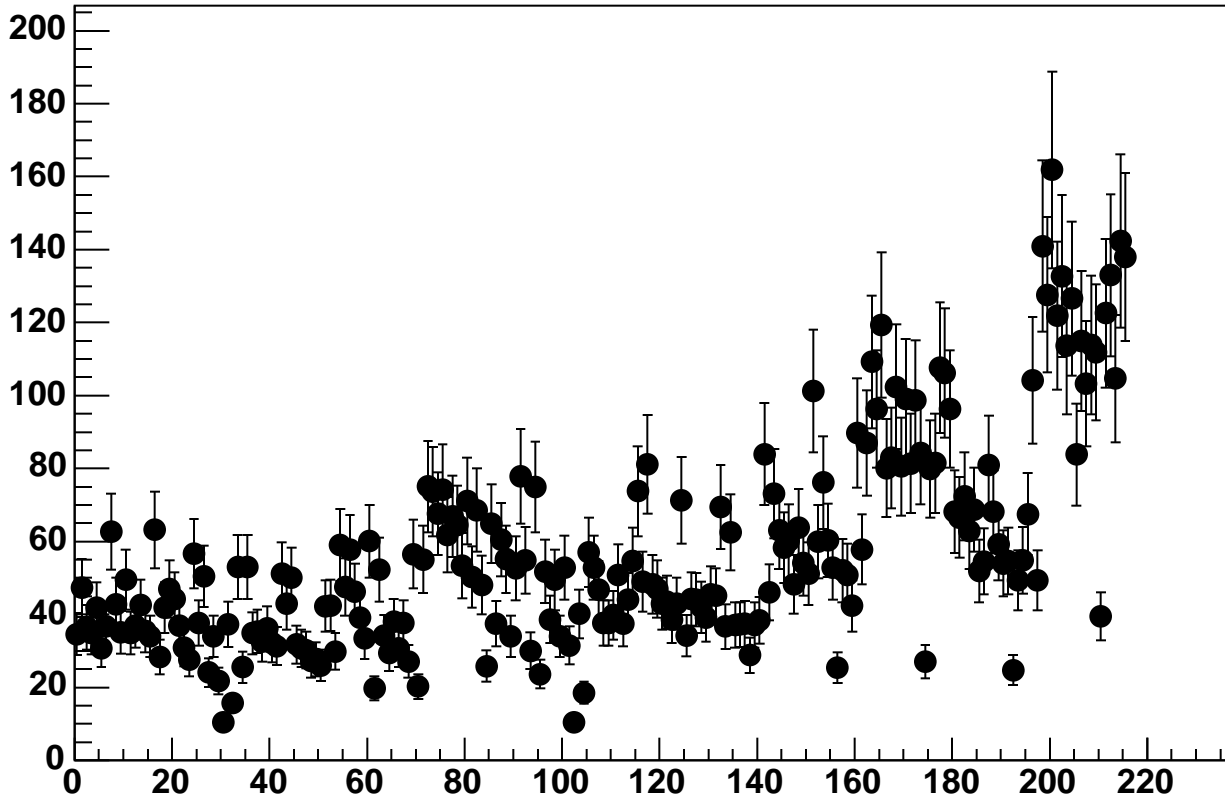
Enable 5, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



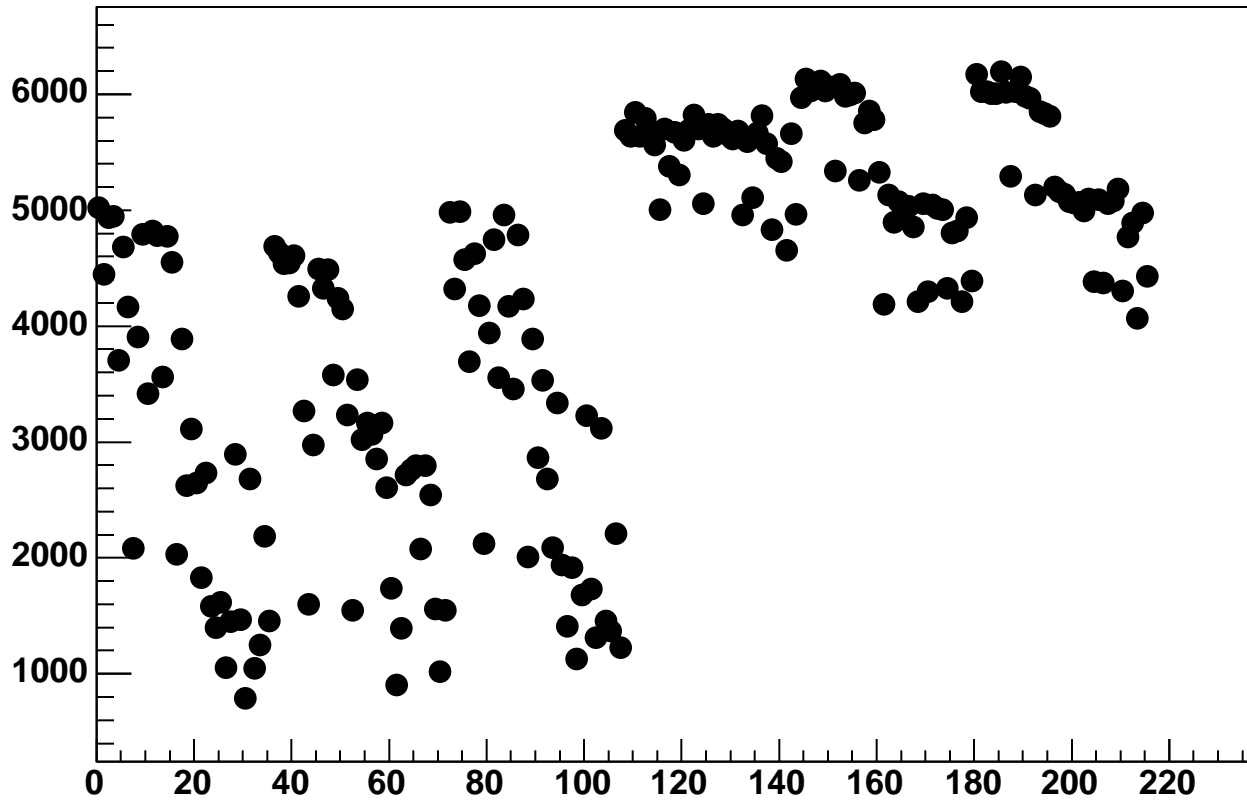
Enable 5, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



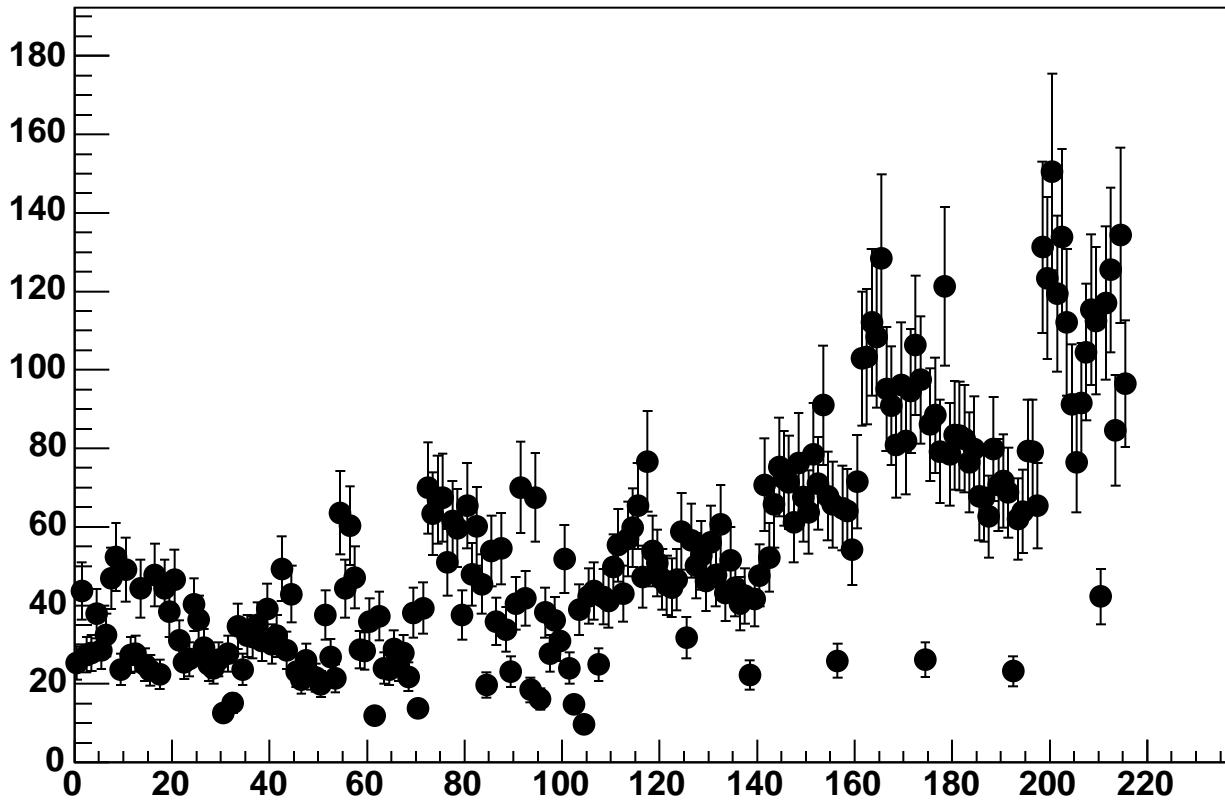
Enable 5, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



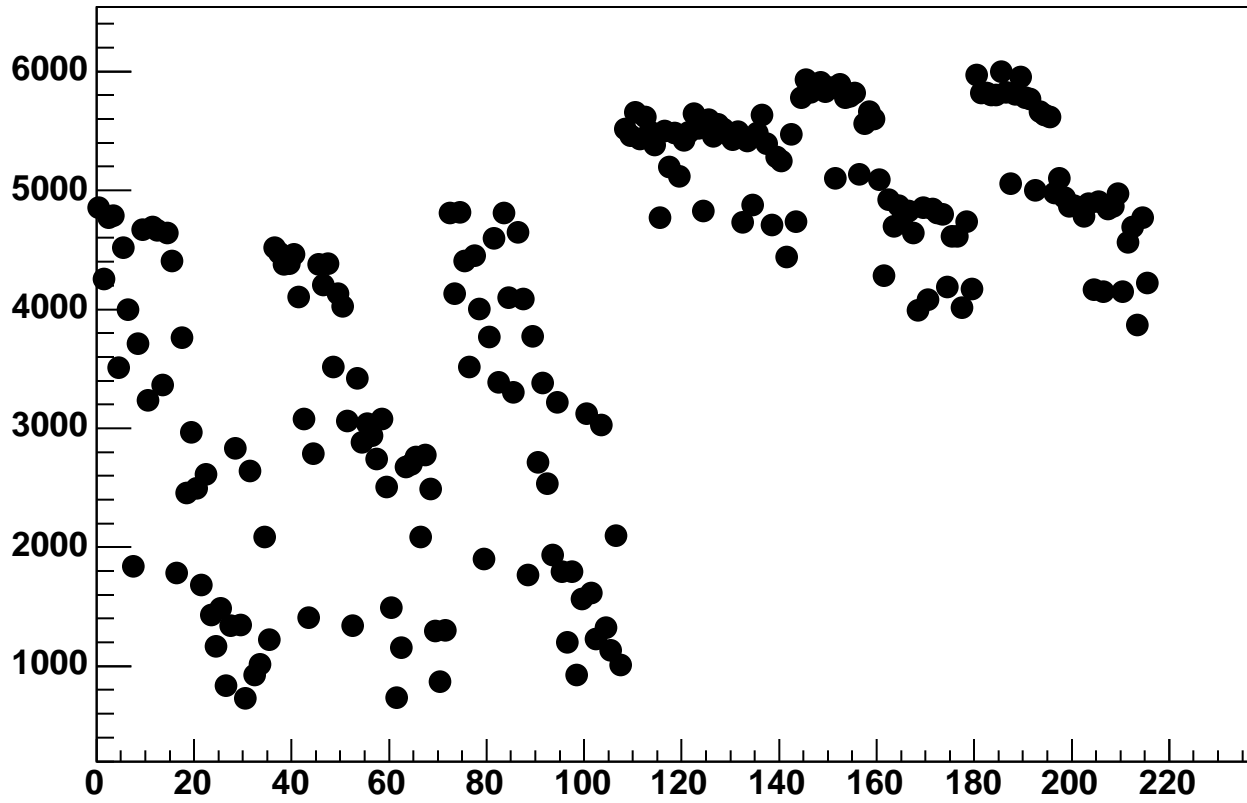
Enable 5, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



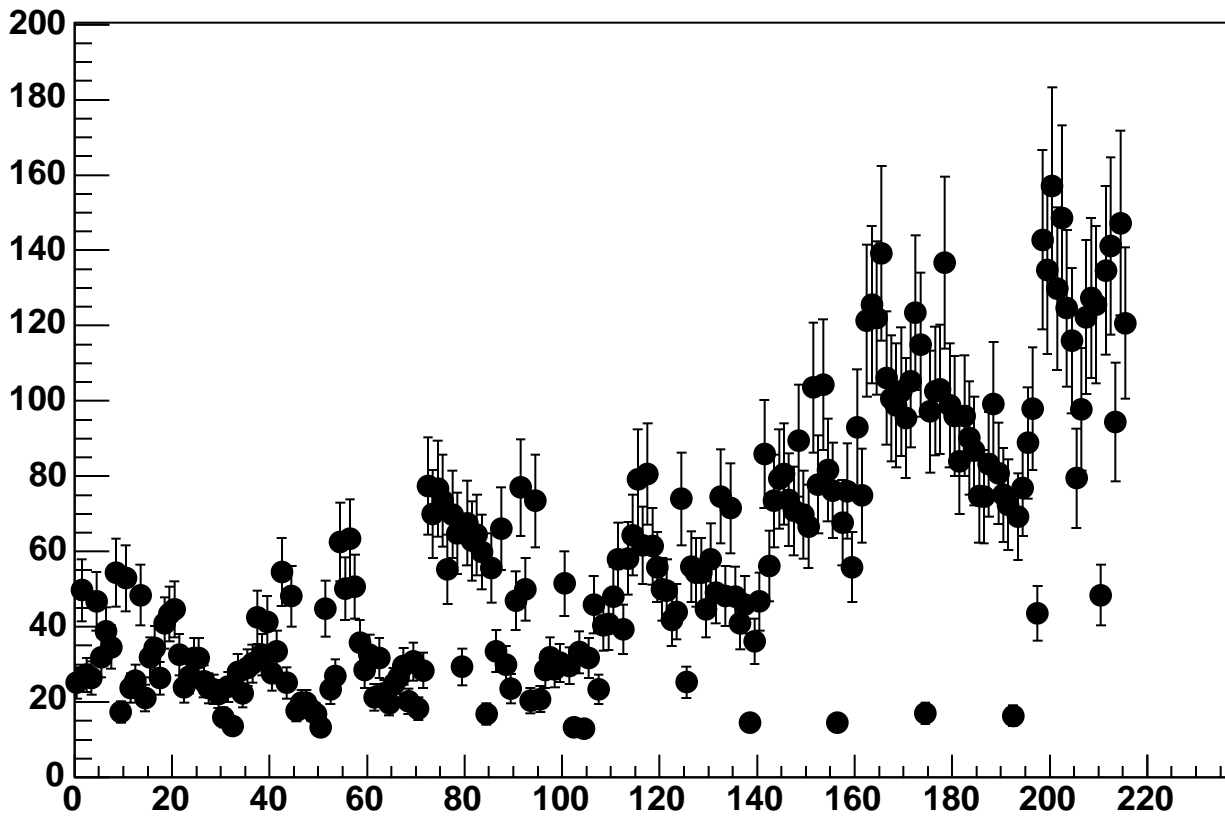
Enable 5, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



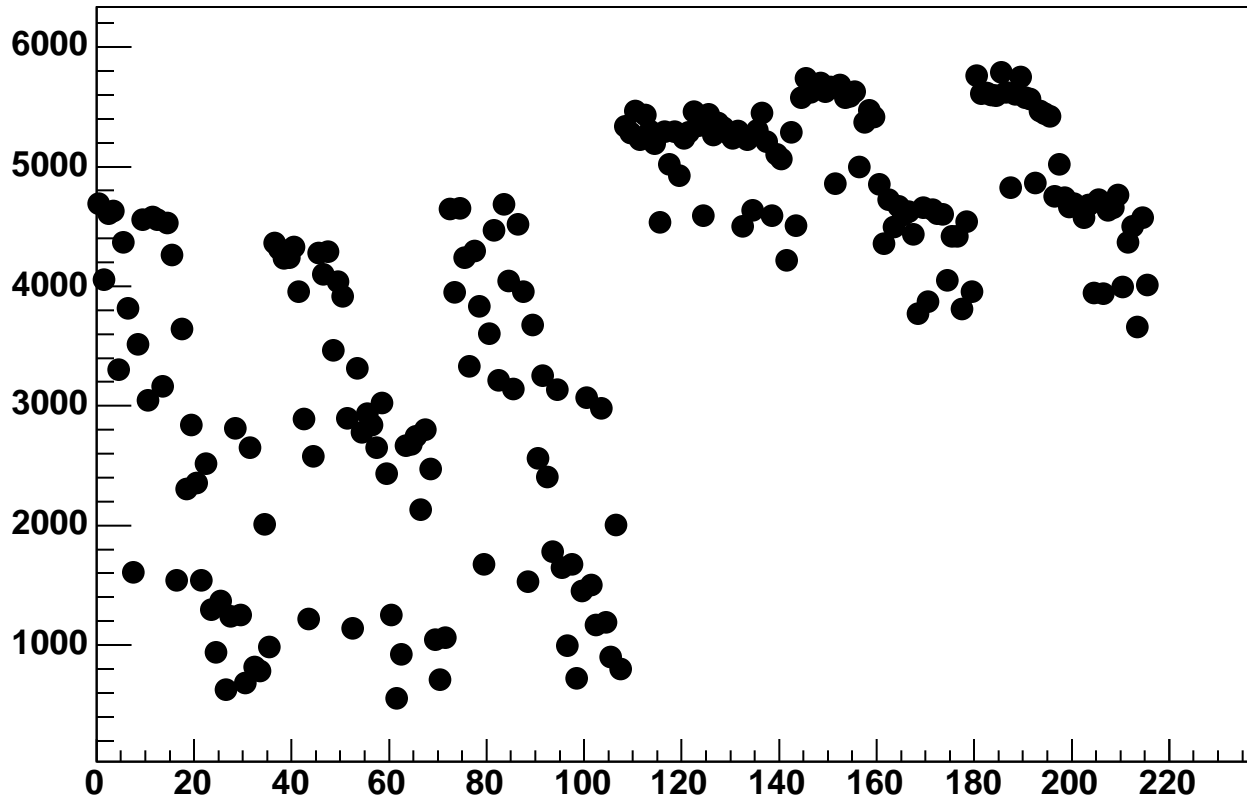
Enable 5, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



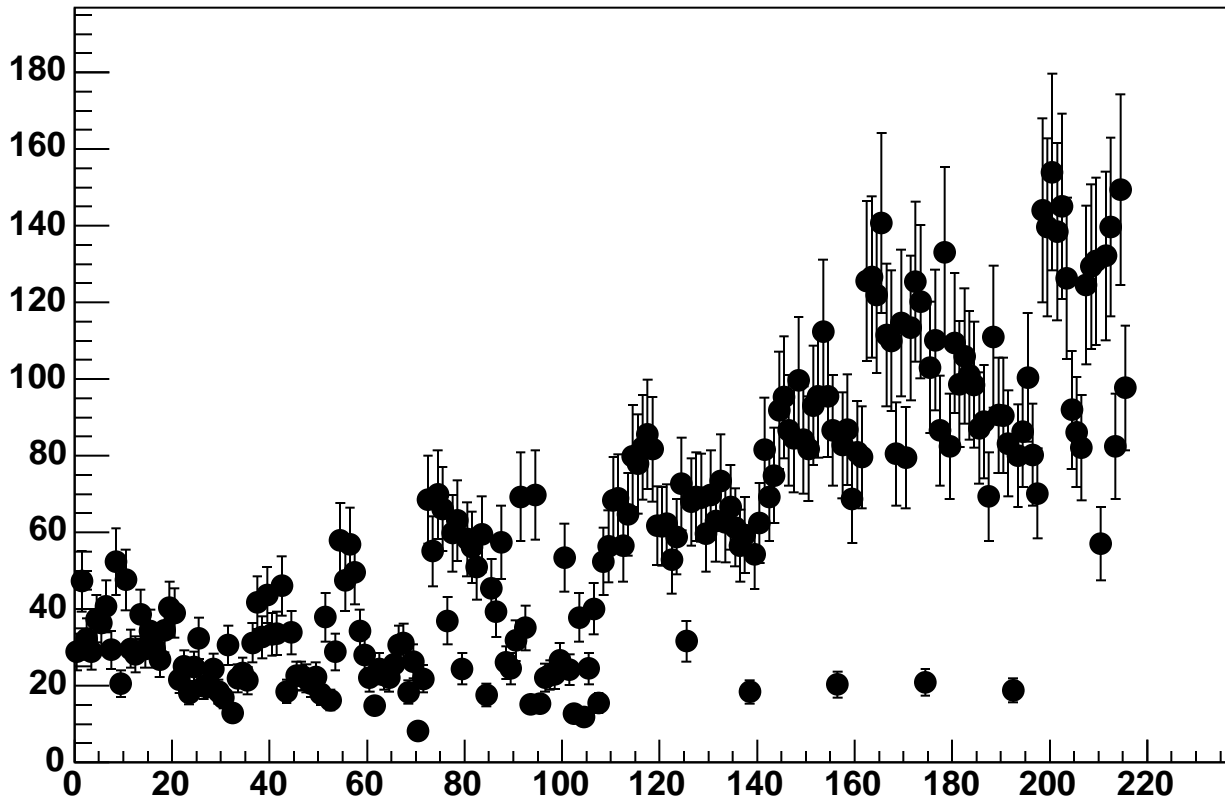
Enable 5, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



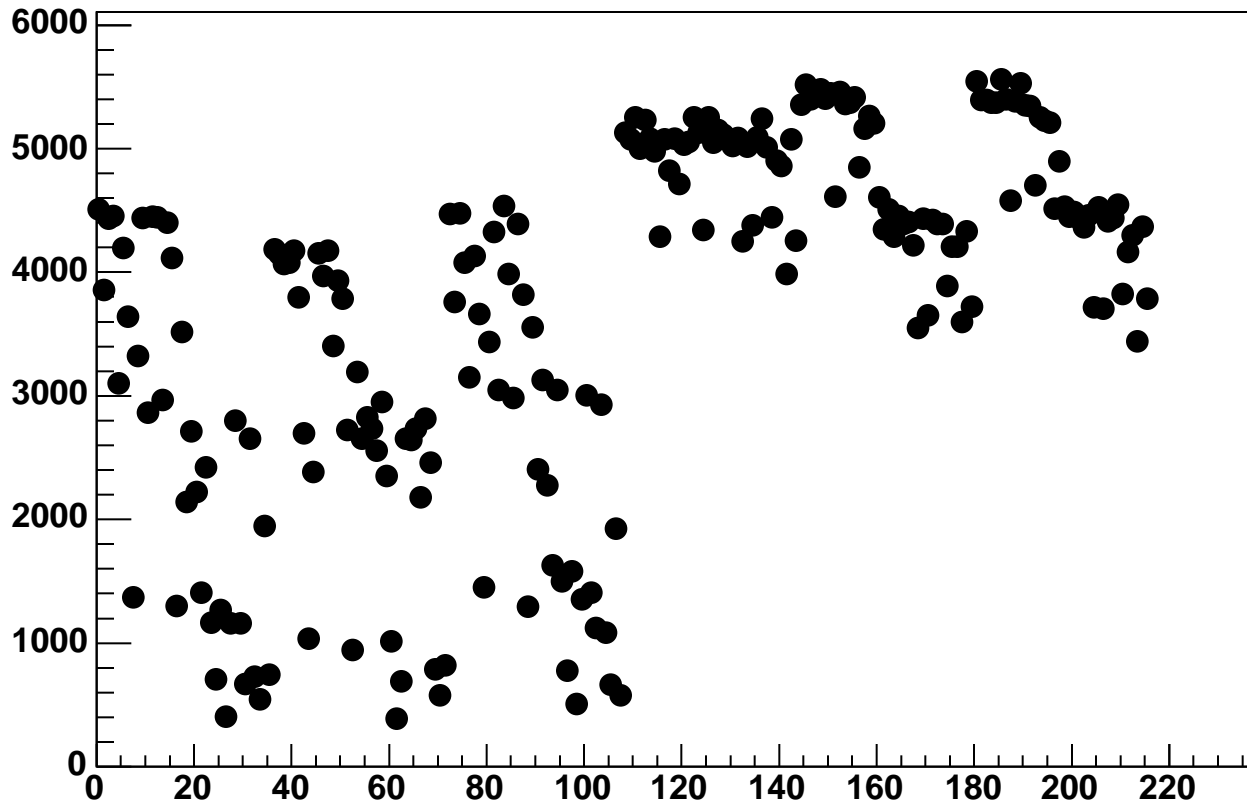
Enable 5, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



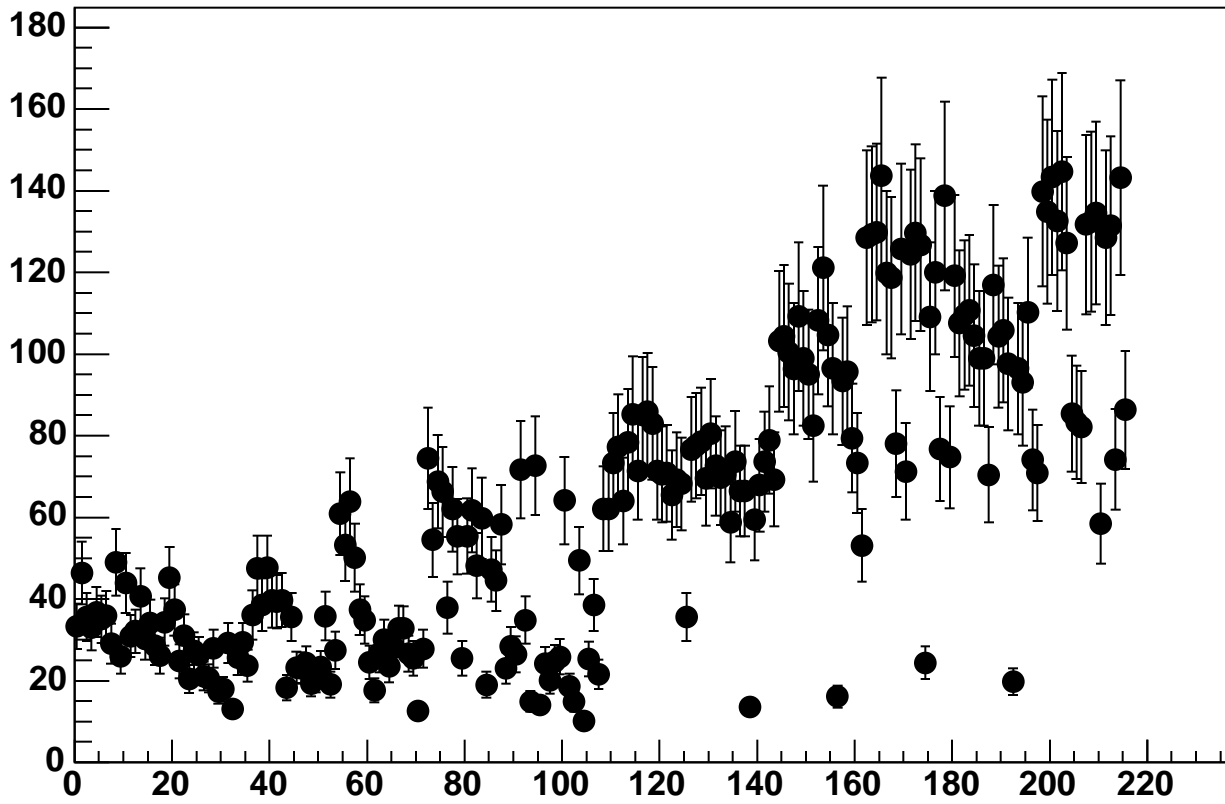
Enable 5, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



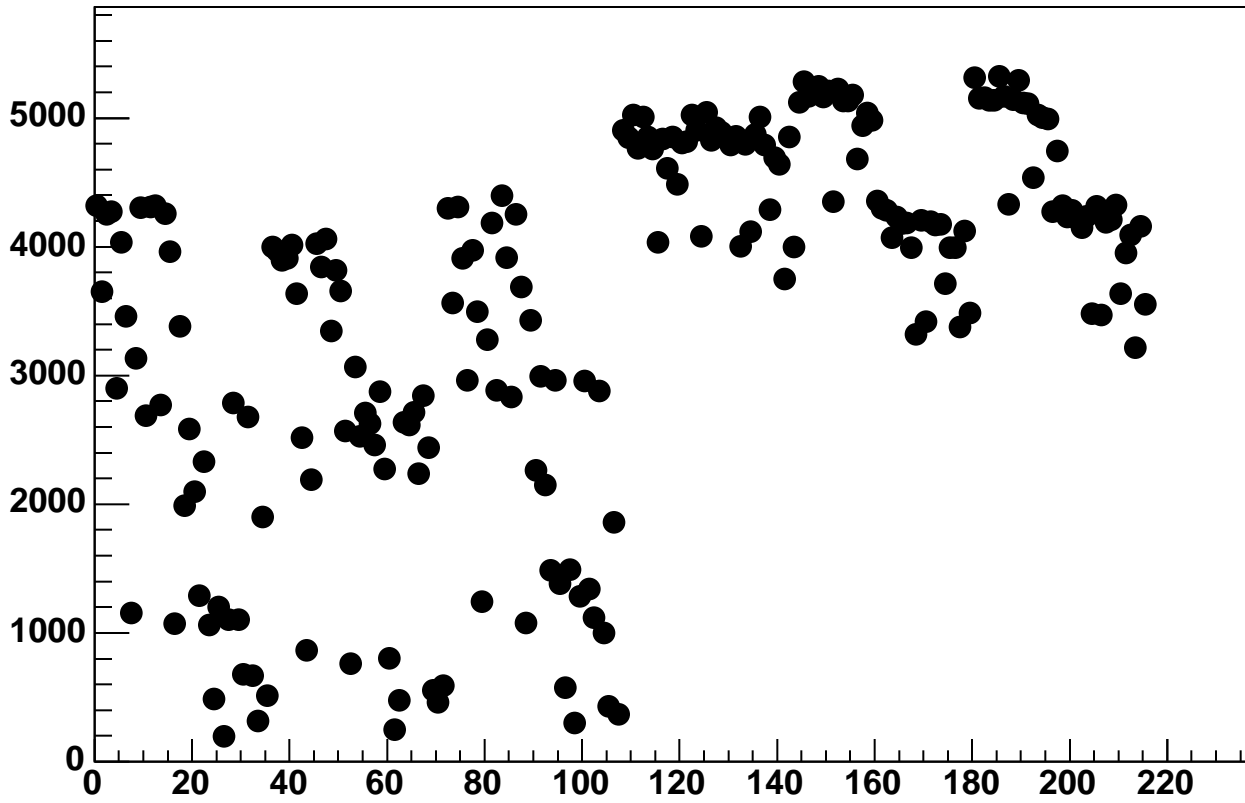
Enable 5, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



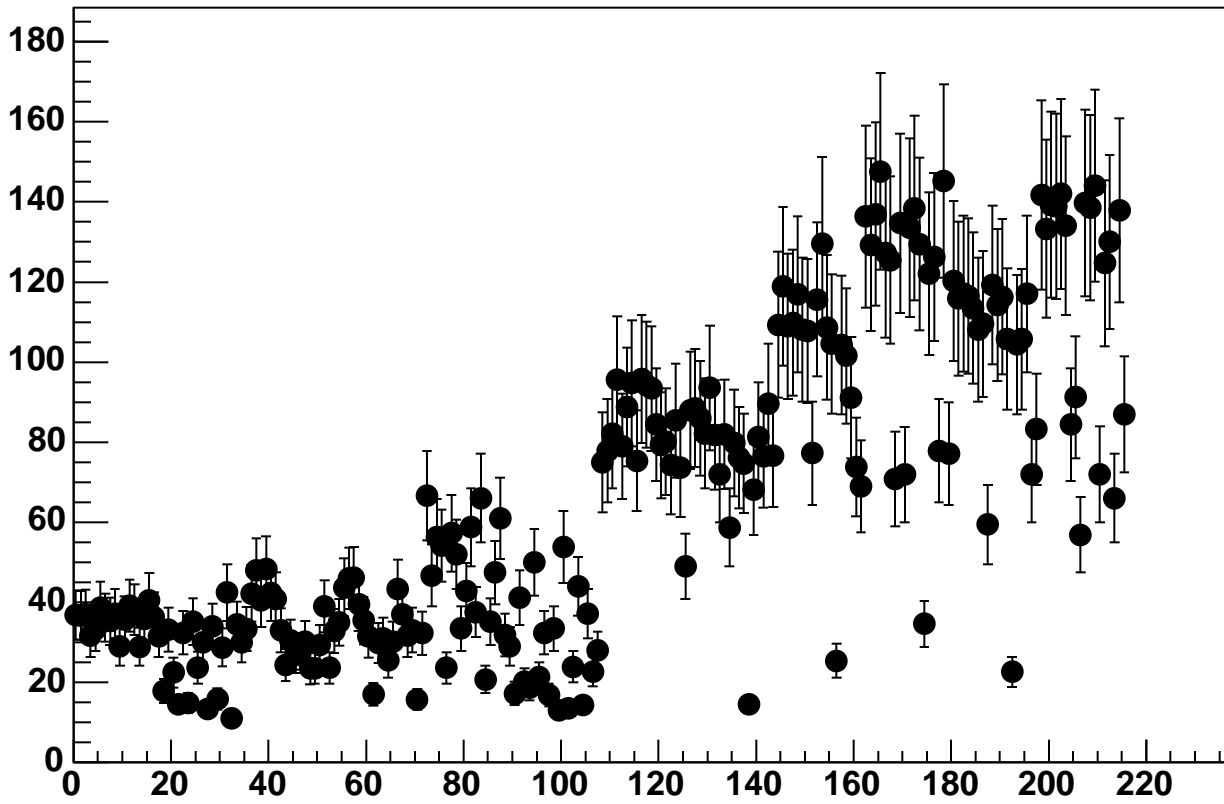
Enable 5, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



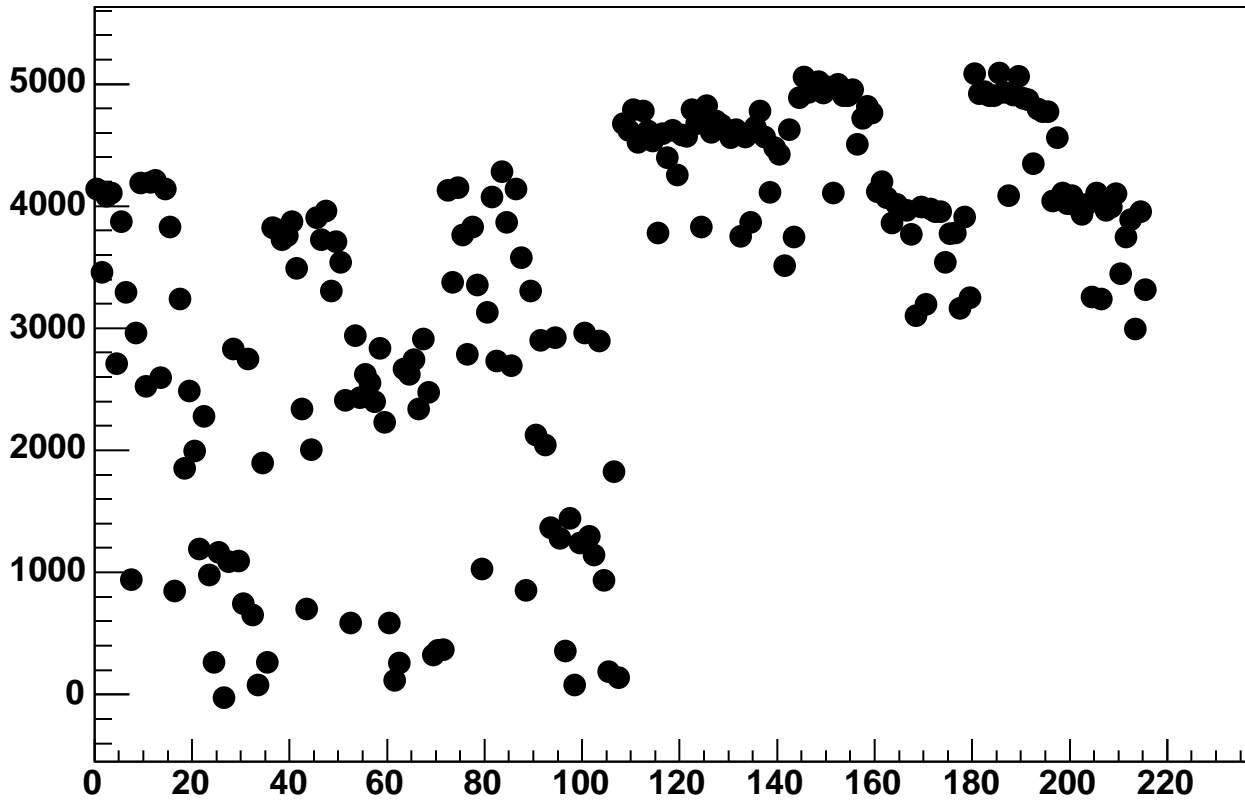
Enable 5, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



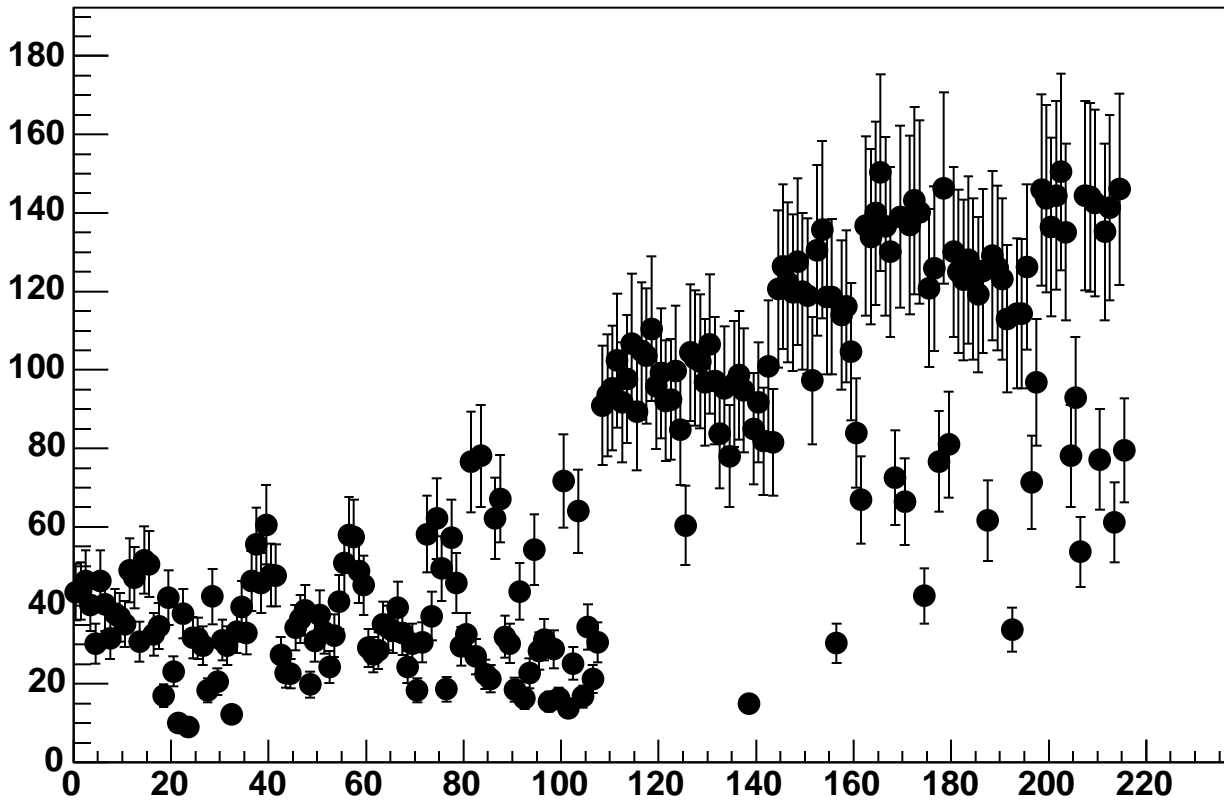
Enable 5, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



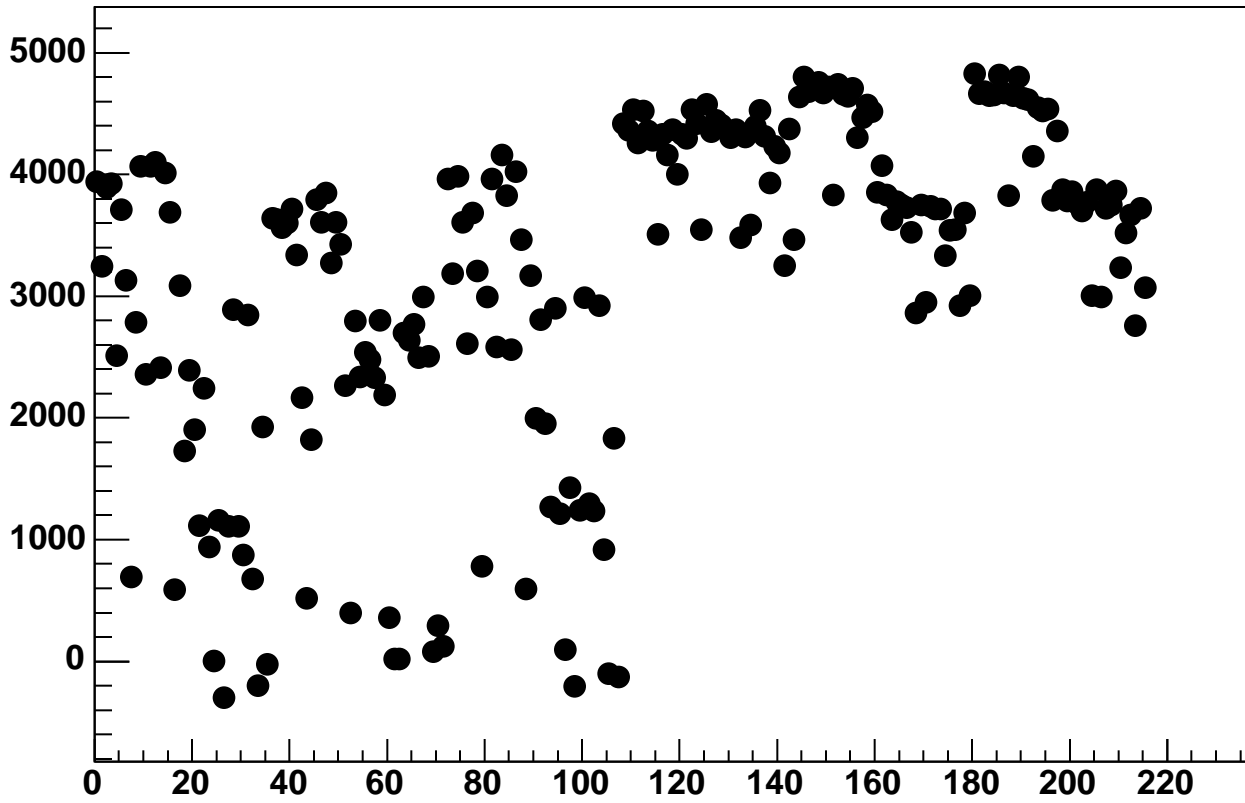
Enable 5, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



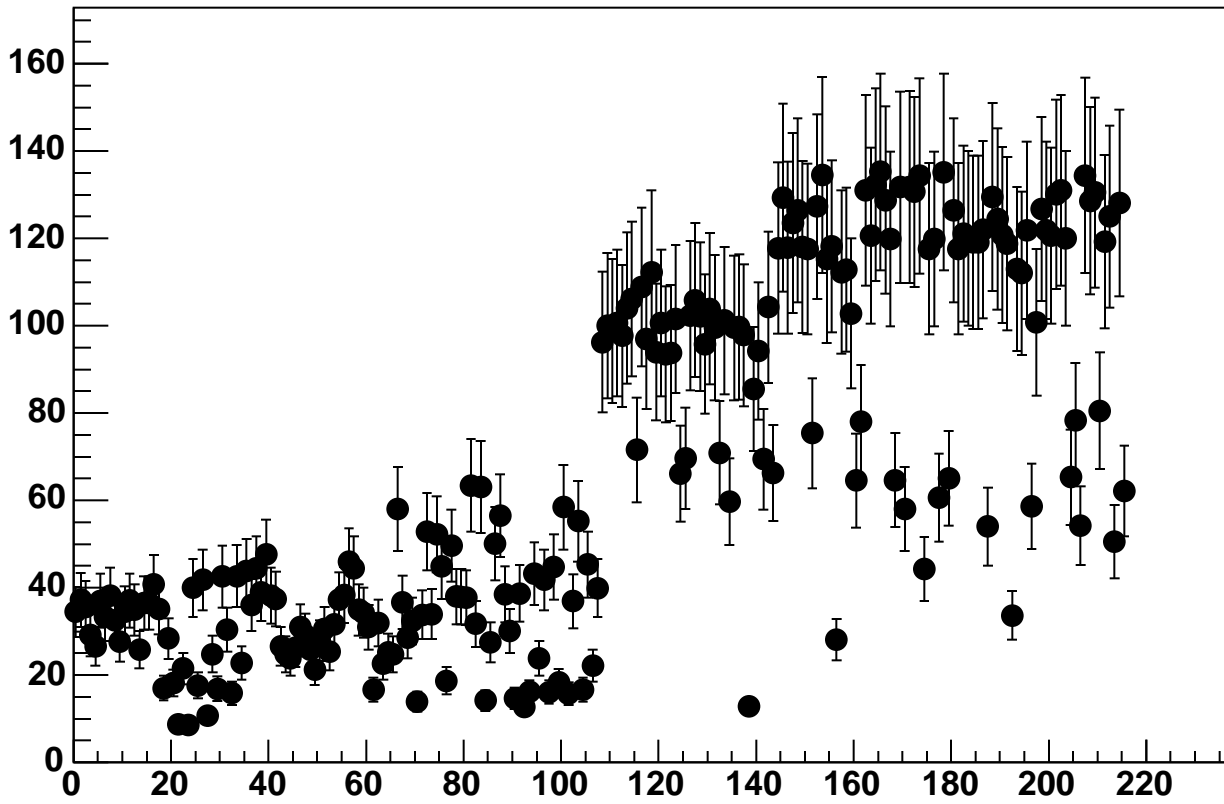
Enable 5, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



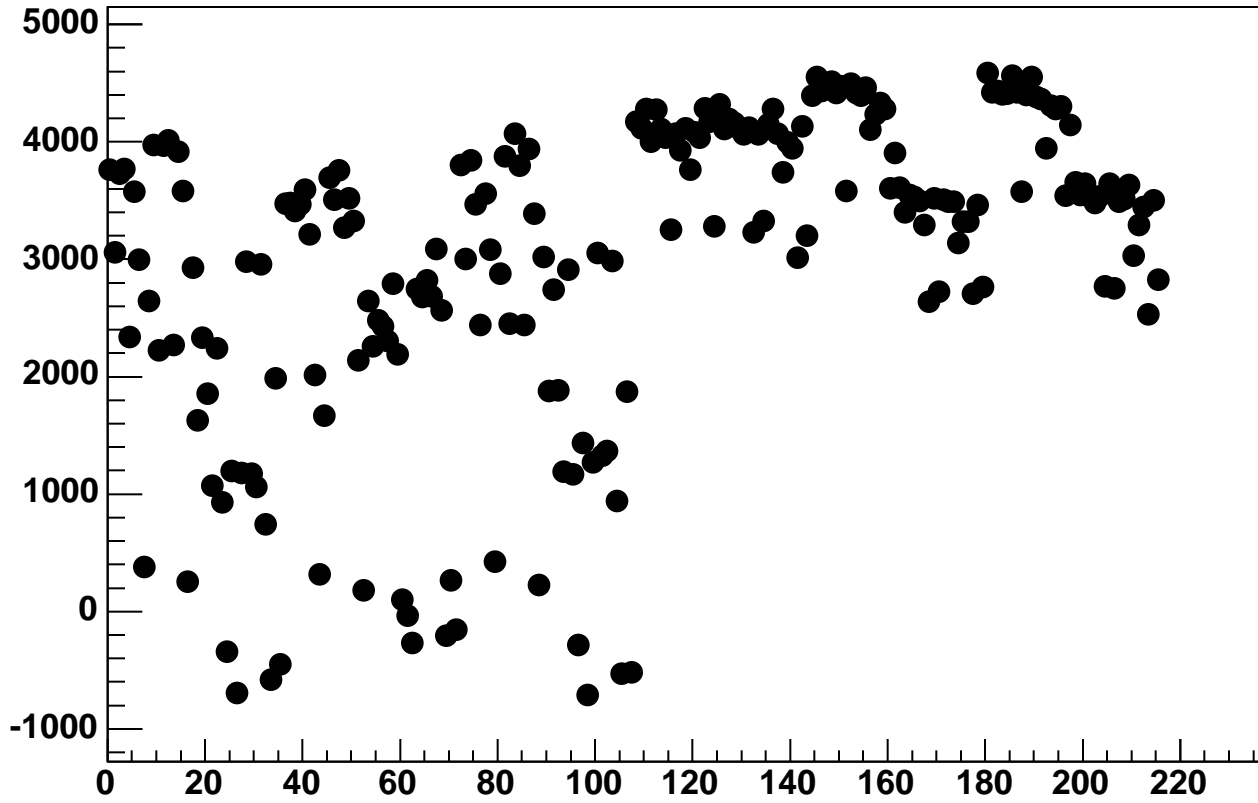
Enable 5, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



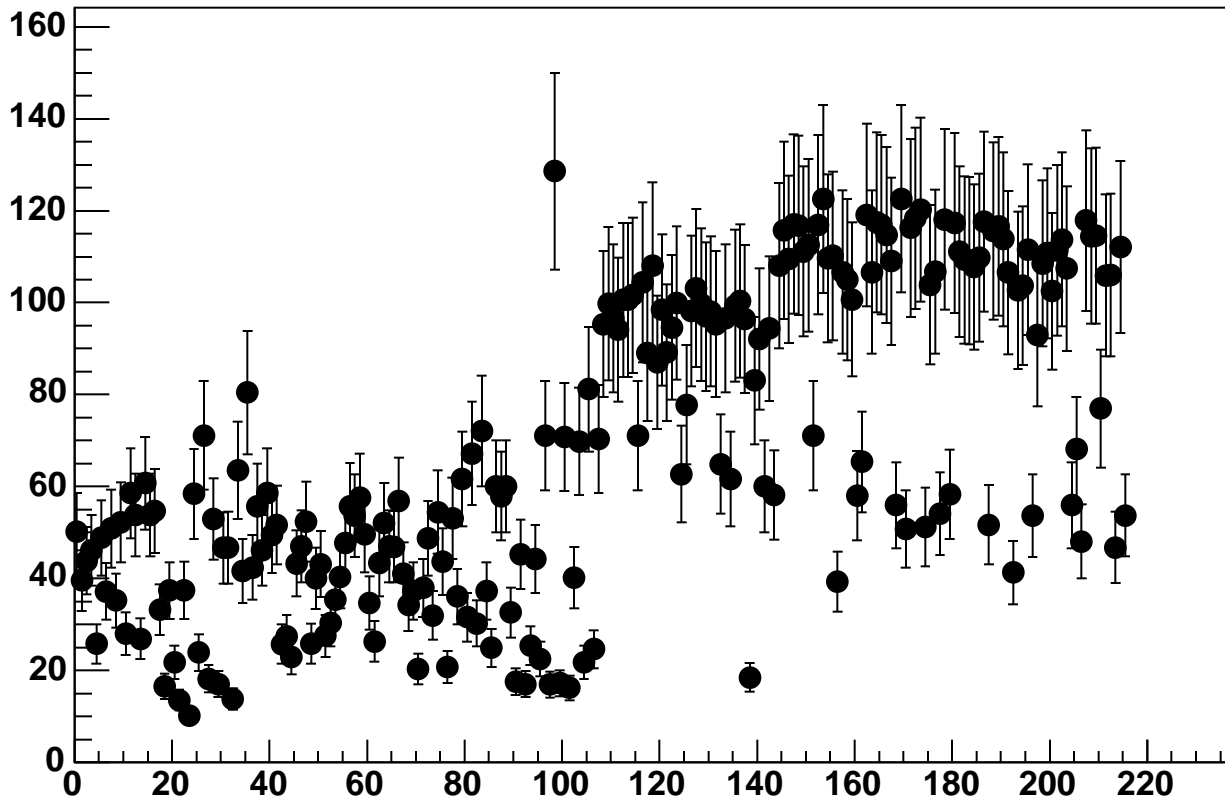
Enable 5, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



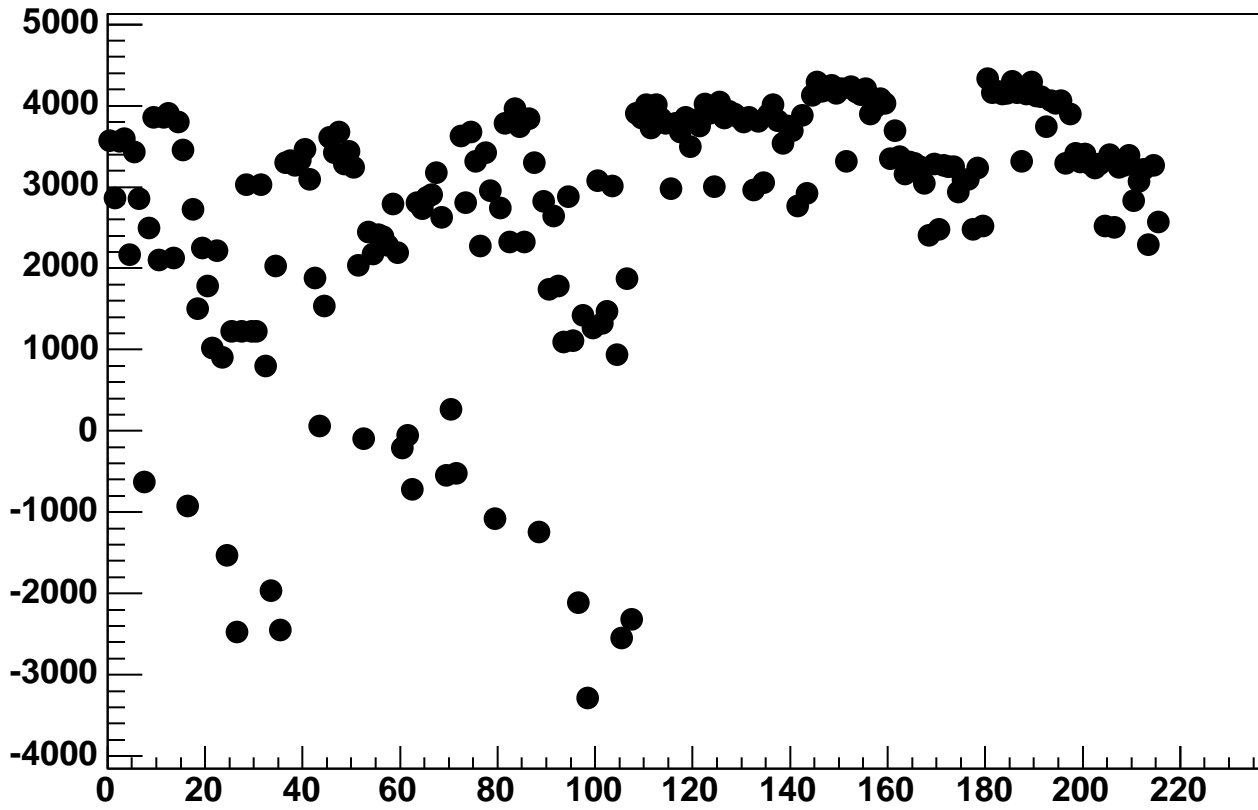
Enable 5, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



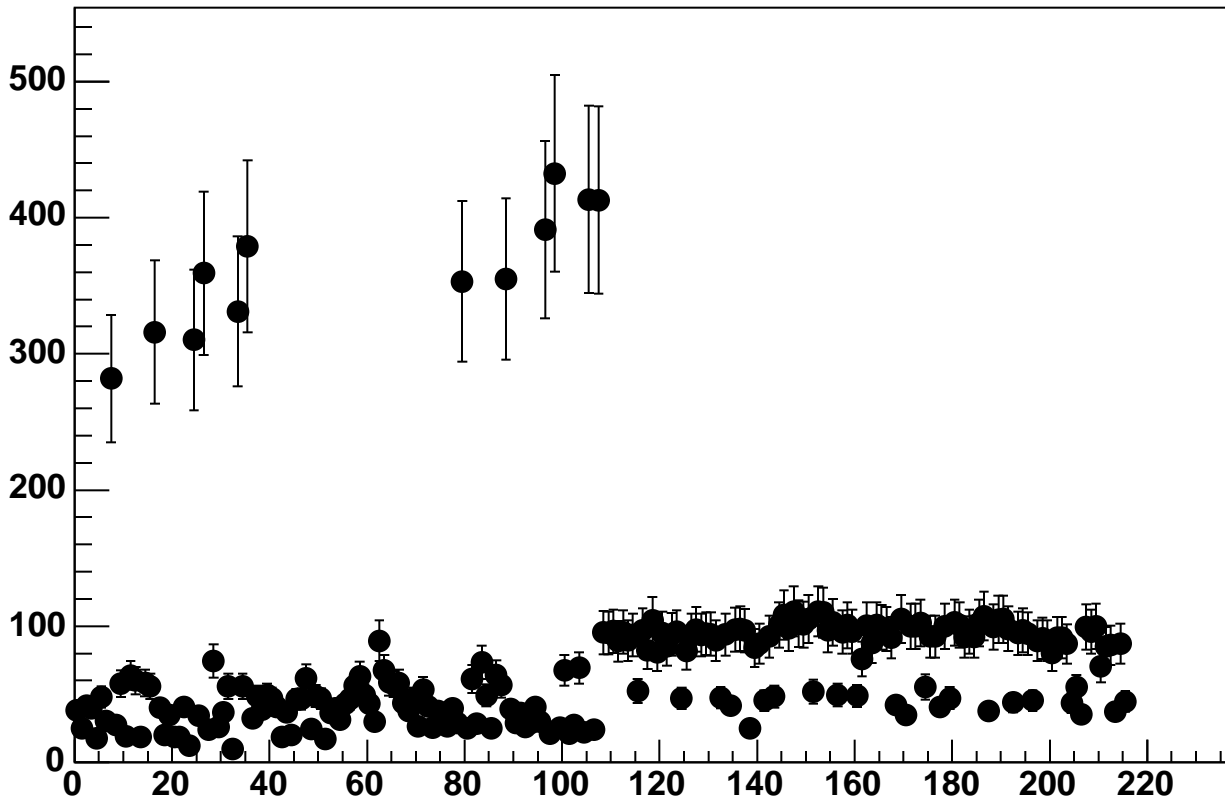
Enable 5, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



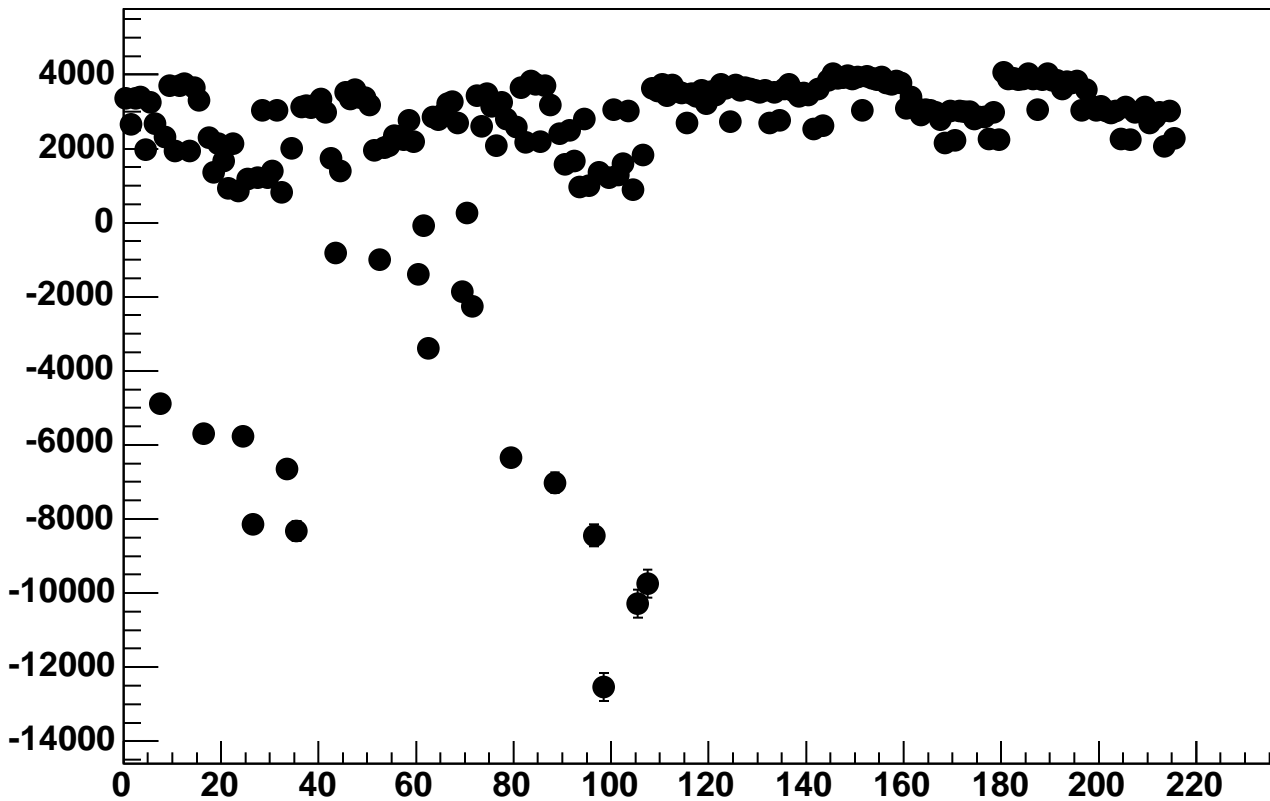
Enable 5, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



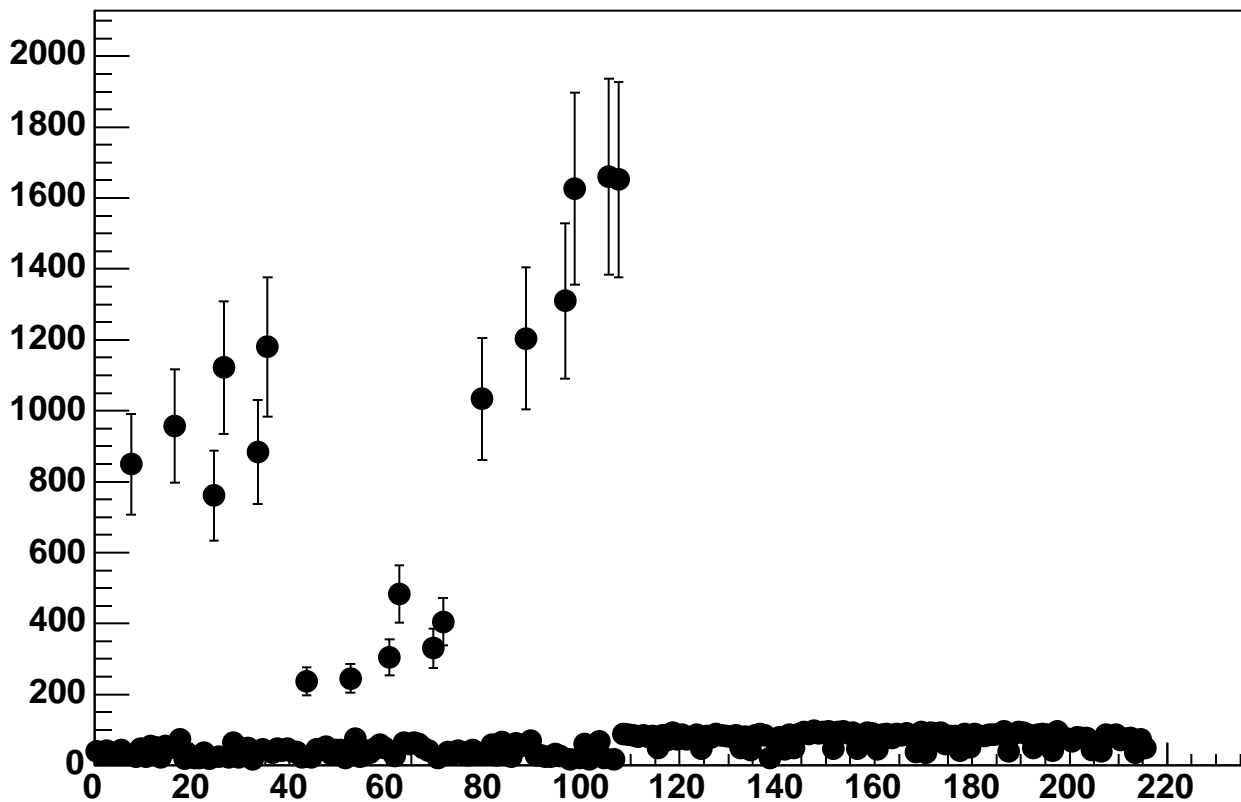
Enable 5, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



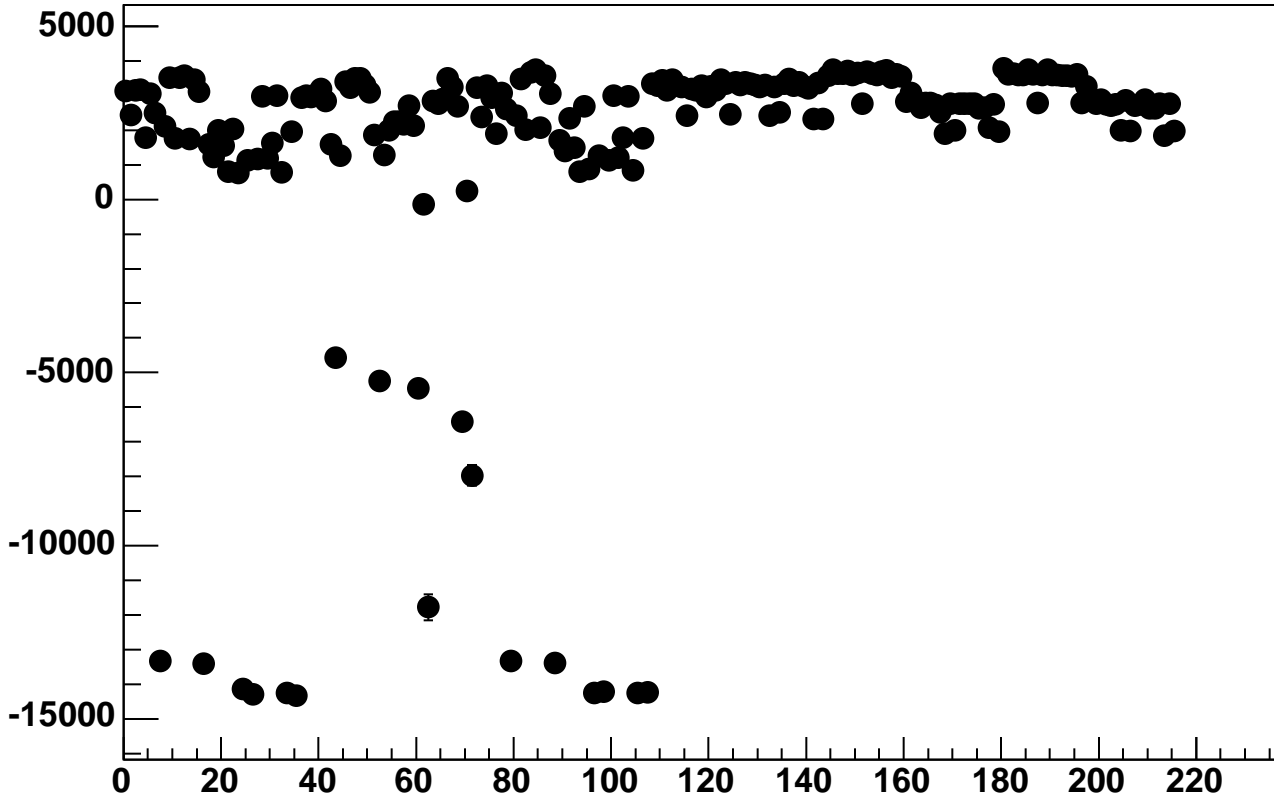
Enable 5, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



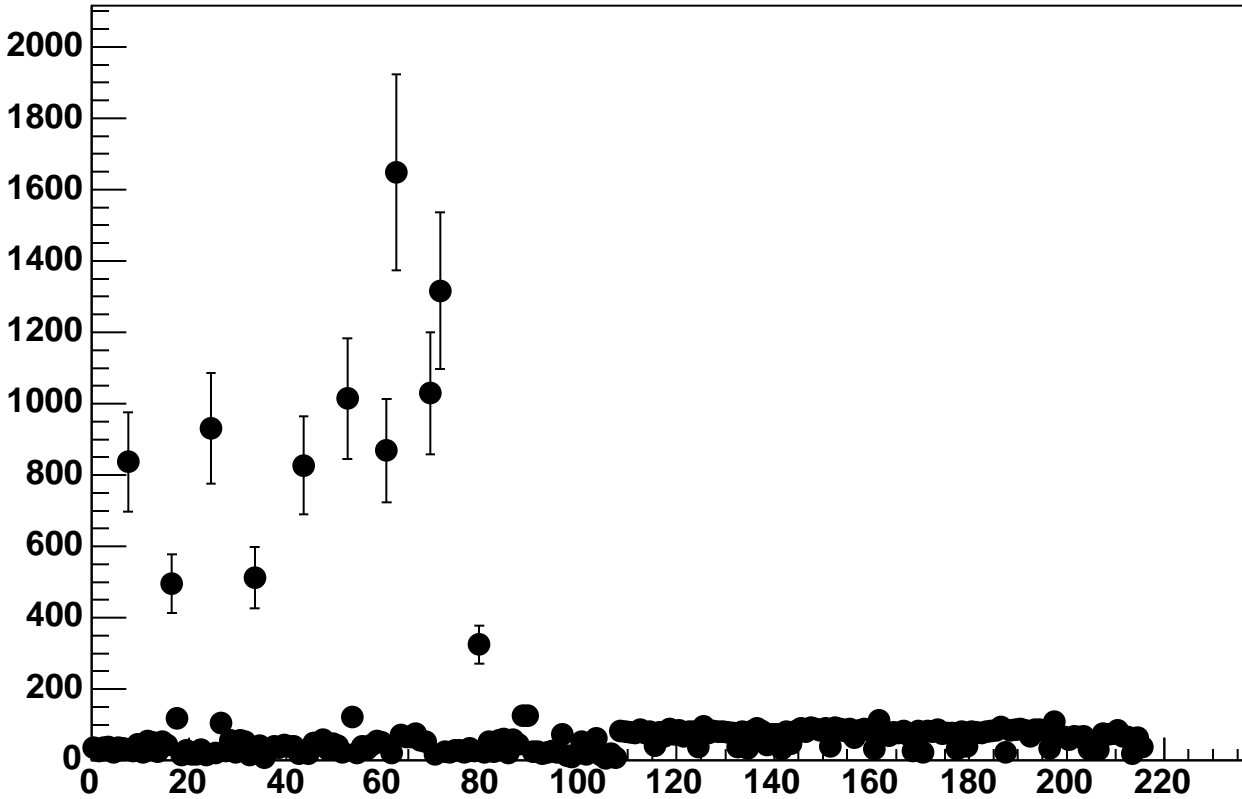
Enable 5, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



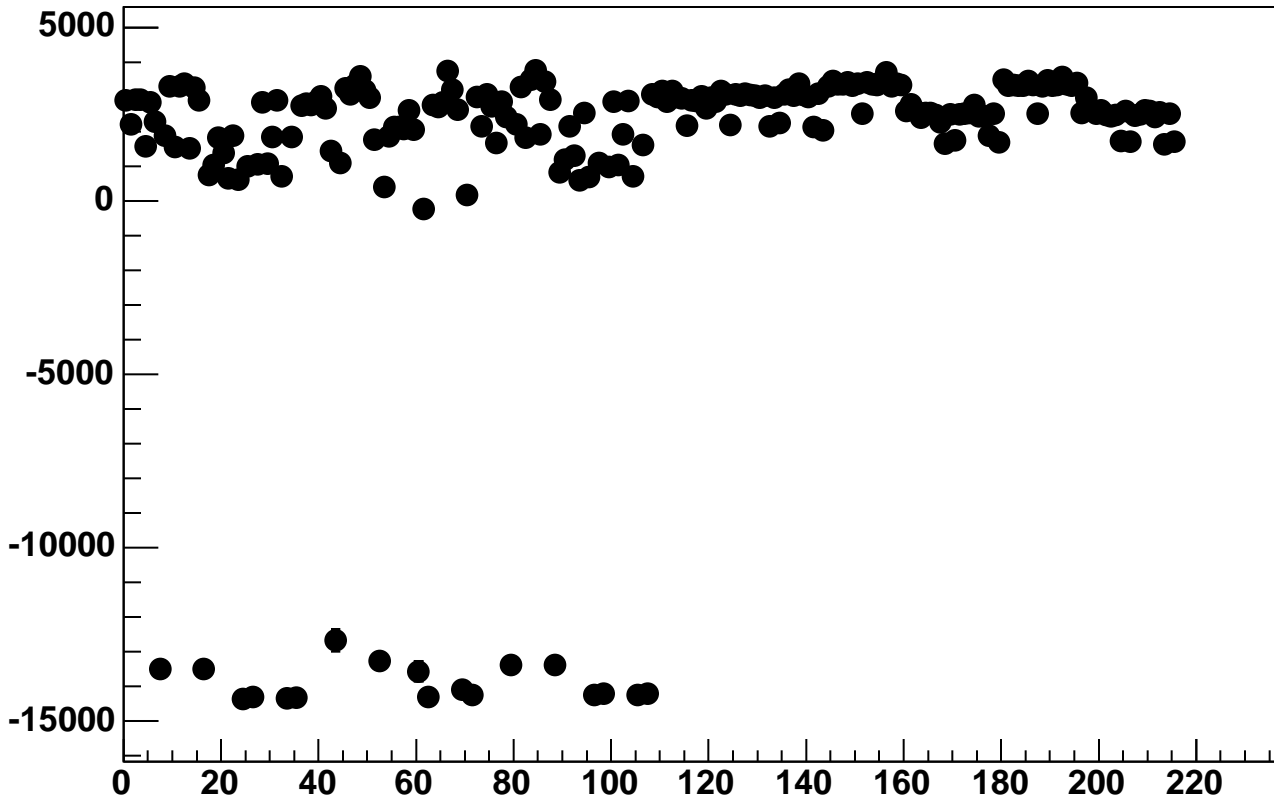
Enable 5, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



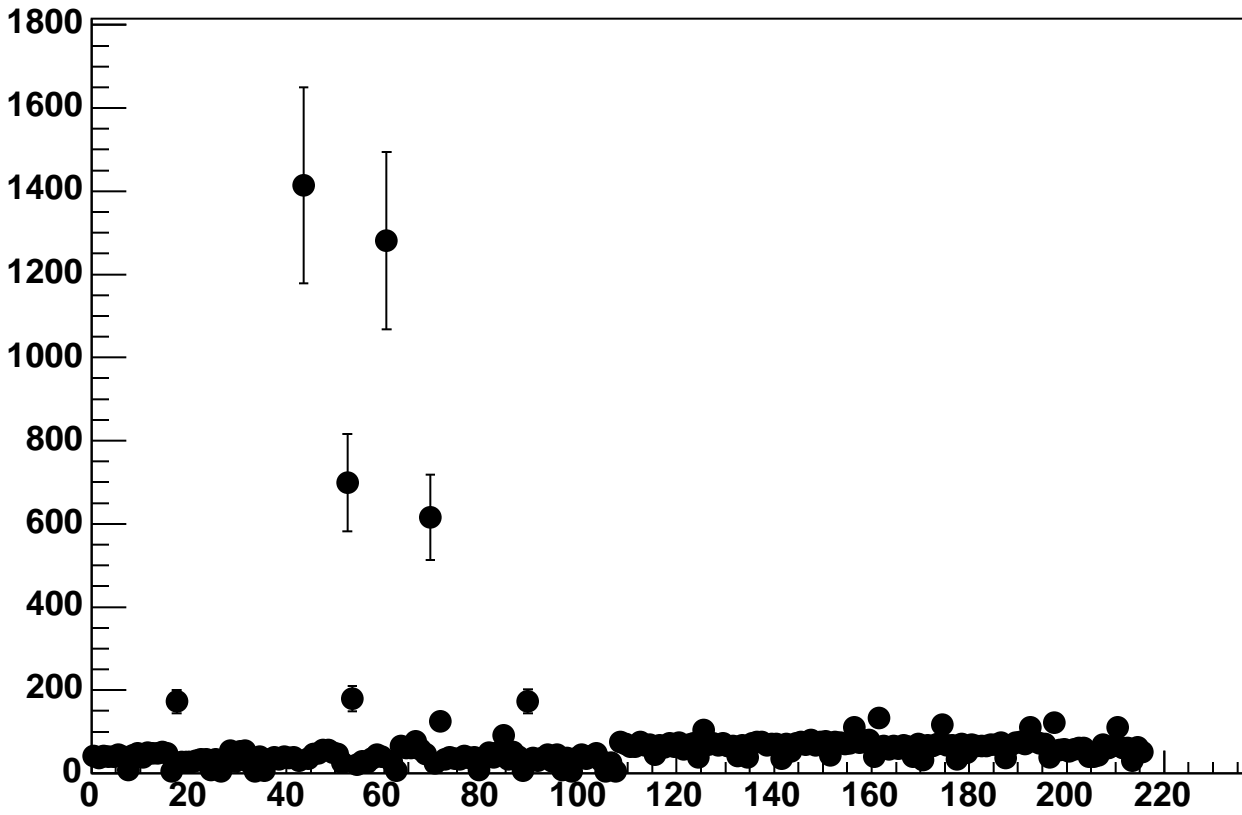
Enable 5, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



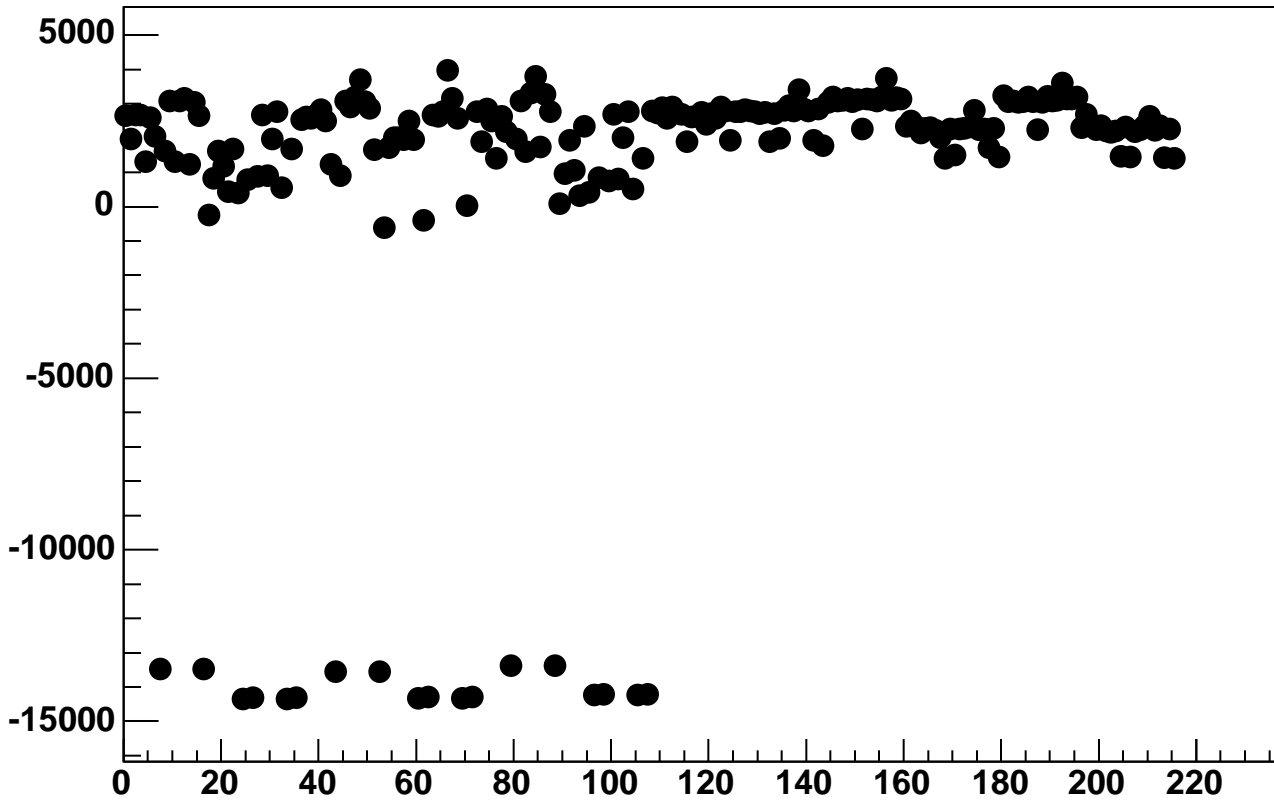
Enable 5, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



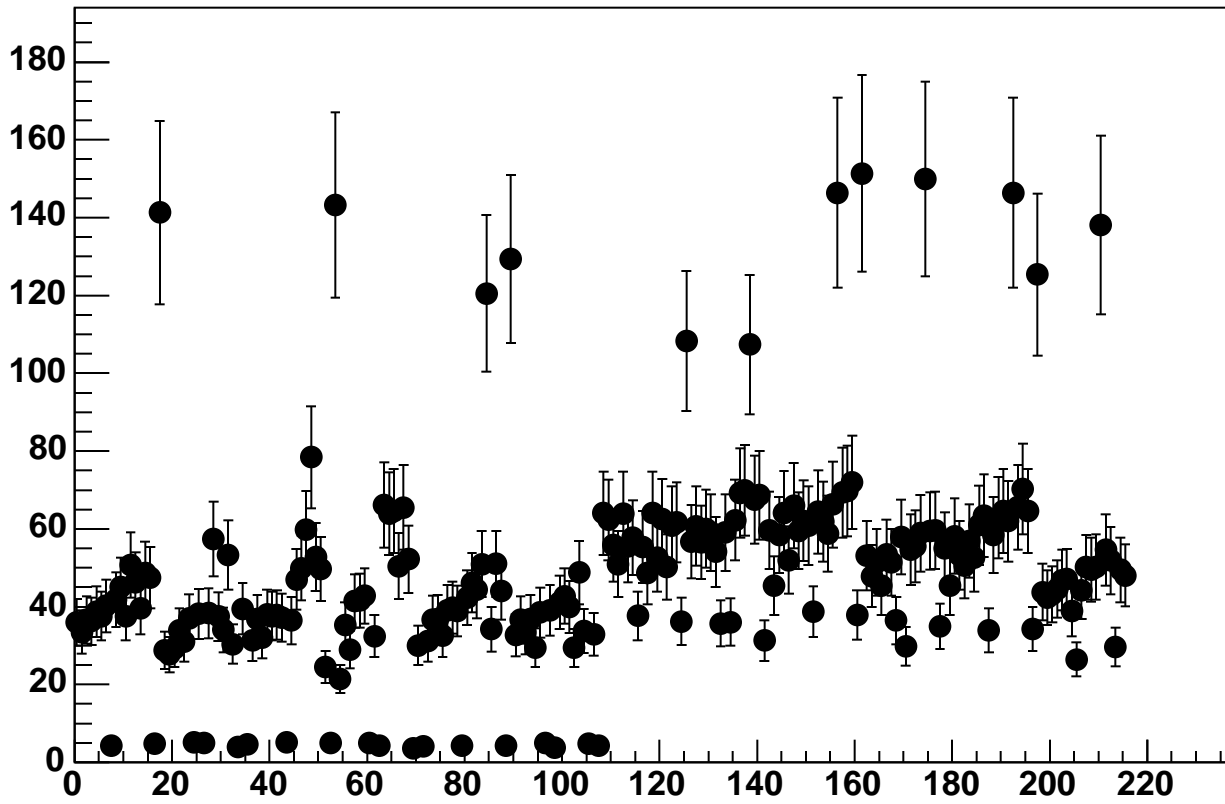
Enable 5, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



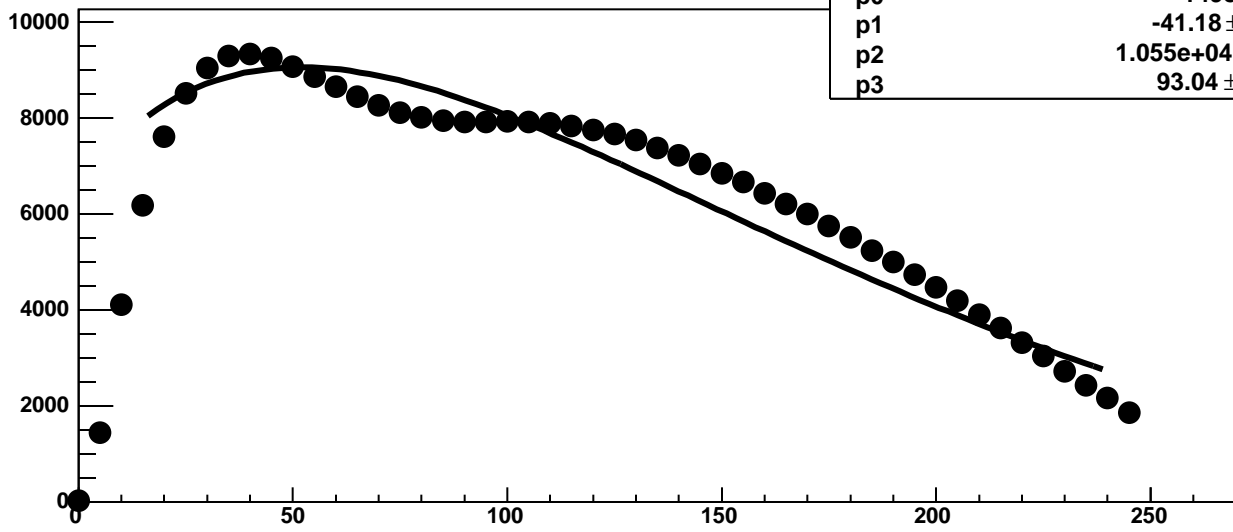
Enable 5, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



Enable 5, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan

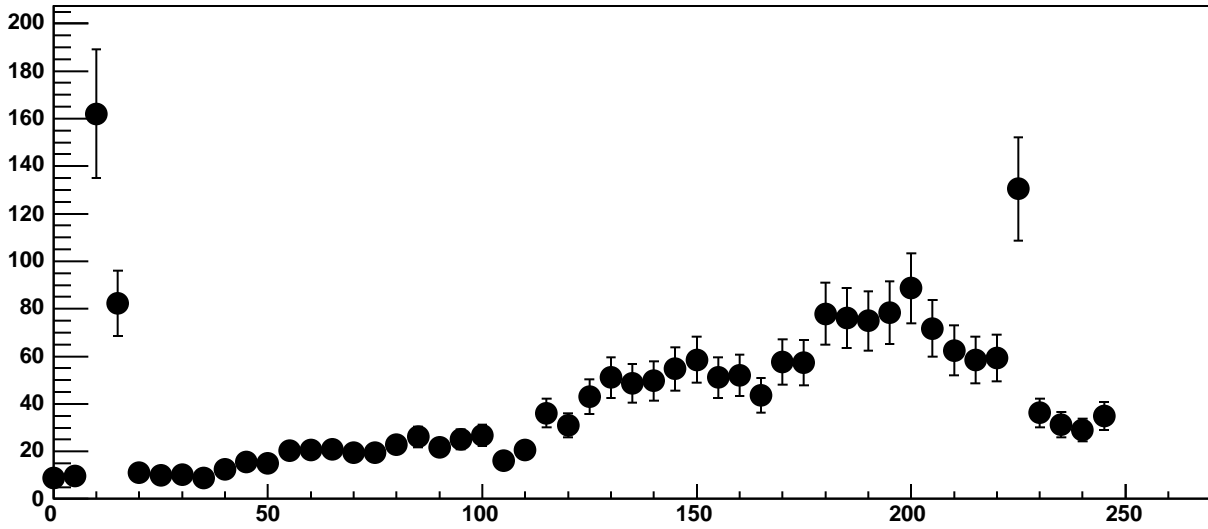


Chip 0, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold

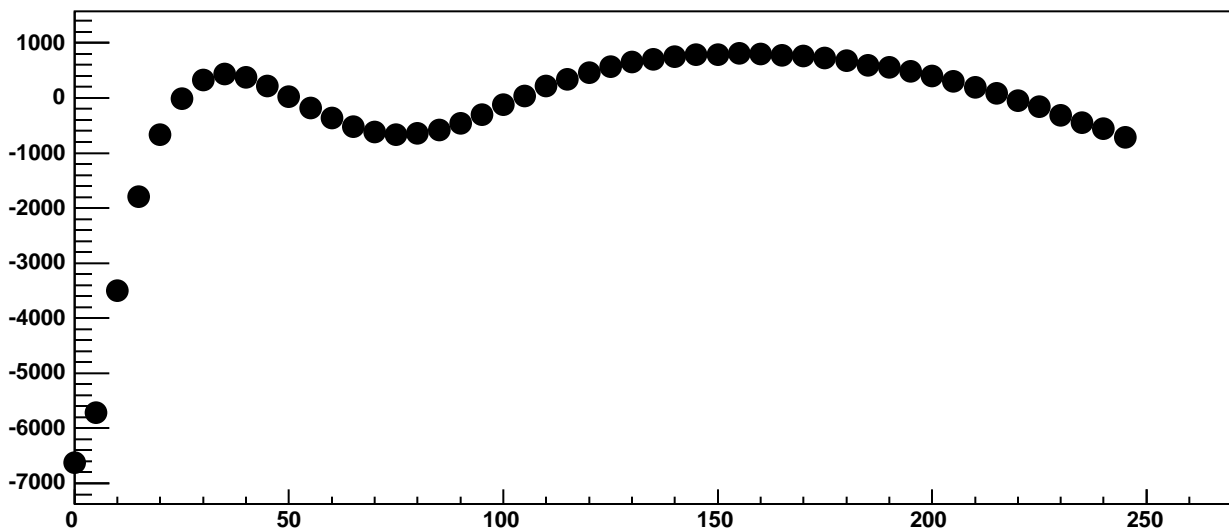


χ^2 / ndf	3.297e+05 / 42
p0	-1498 ± 30.21
p1	-41.18 ± 0.2457
p2	1.055e+04 ± 29.35
p3	93.04 ± 0.2486

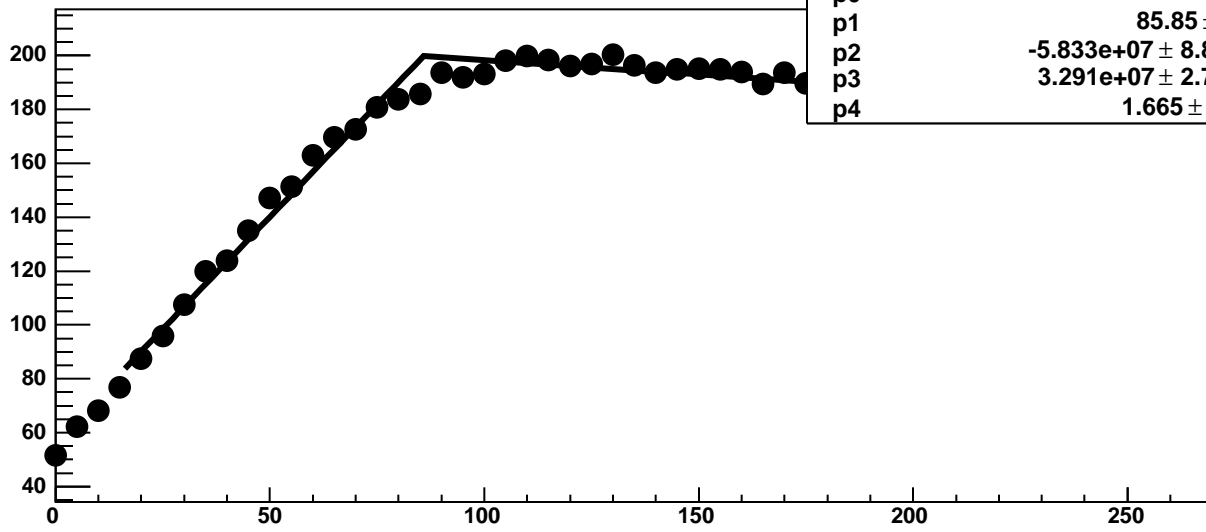
Chip 0, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold

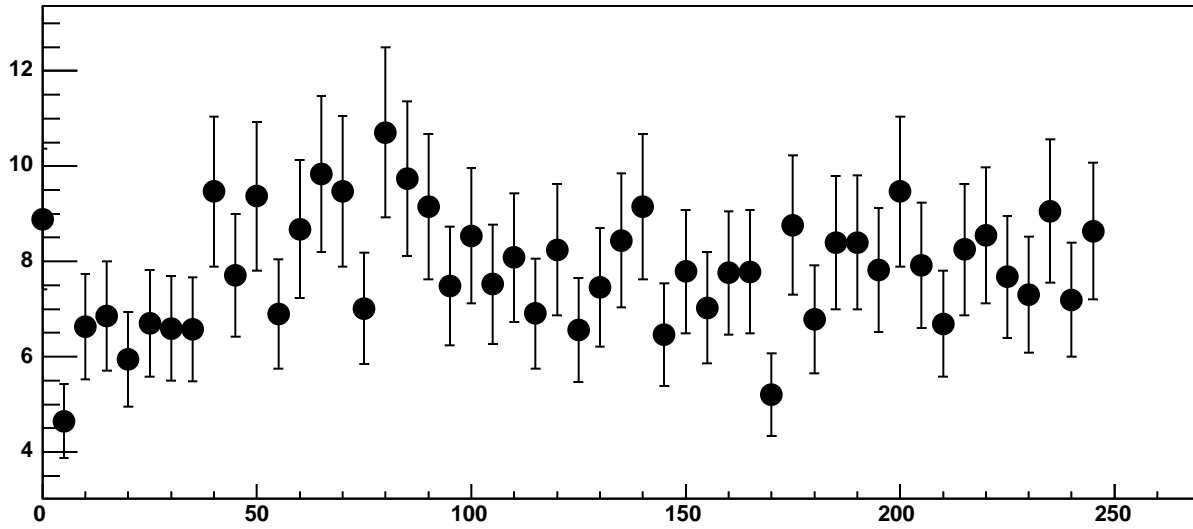


Chip 0, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

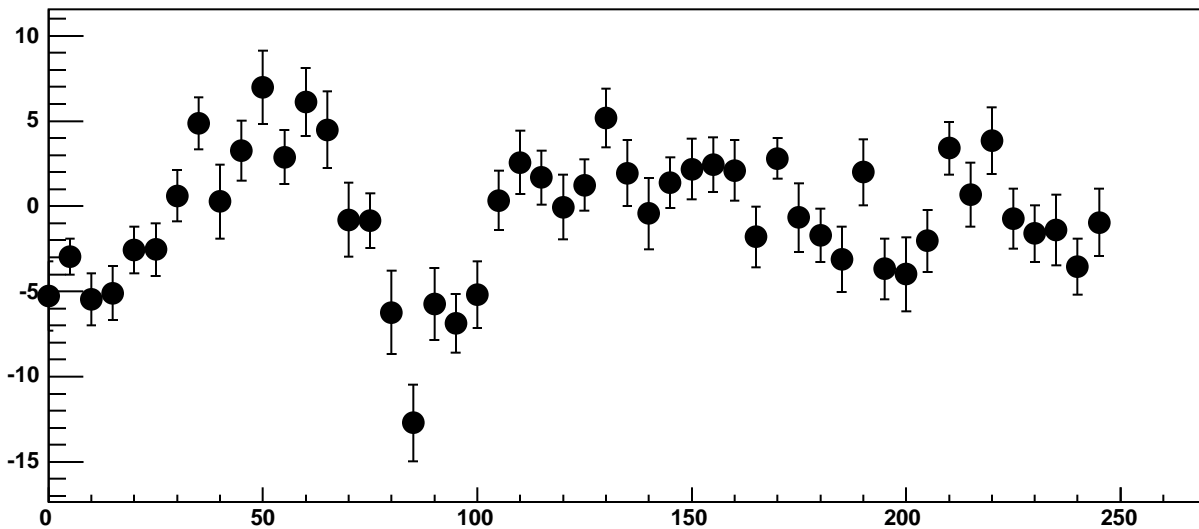


χ^2 / ndf	183.2 / 41
p0	199.8 ± 0.6146
p1	85.85 ± 0.6697
p2	$-5.833\text{e}+07 \pm 8.808\text{e}+05$
p3	$3.291\text{e}+07 \pm 2.726\text{e}+05$
p4	1.665 ± 0.02132

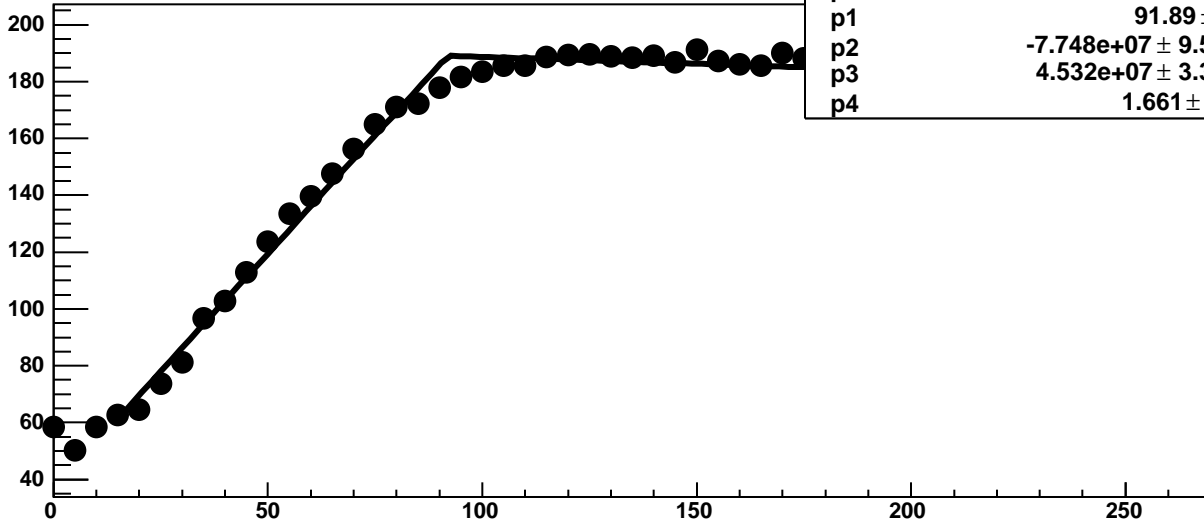
Chip 0, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold

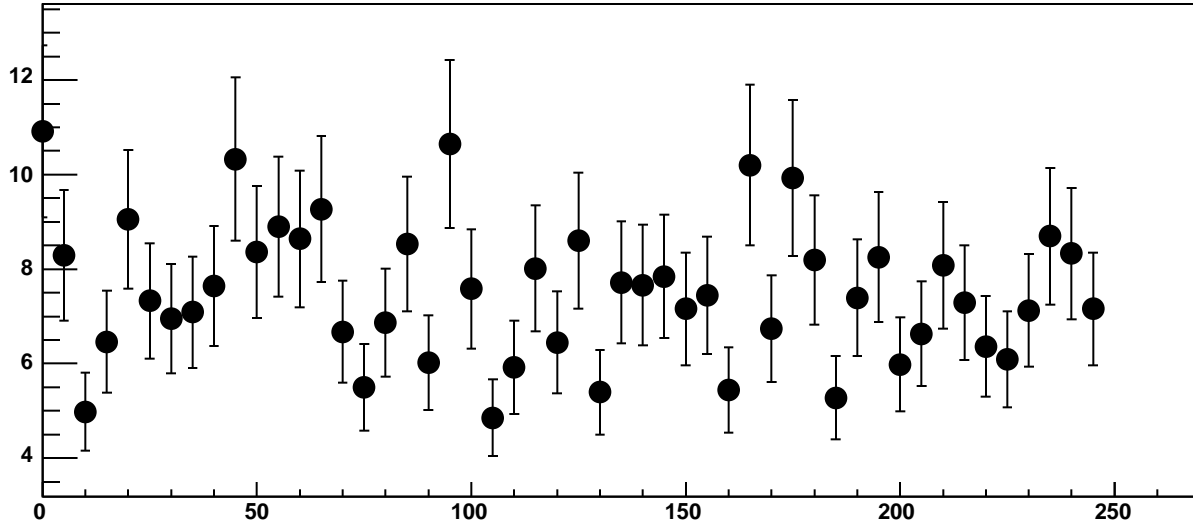


Chip 0, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

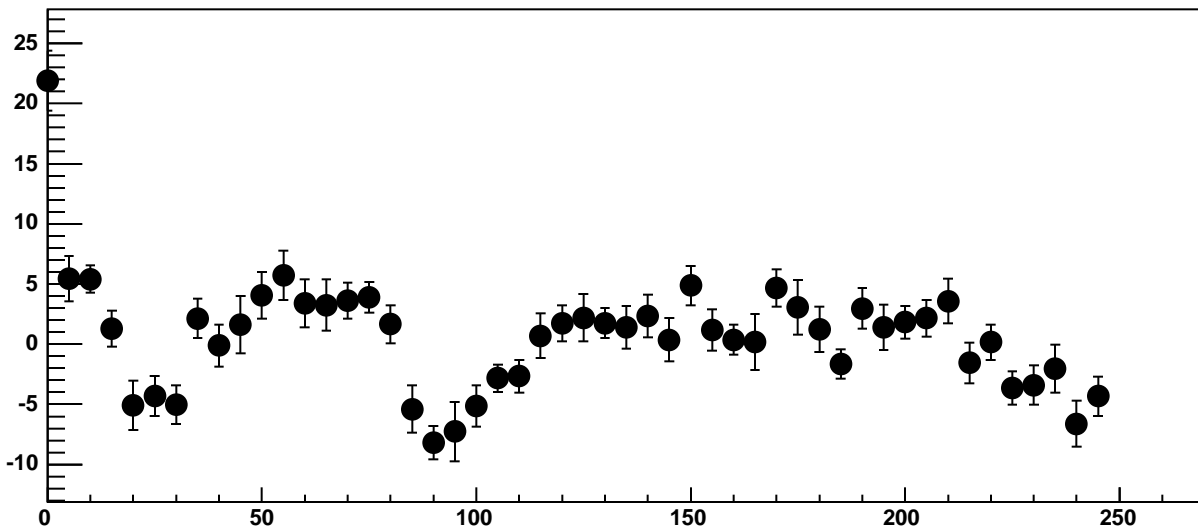


χ^2 / ndf	196.3 / 41
p0	189.1 ± 0.5713
p1	91.89 ± 0.5787
p2	$-7.748e+07 \pm 9.594e+05$
p3	$4.532e+07 \pm 3.306e+05$
p4	1.661 ± 0.01752

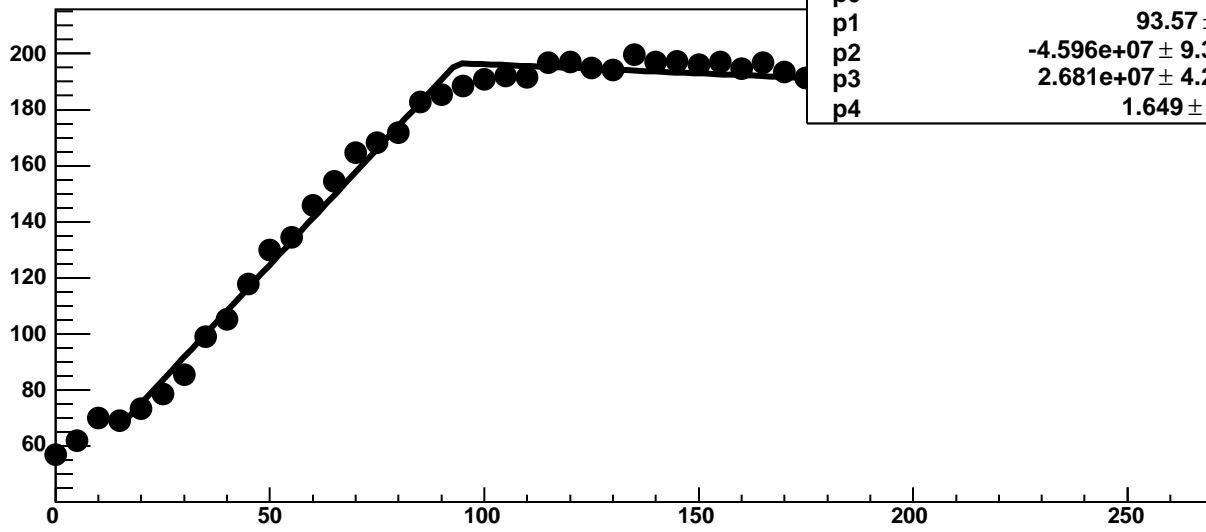
Chip 0, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

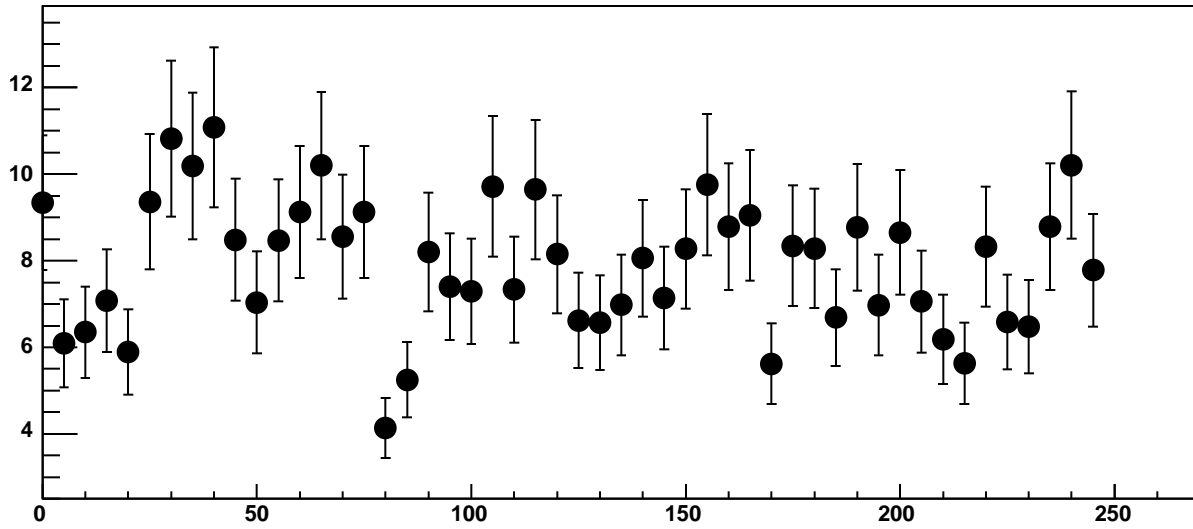


Chip 0, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold

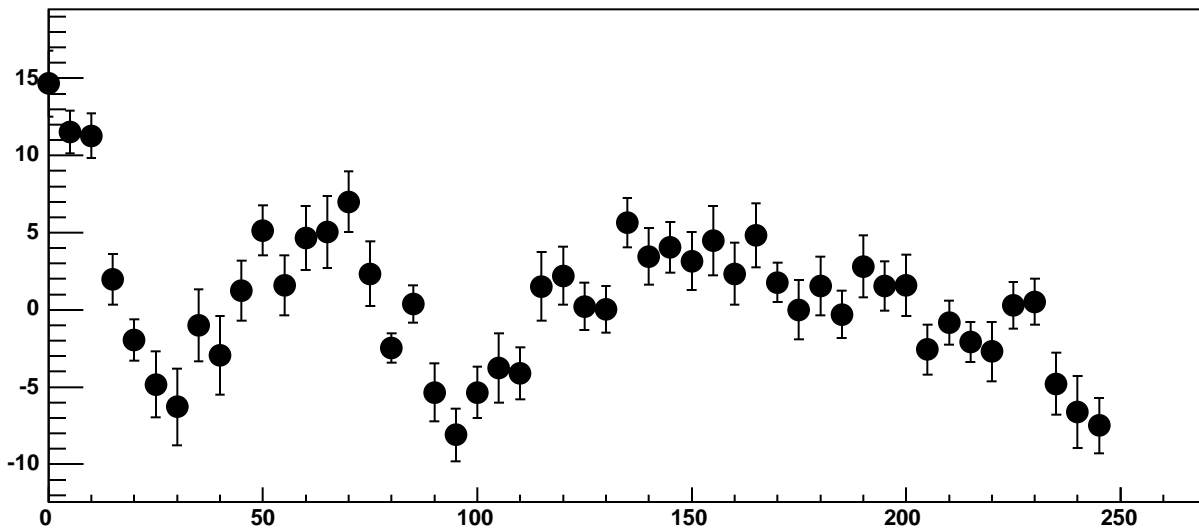


χ^2 / ndf	173.2 / 41
p0	196.6 ± 0.6155
p1	93.57 ± 0.5718
p2	$-4.596e+07 \pm 9.372e+05$
p3	$2.681e+07 \pm 4.285e+05$
p4	1.649 ± 0.01692

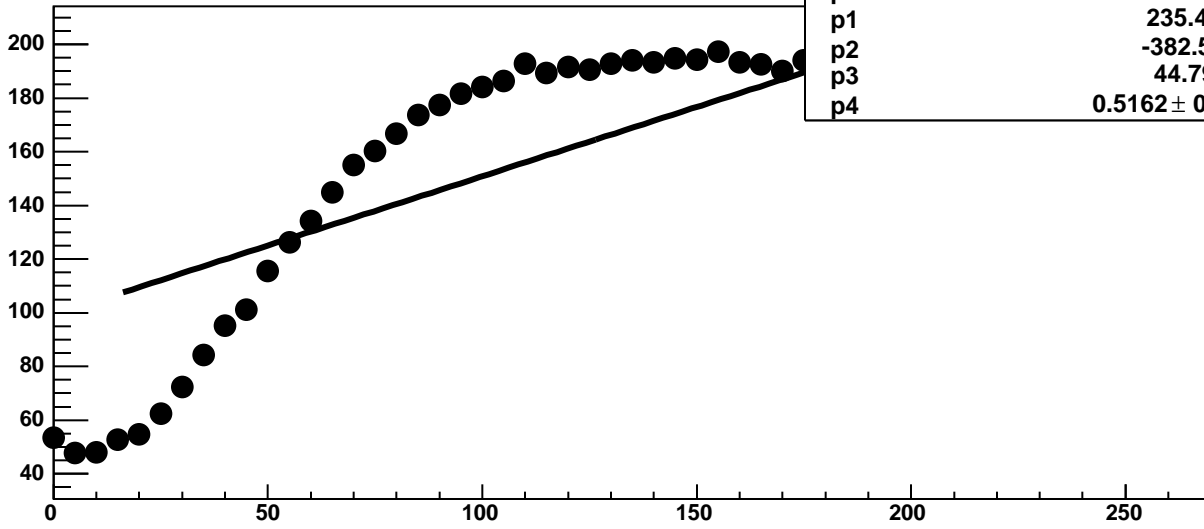
Chip 0, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold

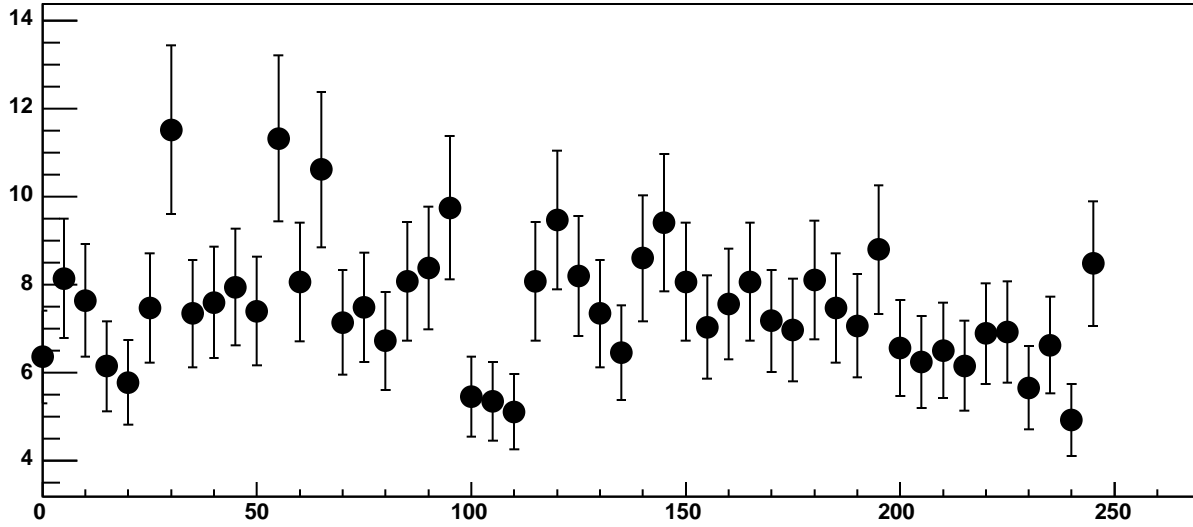


Chip 0, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

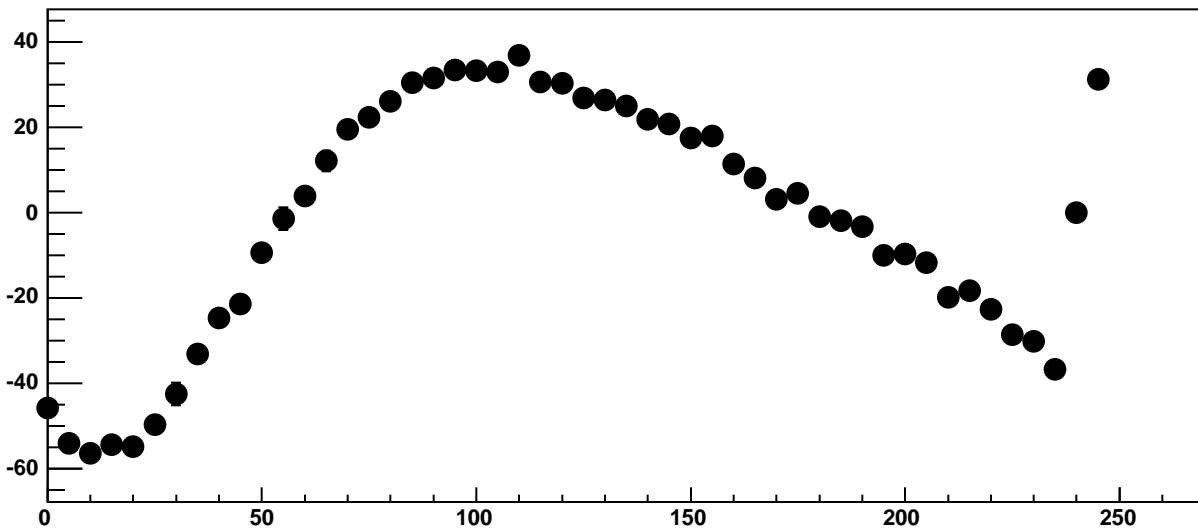


χ^2 / ndf	1.271e+04 / 41
p0	220.8 ± 1.155
p1	235.4 ± 2.064
p2	-382.5 ± 156.1
p3	44.79 ± 15.9
p4	0.5162 ± 0.003685

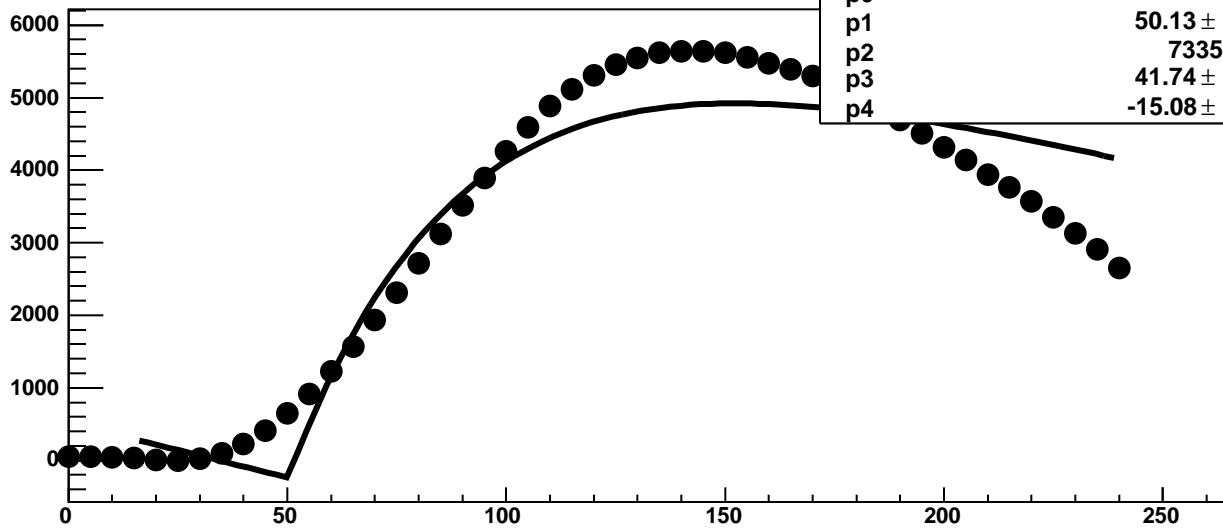
Chip 0, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

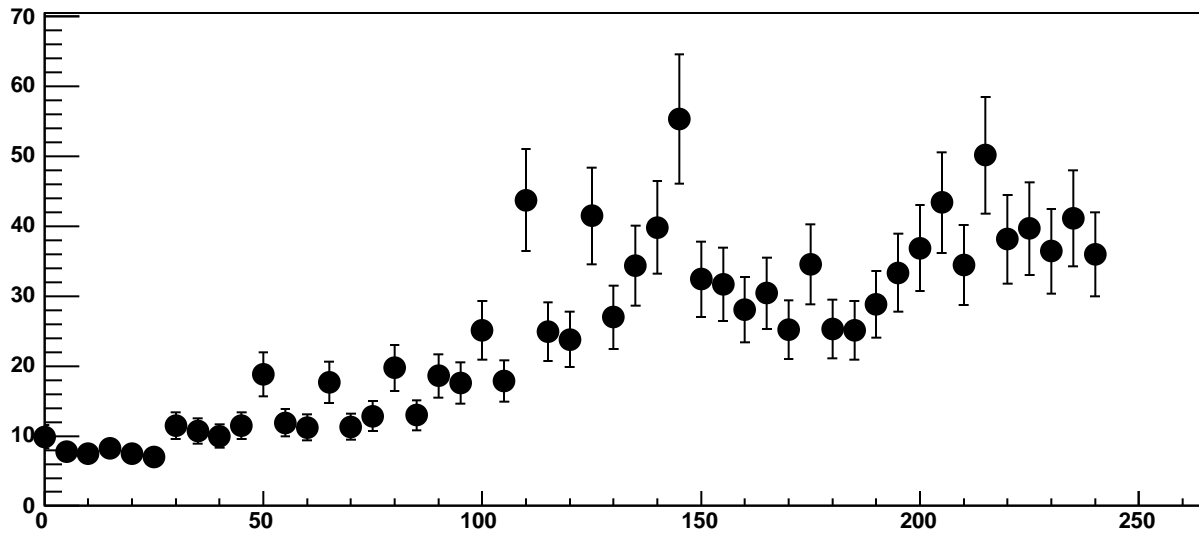


Chip 0, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold

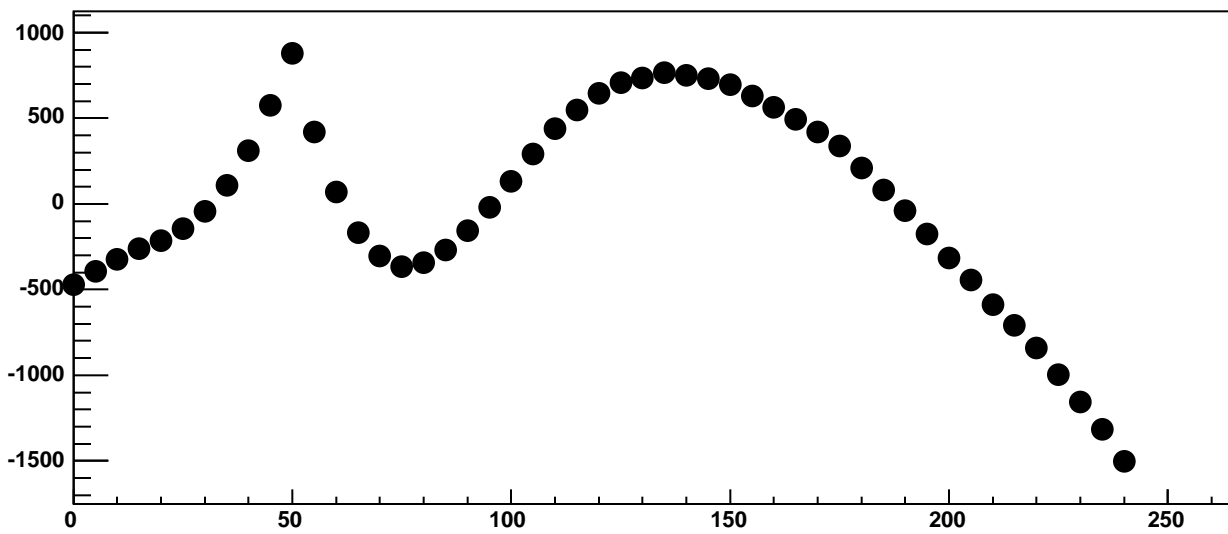


χ^2 / ndf	4.369e+05 / 41
p0	-237.4 ± 1.238
p1	50.13 ± 0.01688
p2	7335 ± 9.239
p3	41.74 ± 0.05825
p4	-15.08 ± 0.04954

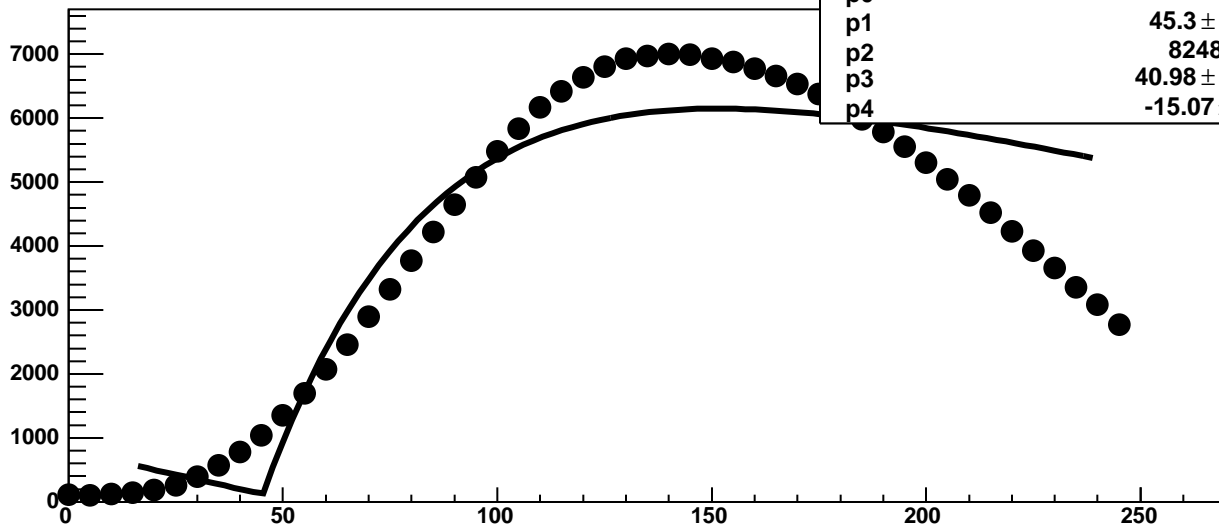
Chip 0, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold

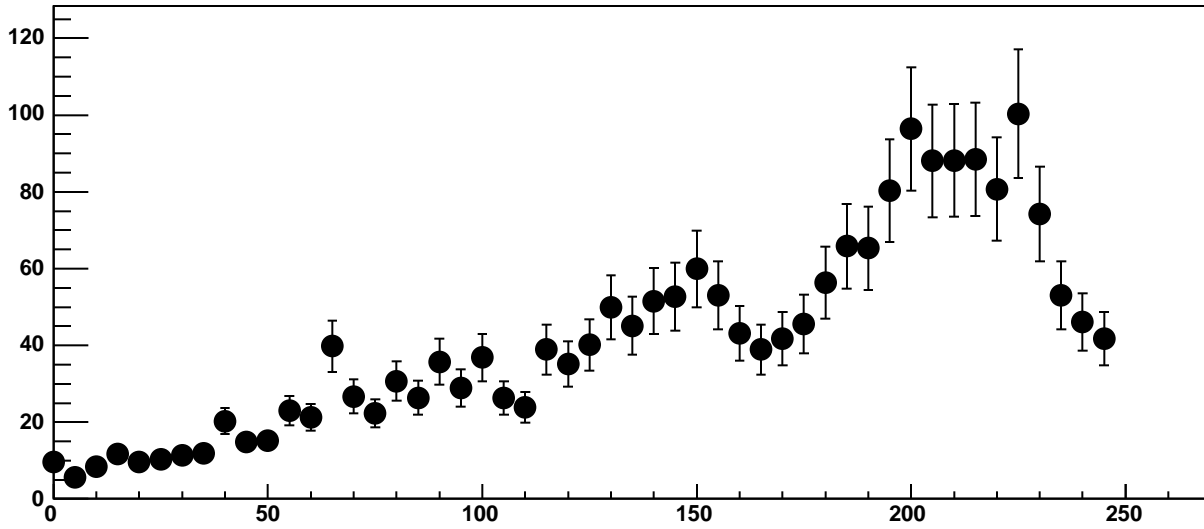


Chip 0, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold

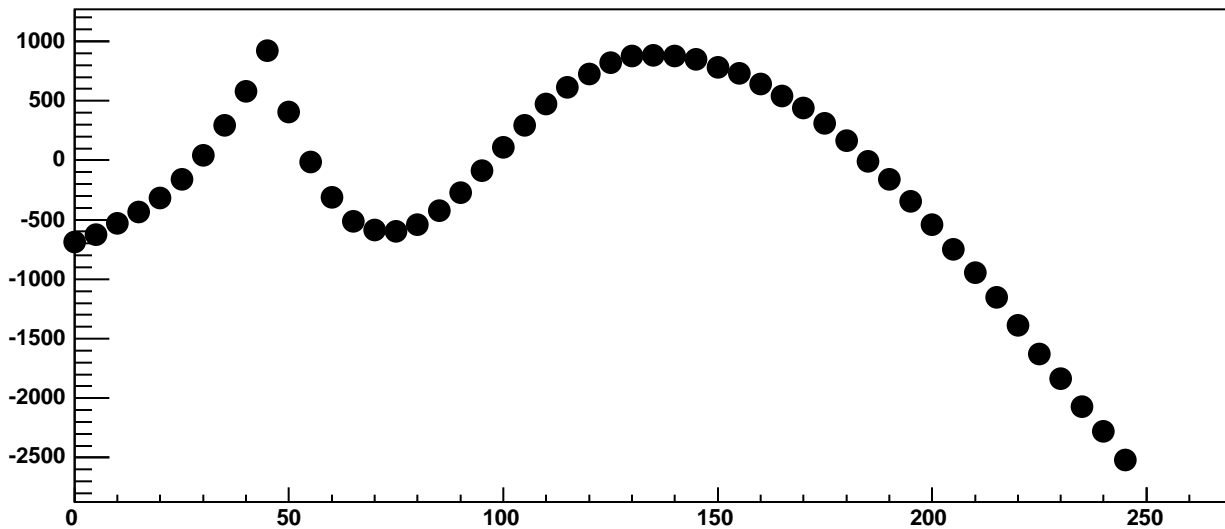


χ^2 / ndf	3.873e+05 / 41
p0	119.2 ± 1.523
p1	45.3 ± 0.02013
p2	8248 ± 12.37
p3	40.98 ± 0.07182
p4	-15.07 ± 0.0691

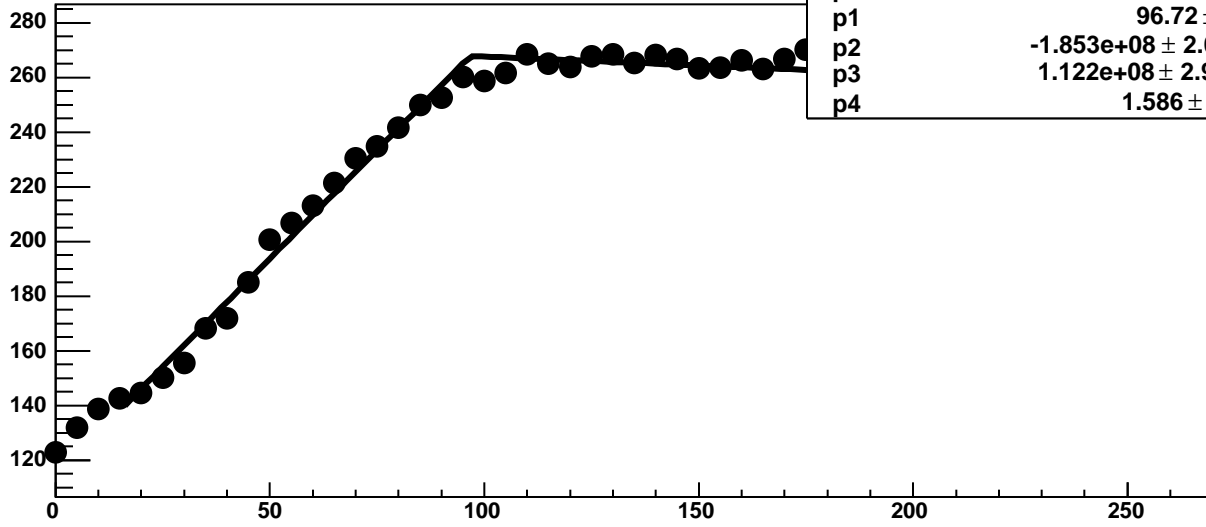
Chip 0, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold

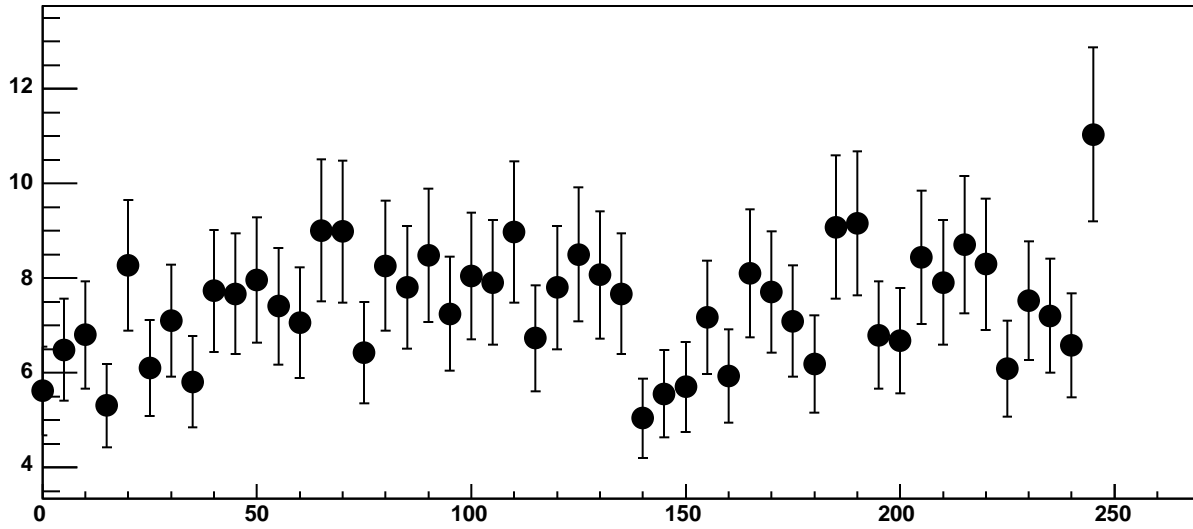


Chip 0, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

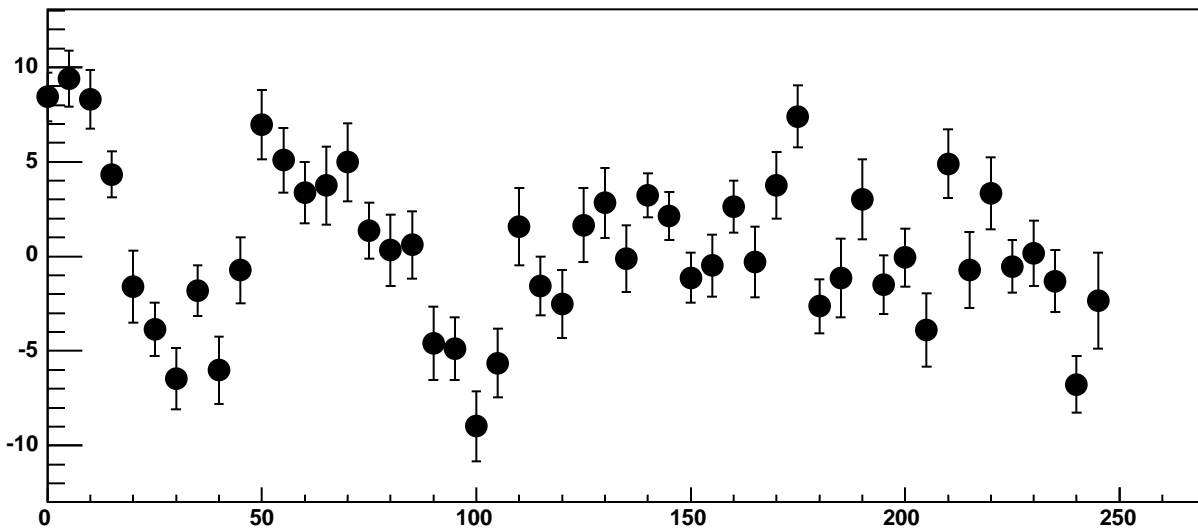


χ^2 / ndf	224.9 / 41
p0	267.8 ± 0.6032
p1	96.72 ± 0.6122
p2	$-1.853\text{e}+08 \pm 2.057\text{e}+06$
p3	$1.122\text{e}+08 \pm 2.988\text{e}+05$
p4	1.586 ± 0.01538

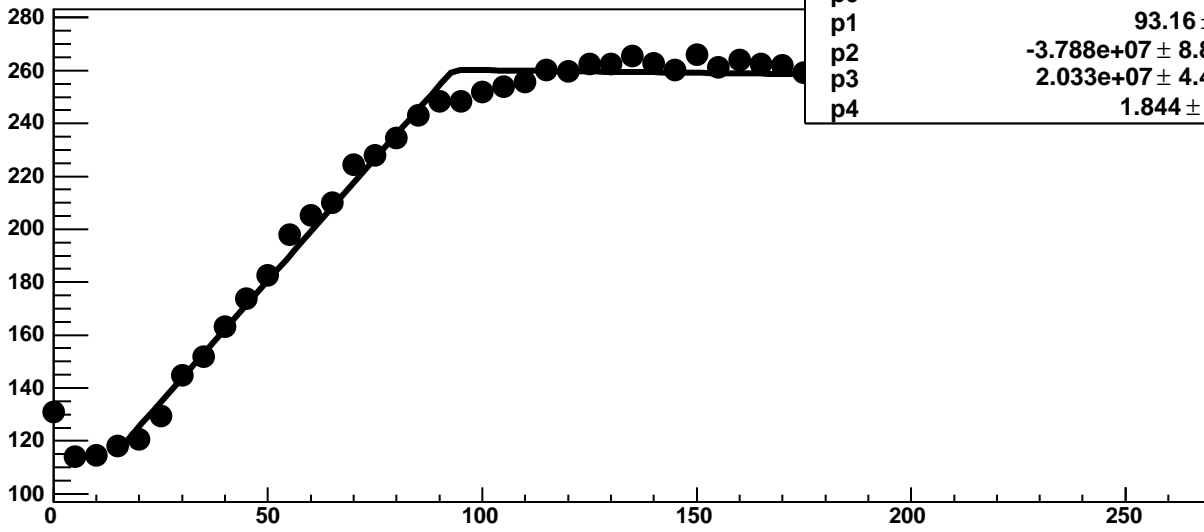
Chip 0, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

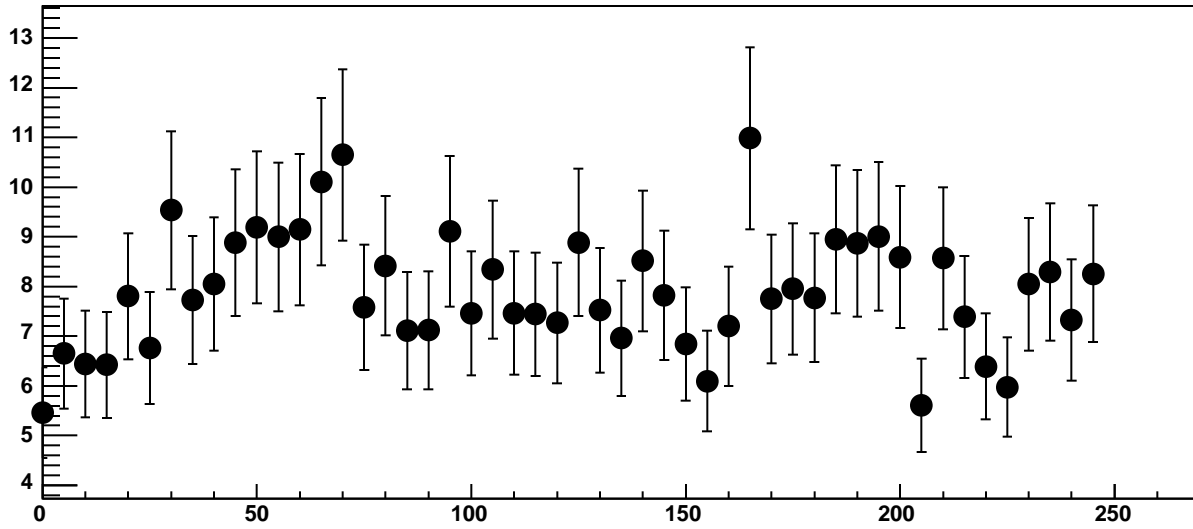


Chip 0, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

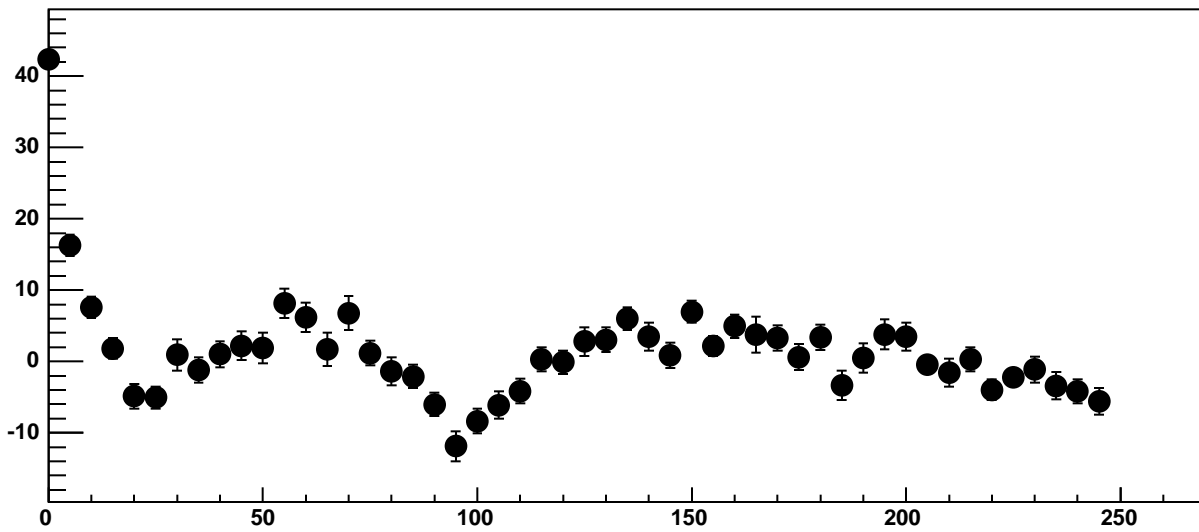


χ^2 / ndf	236.9 / 41
p0	260.3 ± 0.6377
p1	93.16 ± 0.6002
p2	$-3.788e+07 \pm 8.839e+05$
p3	$2.033e+07 \pm 4.425e+05$
p4	1.844 ± 0.01849

Chip 0, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold

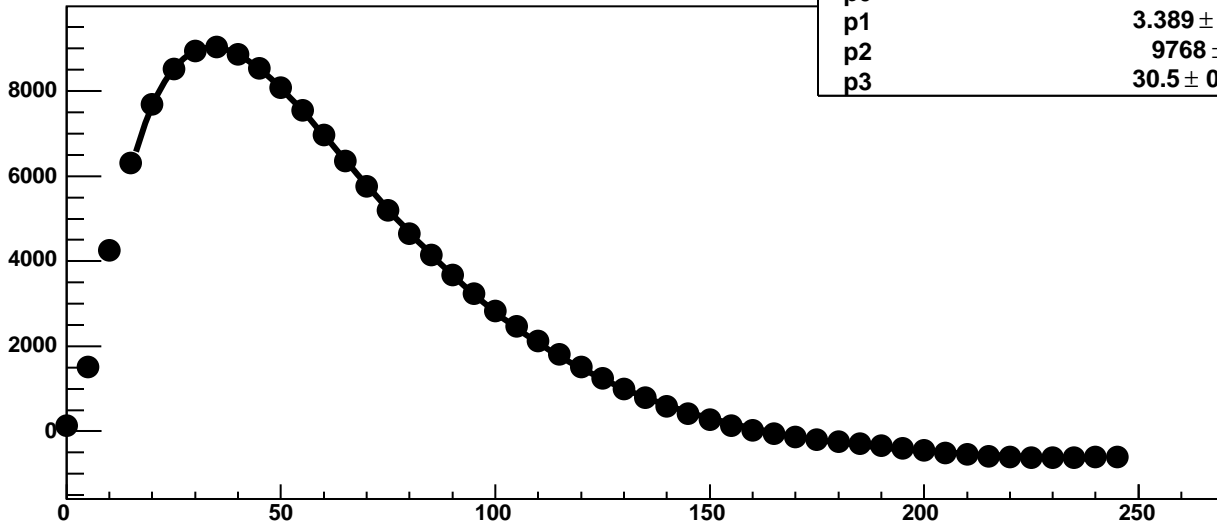


Chip 0, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

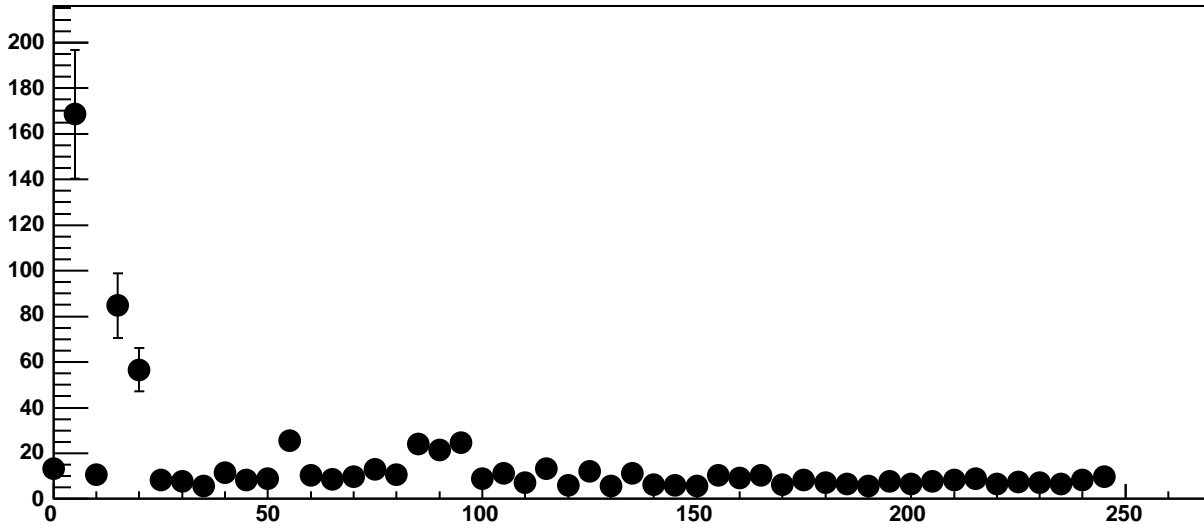


Chip 0, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold

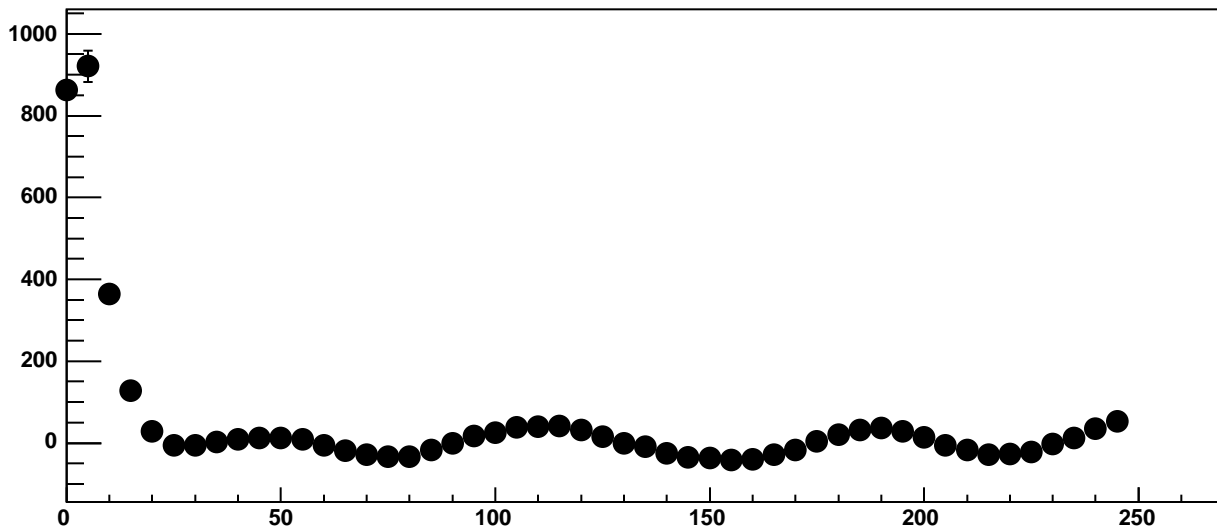
χ^2 / ndf	8068 / 42
p0	-735.4 ± 0.5662
p1	3.389 ± 0.01396
p2	9768 ± 0.8645
p3	30.5 ± 0.006875



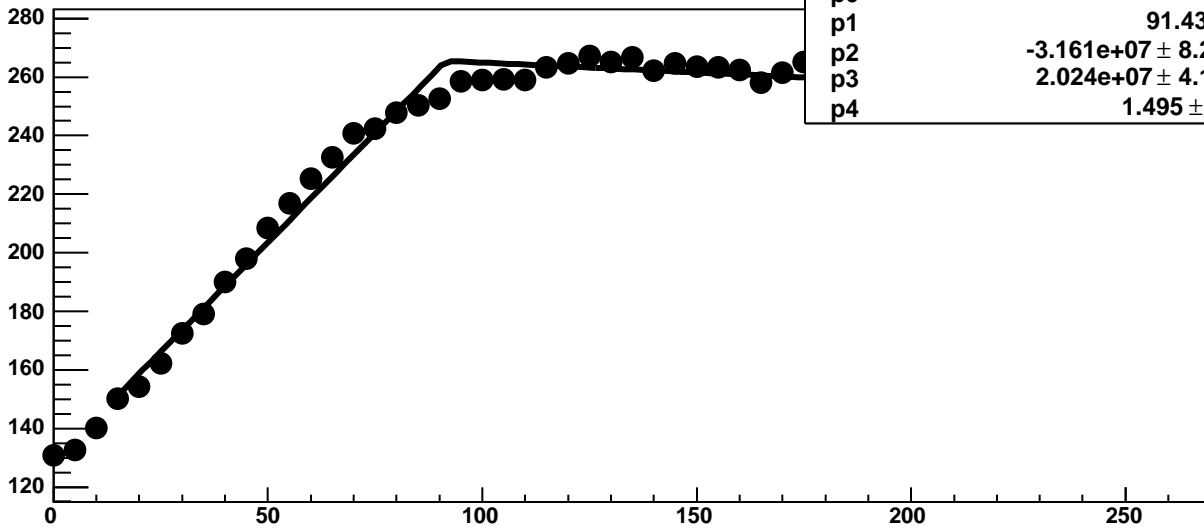
Chip 0, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold

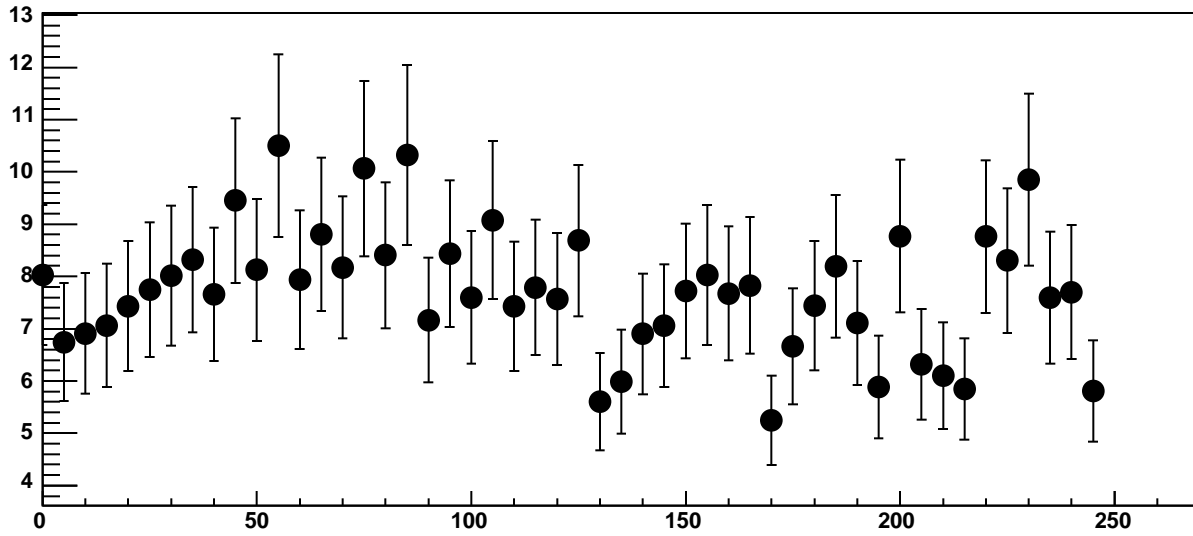


Chip 0, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

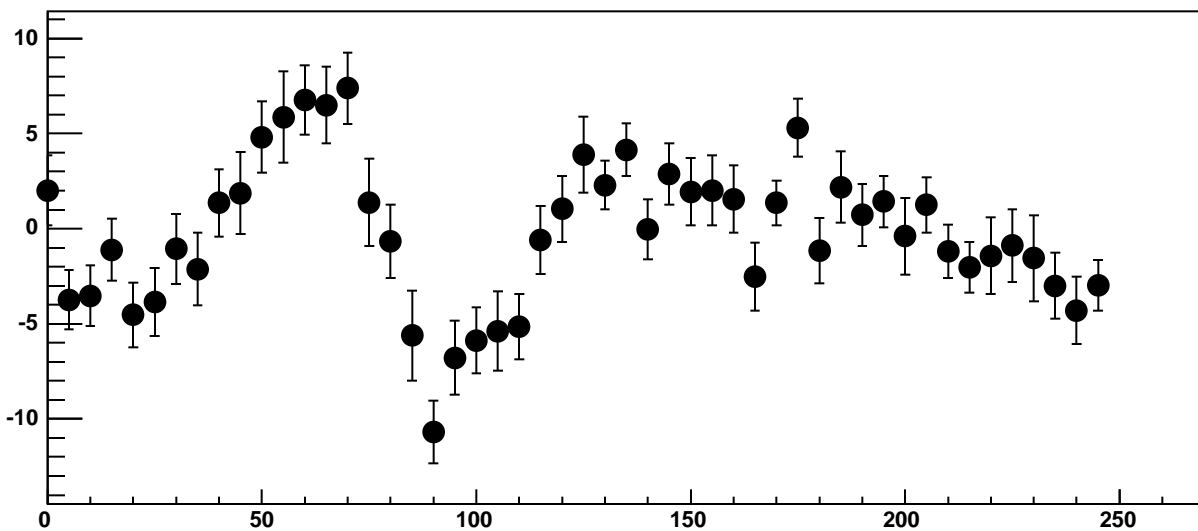


χ^2 / ndf	210.5 / 41
p0	265.5 ± 0.6174
p1	91.43 ± 0.729
p2	$-3.161\text{e}+07 \pm 8.214\text{e}+05$
p3	$2.024\text{e}+07 \pm 4.158\text{e}+05$
p4	1.495 ± 0.01981

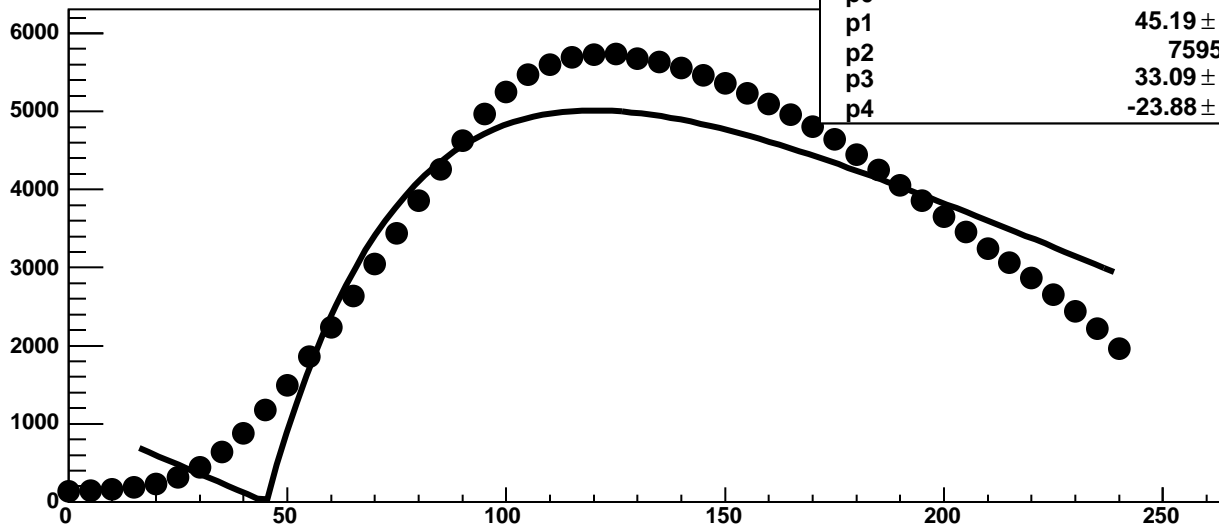
Chip 0, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

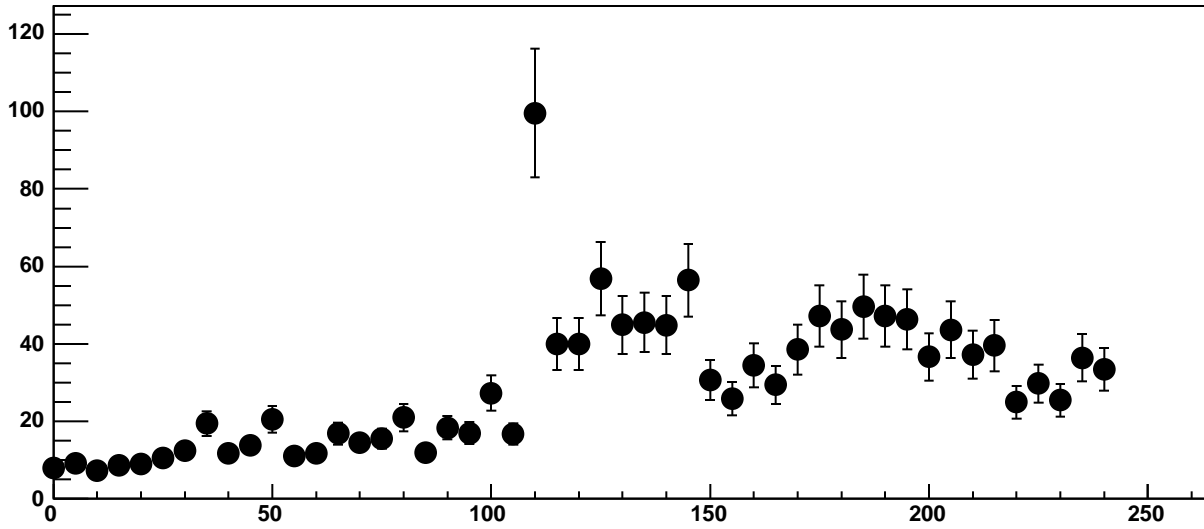


Chip 0, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

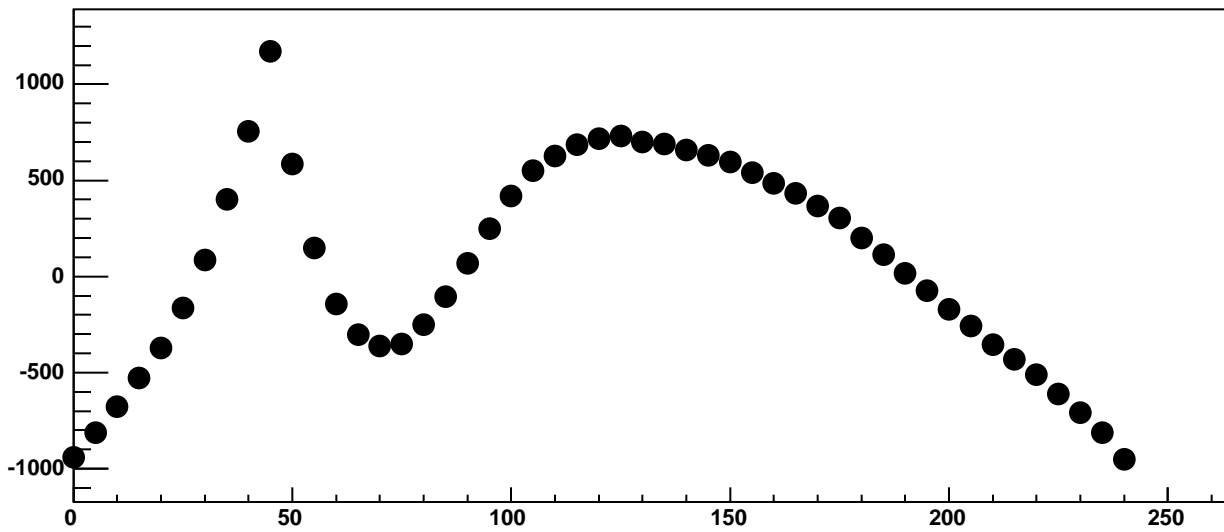


χ^2 / ndf	5.339e+05 / 41
p0	-5.048 ± 1.157
p1	45.19 ± 0.01595
p2	7595 ± 7.996
p3	33.09 ± 0.04732
p4	-23.88 ± 0.04442

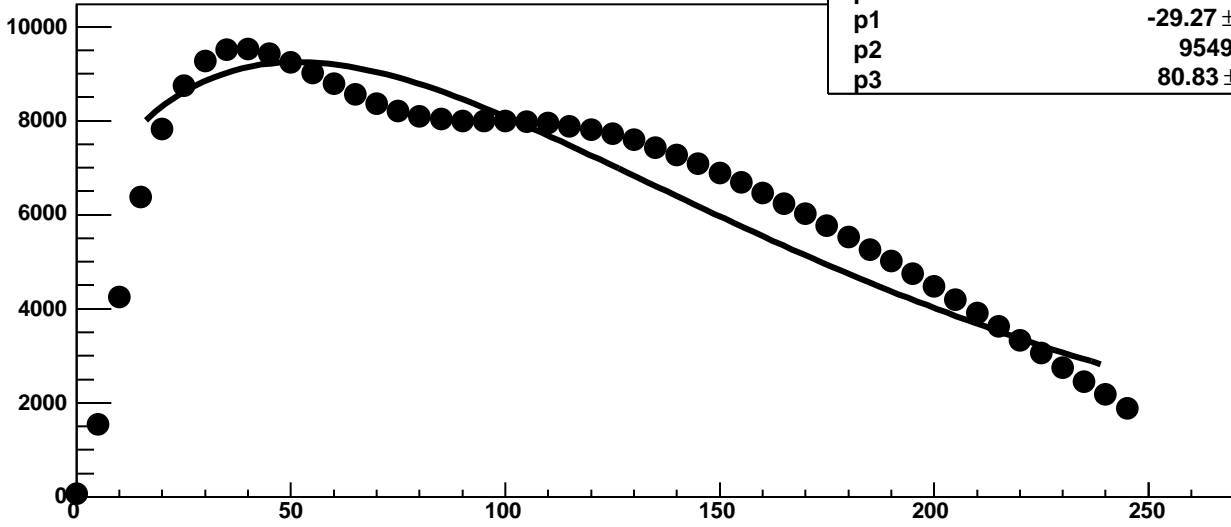
Chip 0, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold

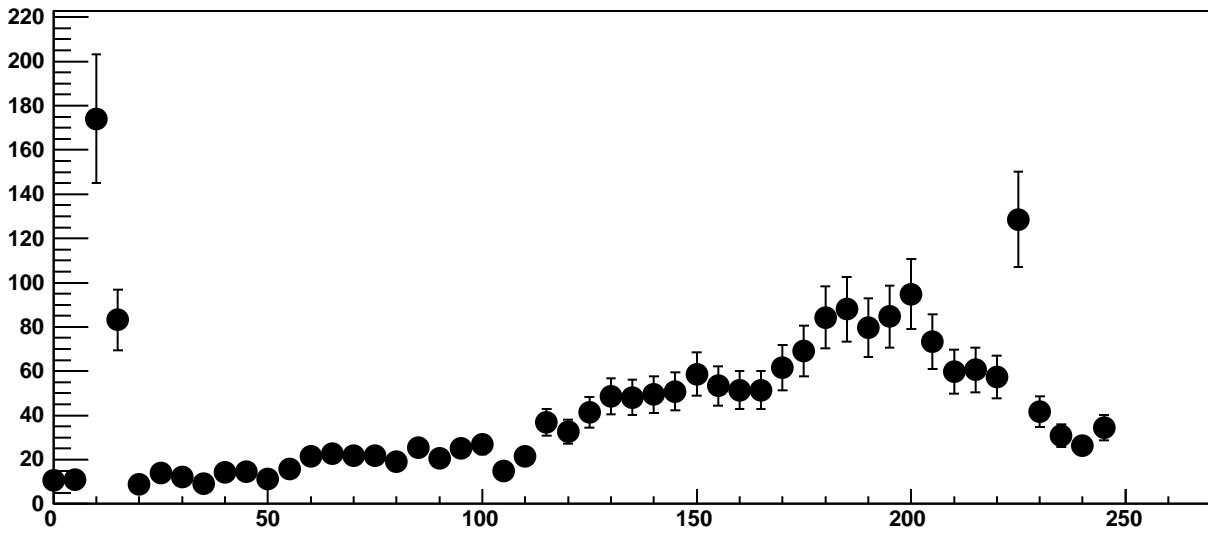


Chip 0, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold

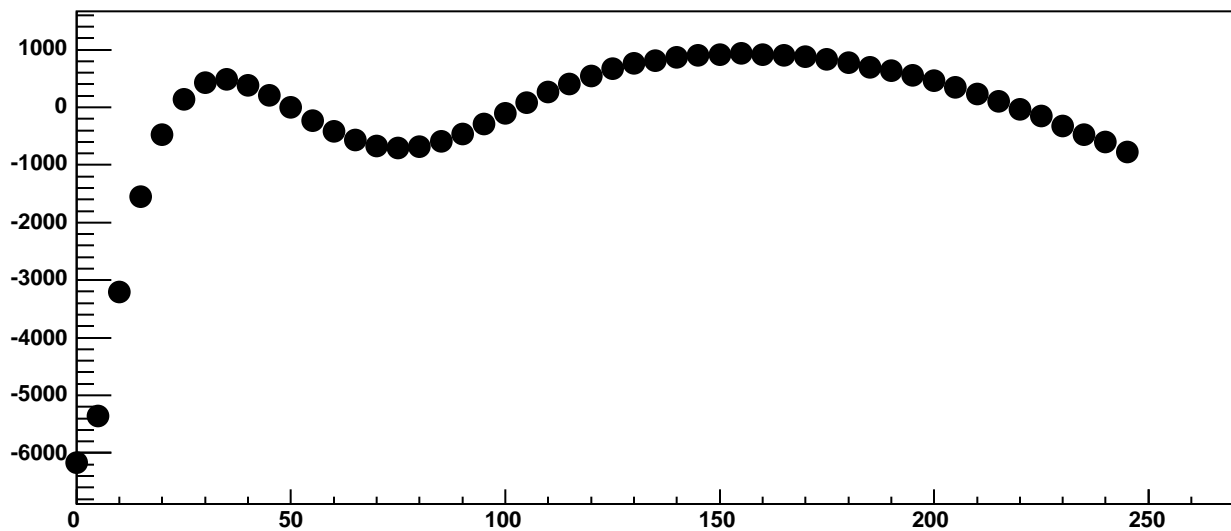


χ^2 / ndf	3.545e+05 / 42
p0	-299 ± 18.89
p1	-29.27 ± 0.1505
p2	9549 ± 18.21
p3	80.83 ± 0.1572

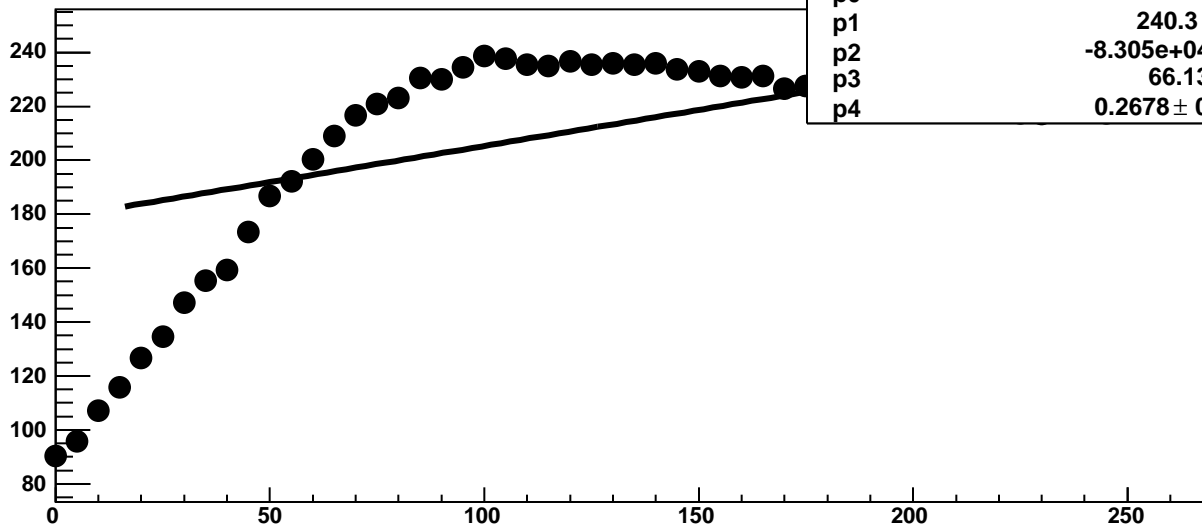
Chip 0, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold

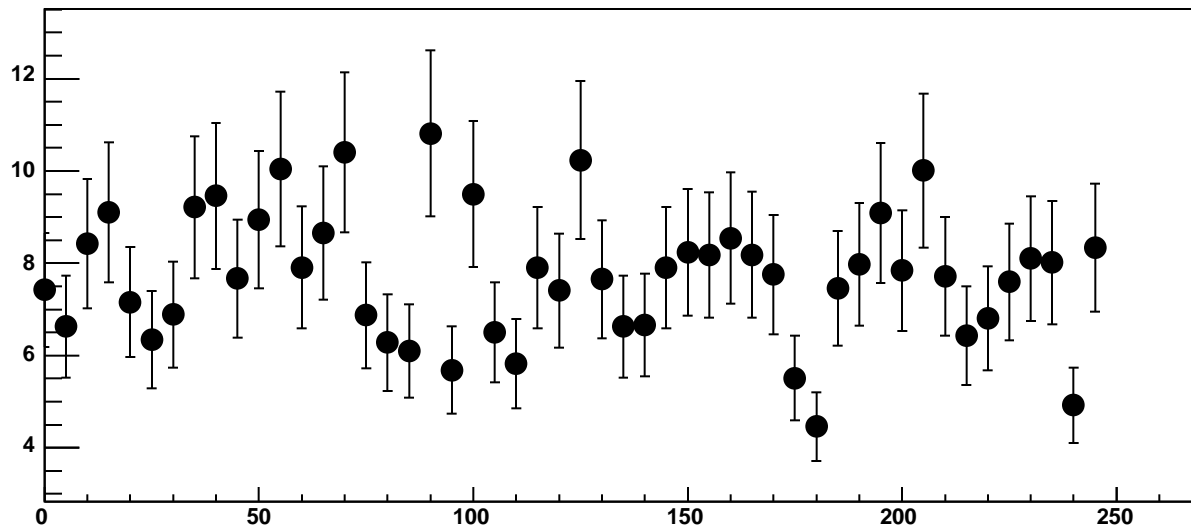


Chip 0, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

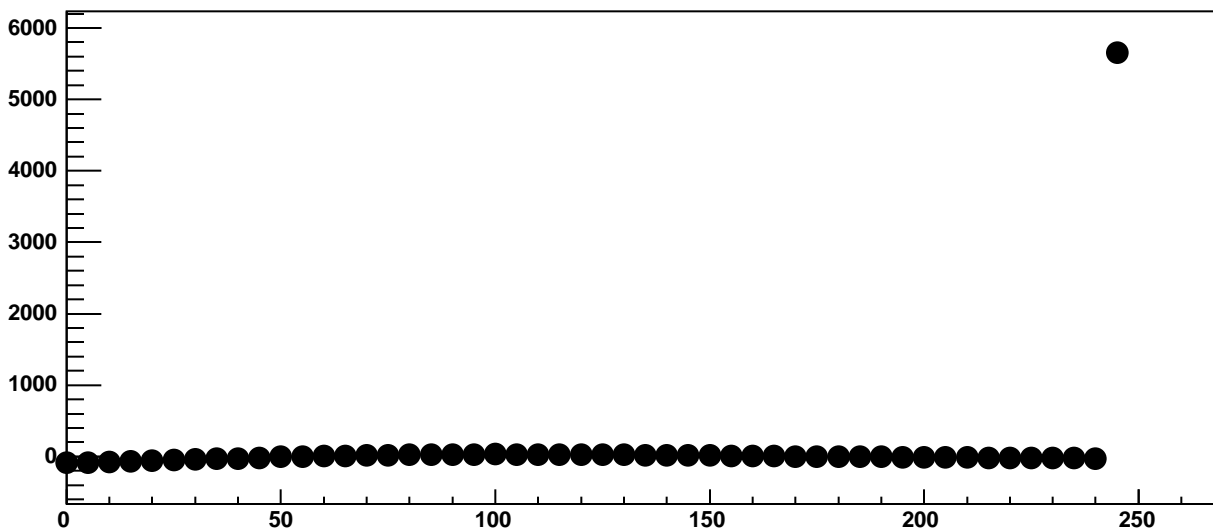


χ^2 / ndf	9940 / 41
p0	242.9 ± 0.2474
p1	240.3 ± 0.9237
p2	$-8.305e+04 \pm 84.85$
p3	66.13 ± 1.414
p4	0.2678 ± 0.001968

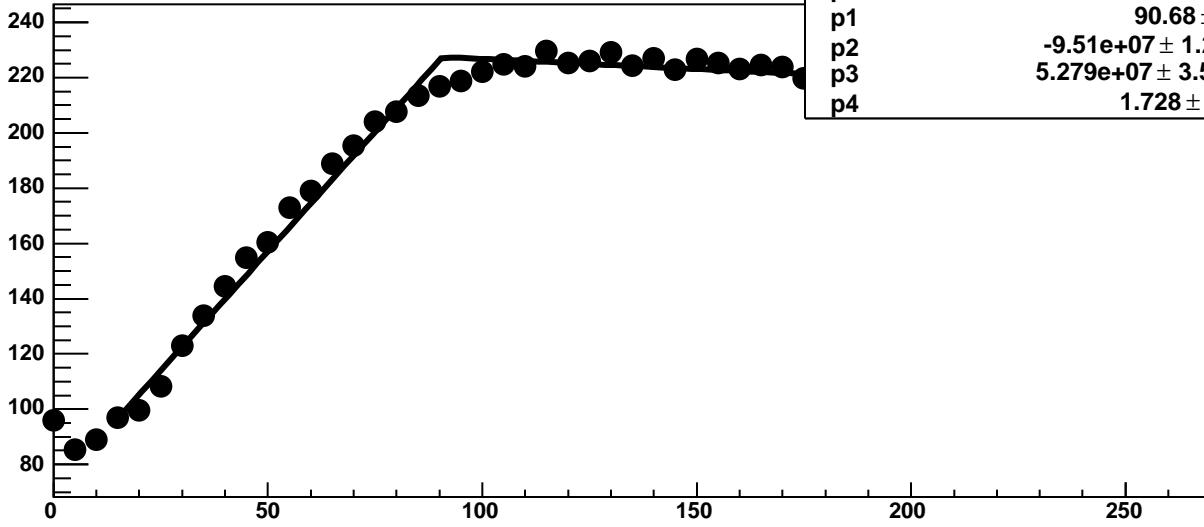
Chip 0, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold

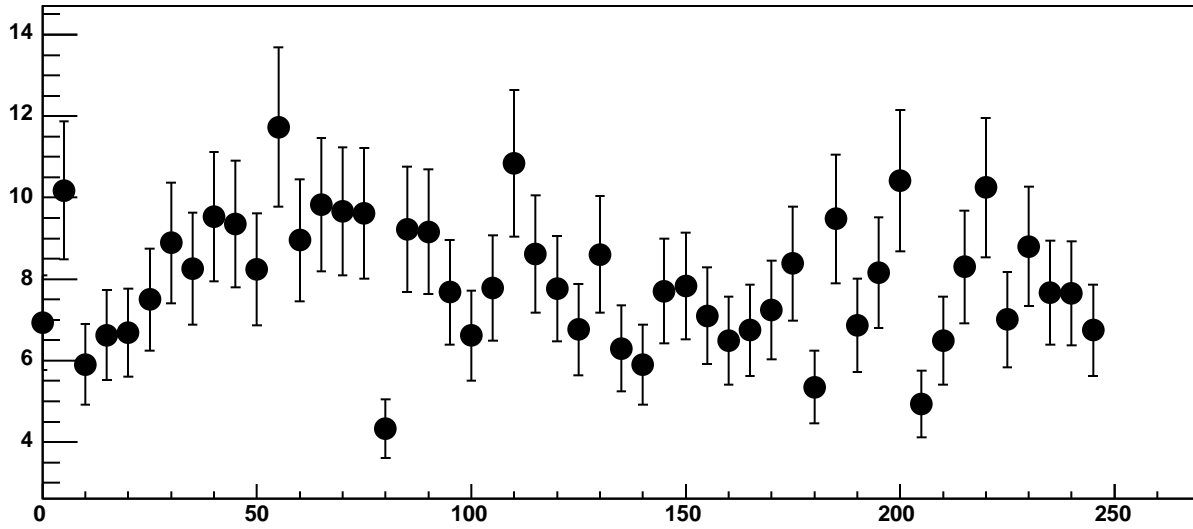


Chip 0, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

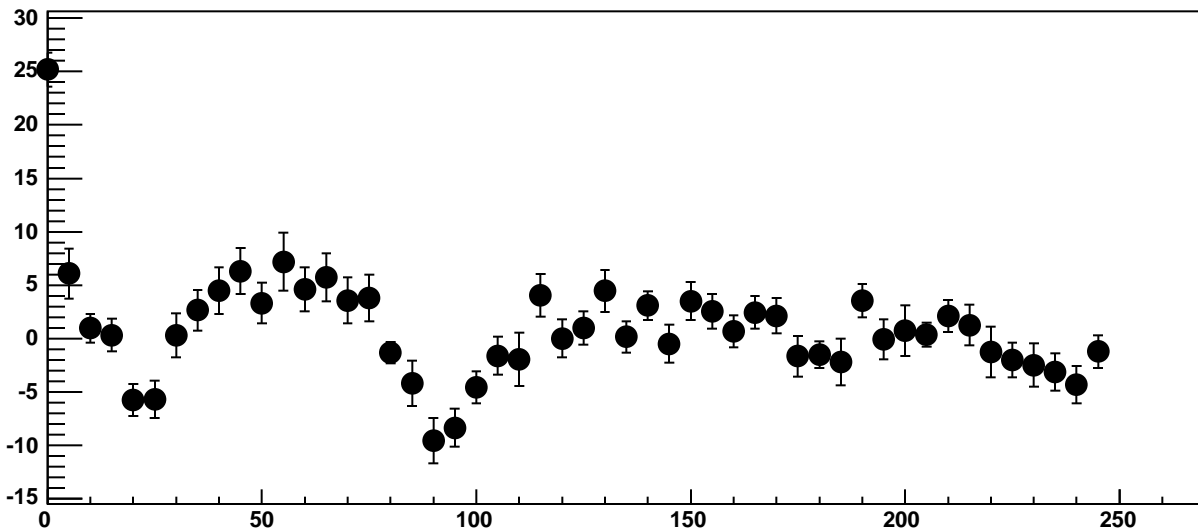


χ^2 / ndf	176.8 / 41
p0	227.5 ± 0.626
p1	90.68 ± 0.5959
p2	$-9.51\text{e}+07 \pm 1.261\text{e}+06$
p3	$5.279\text{e}+07 \pm 3.524\text{e}+05$
p4	1.728 ± 0.01812

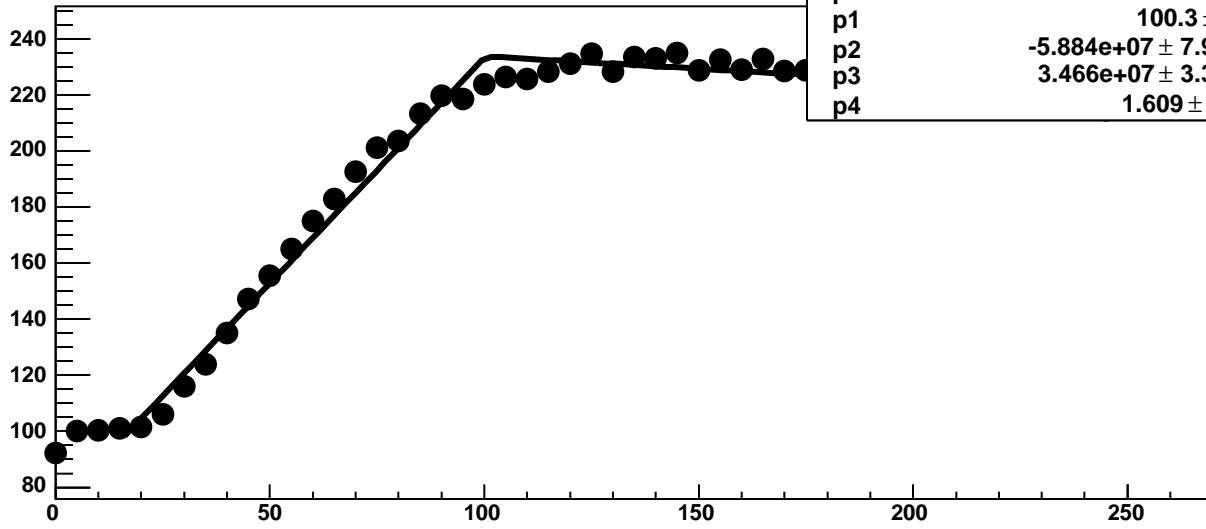
Chip 0, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

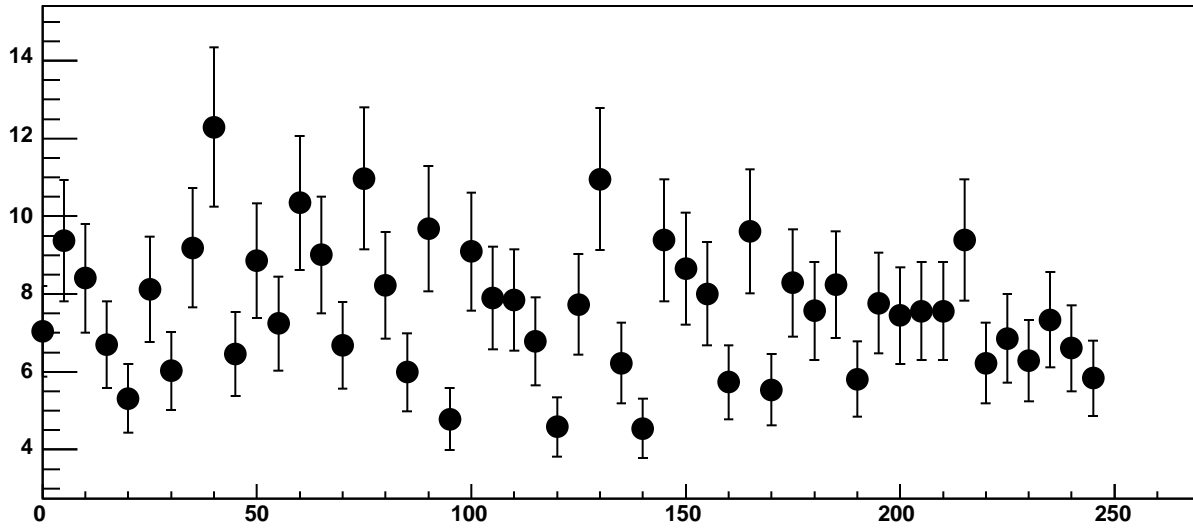


Chip 0, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold

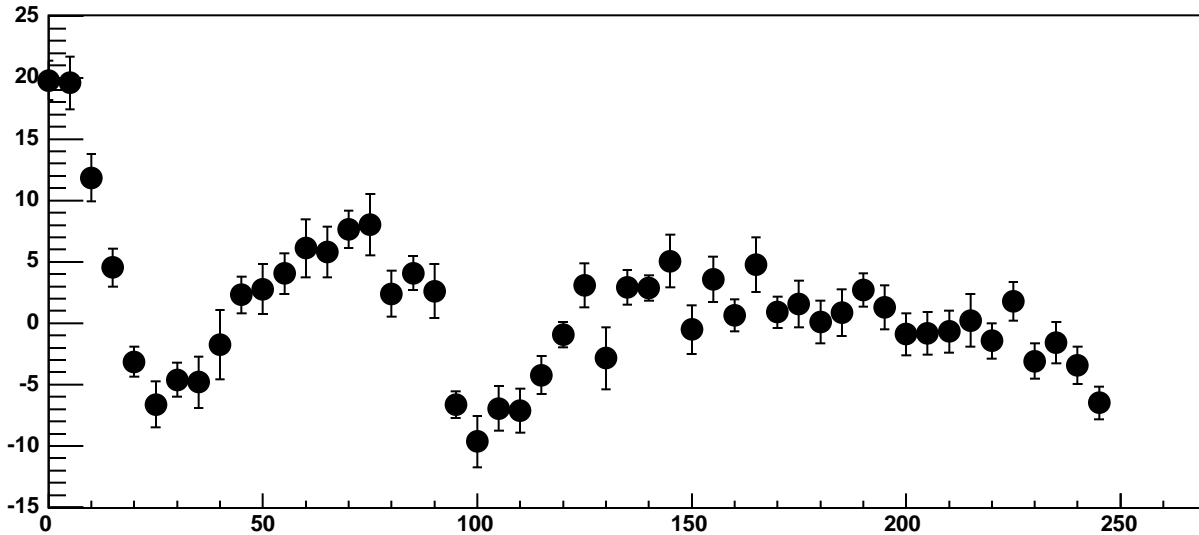


χ^2 / ndf	262.6 / 41
p0	233.7 ± 0.5675
p1	100.3 ± 0.5522
p2	$-5.884\text{e}+07 \pm 7.927\text{e}+05$
p3	$3.466\text{e}+07 \pm 3.385\text{e}+05$
p4	1.609 ± 0.01397

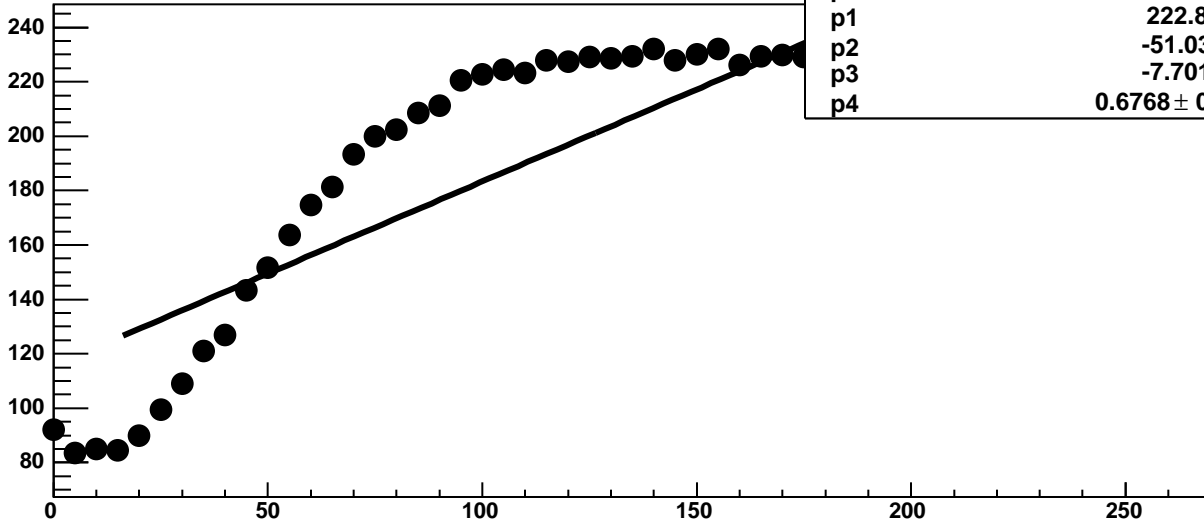
Chip 0, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold

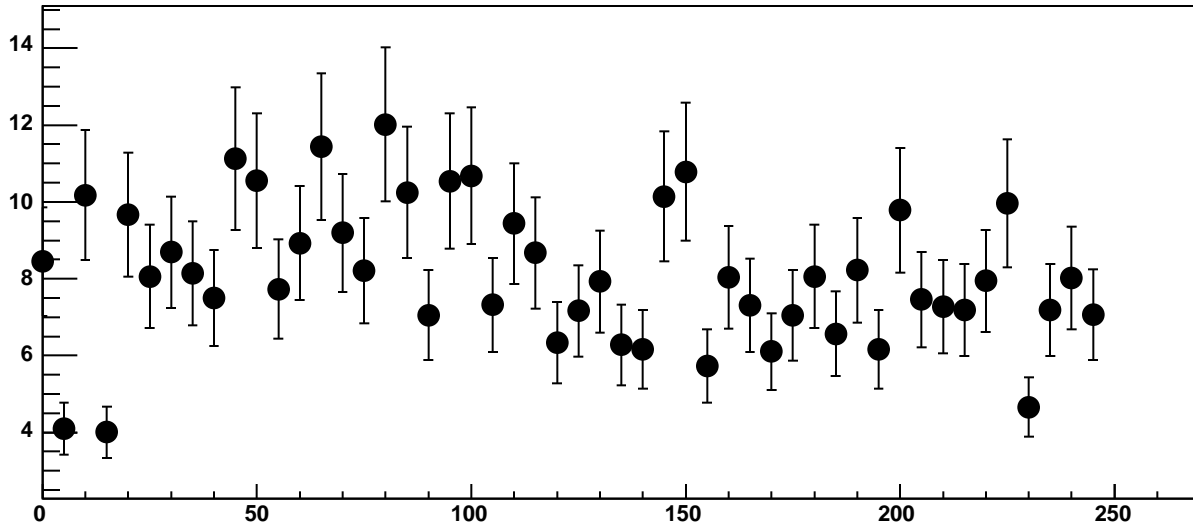


Chip 0, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold

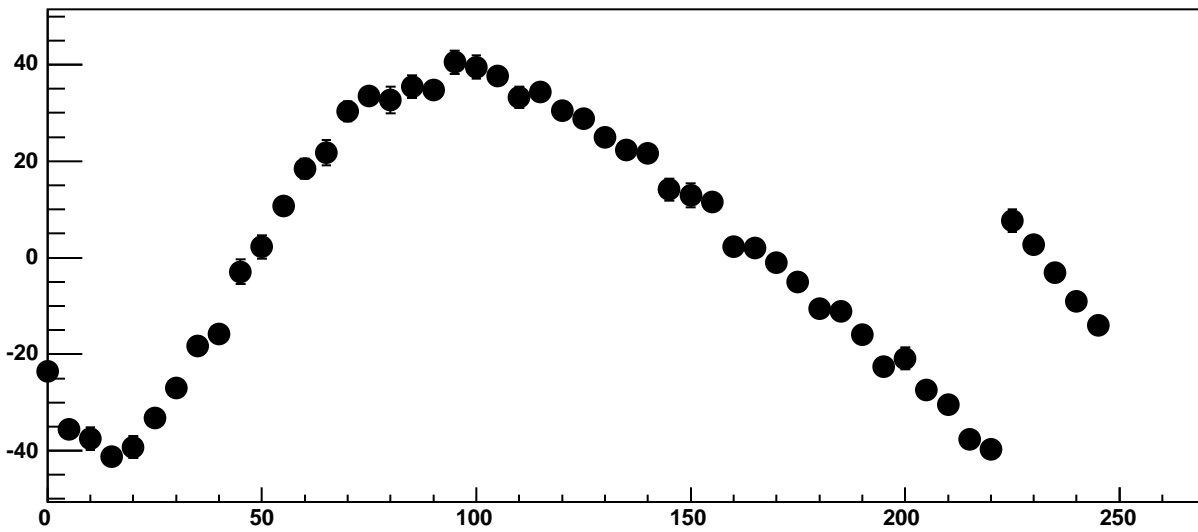


χ^2 / ndf	9819 / 41
p0	266.4 ± 2.004
p1	222.8 ± 4.572
p2	-51.03 ± 3.741
p3	-7.701 ± 9.283
p4	0.6768 ± 0.004332

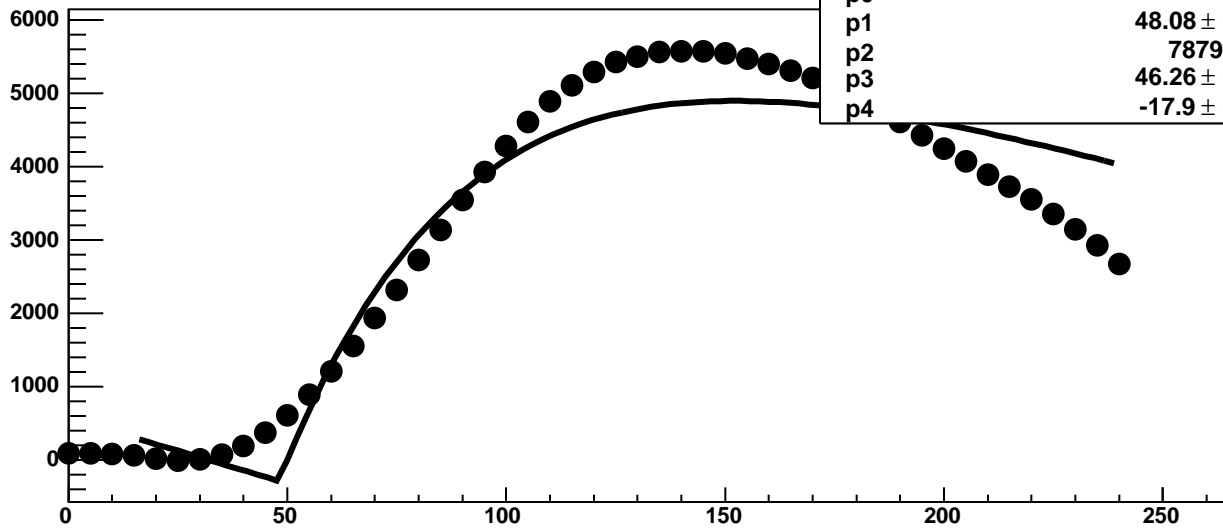
Chip 0, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

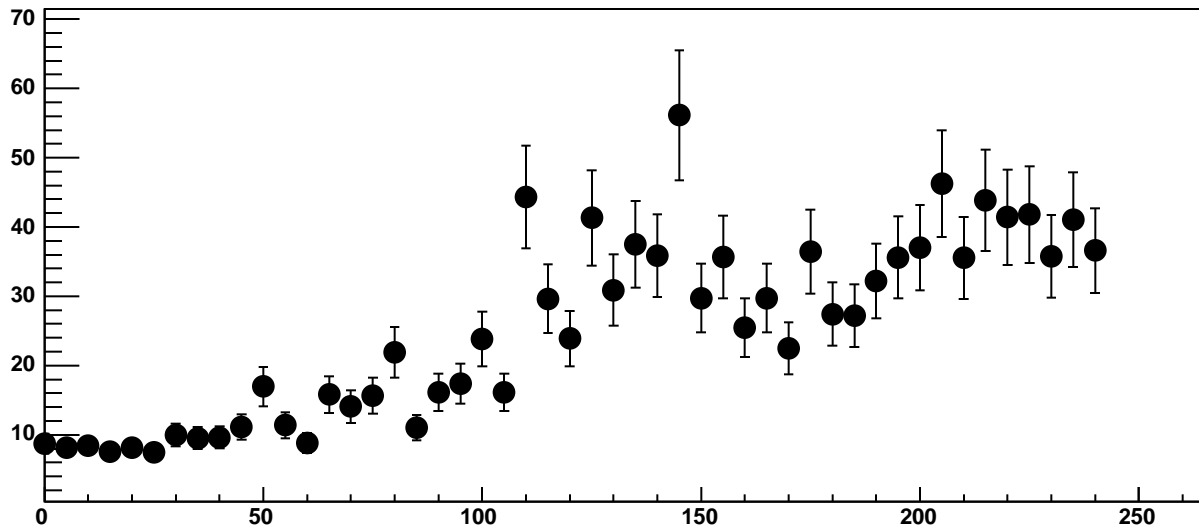


Chip 0, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

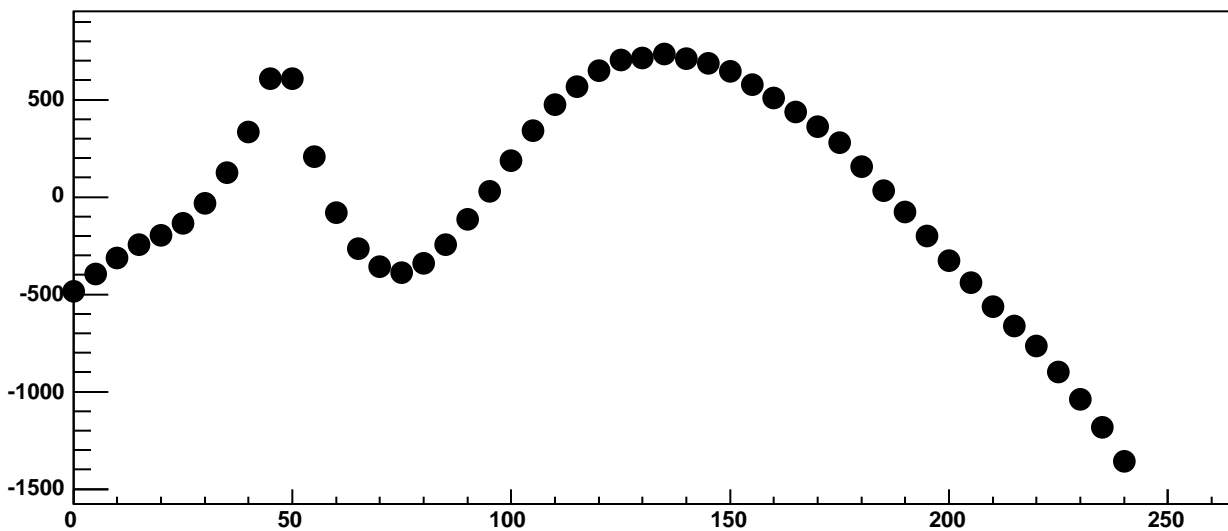


χ^2 / ndf	3.805e+05 / 41
p0	-285.6 ± 1.182
p1	48.08 ± 0.01538
p2	7879 ± 10.09
p3	46.26 ± 0.06224
p4	-17.9 ± 0.05198

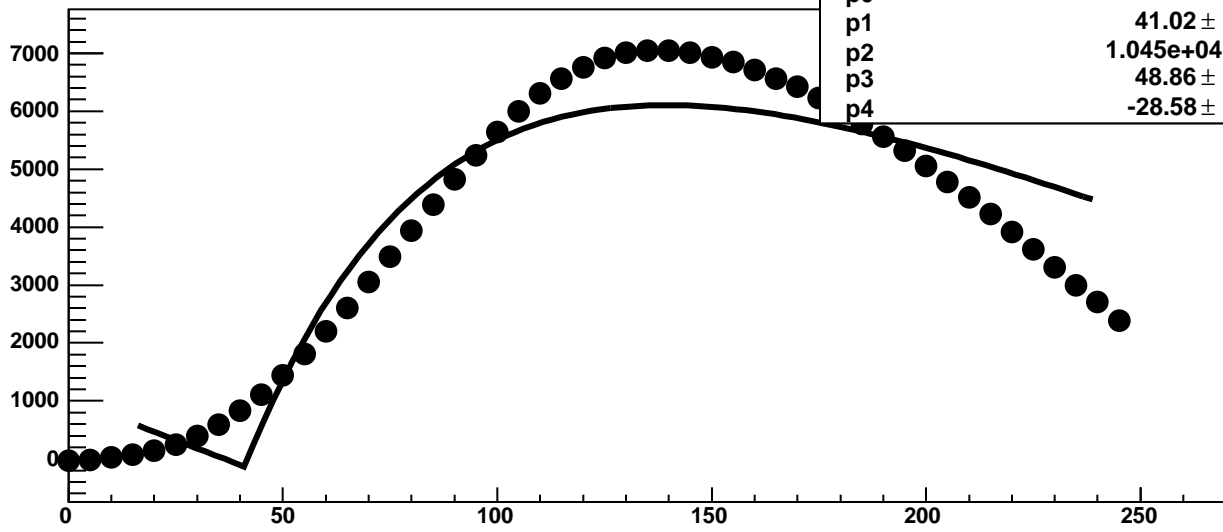
Chip 0, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

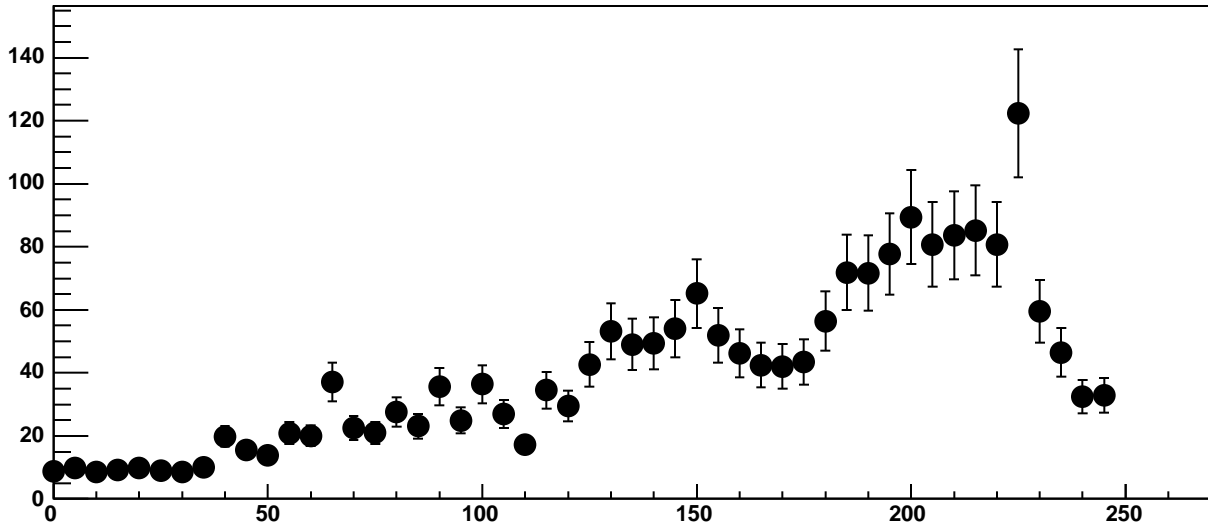


Chip 0, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold

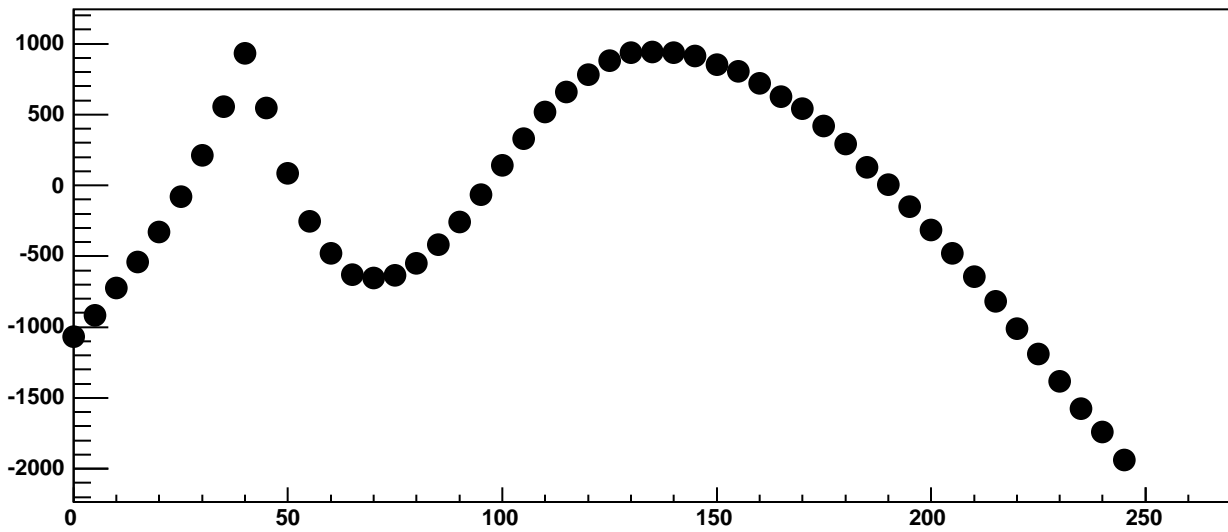


χ^2 / ndf	4.842e+05 / 41
p0	-134.2 ± 1.296
p1	41.02 ± 0.01573
p2	1.045e+04 ± 14.18
p3	48.86 ± 0.07182
p4	-28.58 ± 0.07158

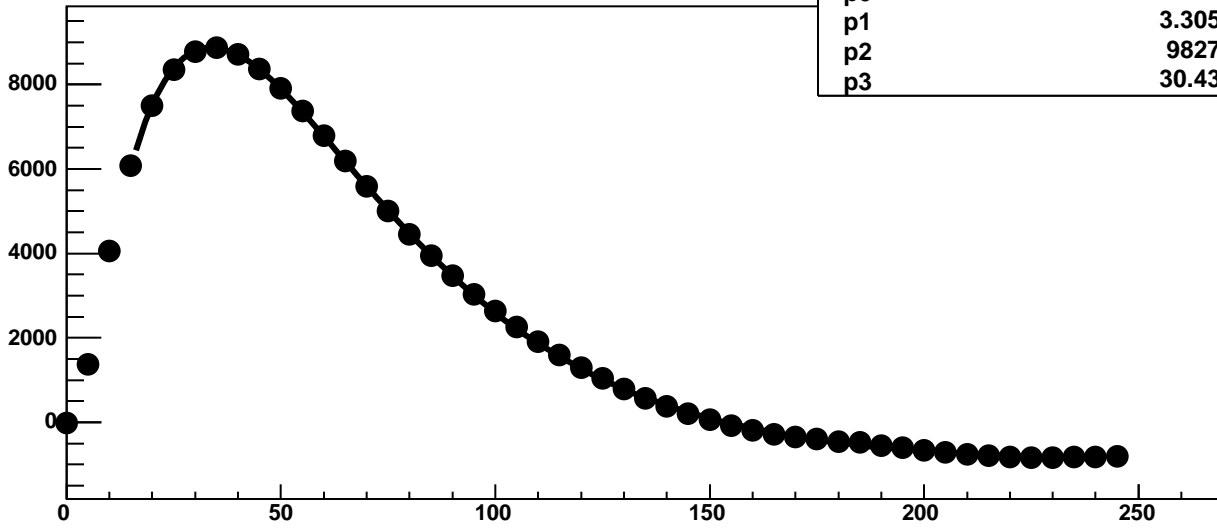
Chip 0, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

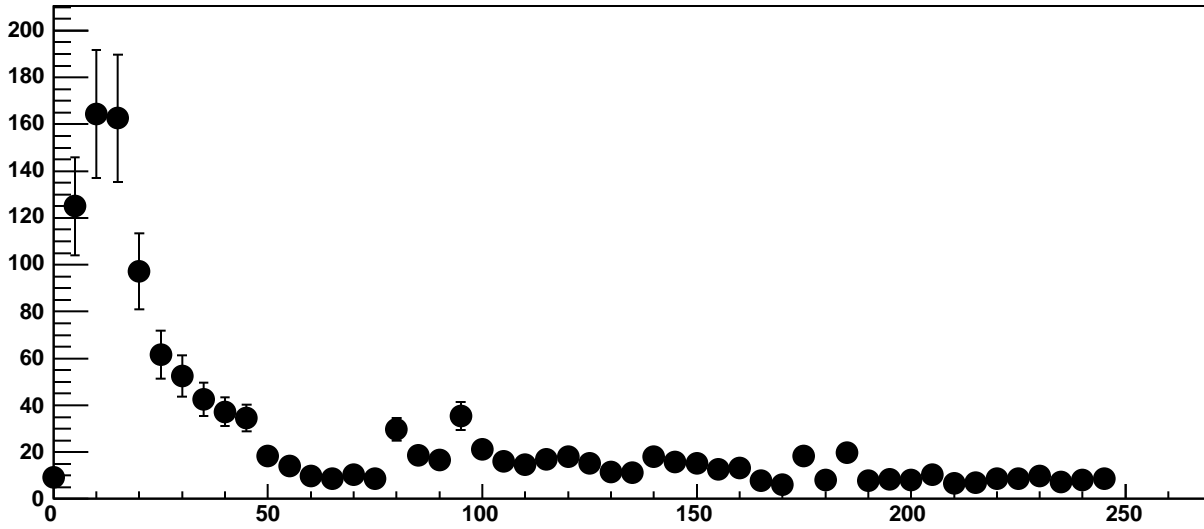


Chip 0, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold

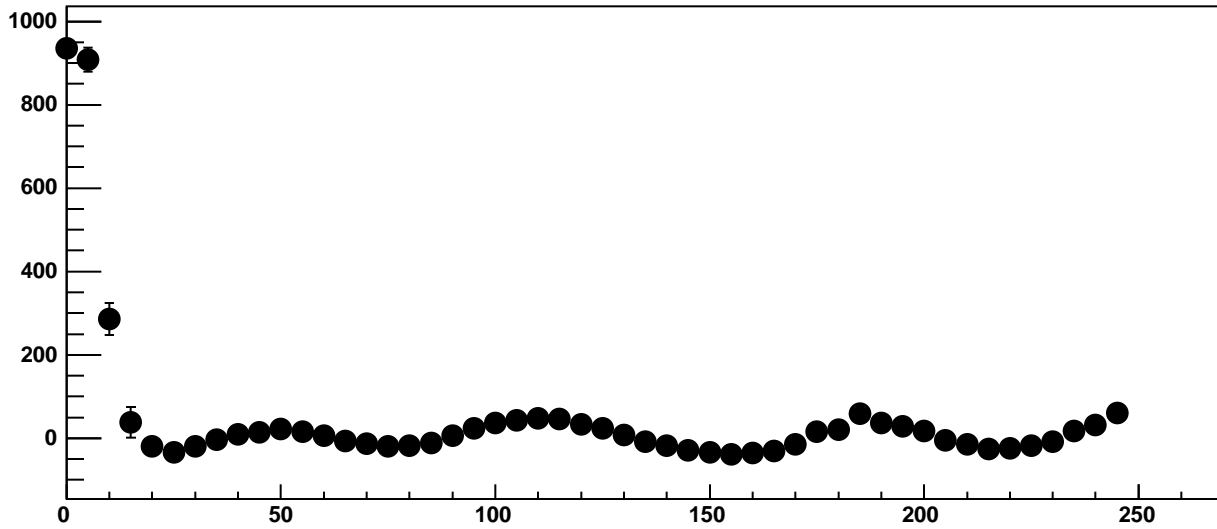


χ^2 / ndf	3826 / 42
p0	-945.3 ± 0.7341
p1	3.305 ± 0.039
p2	9827 ± 3.764
p3	30.43 ± 0.013

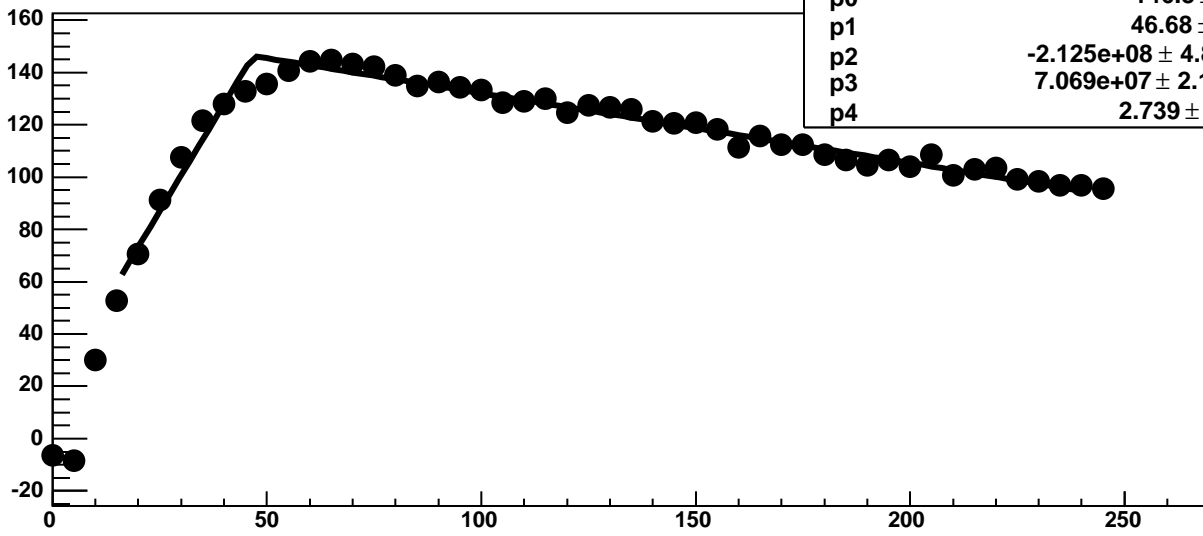
Chip 0, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold

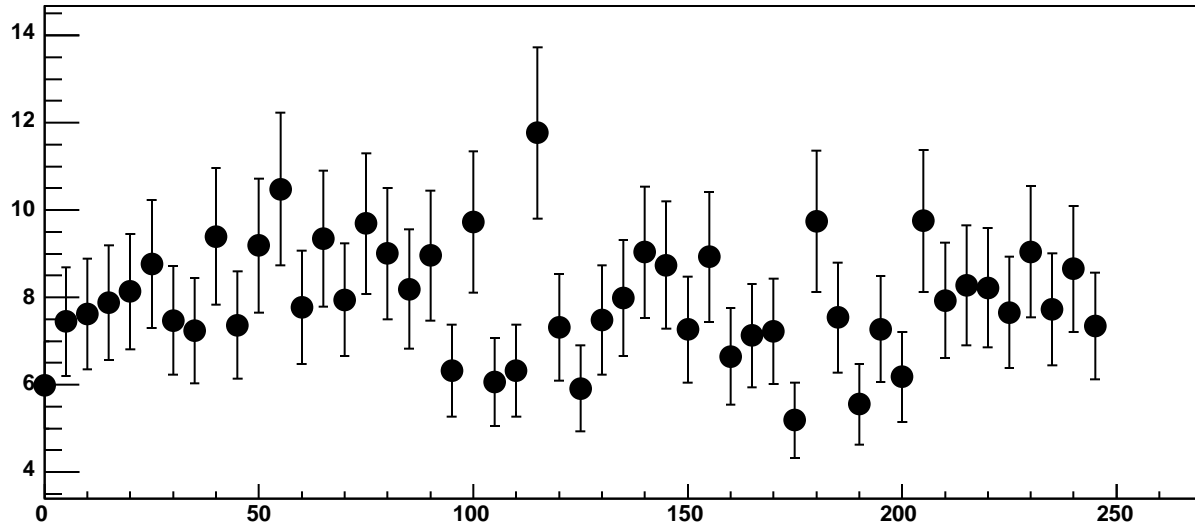


Chip 0, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

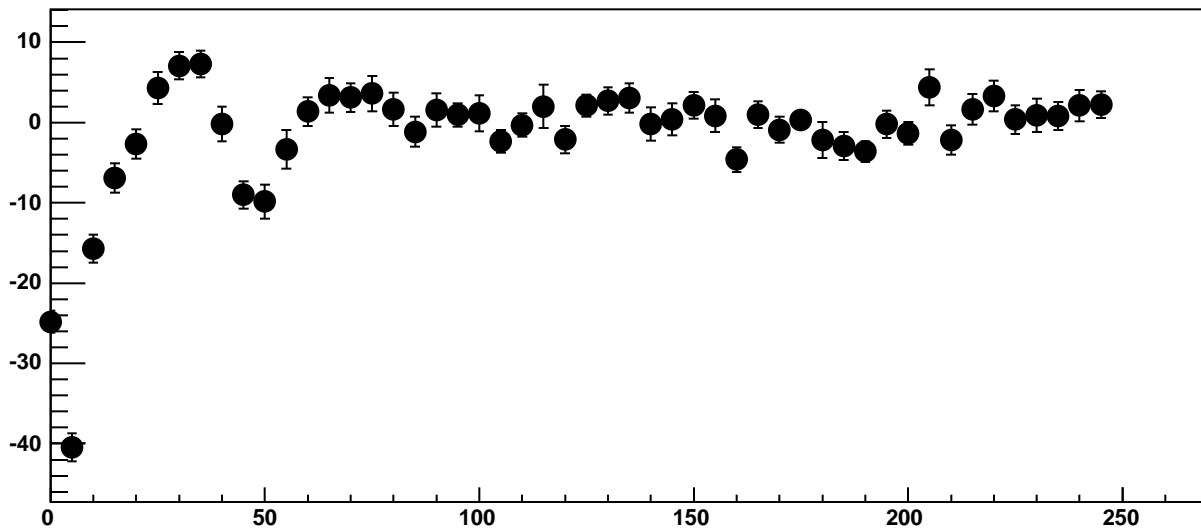


χ^2 / ndf	169 / 41
p0	146.3 ± 0.5688
p1	46.68 ± 0.4855
p2	$-2.125\text{e}+08 \pm 4.844\text{e}+06$
p3	$7.069\text{e}+07 \pm 2.143\text{e}+05$
p4	2.739 ± 0.06842

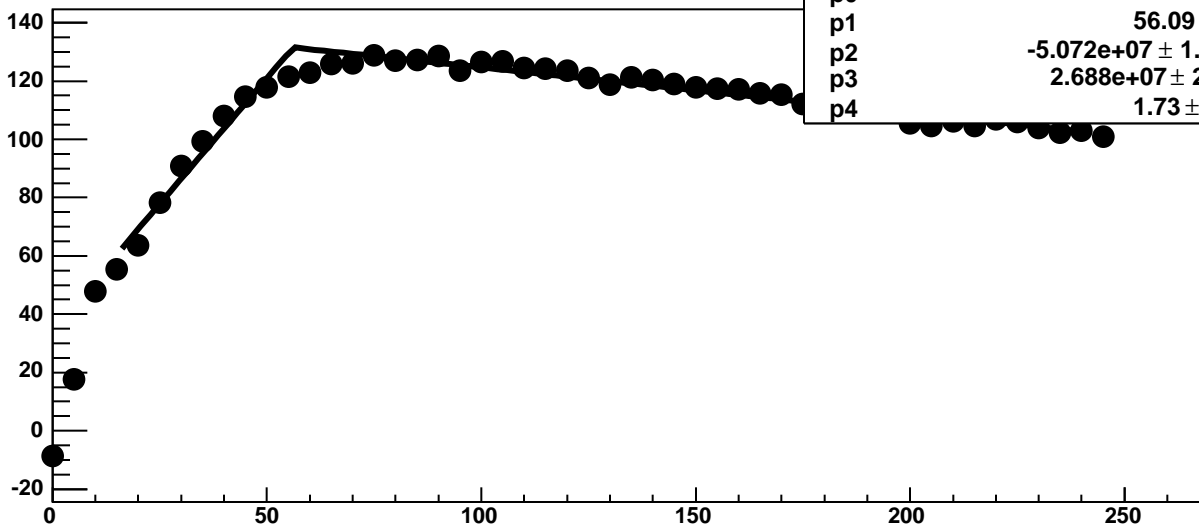
Chip 0, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

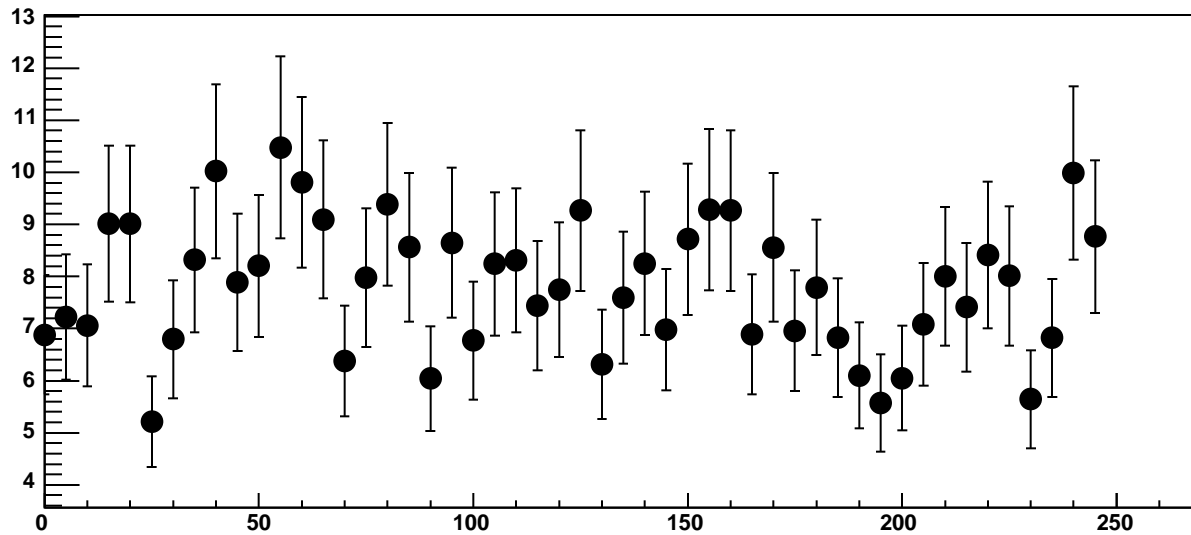


Chip 0, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

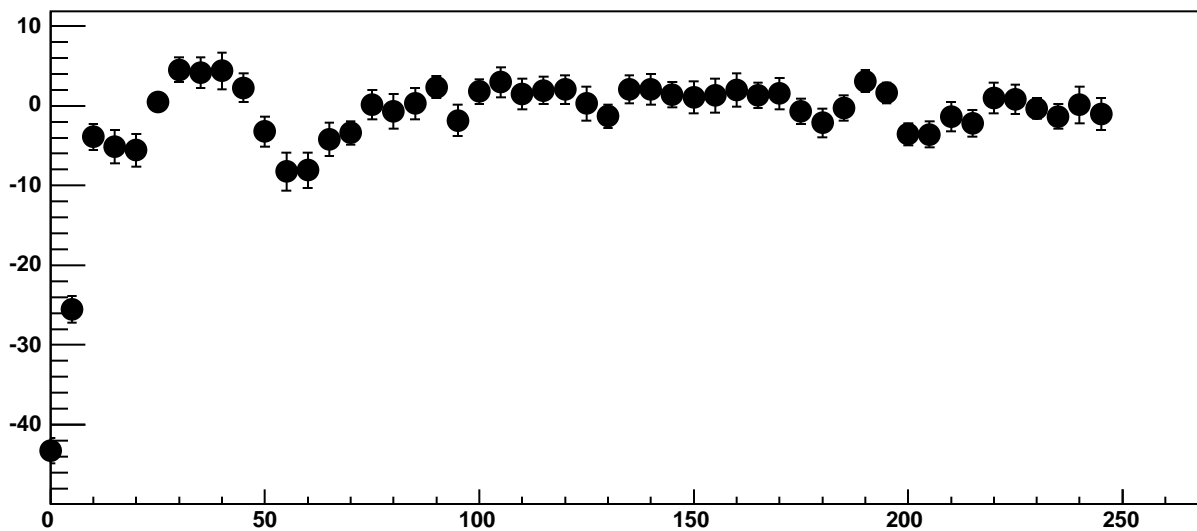


χ^2 / ndf	110 / 41
p0	131.5 ± 0.5572
p1	56.09 ± 0.7667
p2	$-5.072\text{e}+07 \pm 1.466\text{e}+06$
p3	$2.688\text{e}+07 \pm 2.98\text{e}+05$
p4	1.73 ± 0.05102

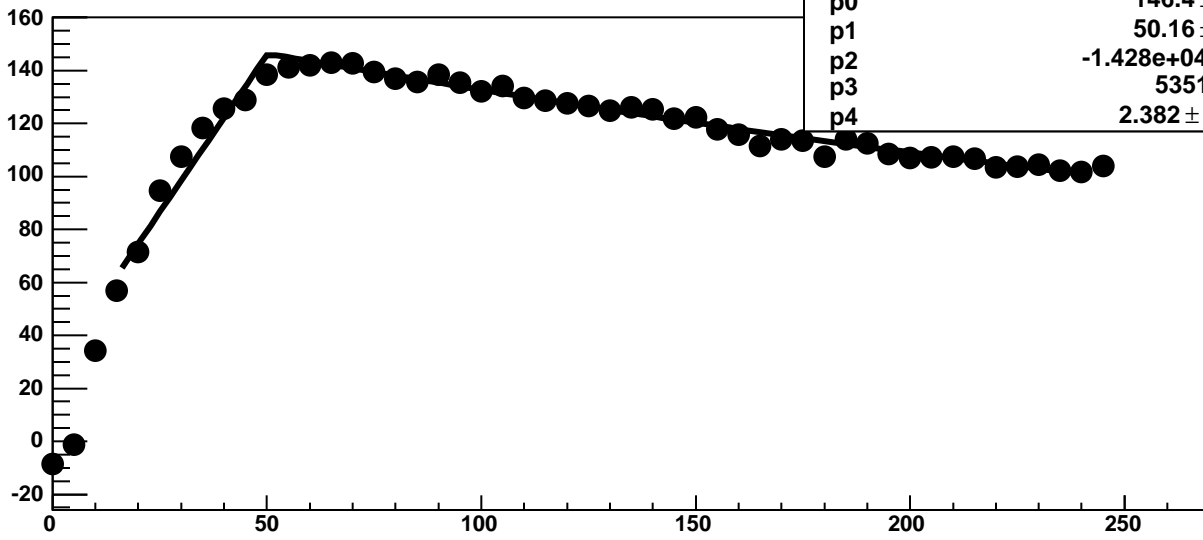
Chip 0, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

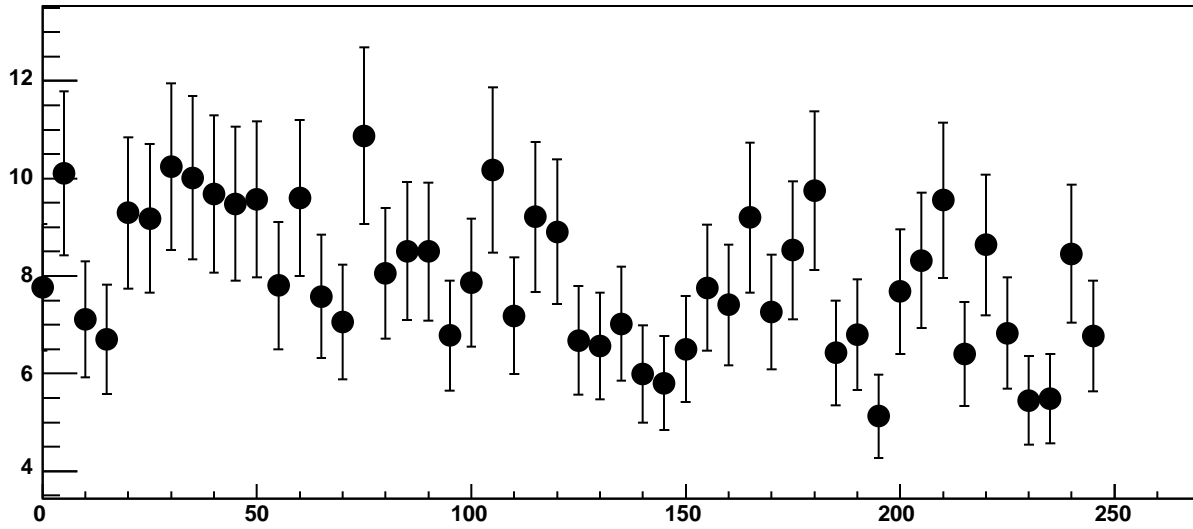


Chip 0, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold

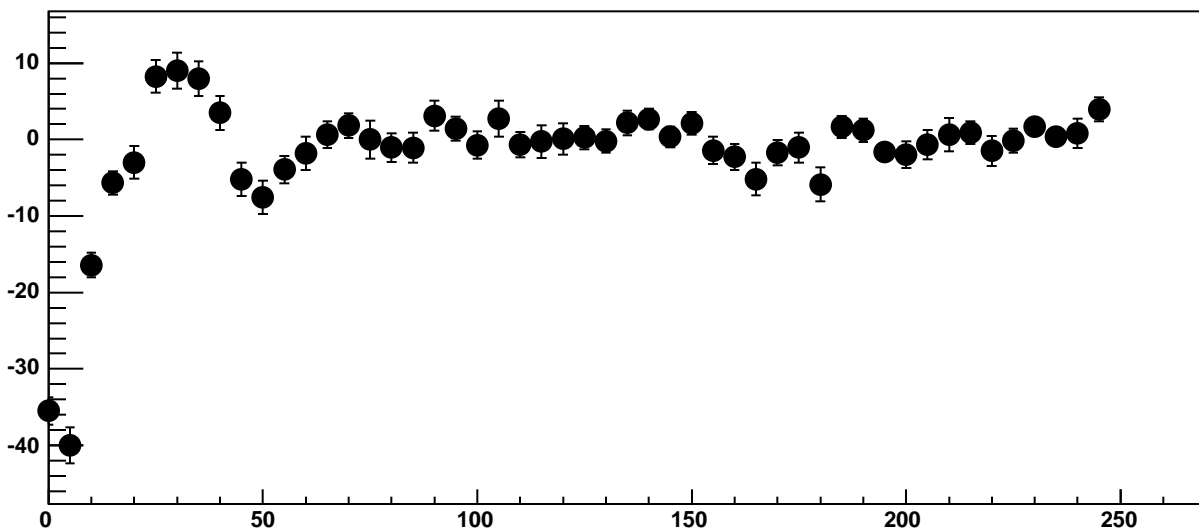


χ^2 / ndf	123 / 41
p0	146.4 ± 0.8509
p1	50.16 ± 0.6302
p2	$-1.428e+04 \pm 5759$
p3	5351 ± 2188
p4	2.382 ± 0.05989

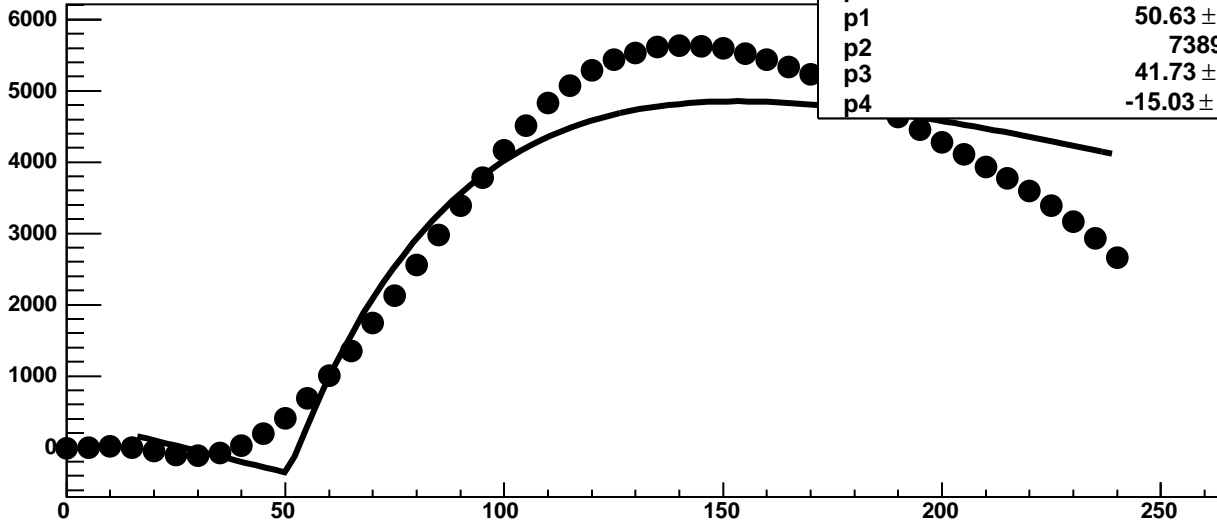
Chip 0, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold

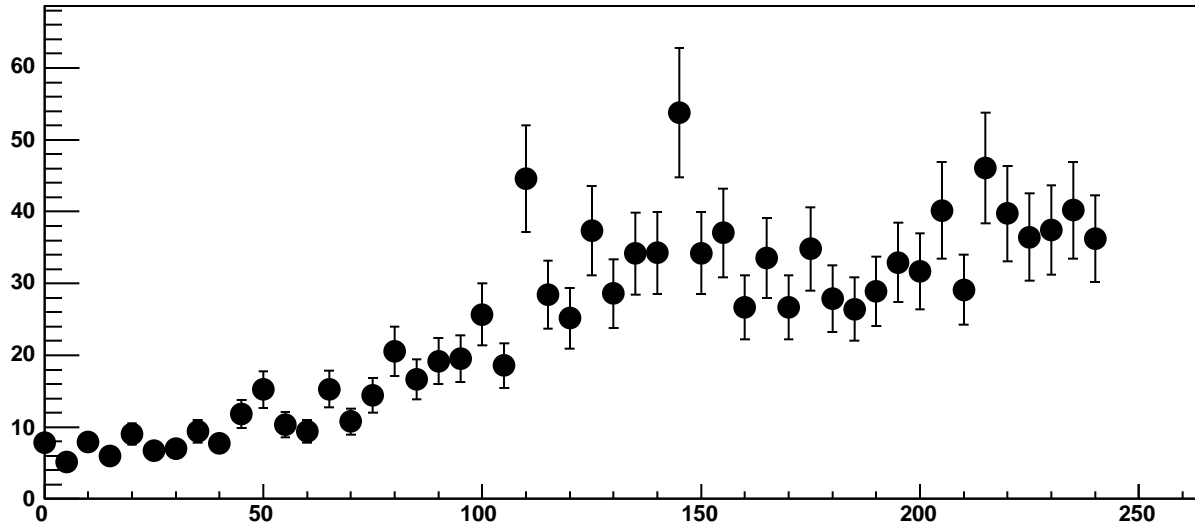


Chip 0, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold

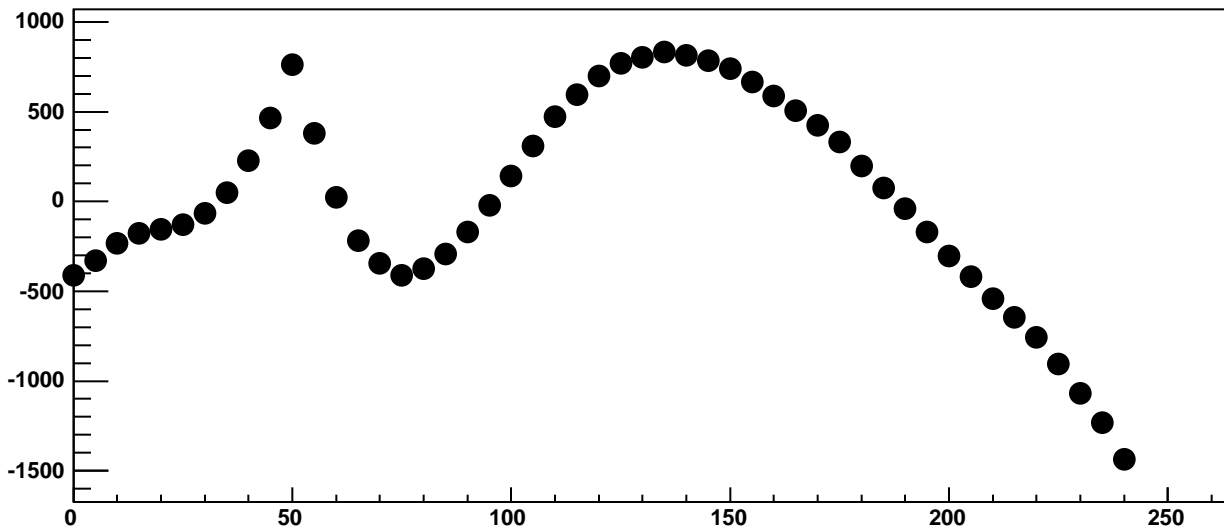


χ^2 / ndf	4.151e+05 / 41
p0	-361.2 ± 1.119
p1	50.63 ± 0.01461
p2	7389 ± 8.541
p3	41.73 ± 0.05451
p4	-15.03 ± 0.04543

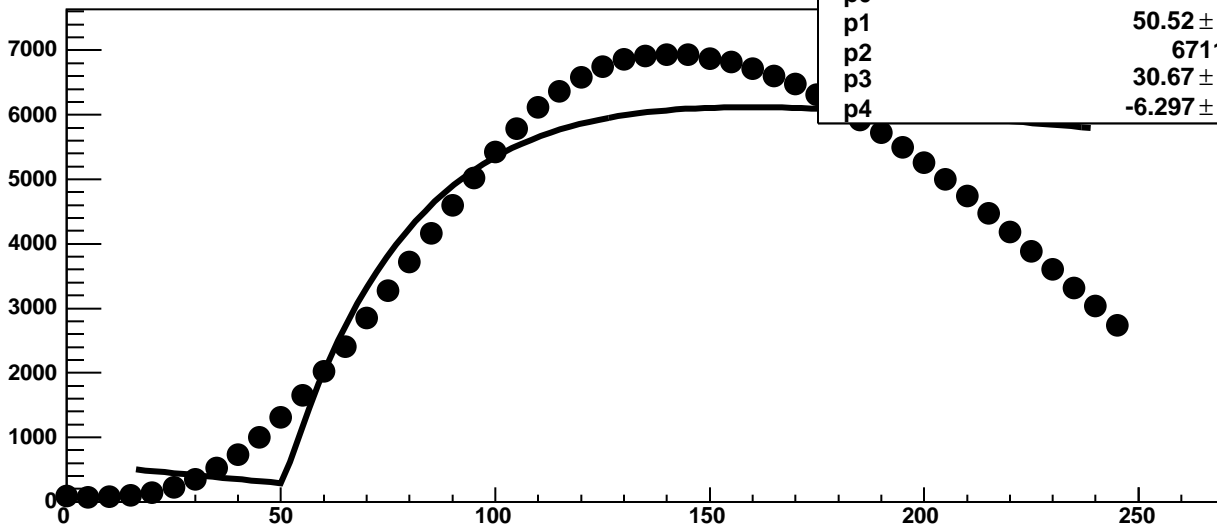
Chip 0, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



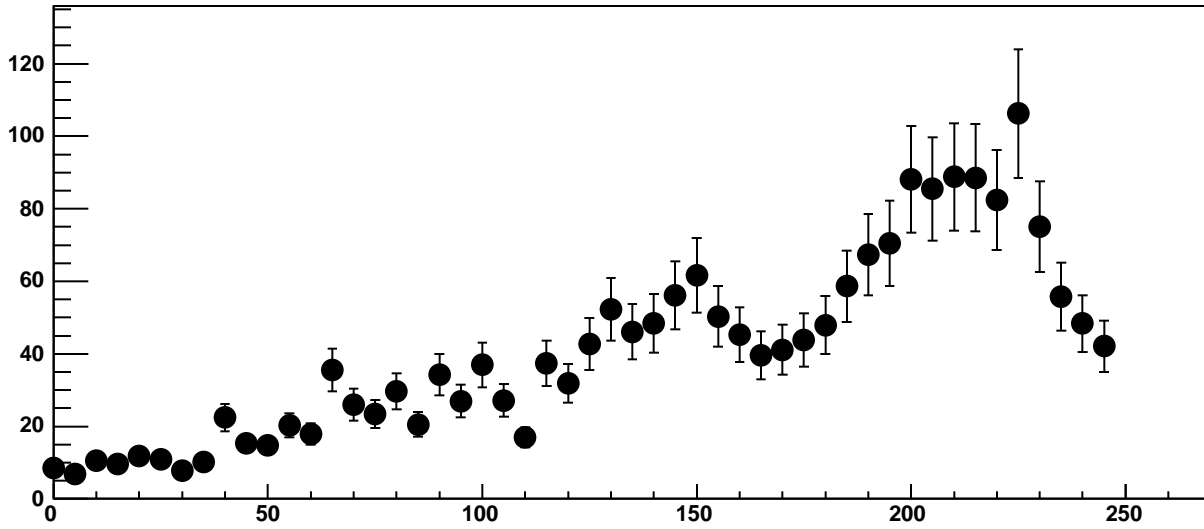
Chip 0, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold



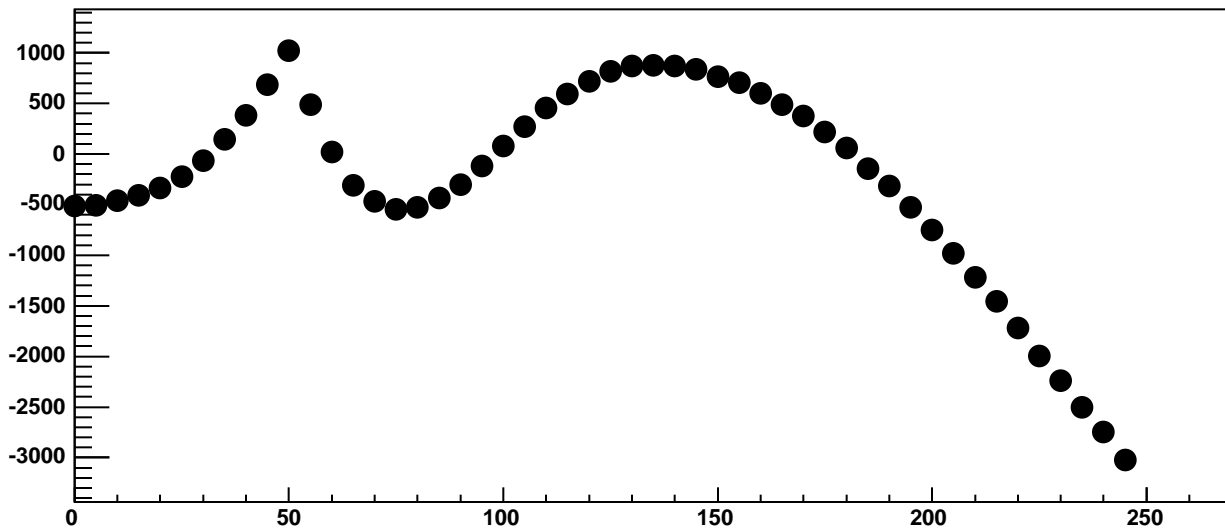
Chip 0, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold



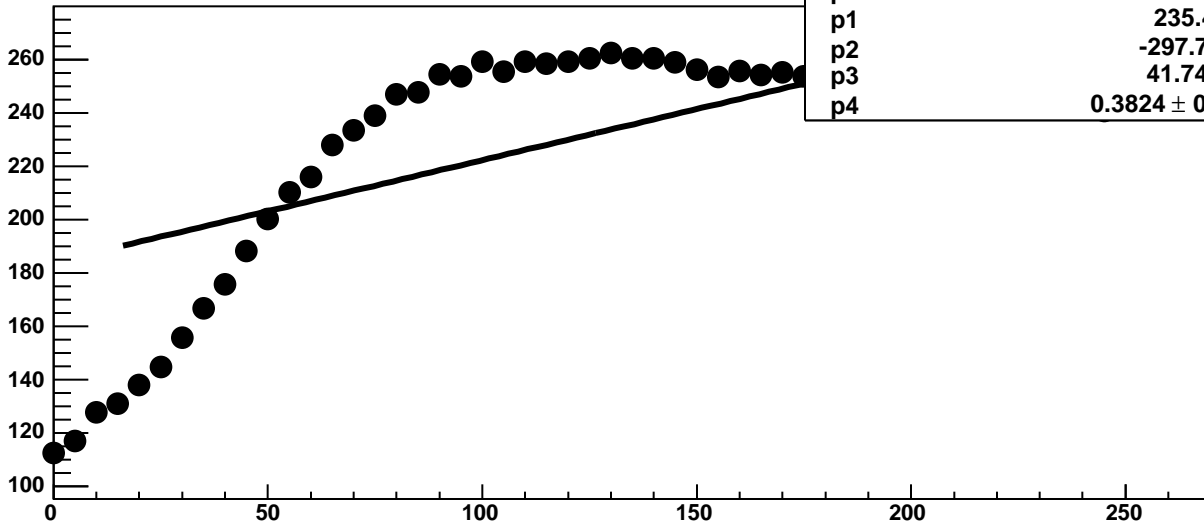
Chip 0, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

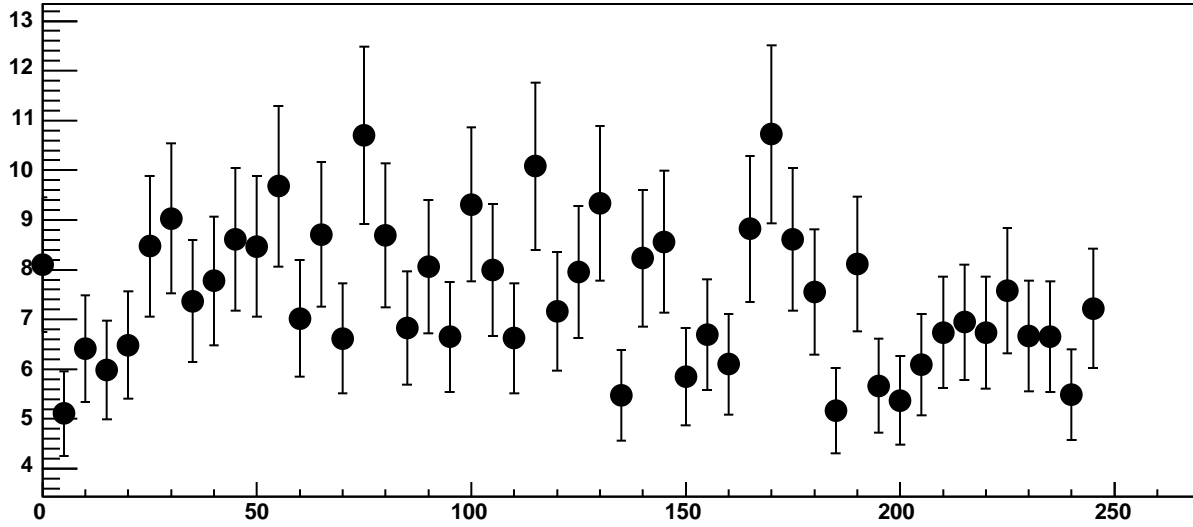


Chip 0, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold

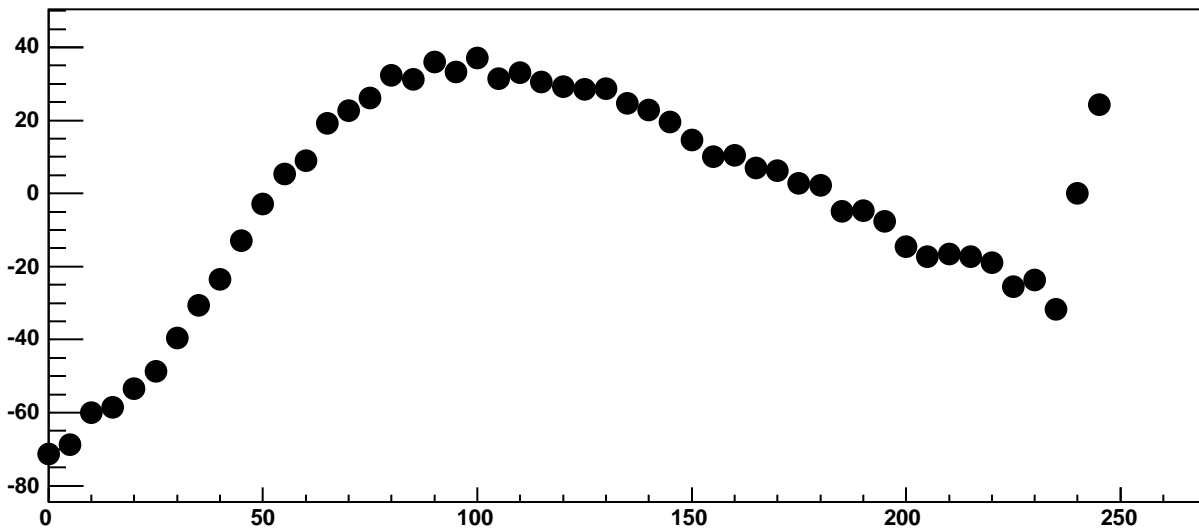


χ^2 / ndf	1.111e+04 / 41
p0	274 ± 1.047
p1	235.4 ± 2.48
p2	-297.7 ± 145.6
p3	41.74 ± 17.85
p4	0.3824 ± 0.003734

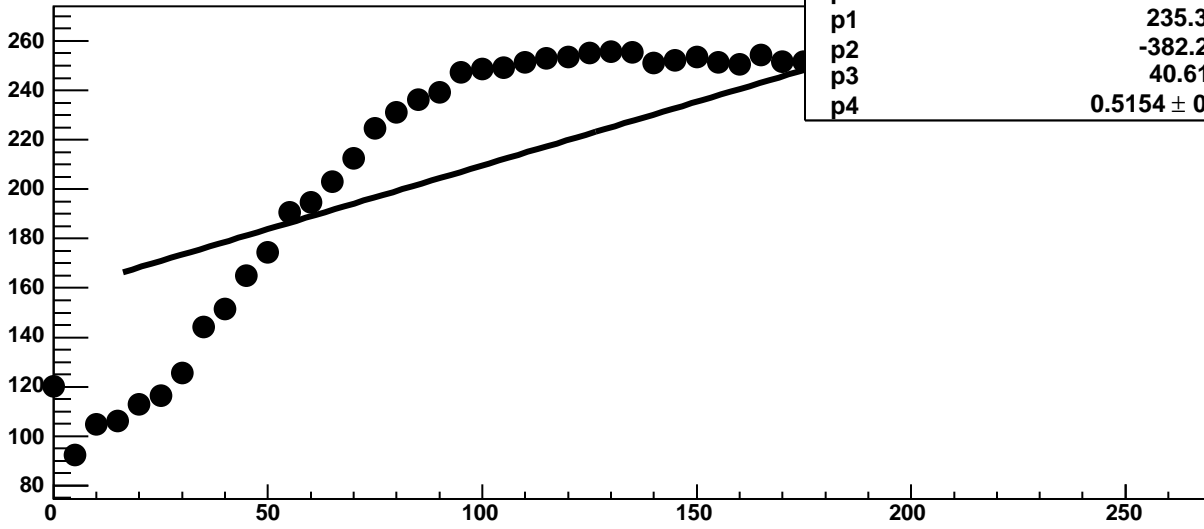
Chip 0, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold

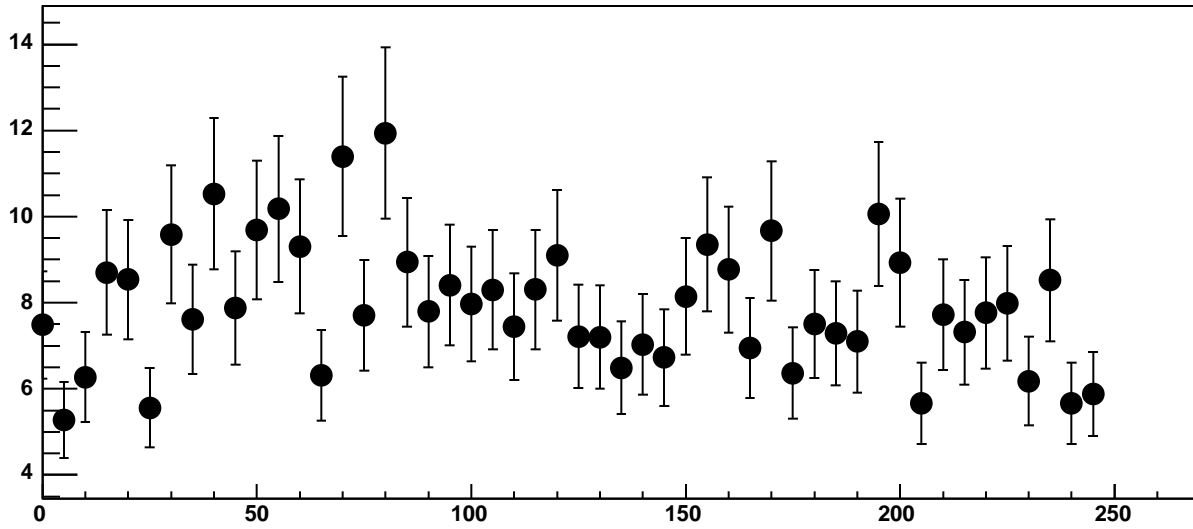


Chip 0, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

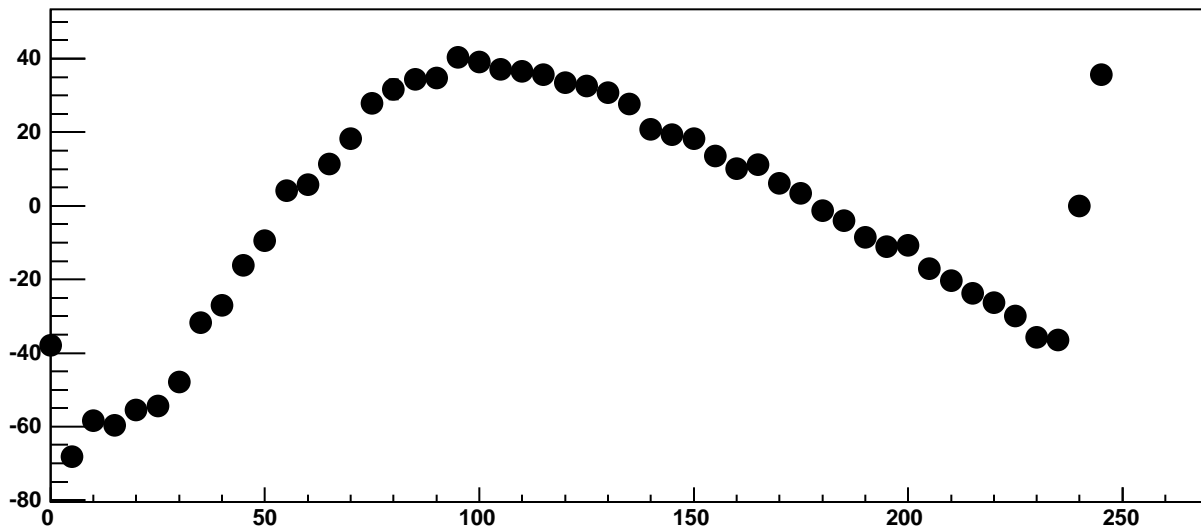


χ^2 / ndf	1.178e+04 / 41
p0	279.3 ± 1.229
p1	235.3 ± 2.188
p2	-382.2 ± 161.7
p3	40.61 ± 15.07
p4	0.5154 ± 0.004085

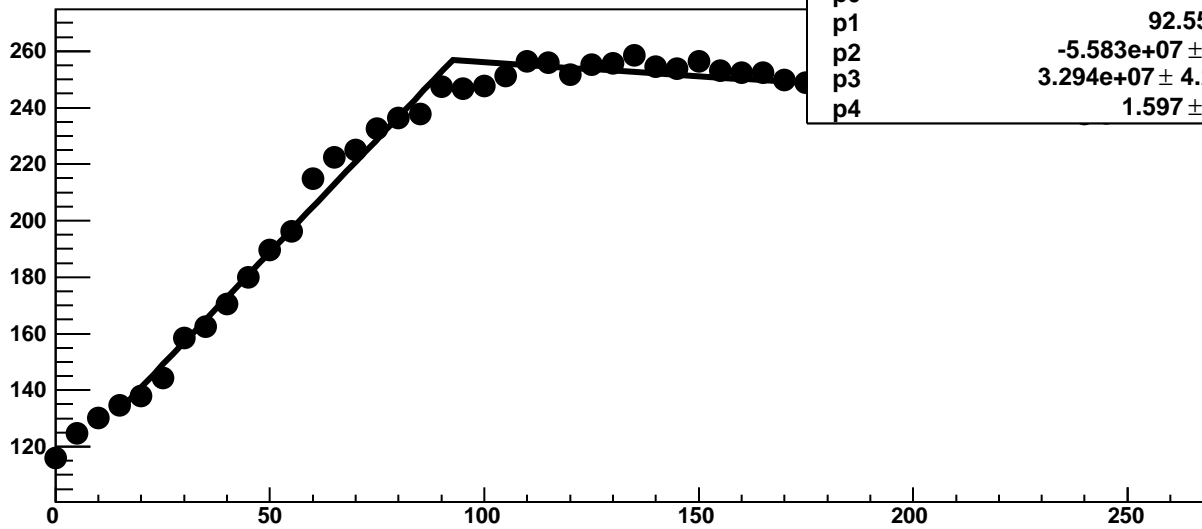
Chip 0, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

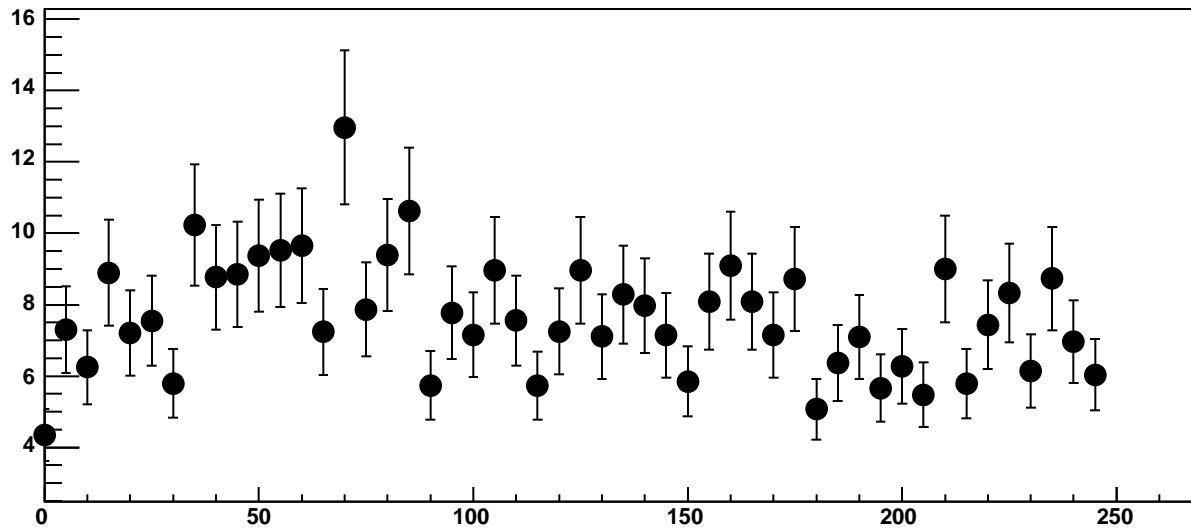


Chip 0, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

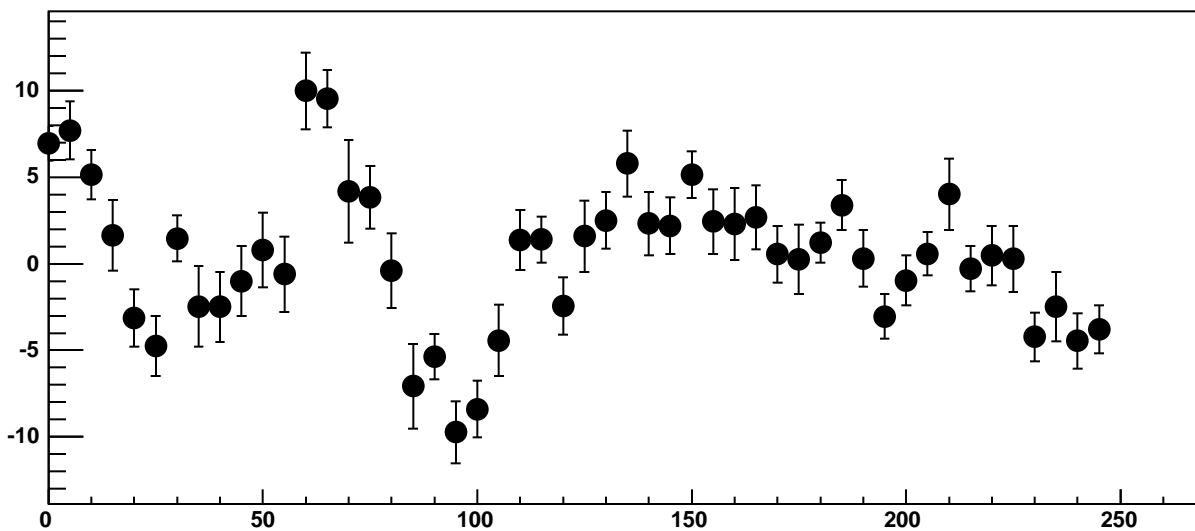


χ^2 / ndf	236.1 / 41
p0	256.8 ± 0.5844
p1	92.55 ± 0.652
p2	$-5.583\text{e}+07 \pm 9.5\text{e}+05$
p3	$3.294\text{e}+07 \pm 4.282\text{e}+05$
p4	1.597 ± 0.01904

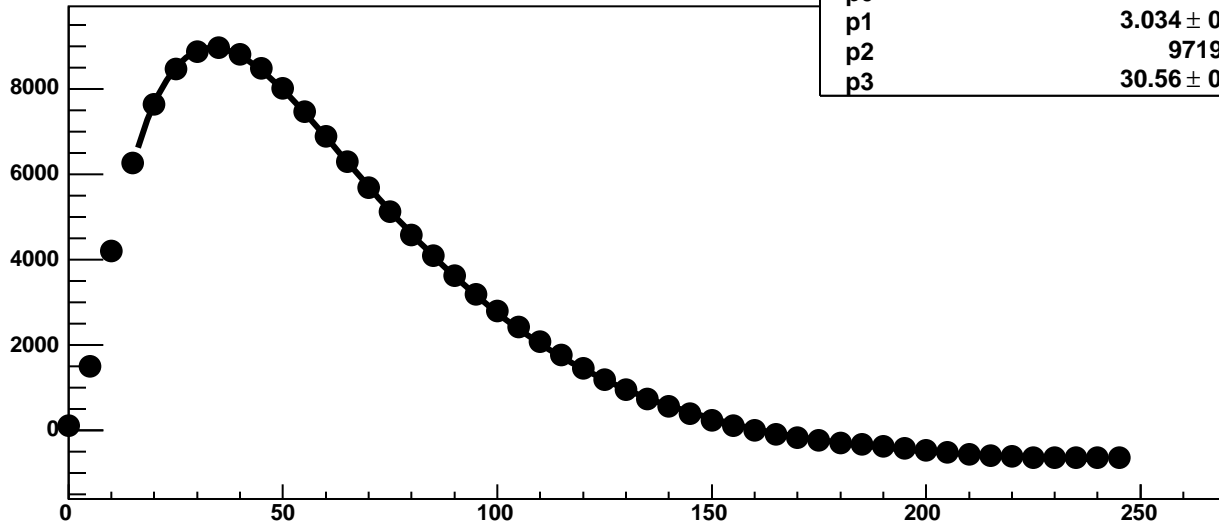
Chip 0, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

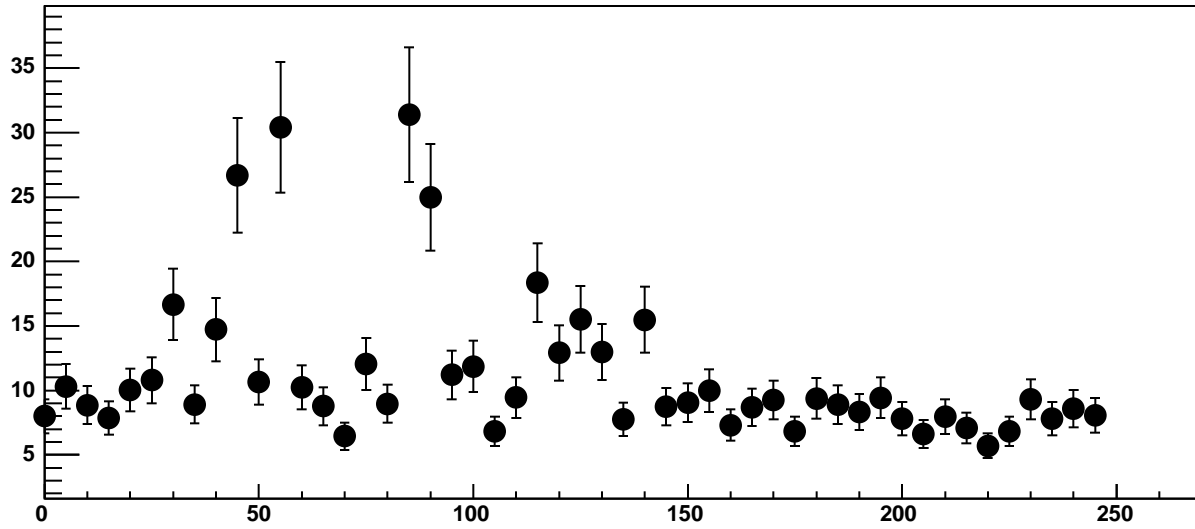


Chip 0, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold

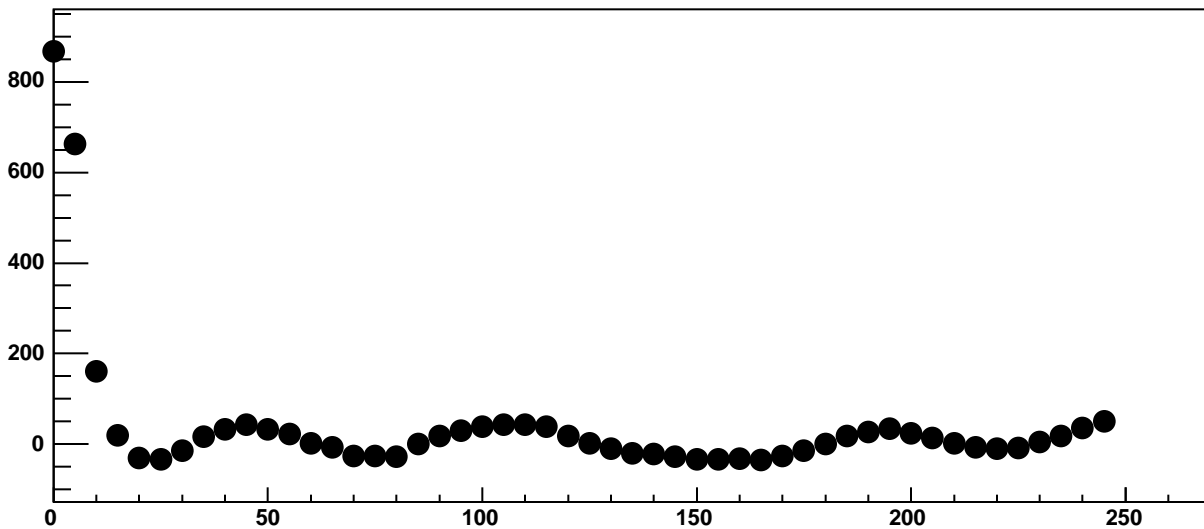


χ^2 / ndf	6204 / 42
p0	-755 ± 0.5572
p1	3.034 ± 0.006985
p2	9719 ± 1.038
p3	30.56 ± 0.005897

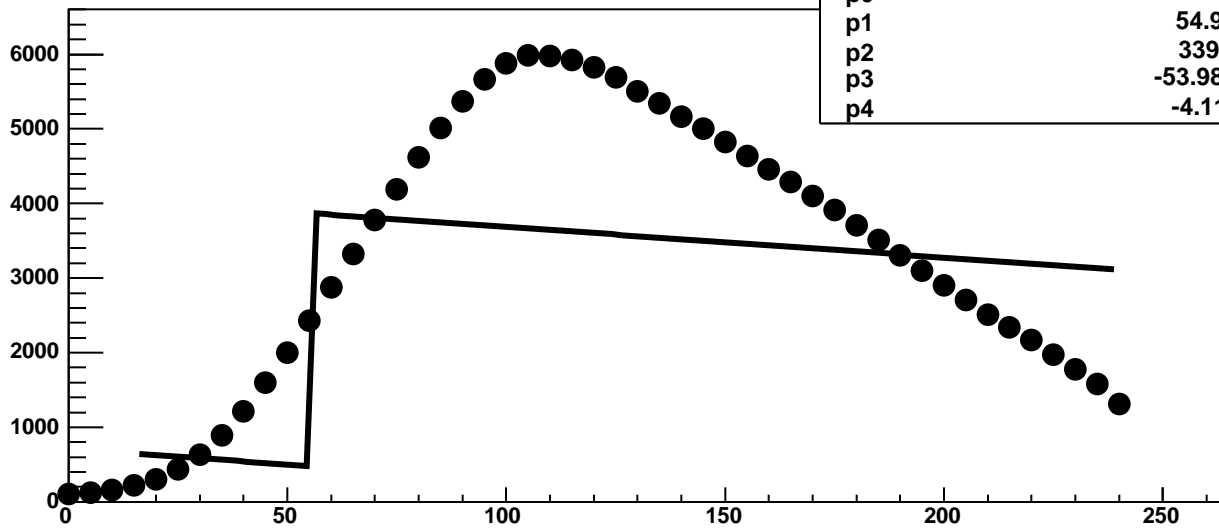
Chip 0, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold

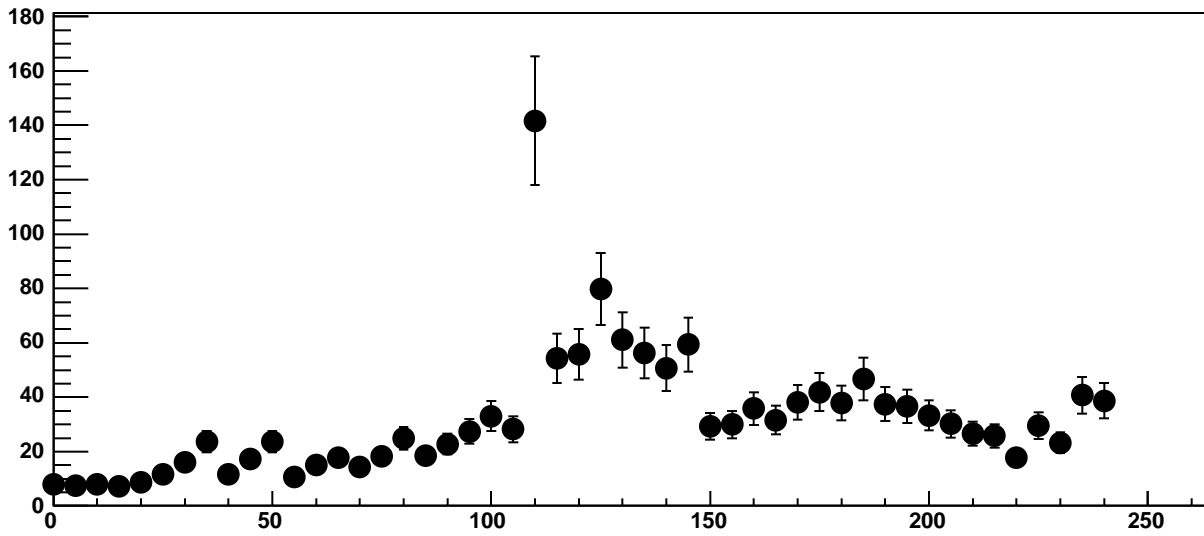


Chip 0, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold

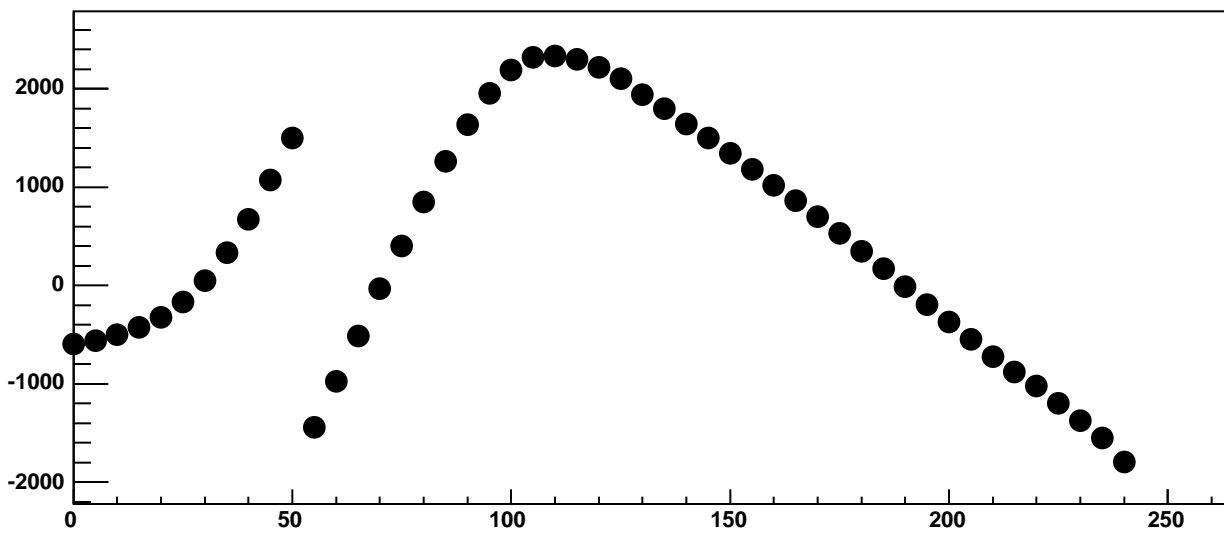


χ^2 / ndf	1.821e+06 / 41
p0	481.1 ± 3.753
p1	54.9 ± 4.266
p2	3391 ± 1.33
p3	-53.98 ± 4.266
p4	-4.11 ± 4.266

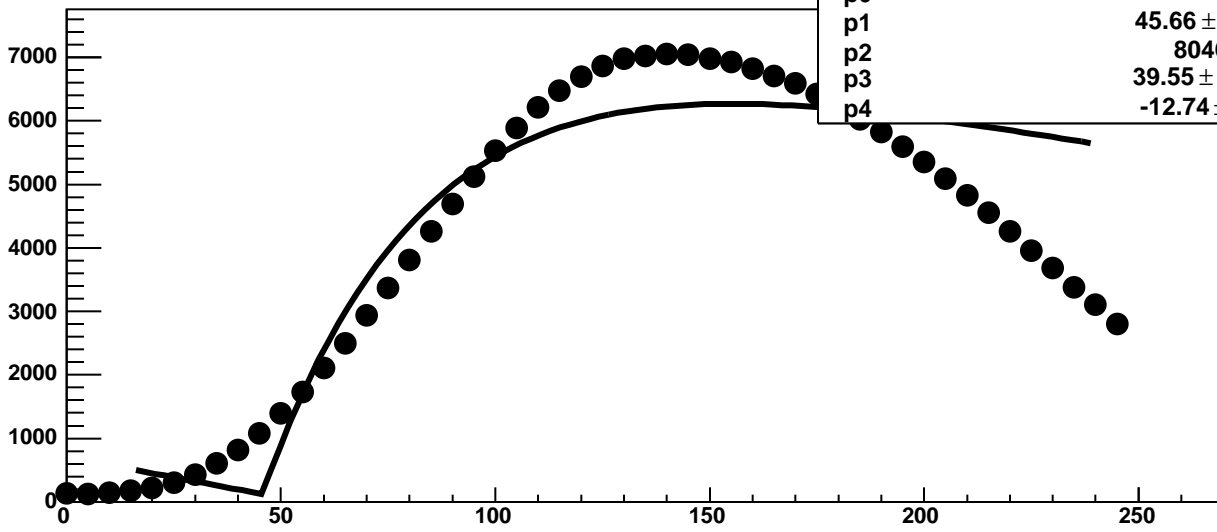
Chip 0, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold

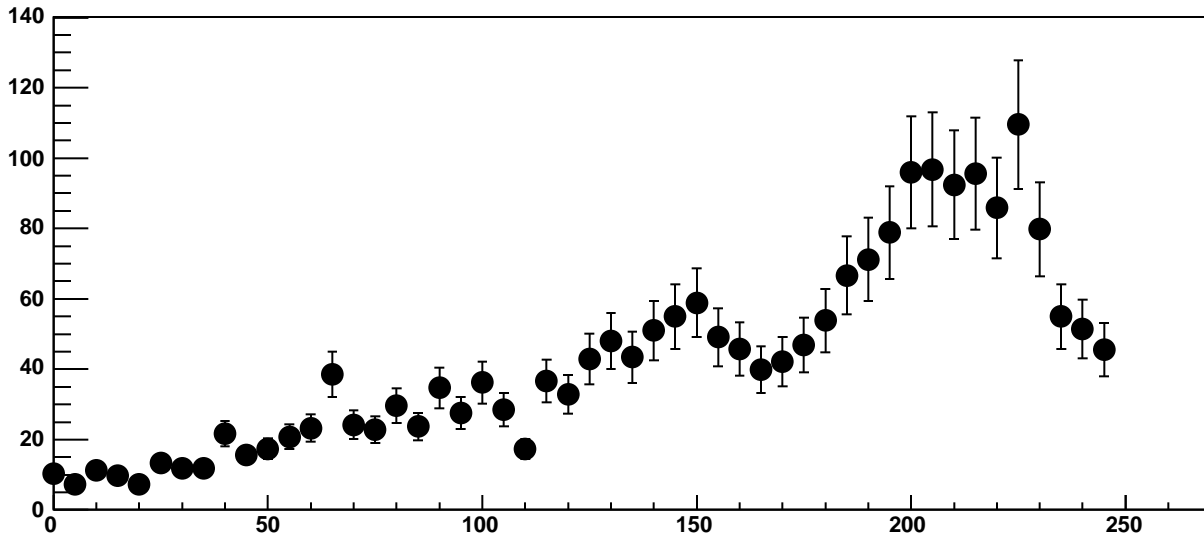


Chip 0, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold

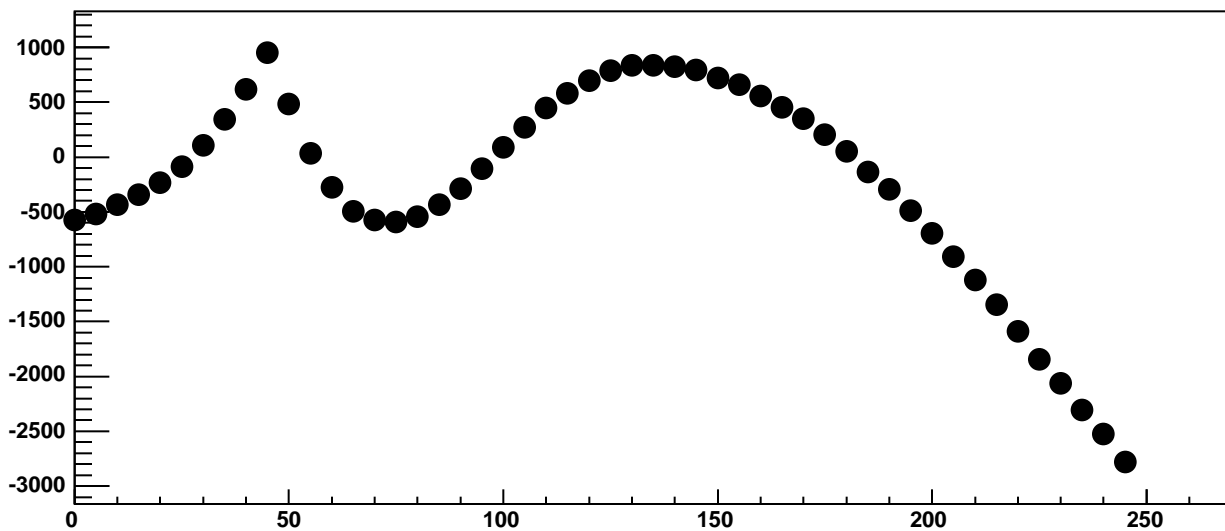


χ^2 / ndf	3.903e+05 / 41
p0	123.4 ± 1.586
p1	45.66 ± 0.02081
p2	8046 ± 11.8
p3	39.55 ± 0.06869
p4	-12.74 ± 0.0672

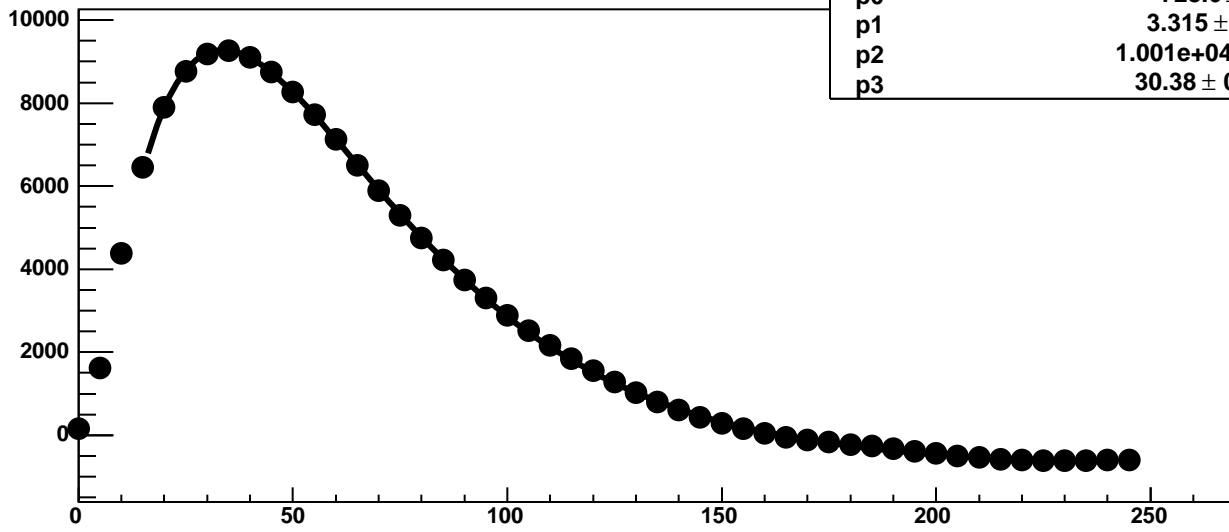
Chip 0, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

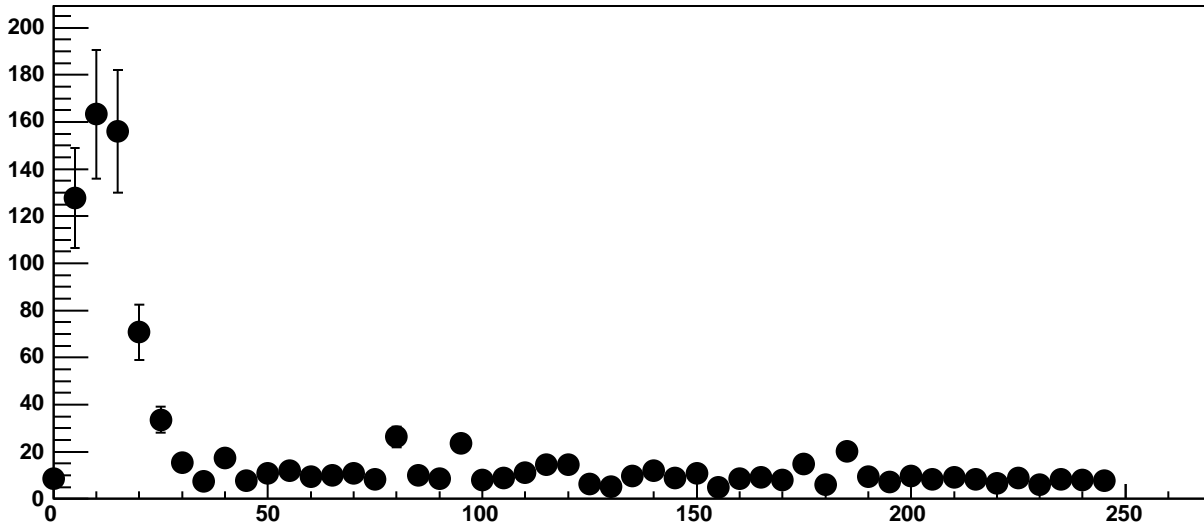


Chip 0, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold

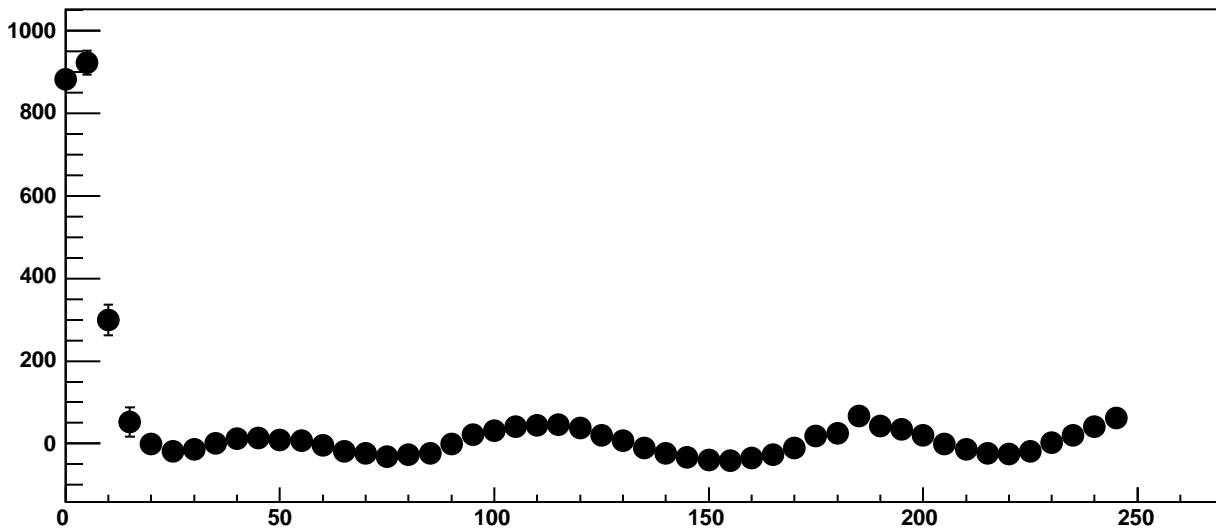


χ^2 / ndf	7225 / 42
p0	-728.6 ± 0.6377
p1	3.315 ± 0.02221
p2	1.001e+04 ± 1.475
p3	30.38 ± 0.008481

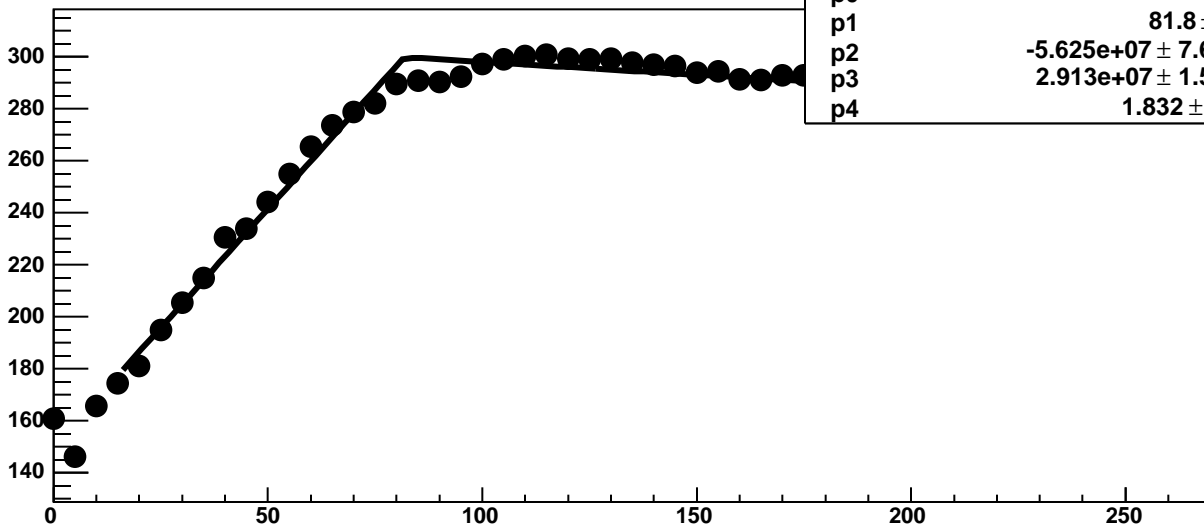
Chip 0, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold

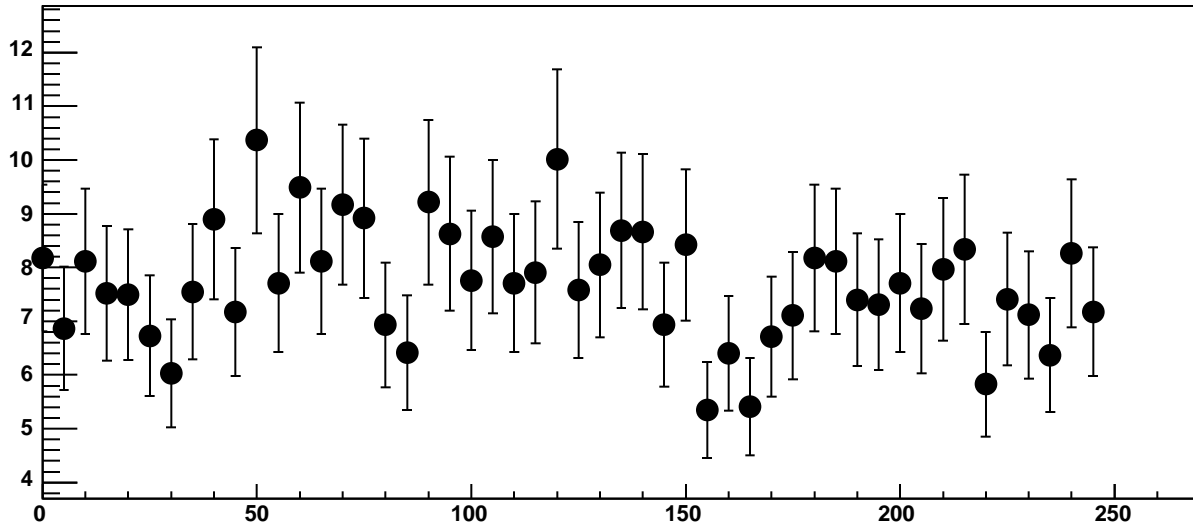


Chip 0, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

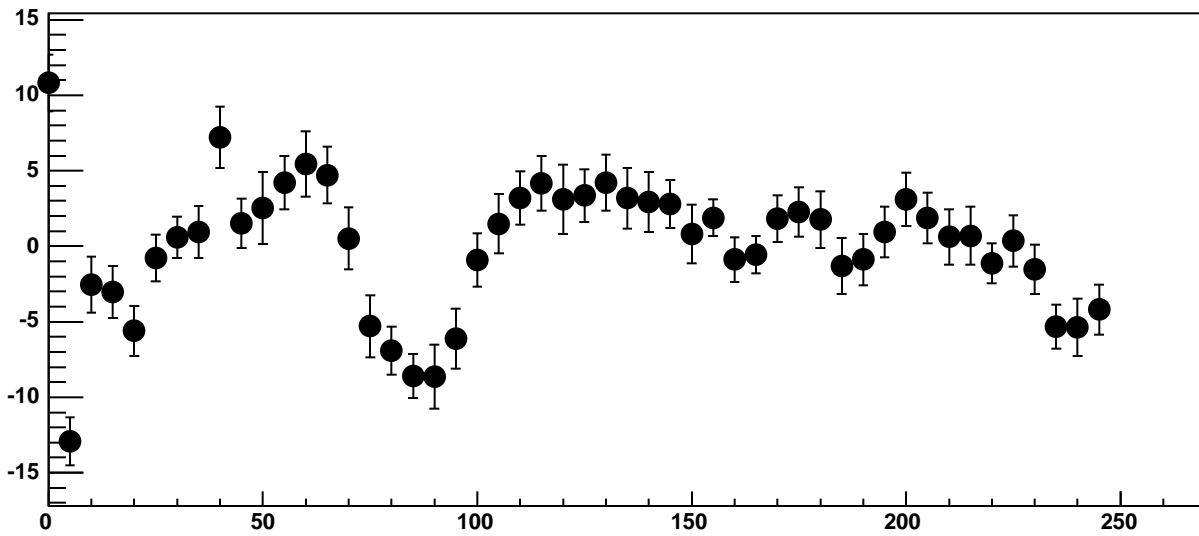


χ^2 / ndf	197.8 / 41
p0	299.8 ± 0.6059
p1	81.8 ± 0.5987
p2	$-5.625\text{e}+07 \pm 7.648\text{e}+05$
p3	$2.913\text{e}+07 \pm 1.596\text{e}+05$
p4	1.832 ± 0.02291

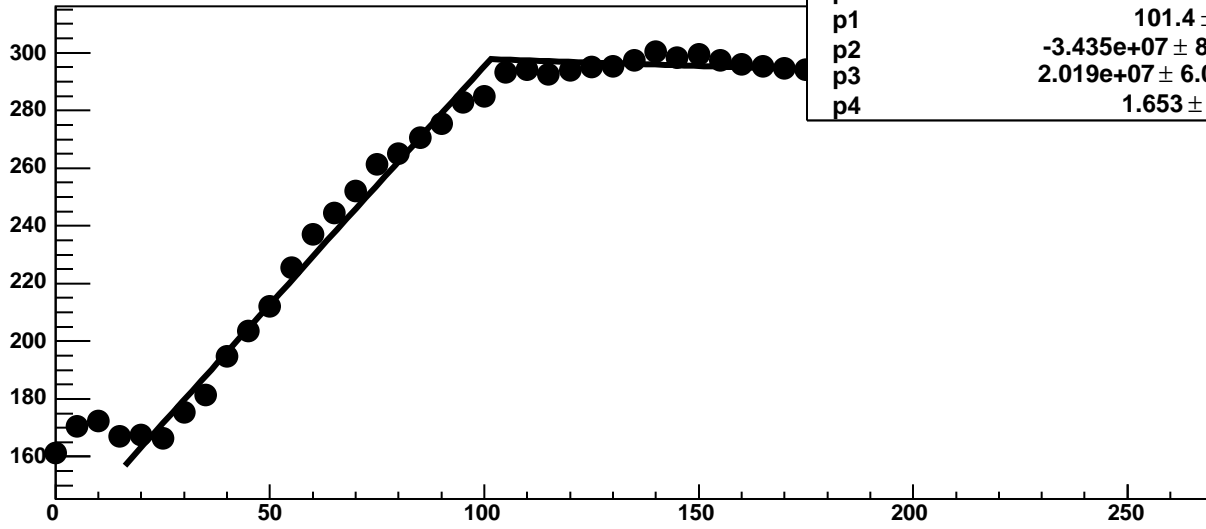
Chip 0, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

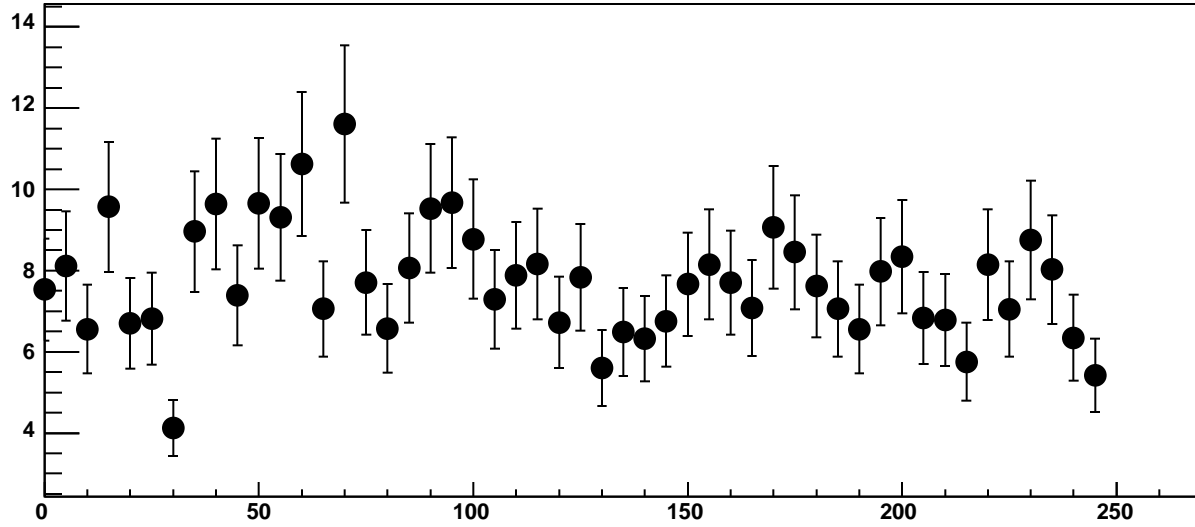


Chip 0, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

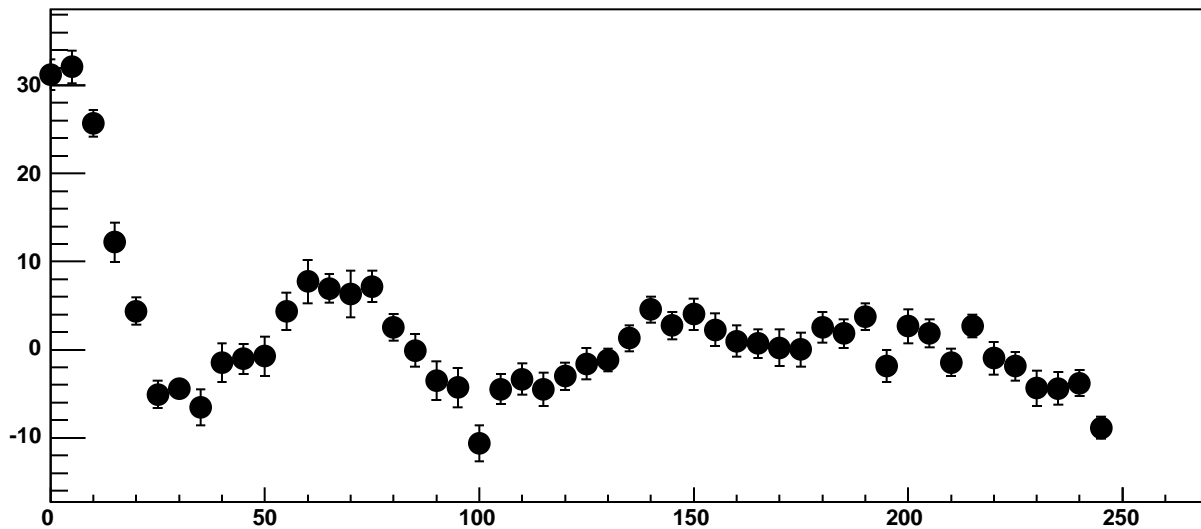


χ^2 / ndf	254.3 / 41
p0	297.8 ± 0.5975
p1	101.4 ± 0.5744
p2	$-3.435\text{e}+07 \pm 8.97\text{e}+05$
p3	$2.019\text{e}+07 \pm 6.028\text{e}+05$
p4	1.653 ± 0.01445

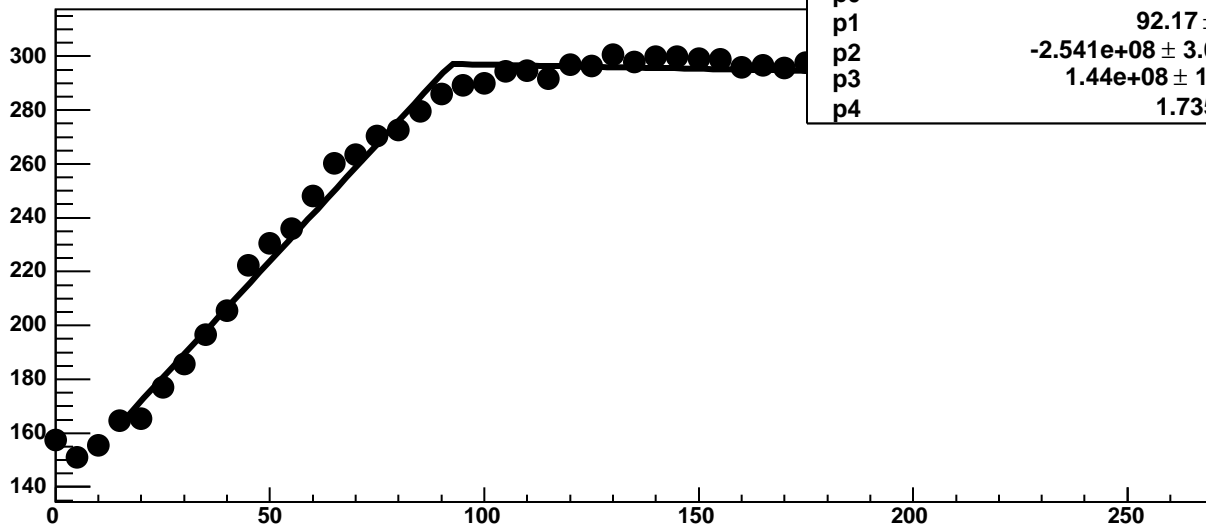
Chip 0, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

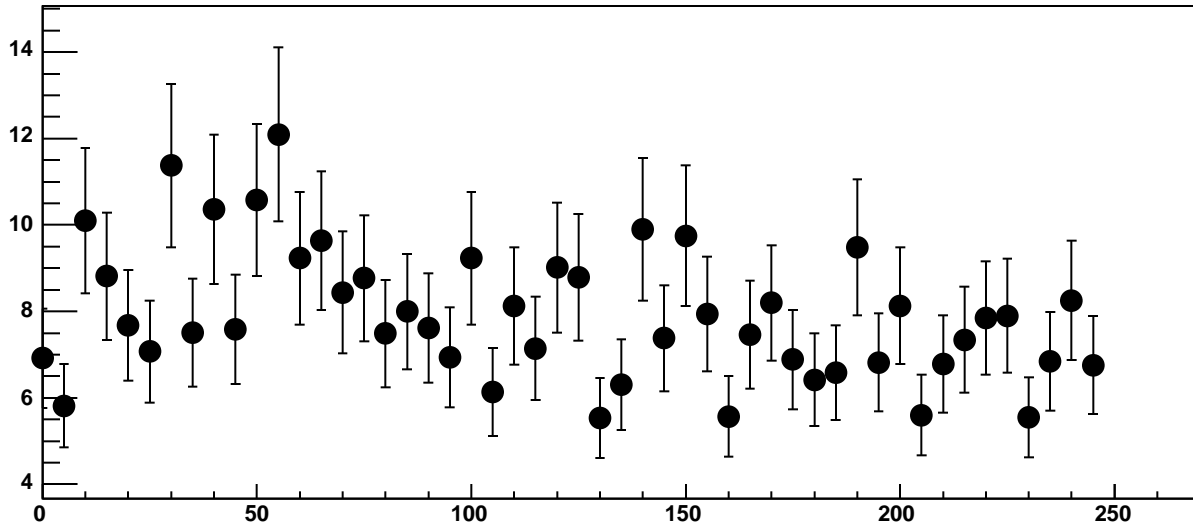


Chip 0, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold

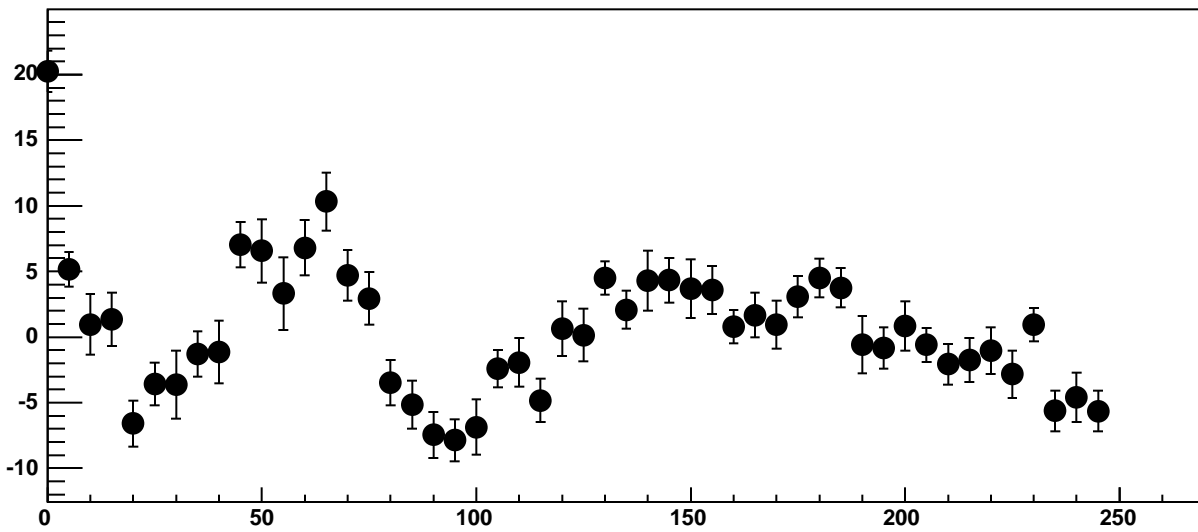


χ^2 / ndf	243.4 / 41
p0	297.1 ± 0.603
p1	92.17 ± 0.6262
p2	$-2.541\text{e}+08 \pm 3.056\text{e}+06$
p3	$1.44\text{e}+08 \pm 1.39\text{e}+05$
p4	1.735 ± 0.02

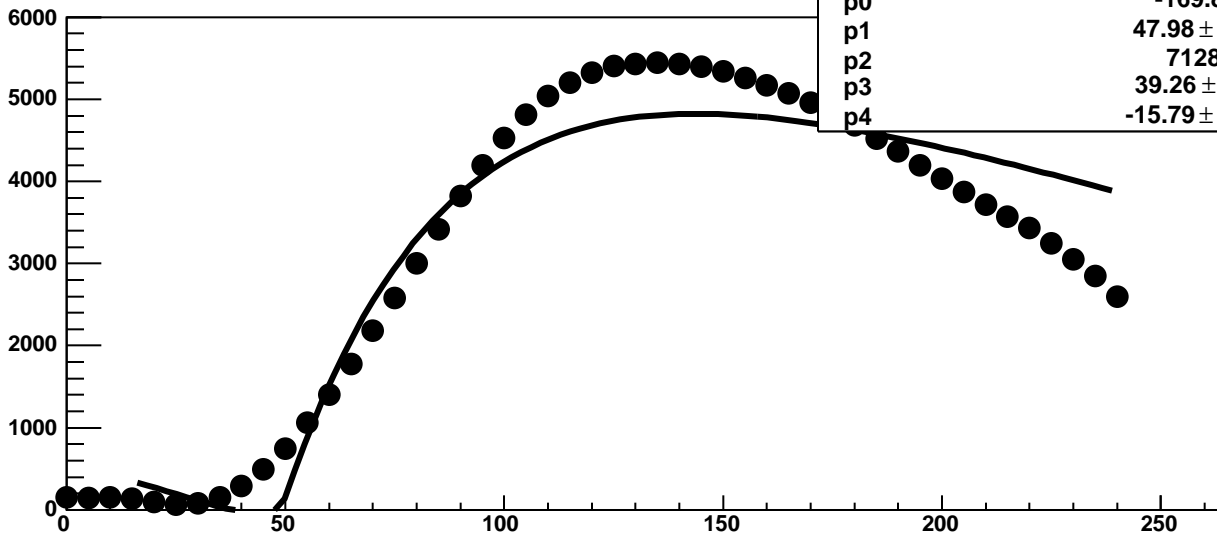
Chip 0, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold

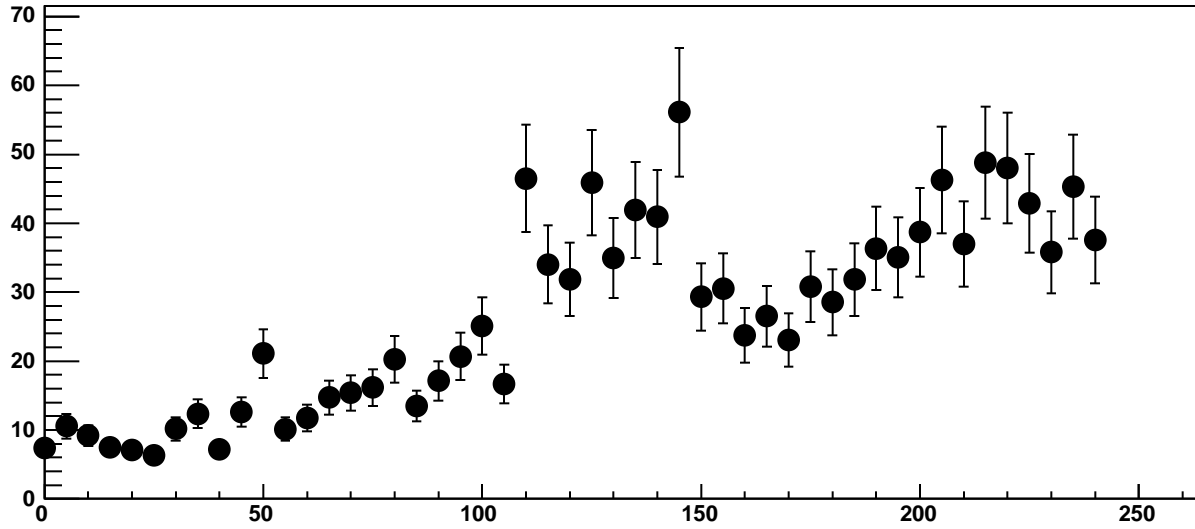


Chip 0, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

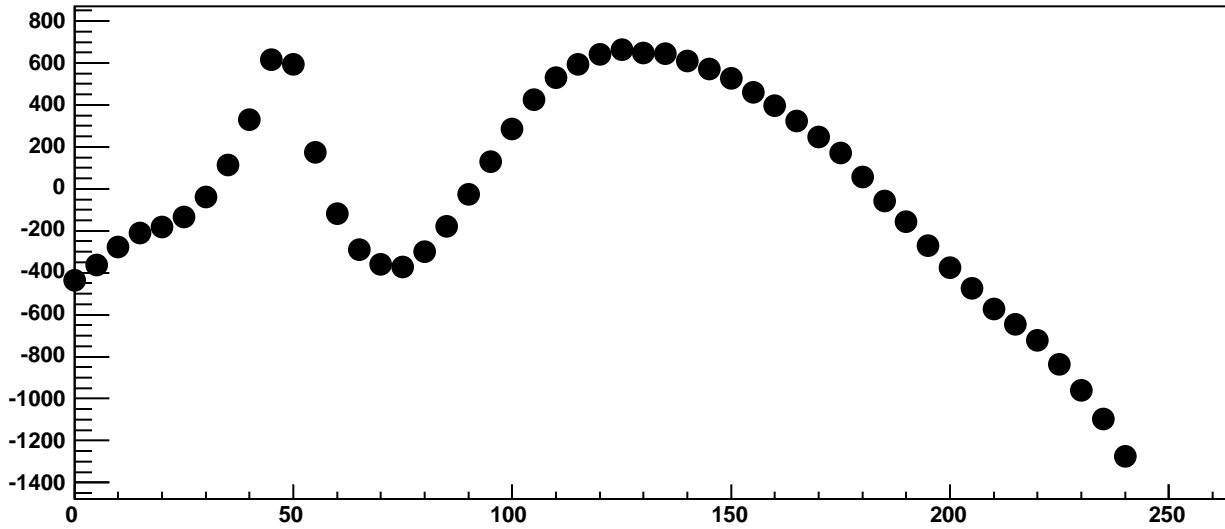


χ^2 / ndf	3.253e+05 / 41
p0	-169.8 ± 1.121
p1	47.98 ± 0.01582
p2	7128 ± 8.804
p3	39.26 ± 0.05631
p4	-15.79 ± 0.04859

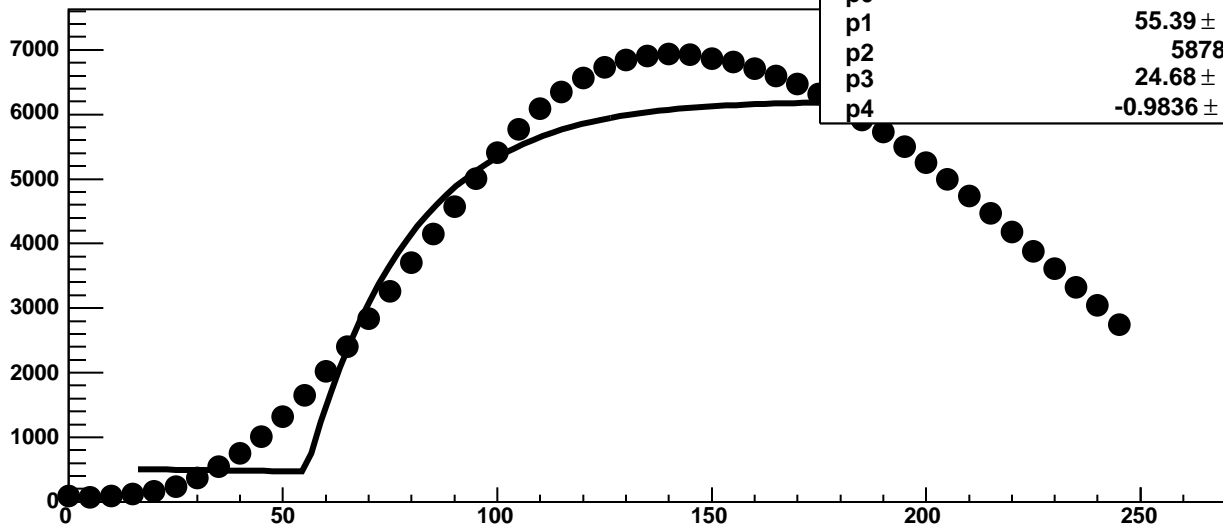
Chip 0, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



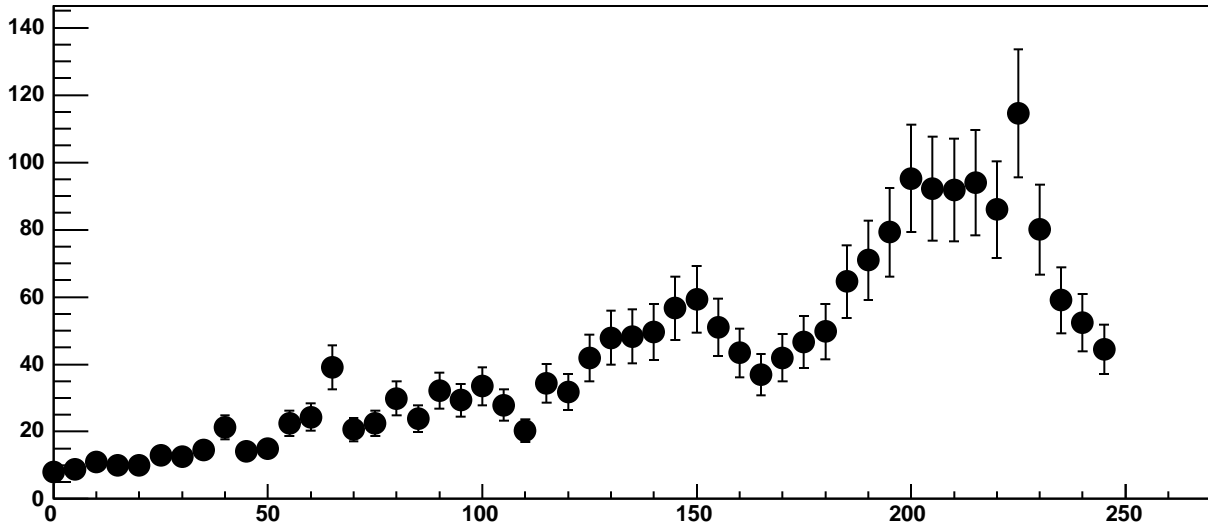
Chip 0, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold



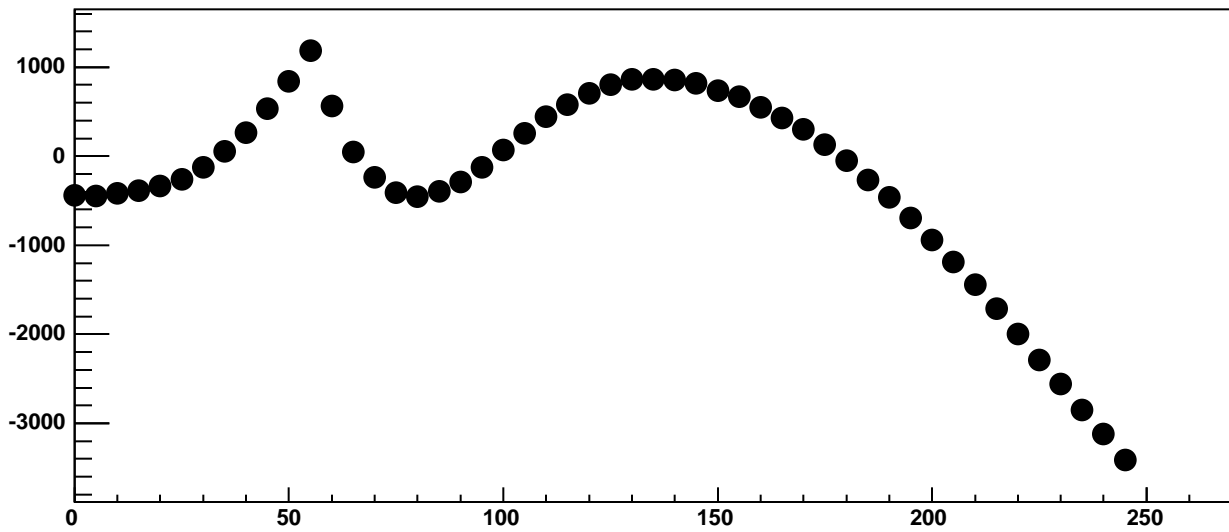
Chip 0, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold



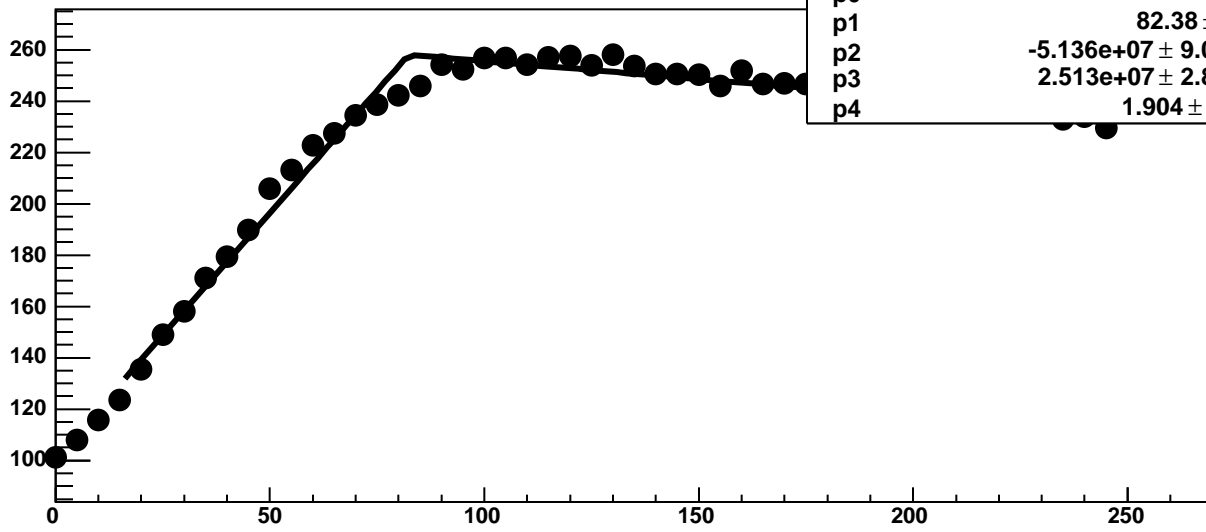
Chip 0, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold

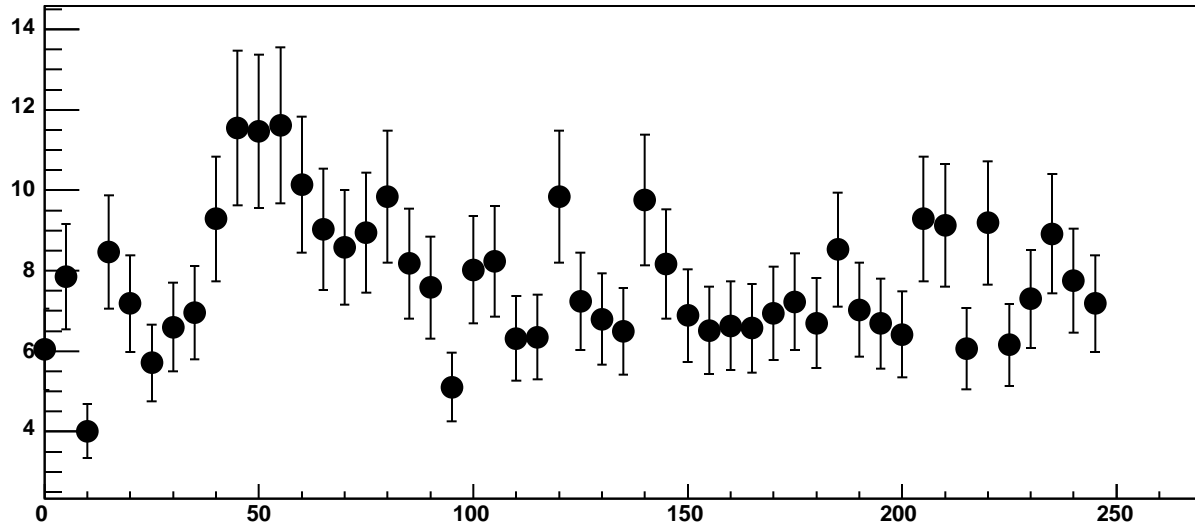


Chip 0, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold

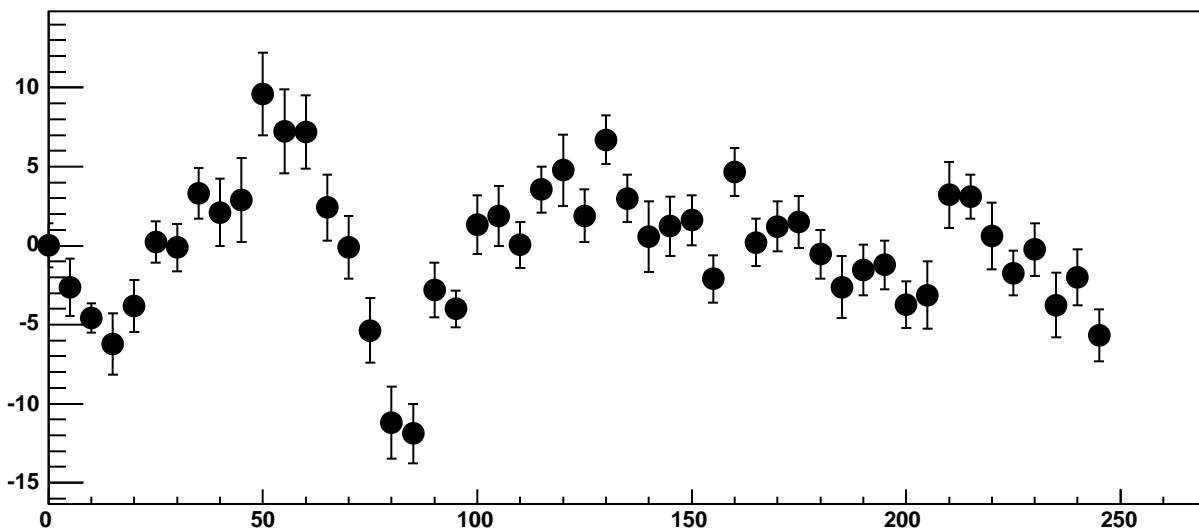


χ^2 / ndf	215.2 / 41
p0	258 ± 0.5445
p1	82.38 ± 0.6229
p2	$-5.136\text{e}+07 \pm 9.027\text{e}+05$
p3	$2.513\text{e}+07 \pm 2.865\text{e}+05$
p4	1.904 ± 0.02517

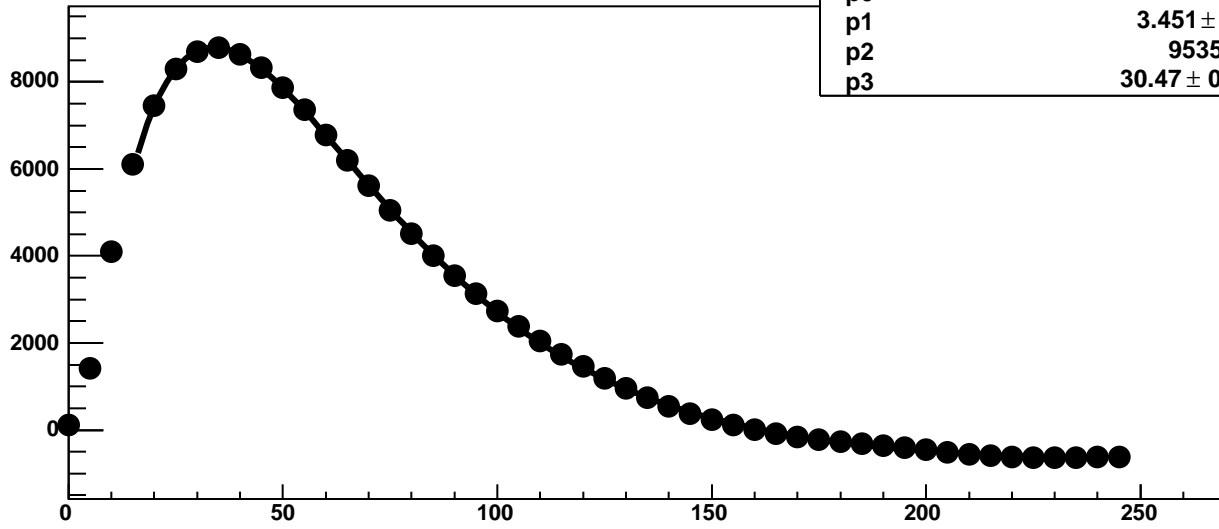
Chip 0, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold

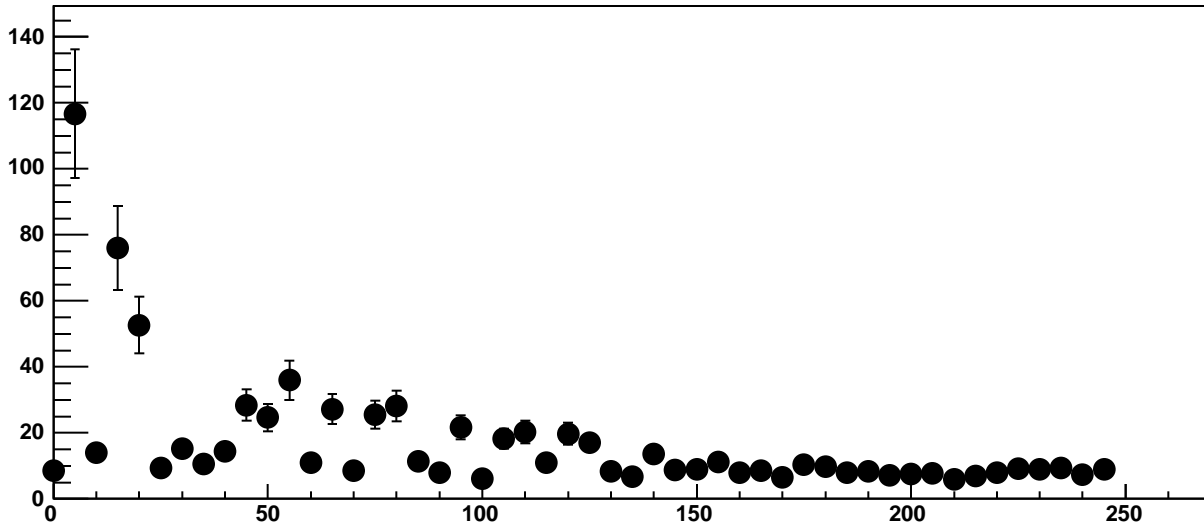


Chip 0, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold

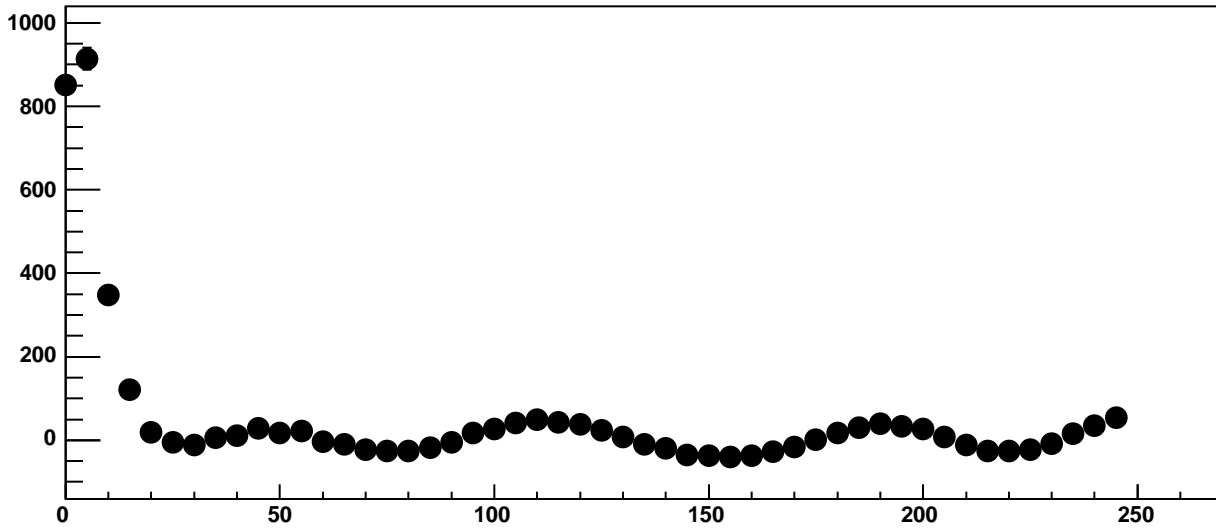


χ^2 / ndf	5571 / 42
p0	-742.1 ± 0.6243
p1	3.451 ± 0.01829
p2	9535 ± 1.232
p3	30.47 ± 0.008915

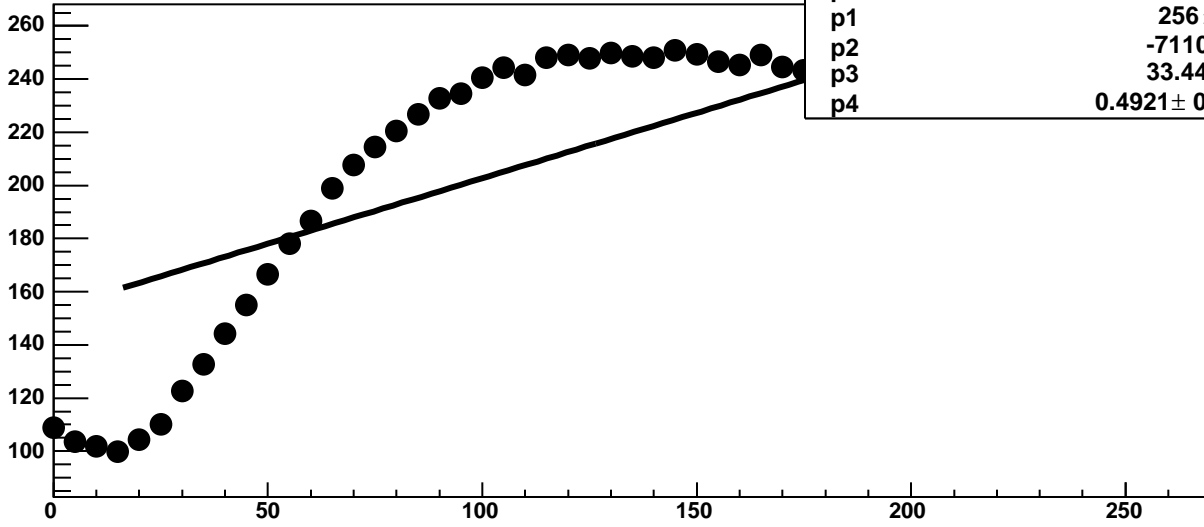
Chip 0, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold

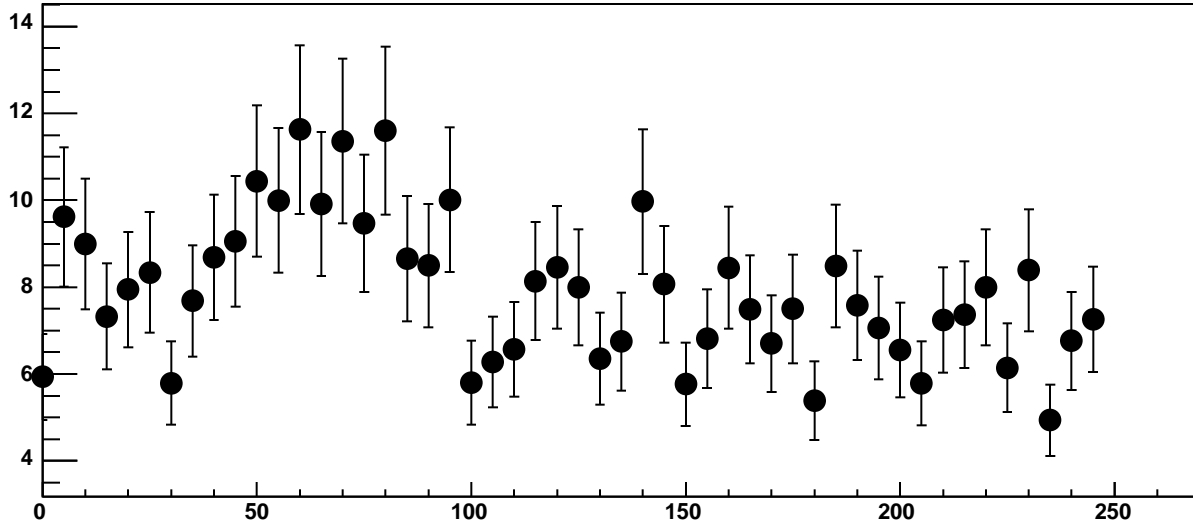


Chip 0, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

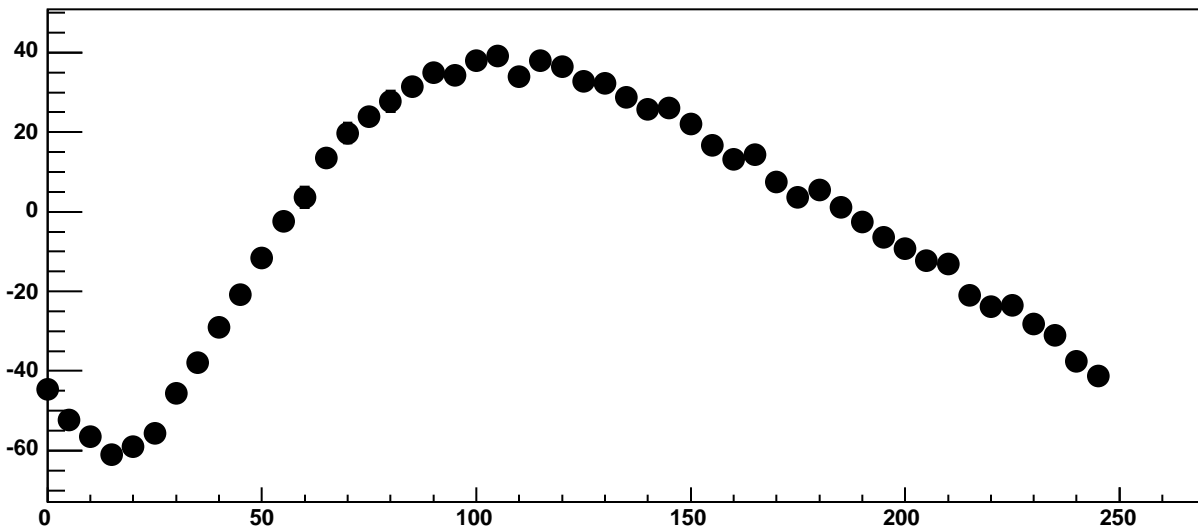


χ^2 / ndf	1.352e+04 / 41
p0	279.5 ± 0.251
p1	256 ± 0.5101
p2	-7110 ± 84.85
p3	33.44 ± 1.414
p4	0.4921 ± 0.001868

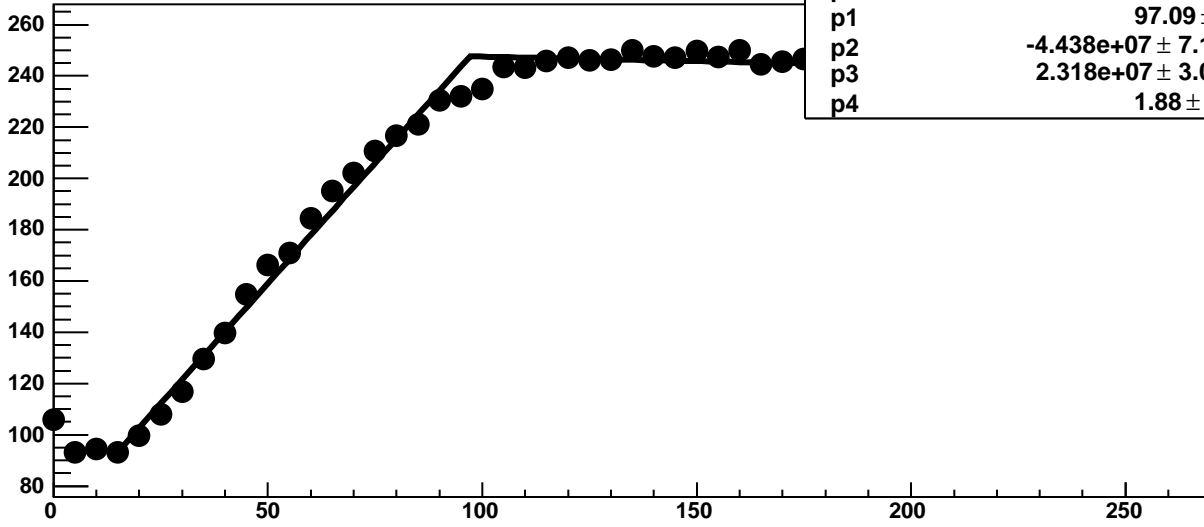
Chip 0, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

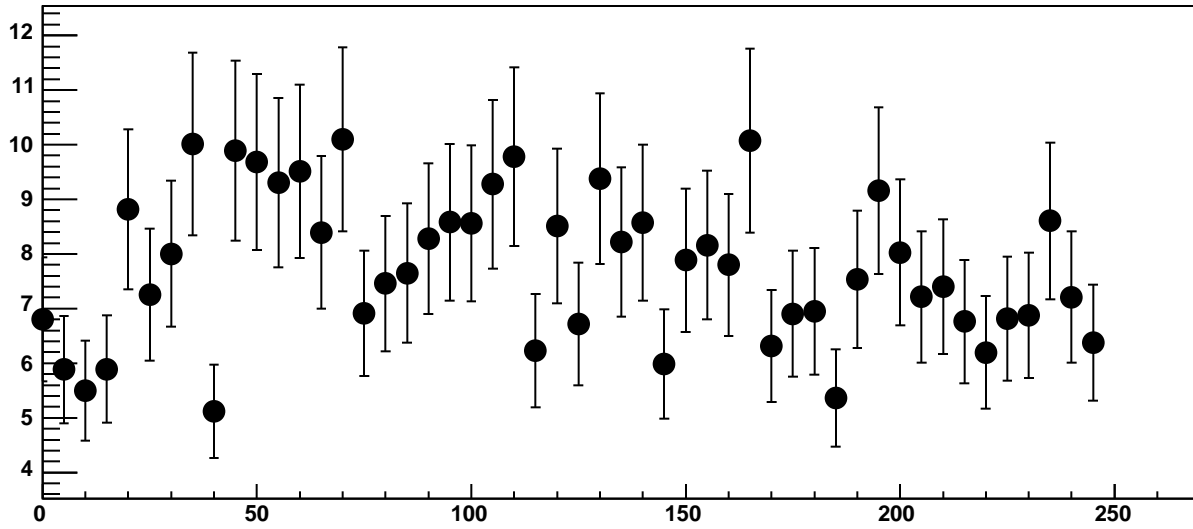


Chip 0, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

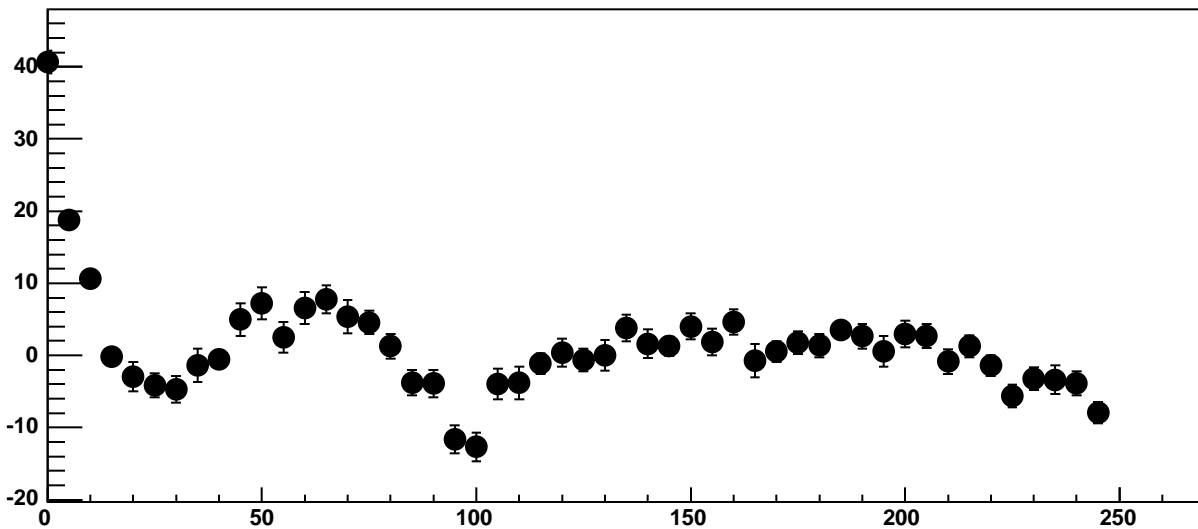


χ^2 / ndf	228.4 / 41
p0	247.6 ± 0.6616
p1	97.09 ± 0.5787
p2	$-4.438e+07 \pm 7.157e+05$
p3	$2.318e+07 \pm 3.042e+05$
p4	1.88 ± 0.01713

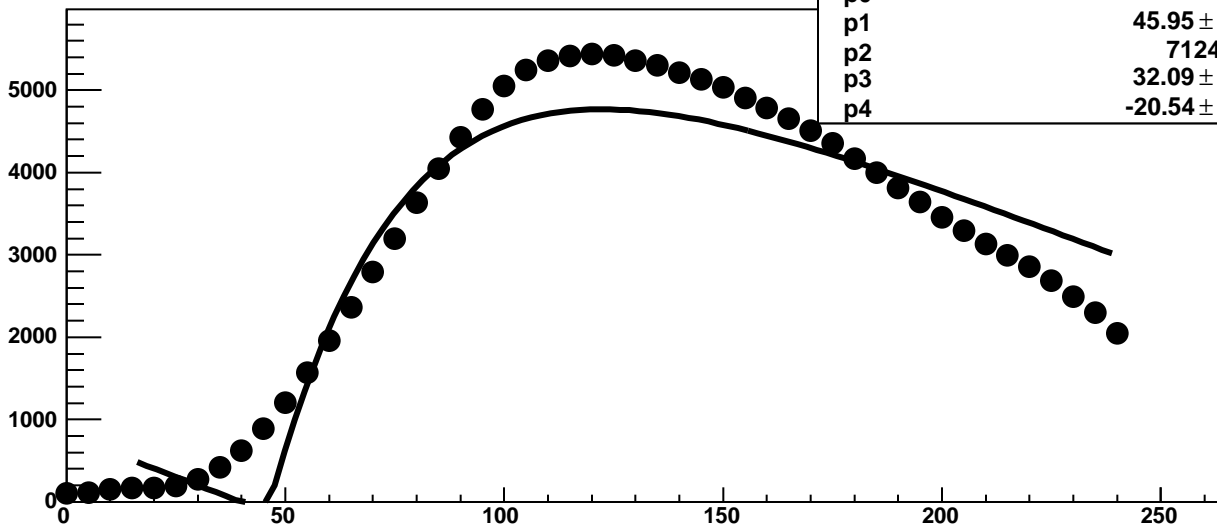
Chip 0, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

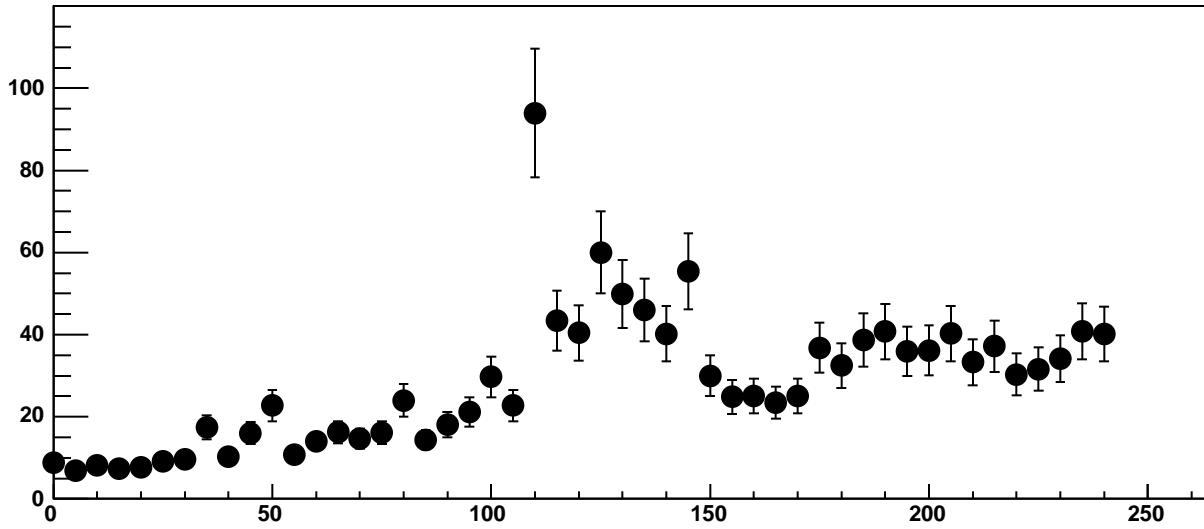


Chip 0, Channel 6, Enable 5, DAC=4000, ADC Mean vs Hold

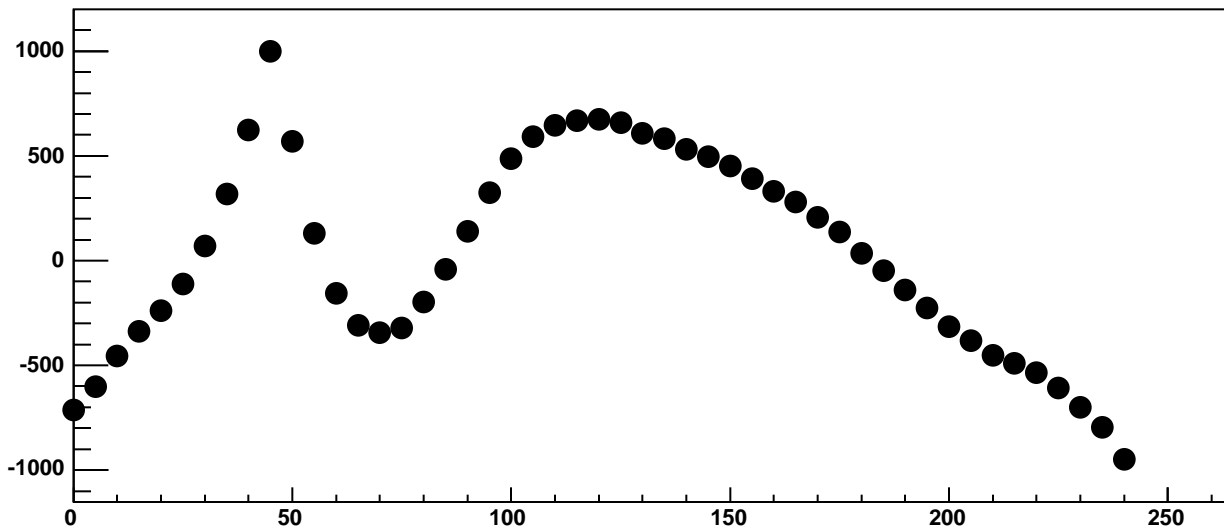


χ^2 / ndf	3.697e+05 / 41
p0	-127.1 ± 1.157
p1	45.95 ± 0.01744
p2	7124 ± 8.046
p3	32.09 ± 0.04934
p4	-20.54 ± 0.04602

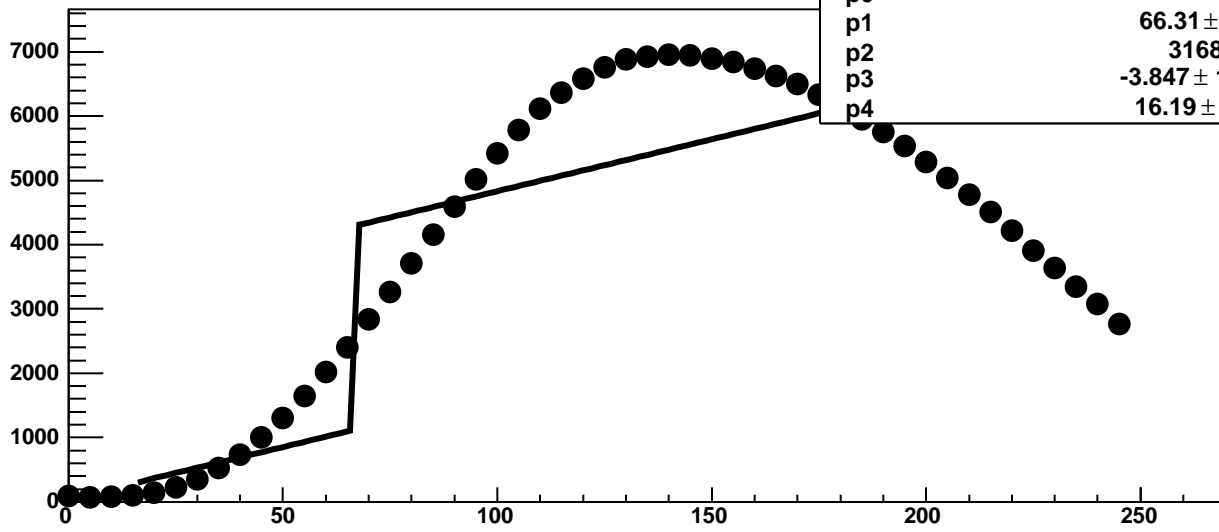
Chip 0, Channel 6, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 6, Enable 5, DAC=4000, ADC Residuals vs Hold

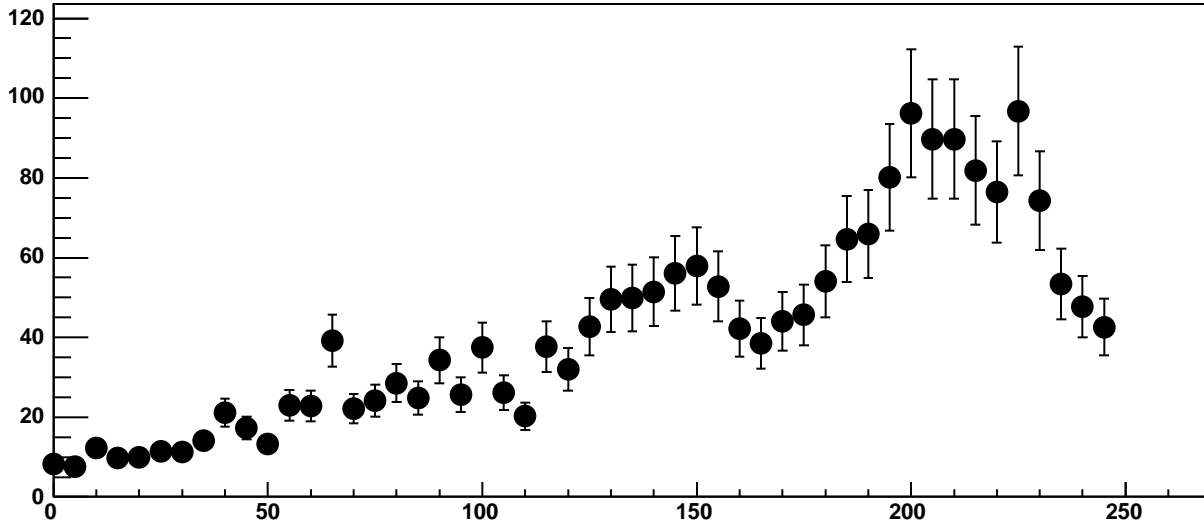


Chip 0, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

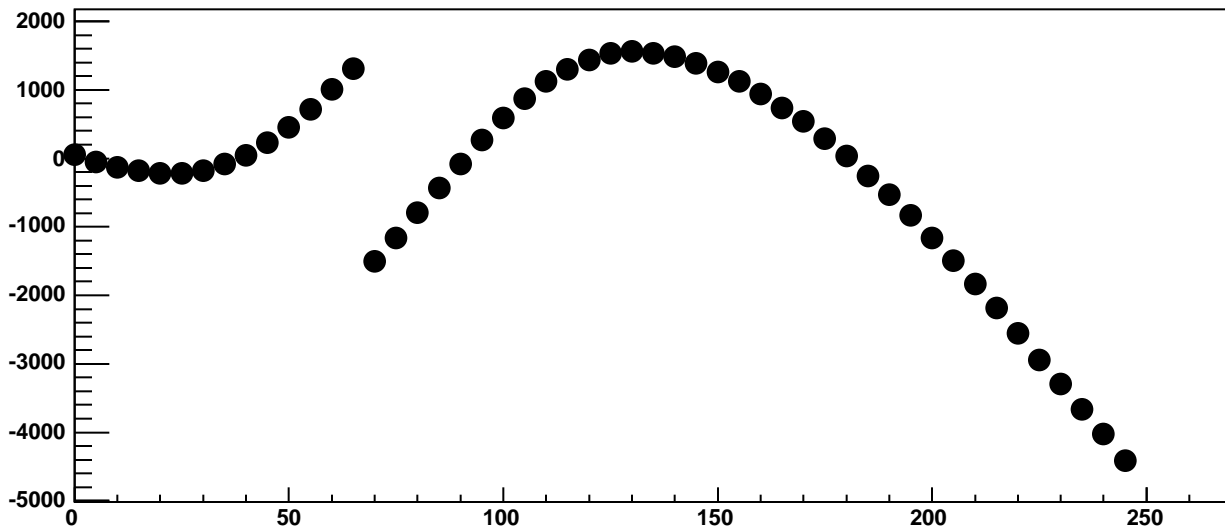


χ^2 / ndf	8.957e+05 / 41
p0	1116 ± 1.103
p1	66.31 ± 0.04621
p2	3168 ± 3.185
p3	-3.847 ± 1.22e-05
p4	16.19 ± 0.02919

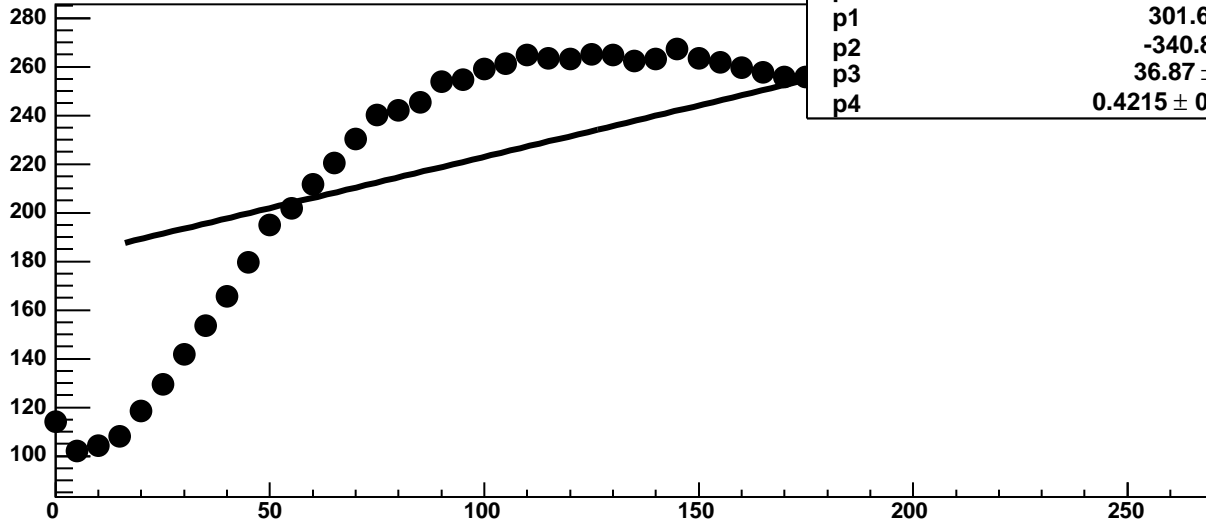
Chip 0, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

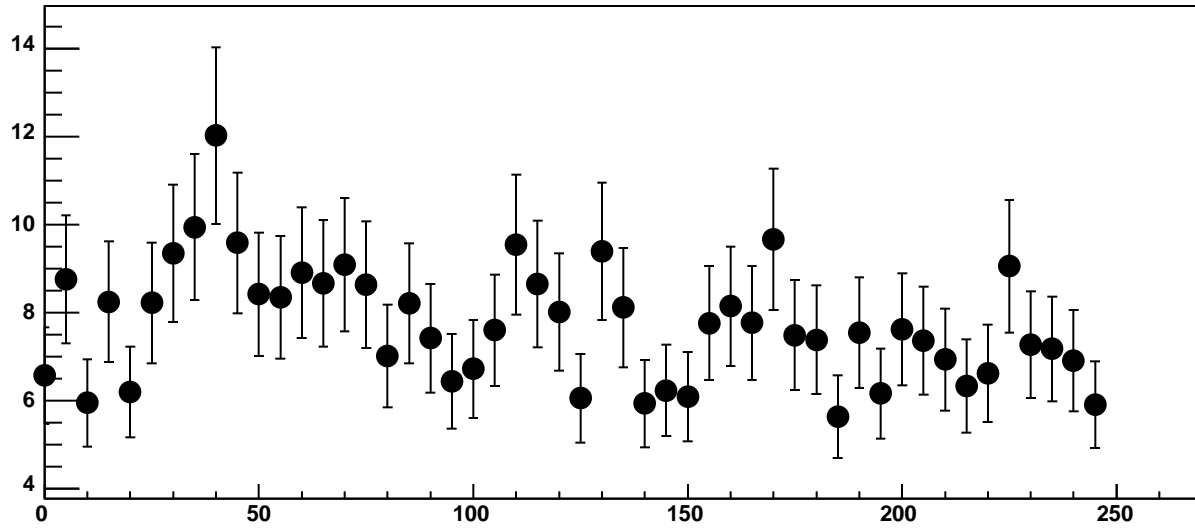


Chip 0, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold

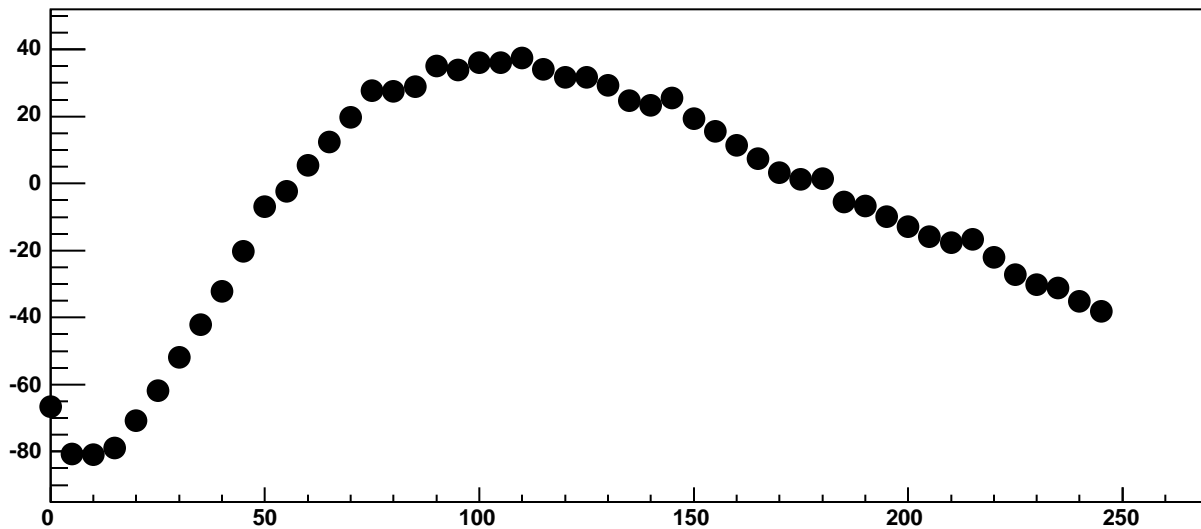


χ^2 / ndf	1.398e+04 / 41
p0	308 ± 0.3352
p1	301.6 ± 1.067
p2	-340.8 ± 9.841
p3	36.87 ± 0.4495
p4	0.4215 ± 0.003913

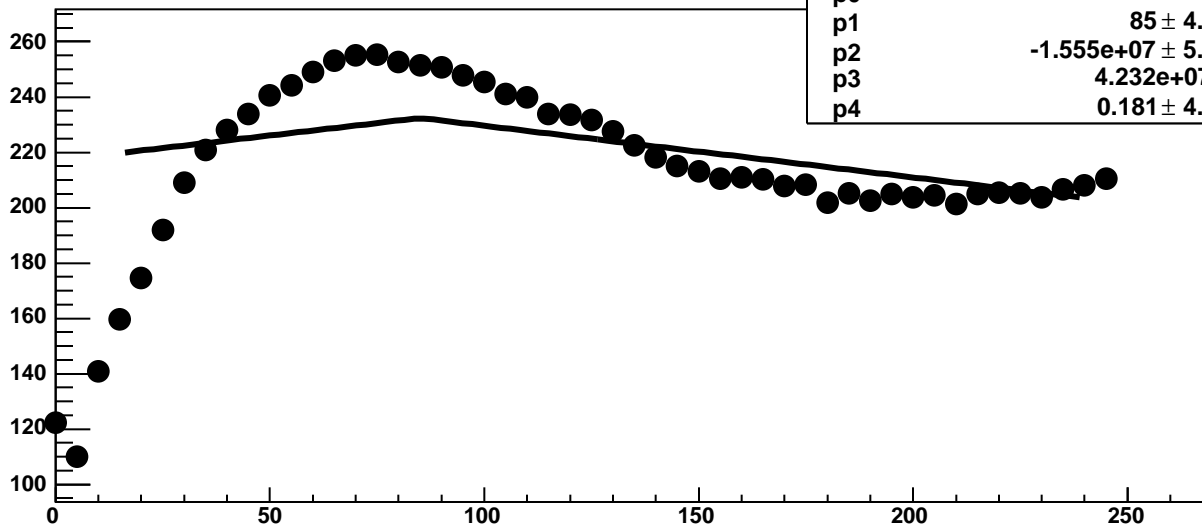
Chip 0, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

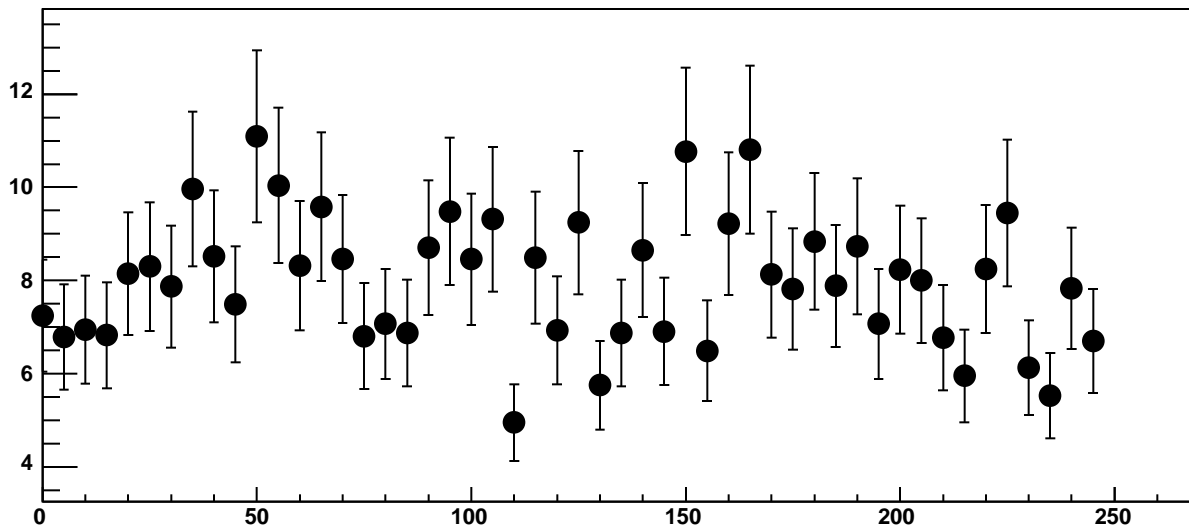


Chip 0, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold

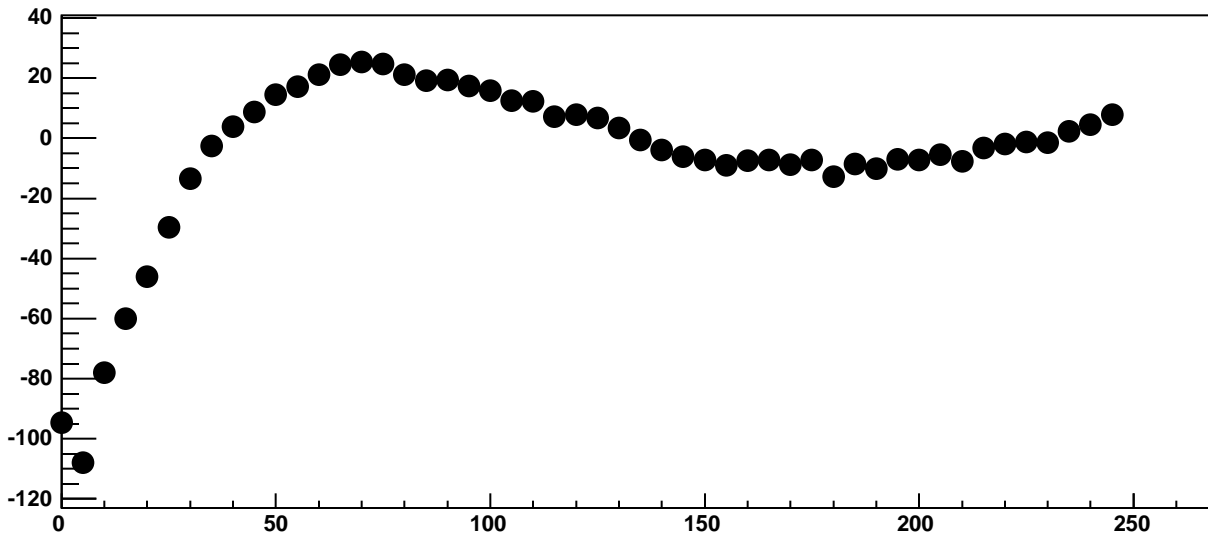


χ^2 / ndf	4245 / 41
p0	$232.4 \pm 4.695e+08$
p1	$85 \pm 4.695e+08$
p2	$-1.555e+07 \pm 5.098e+08$
p3	$4.232e+07 \pm 471.3$
p4	$0.181 \pm 4.695e+08$

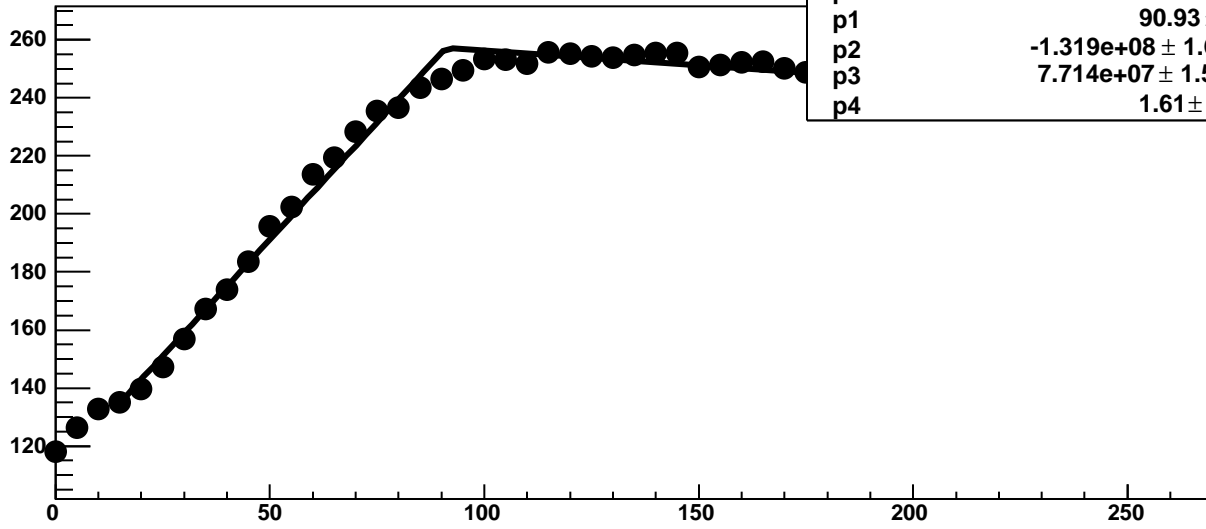
Chip 0, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold

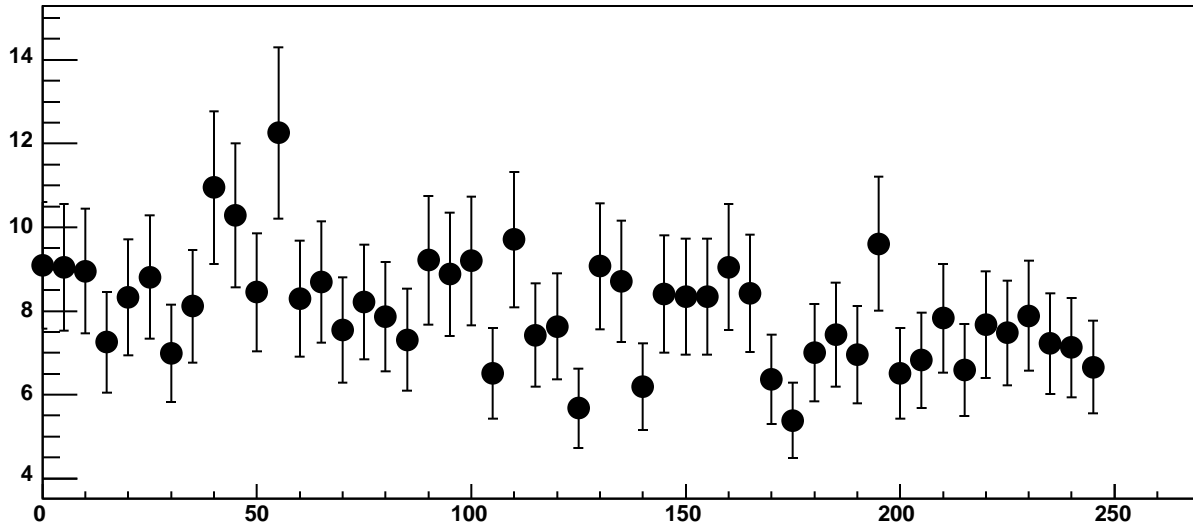


Chip 0, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

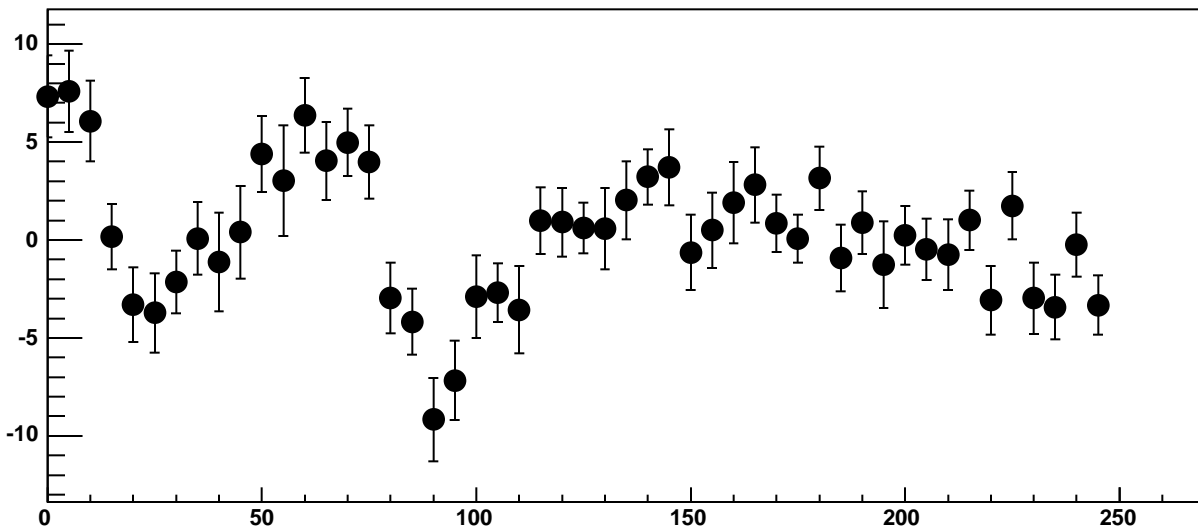


χ^2 / ndf	121.2 / 41
p0	257.1 ± 0.6193
p1	90.93 ± 0.6521
p2	$-1.319\text{e}+08 \pm 1.656\text{e}+06$
p3	$7.714\text{e}+07 \pm 1.543\text{e}+05$
p4	1.61 ± 0.01986

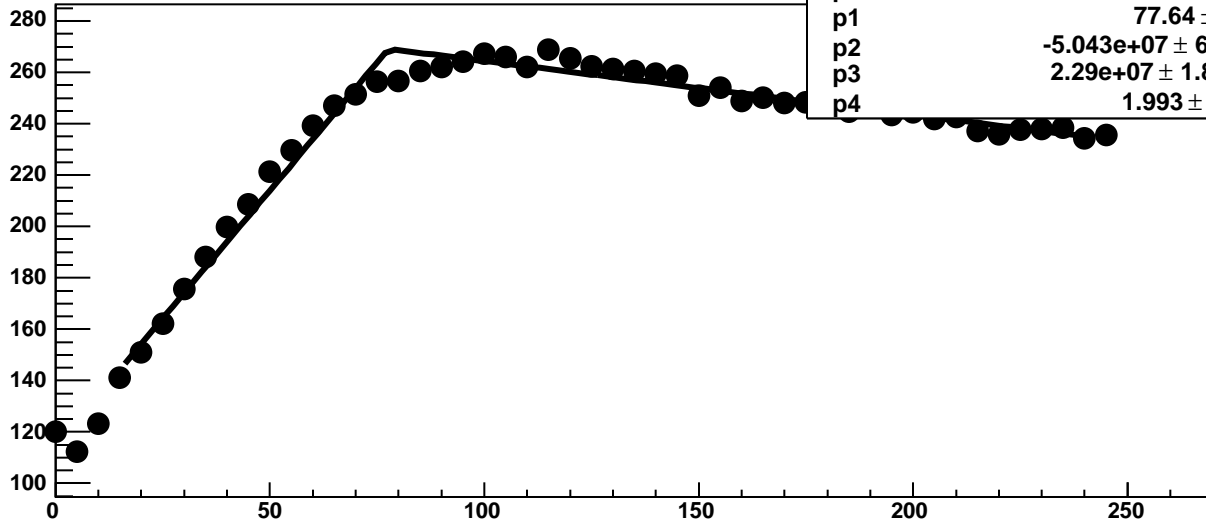
Chip 0, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

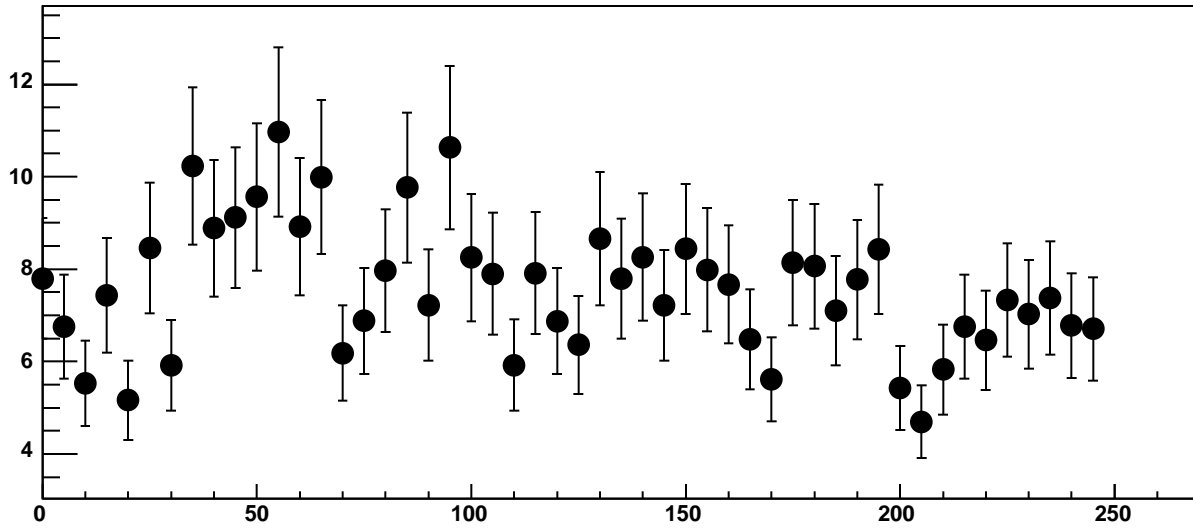


Chip 0, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

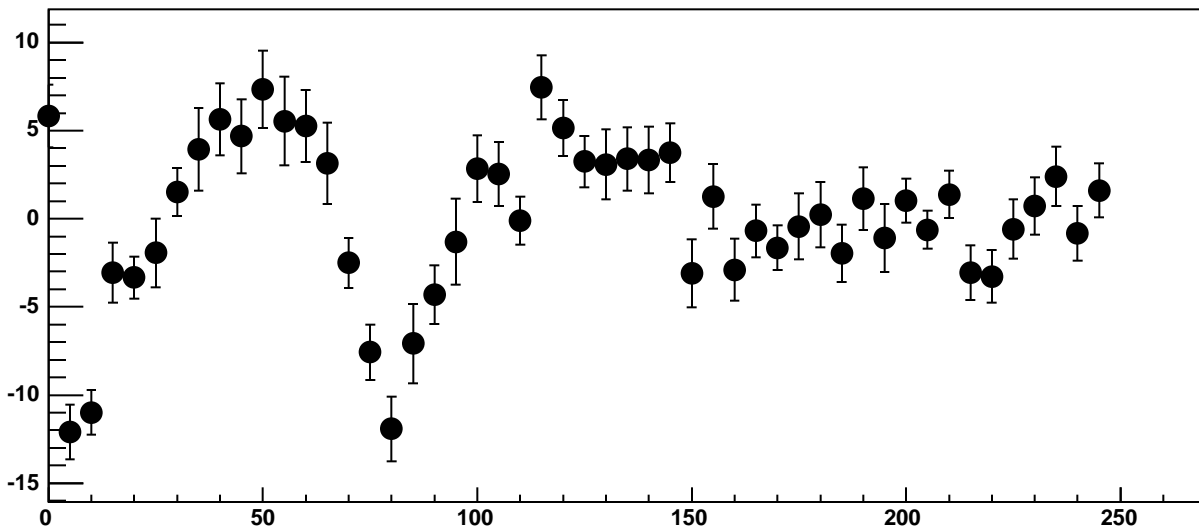


χ^2 / ndf	213.5 / 41
p0	269.1 ± 0.5625
p1	77.64 ± 0.5166
p2	$-5.043\text{e}+07 \pm 6.63\text{e}+05$
p3	$2.29\text{e}+07 \pm 1.897\text{e}+05$
p4	1.993 ± 0.02353

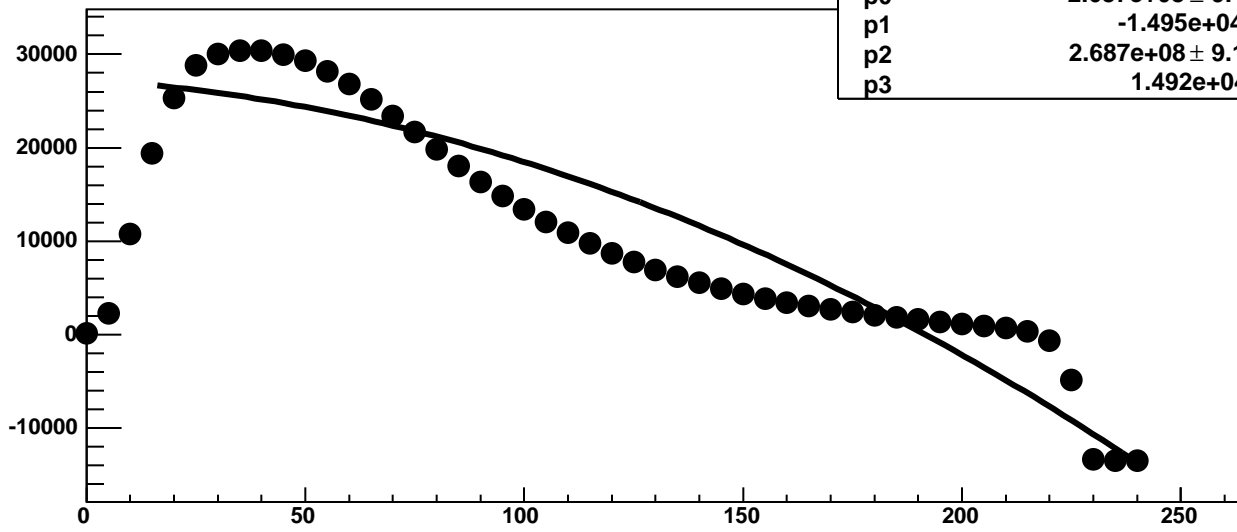
Chip 0, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



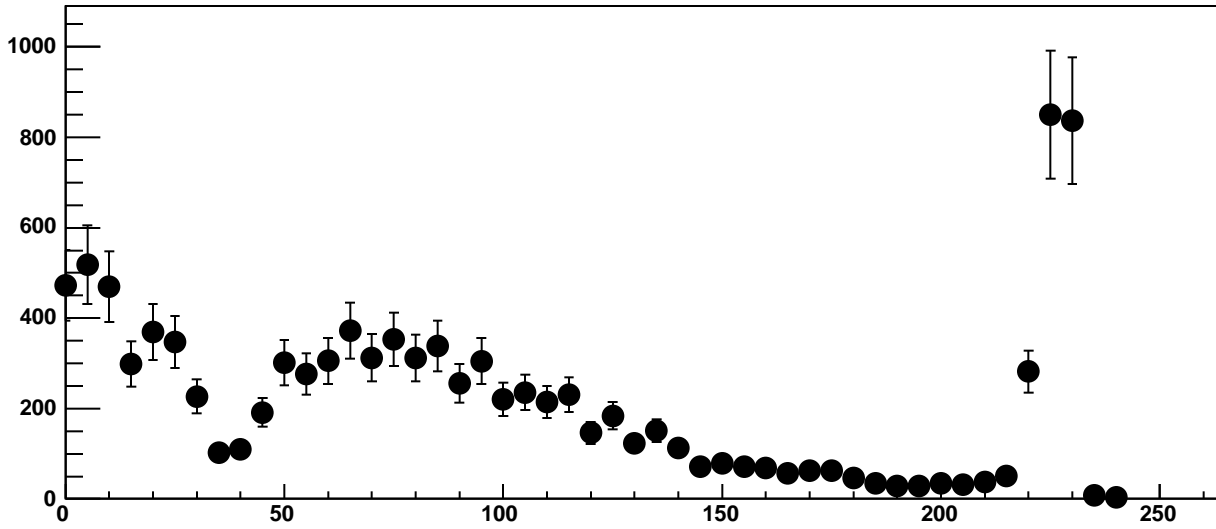
Chip 0, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold



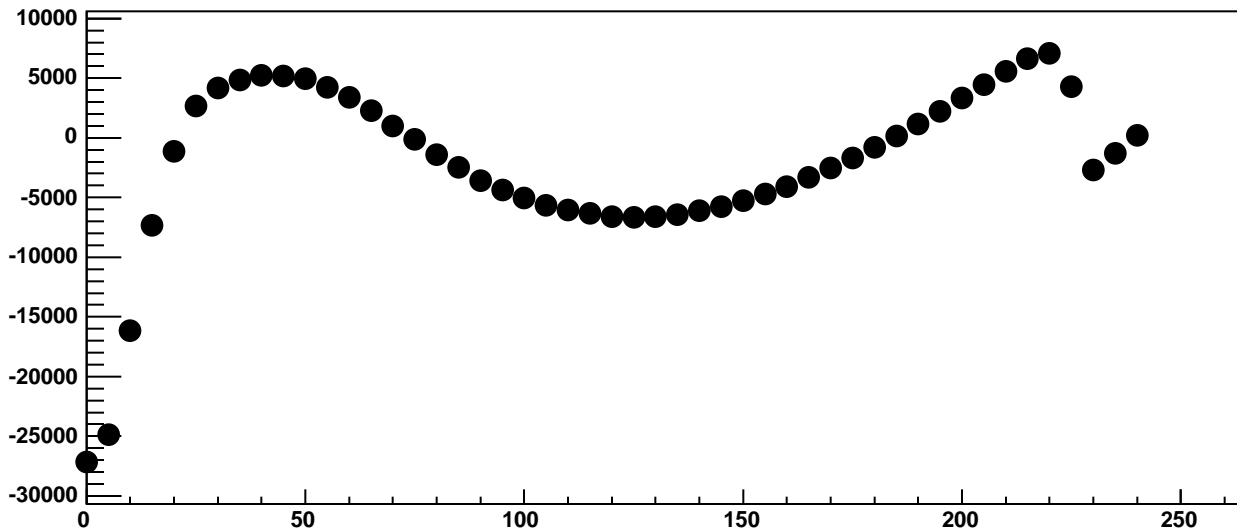
Chip 0, Channel 7, Enable 5!, DAC=4000, ADC Mean vs Hold



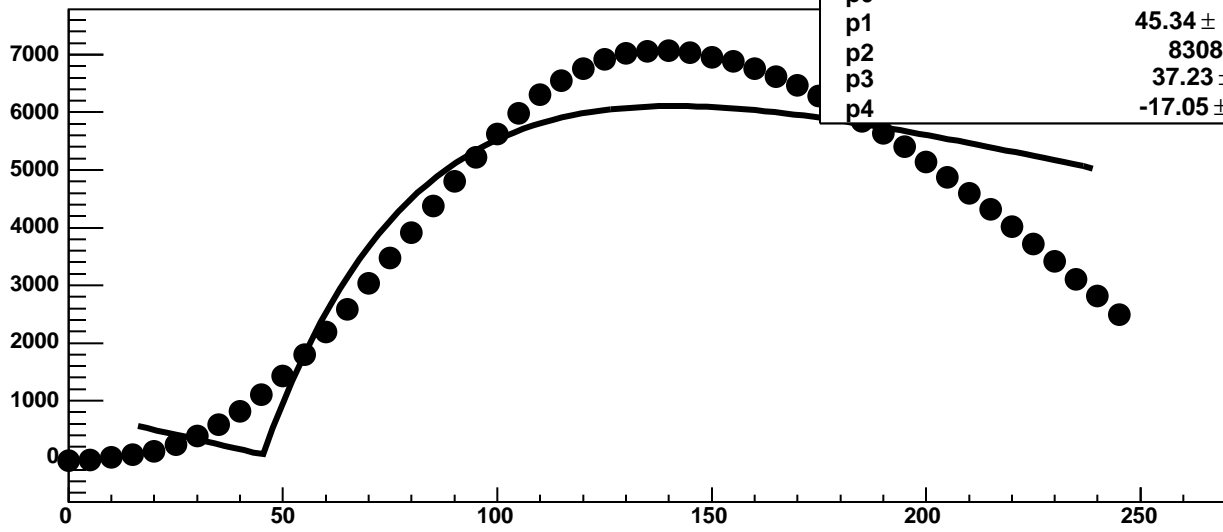
Chip 0, Channel 7, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 7, Enable 5!, DAC=4000, ADC Residuals vs Hold

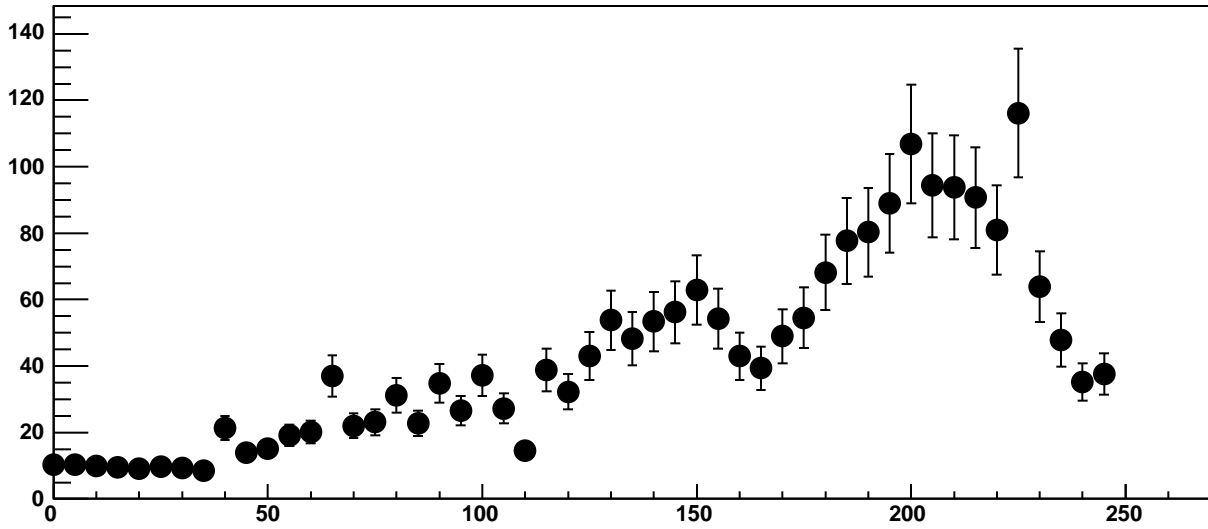


Chip 0, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

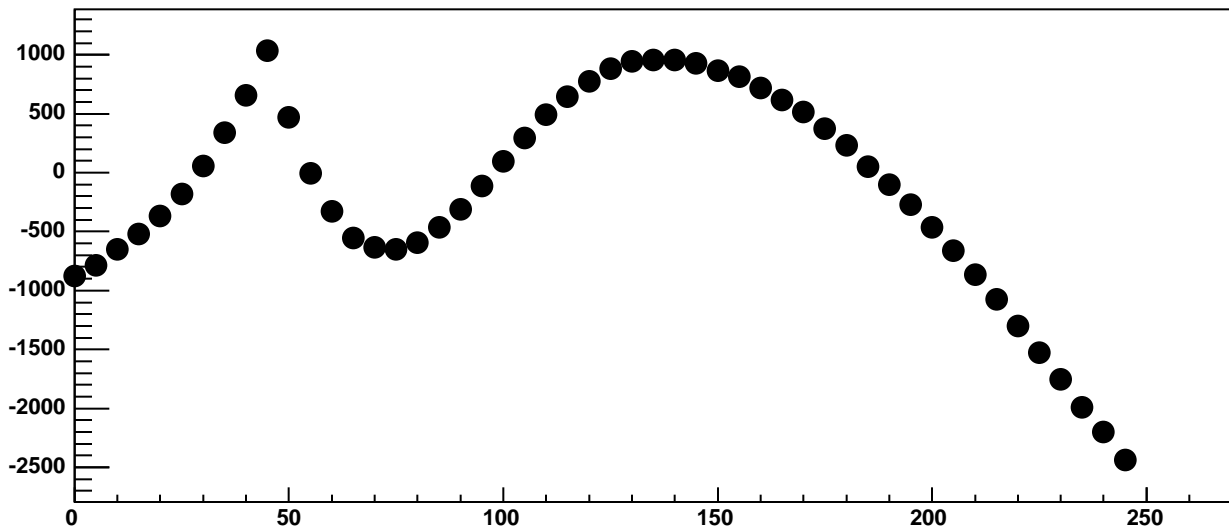


χ^2 / ndf	5.544e+05 / 41
p0	65.01 ± 1.282
p1	45.34 ± 0.01743
p2	8308 ± 9.992
p3	37.23 ± 0.0571
p4	-17.05 ± 0.0576

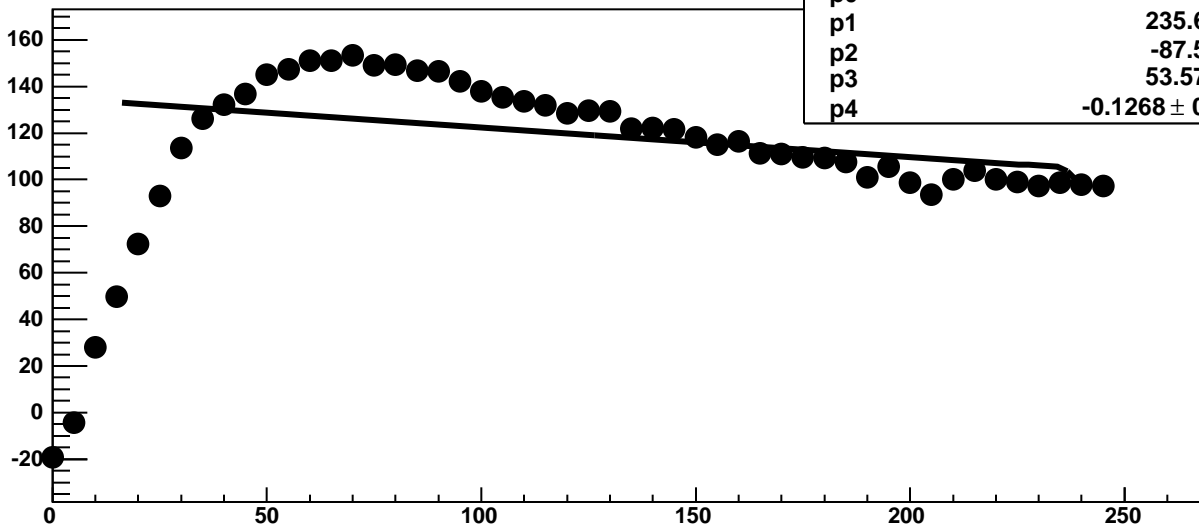
Chip 0, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

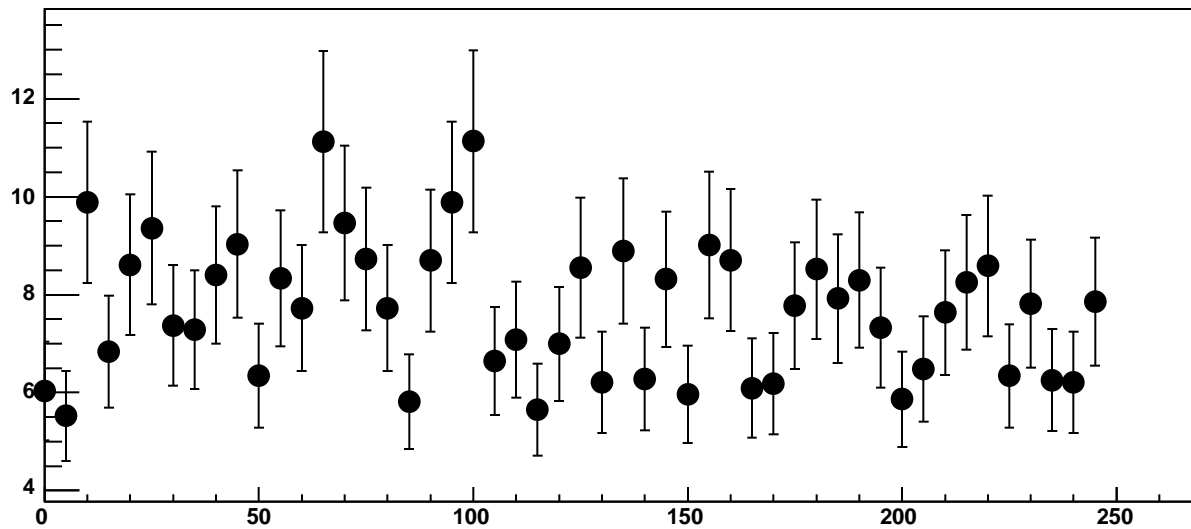


Chip 0, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

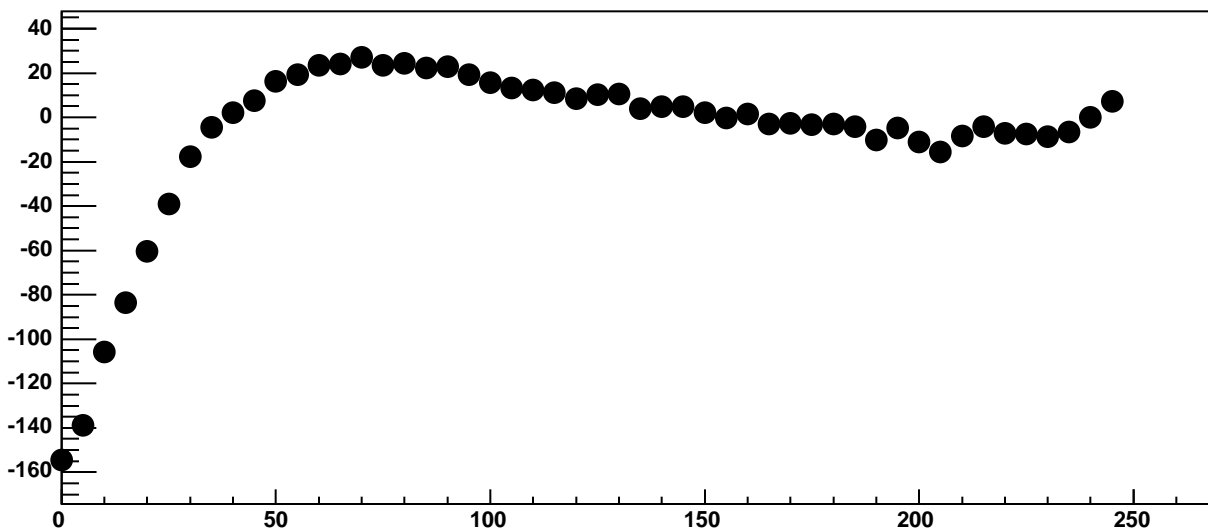


χ^2 / ndf	6411 / 41
p0	105.3 ± 0.919
p1	235.6 ± 6.054
p2	-87.51 ± 112
p3	53.57 ± 58.84
p4	-0.1268 ± 0.003953

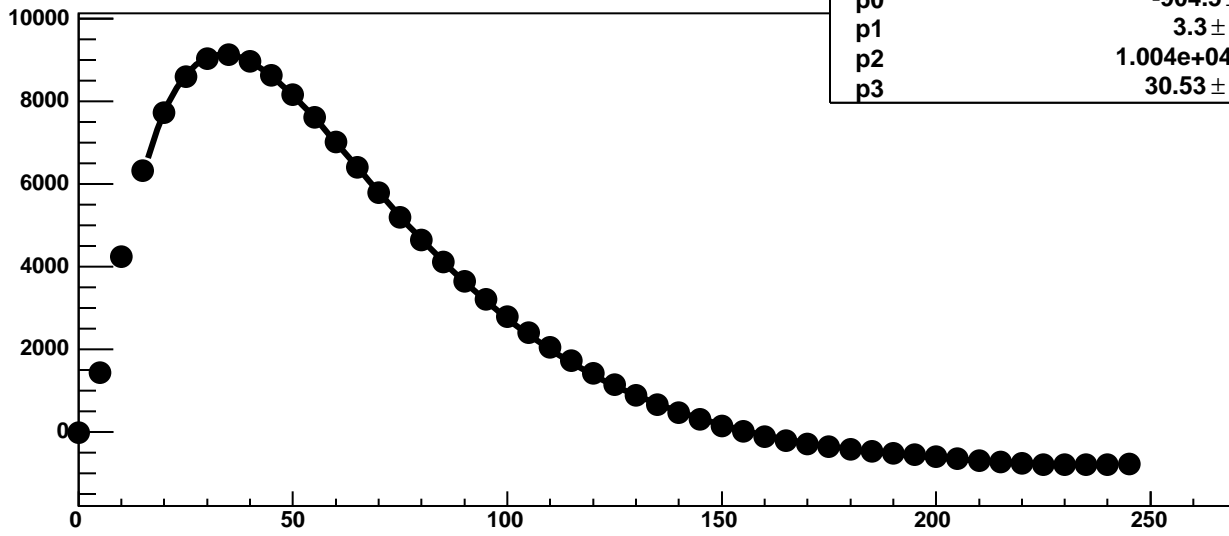
Chip 0, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

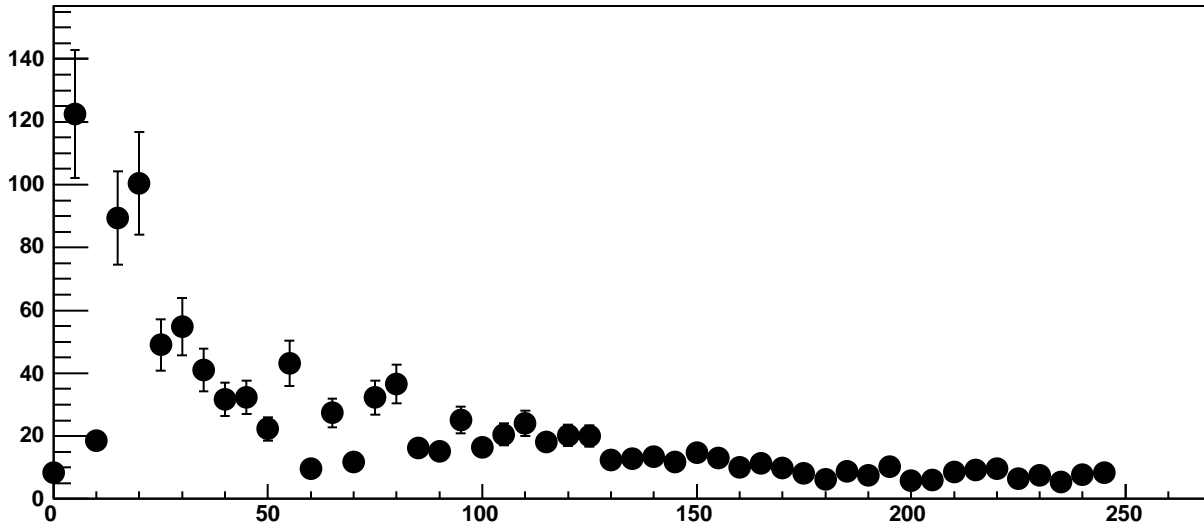


Chip 0, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold

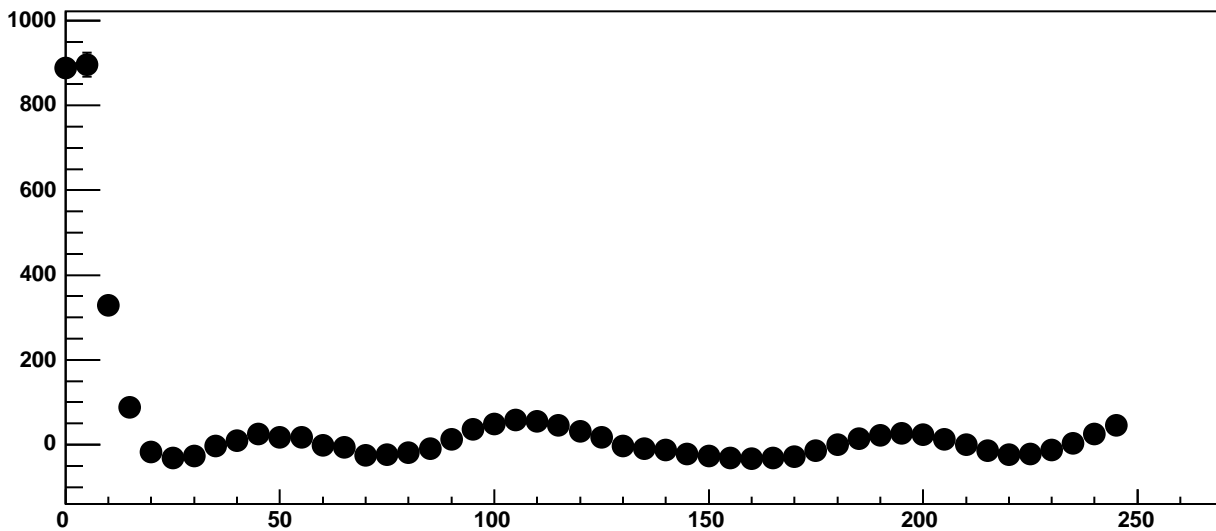


χ^2 / ndf	3020 / 42
p0	-904.5 ± 0.6708
p1	3.3 ± 0.03269
p2	1.004e+04 ± 3.248
p3	30.53 ± 0.01223

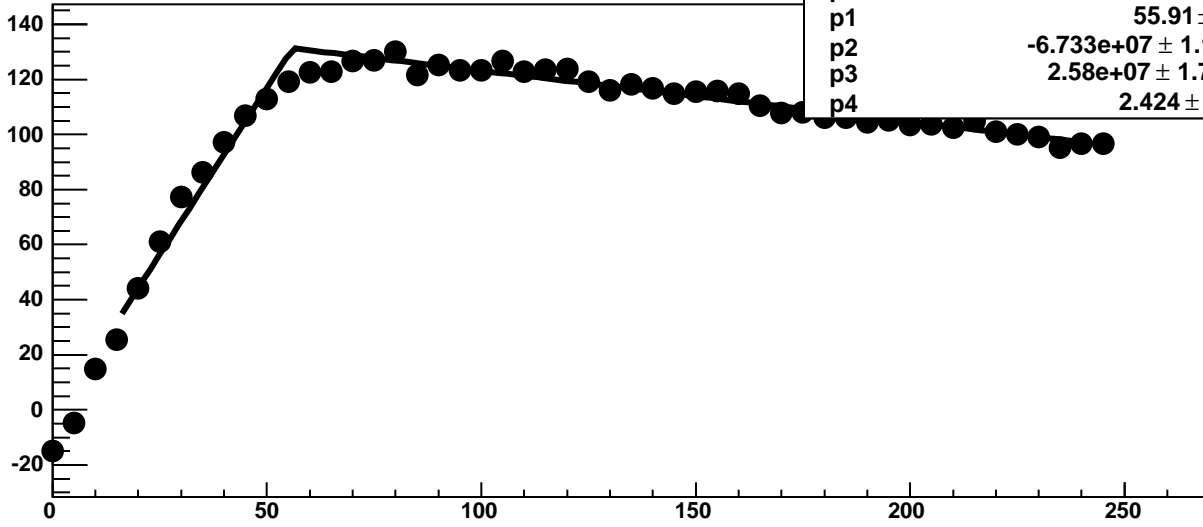
Chip 0, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold

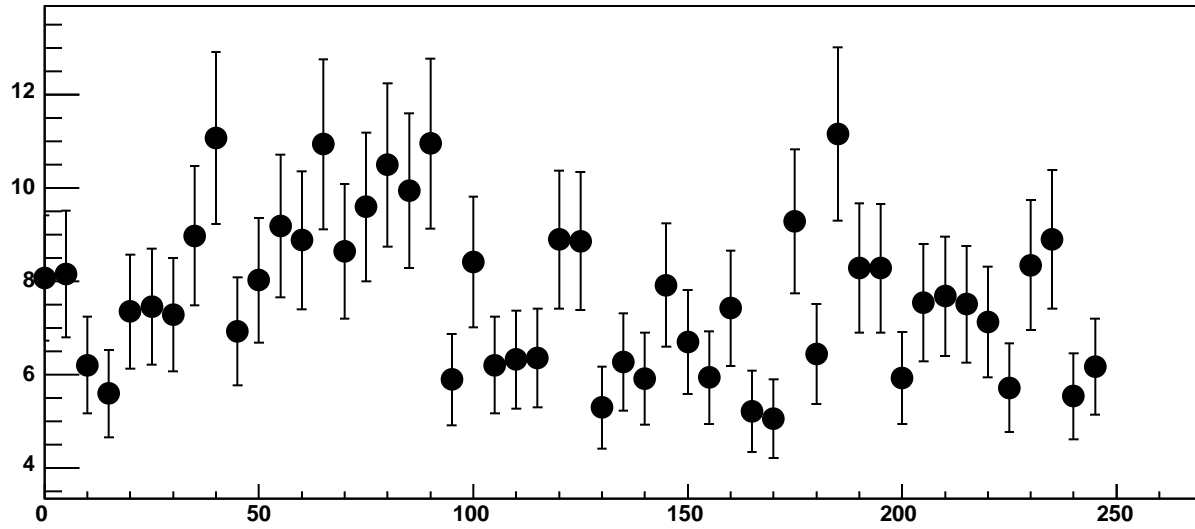


Chip 0, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

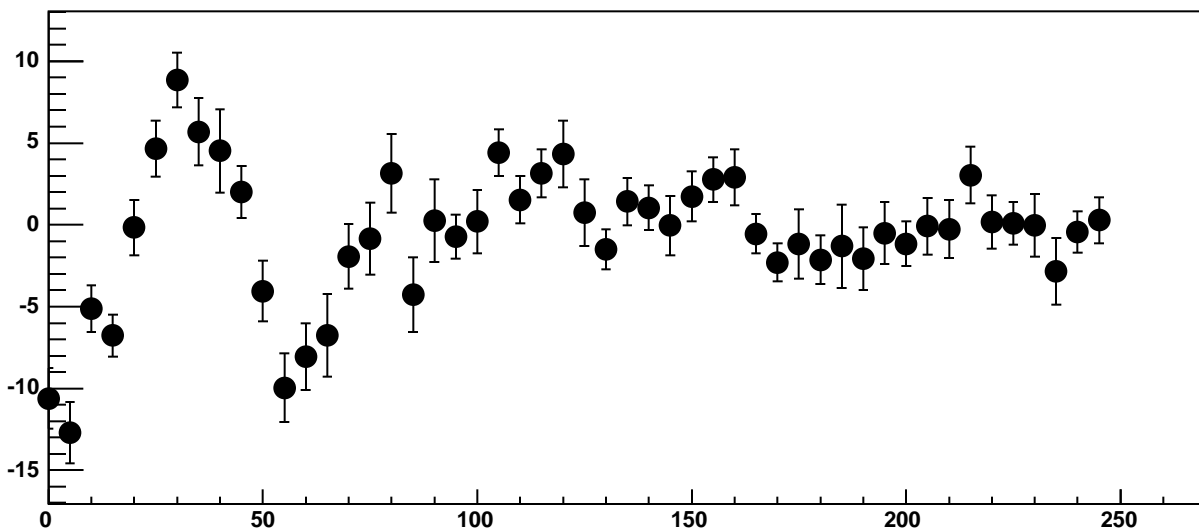


χ^2 / ndf	177.9 / 41
p0	131.3 ± 0.5698
p1	55.91 ± 0.5186
p2	$-6.733\text{e}+07 \pm 1.198\text{e}+06$
p3	$2.58\text{e}+07 \pm 1.708\text{e}+05$
p4	2.424 ± 0.04374

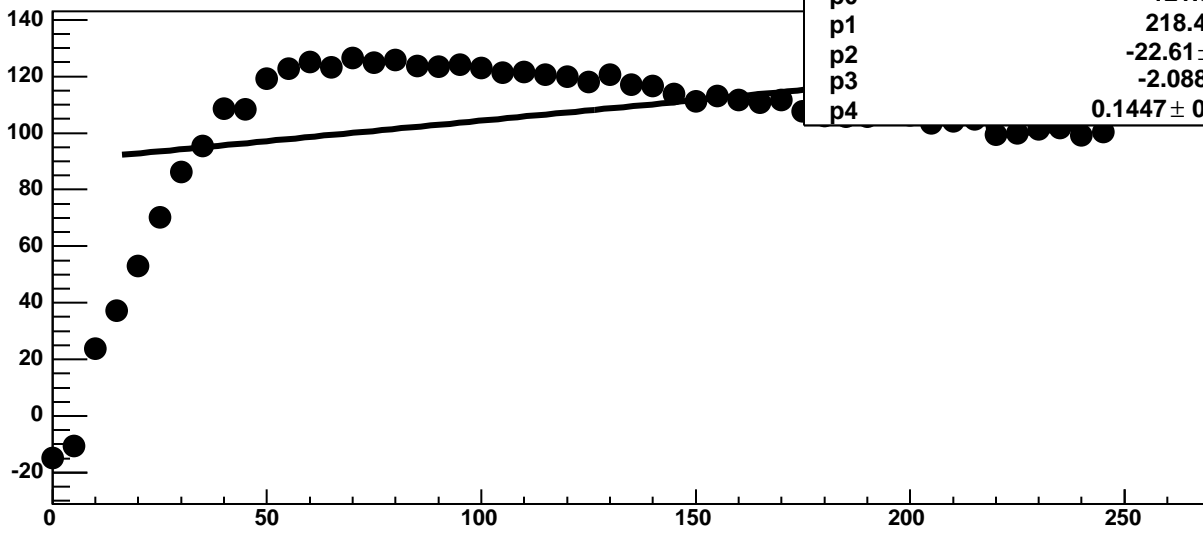
Chip 0, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

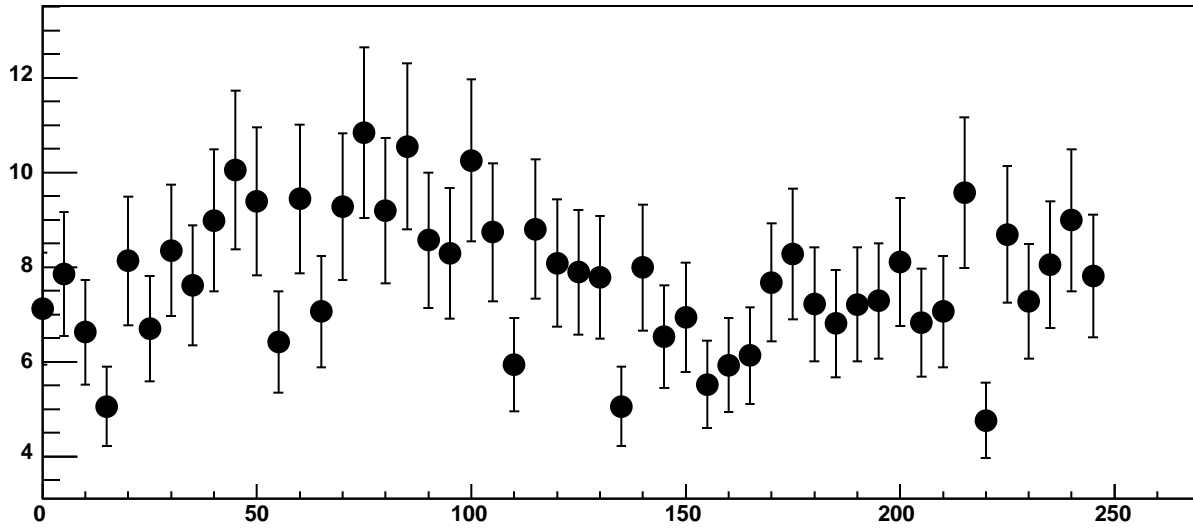


Chip 0, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

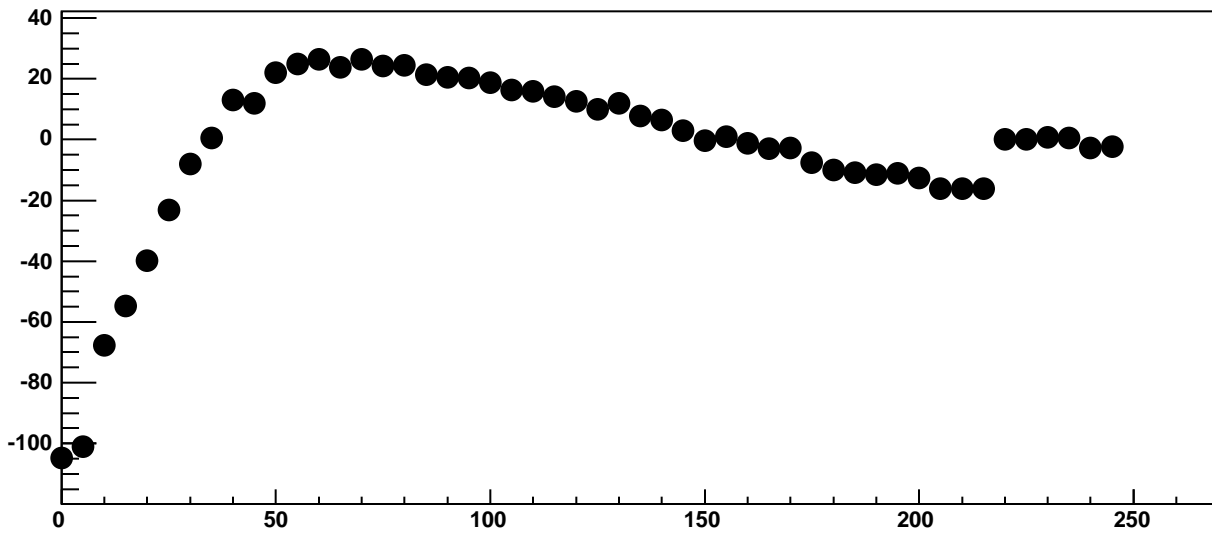


χ^2 / ndf	5438 / 41
p0	121.5 ± 0.211
p1	218.4 ± 11.34
p2	-22.61 ± 0.4104
p3	-2.088 ± 3.898
p4	0.1447 ± 0.001215

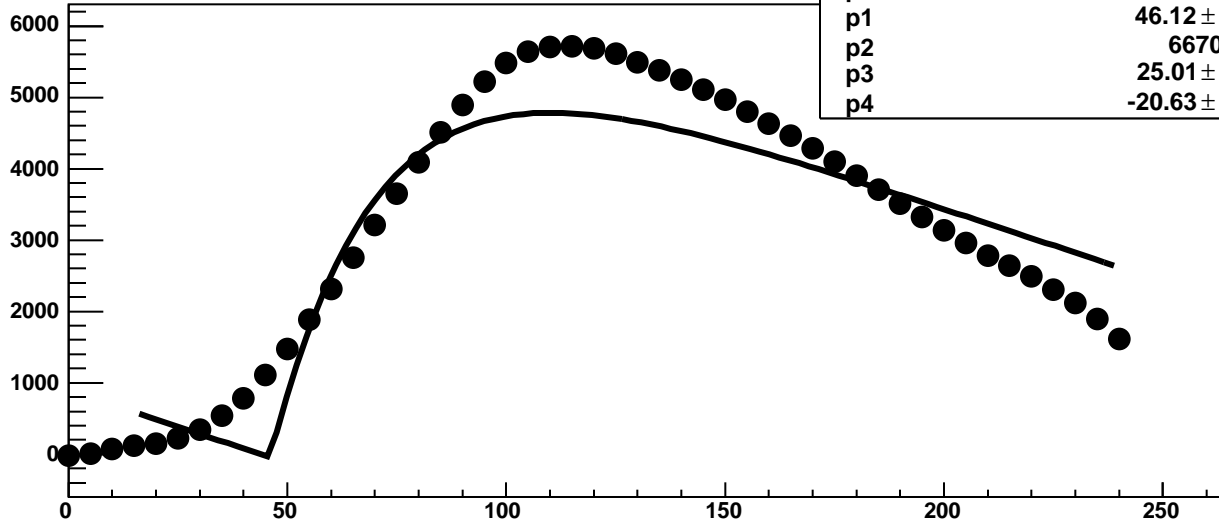
Chip 0, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

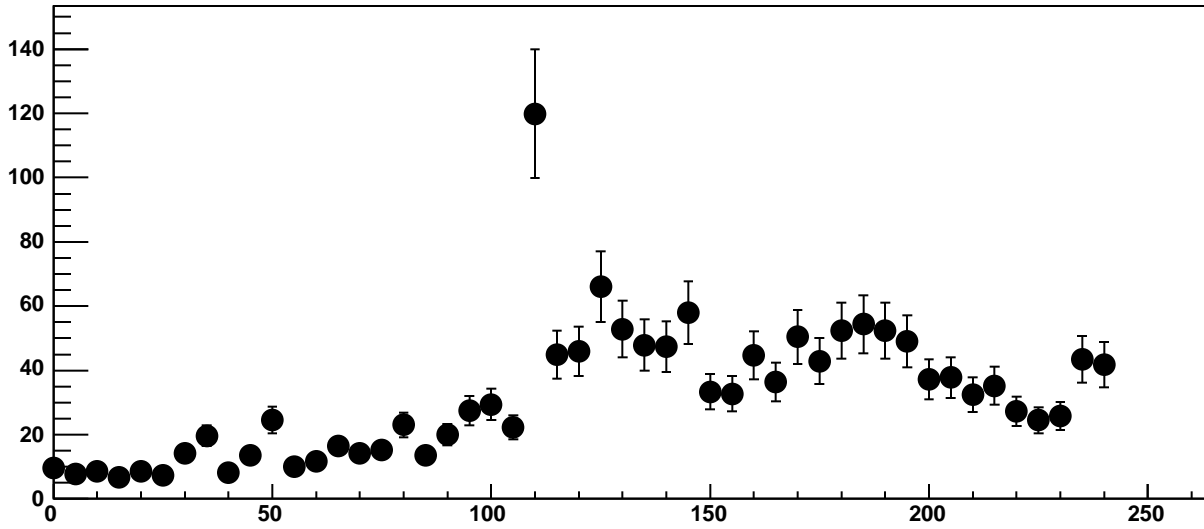


Chip 0, Channel 8, Enable 5, DAC=4000, ADC Mean vs Hold

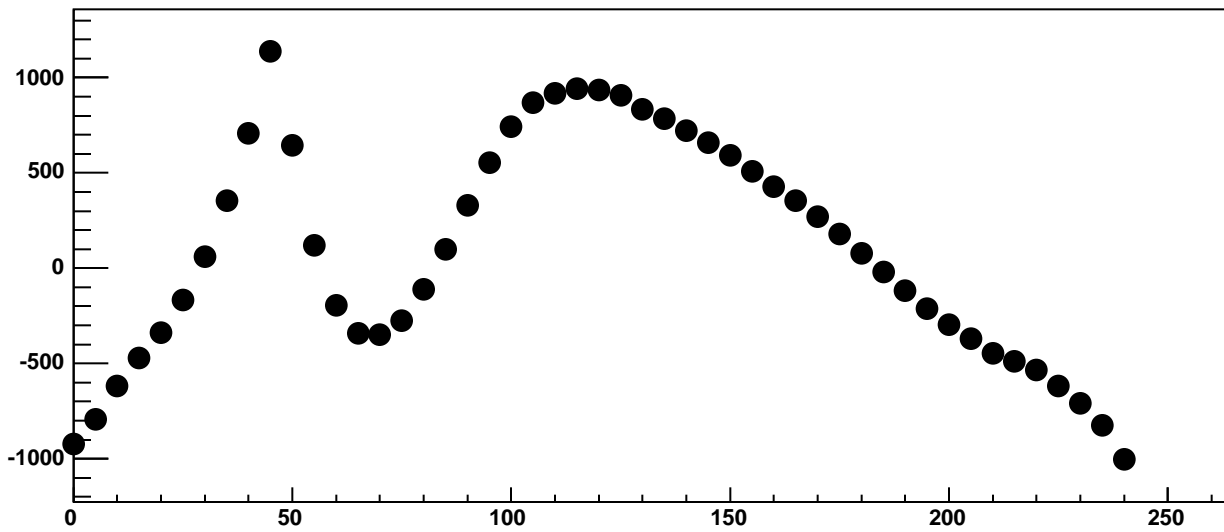


χ^2 / ndf	6.247e+05 / 41
p0	-49.22 ± 1.016
p1	46.12 ± 0.01557
p2	6670 ± 6.206
p3	25.01 ± 0.03752
p4	-20.63 ± 0.03659

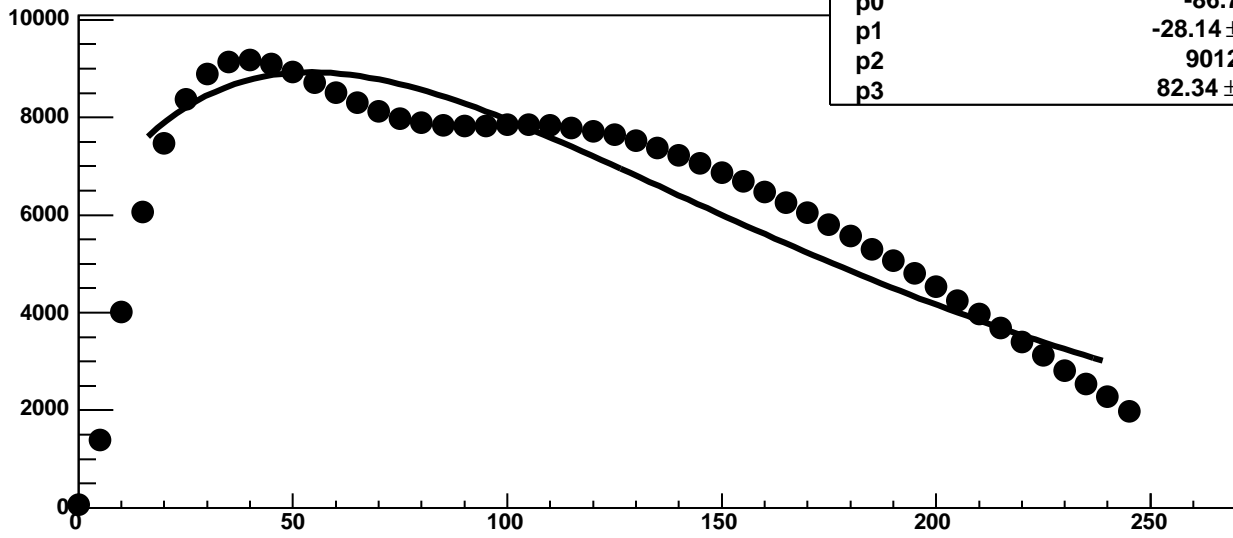
Chip 0, Channel 8, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 8, Enable 5, DAC=4000, ADC Residuals vs Hold

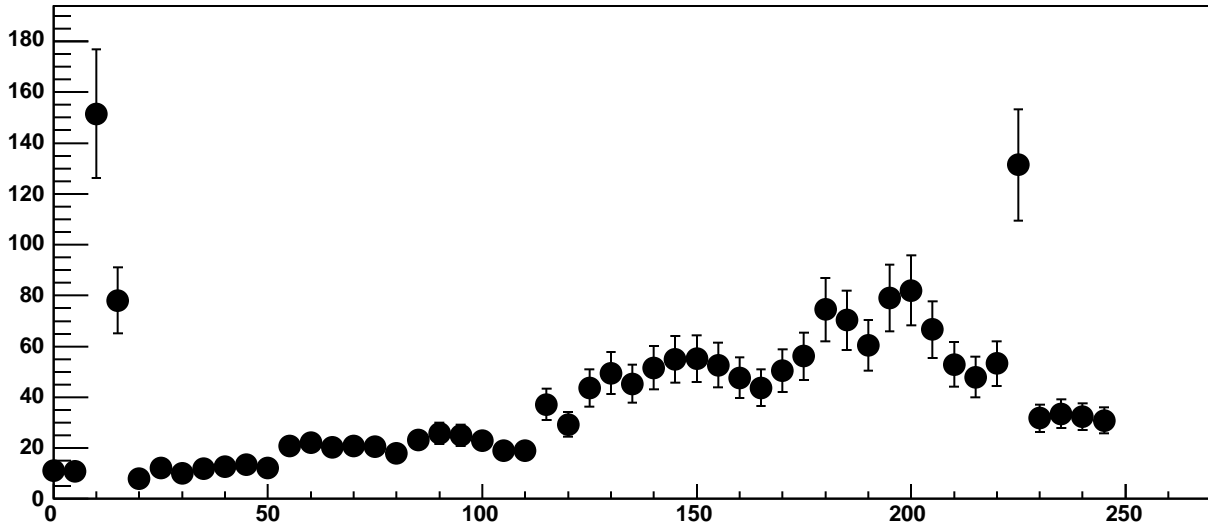


Chip 0, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold

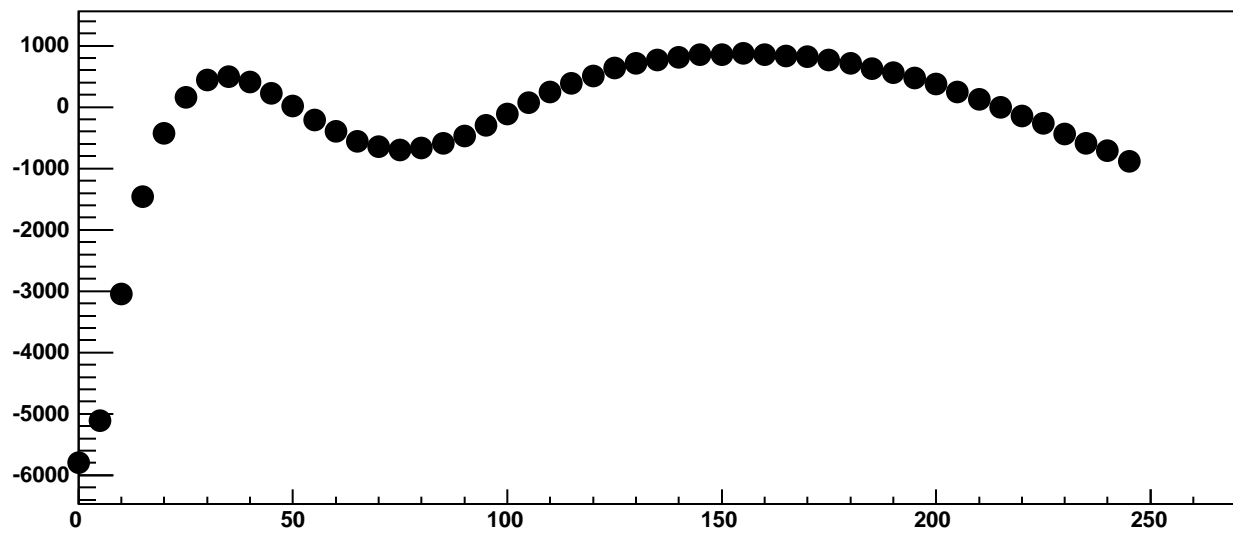


χ^2 / ndf	3.591e+05 / 42
p0	-86.7 \pm 21.2
p1	-28.14 \pm 0.1659
p2	9012 \pm 20.5
p3	82.34 \pm 0.1806

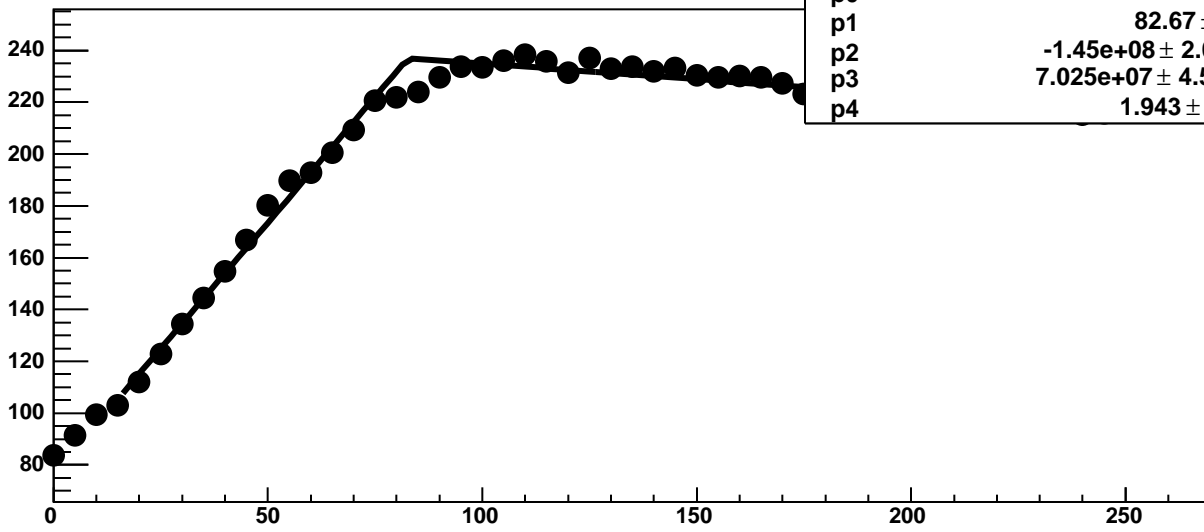
Chip 0, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold

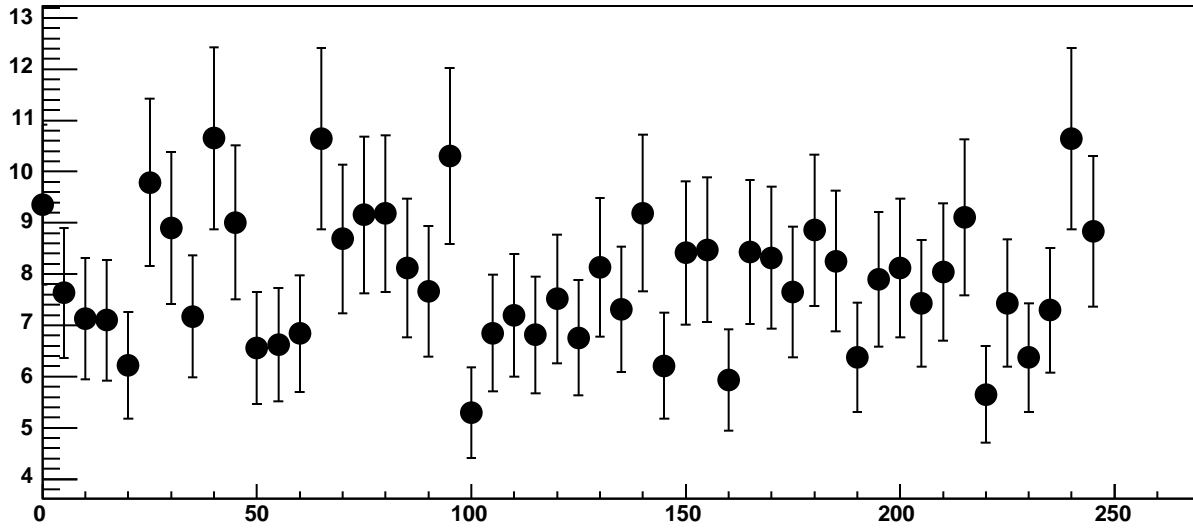


Chip 0, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

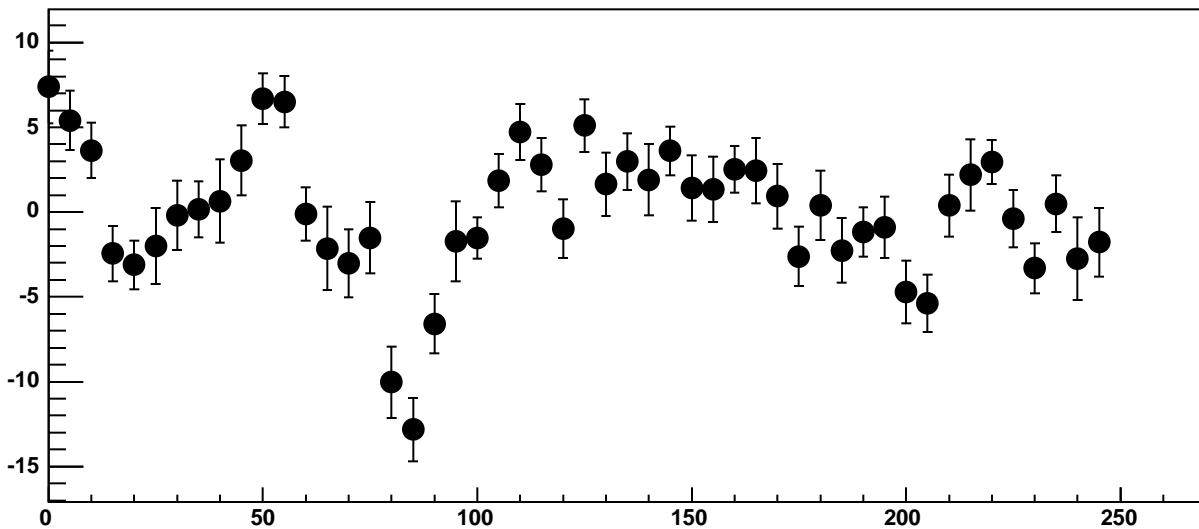


χ^2 / ndf	213.1 / 41
p0	237 ± 0.5623
p1	82.67 ± 0.5783
p2	$-1.45\text{e}+08 \pm 2.048\text{e}+06$
p3	$7.025\text{e}+07 \pm 4.533\text{e}+05$
p4	1.943 ± 0.02447

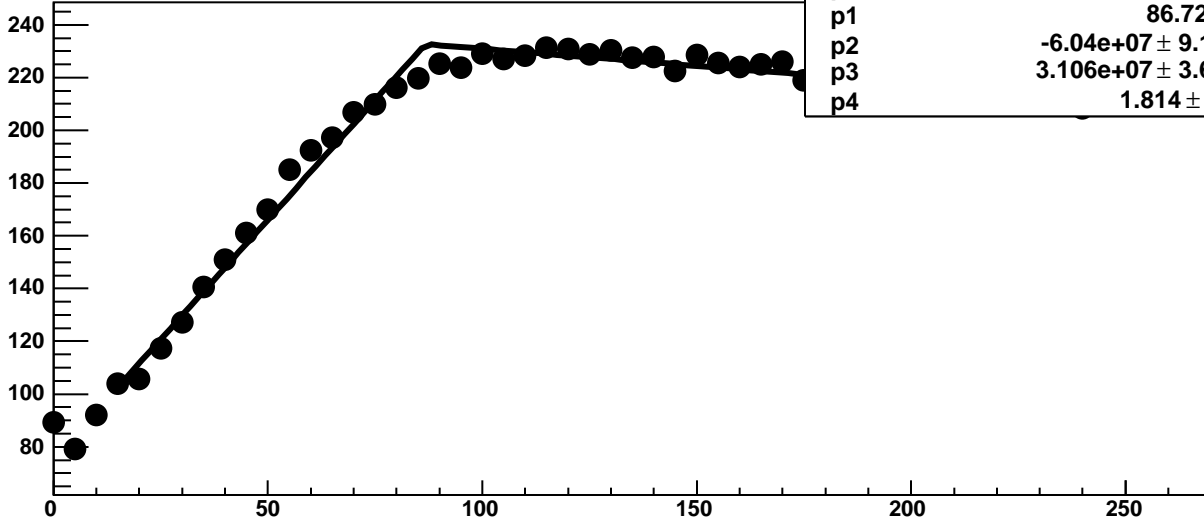
Chip 0, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

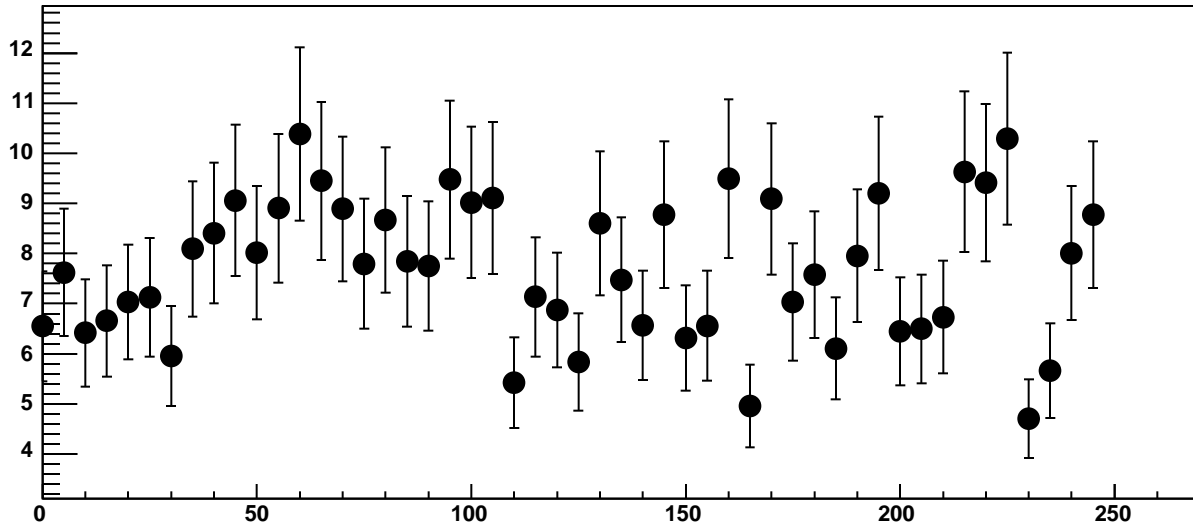


Chip 0, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

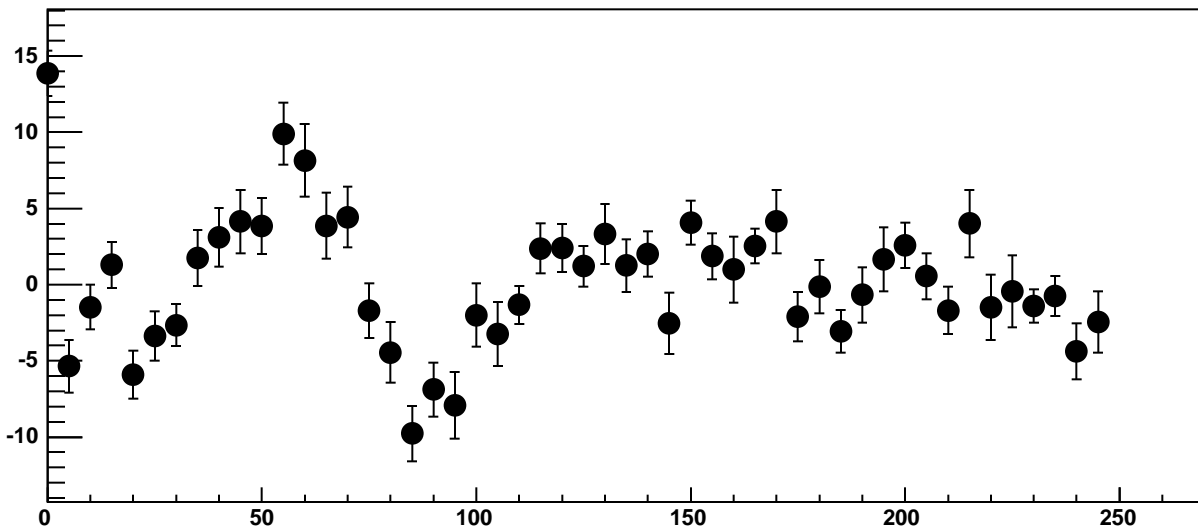


χ^2 / ndf	199.9 / 41
p0	232.7 ± 0.5699
p1	86.72 ± 0.582
p2	$-6.04\text{e}+07 \pm 9.189\text{e}+05$
p3	$3.106\text{e}+07 \pm 3.639\text{e}+05$
p4	1.814 ± 0.02075

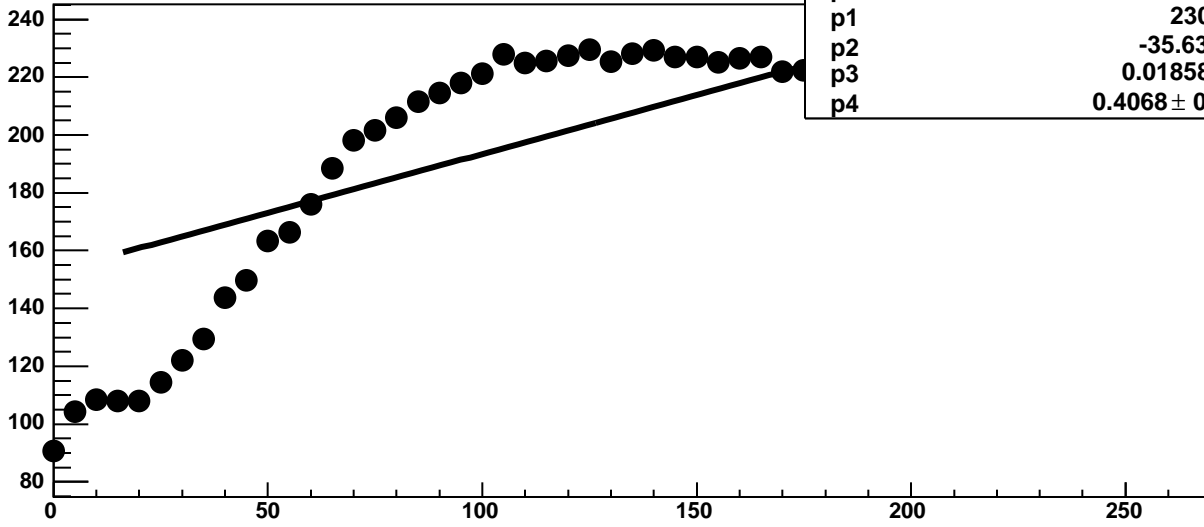
Chip 0, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold

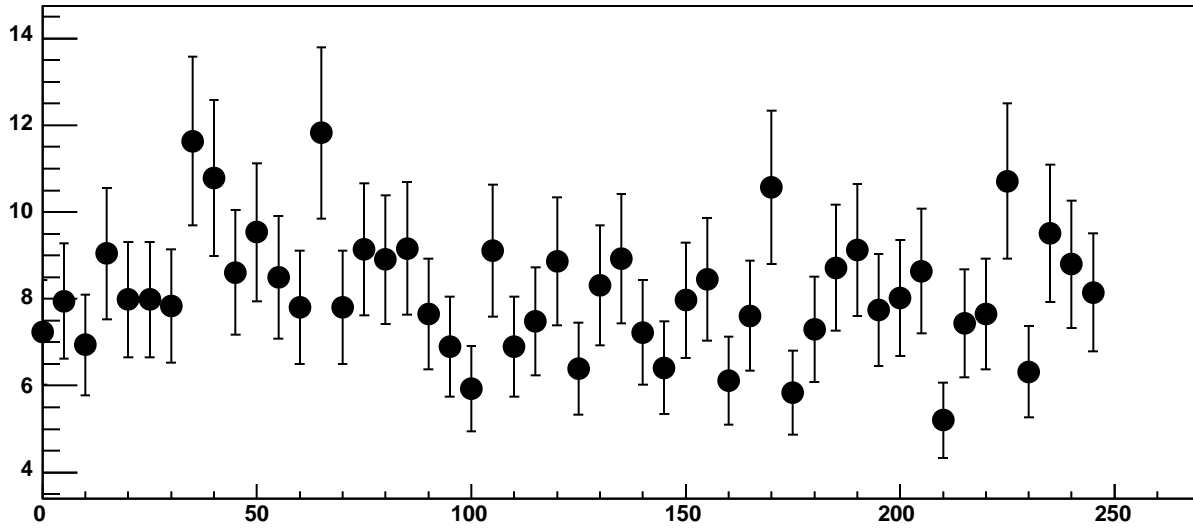


Chip 0, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold

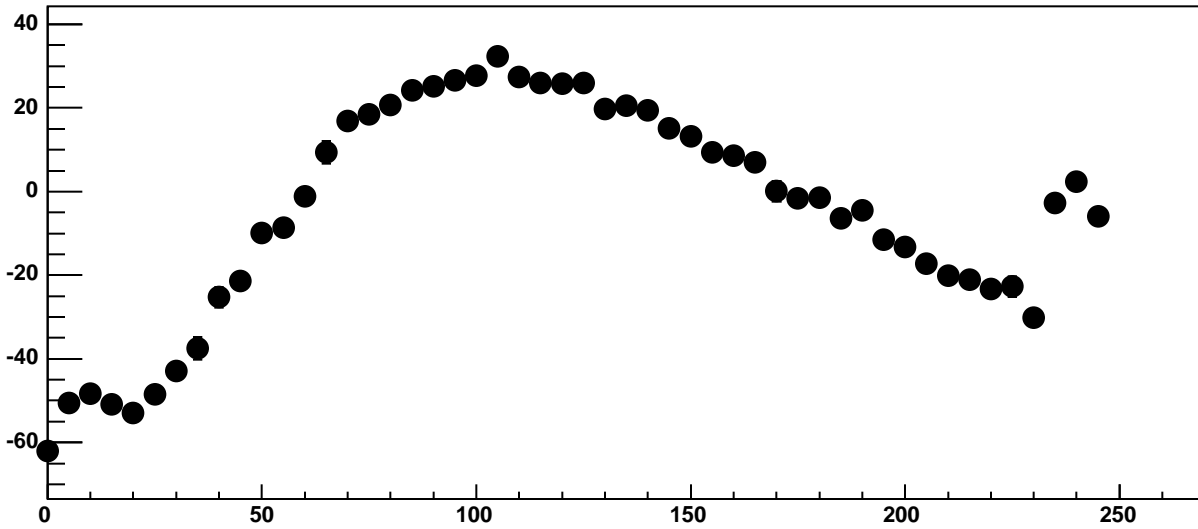


χ^2 / ndf	7628 / 41
p0	246.3 ± 2.377
p1	230 ± 8.771
p2	-35.63 ± 6.158
p3	0.01858 ± 11.03
p4	0.4068 ± 0.007648

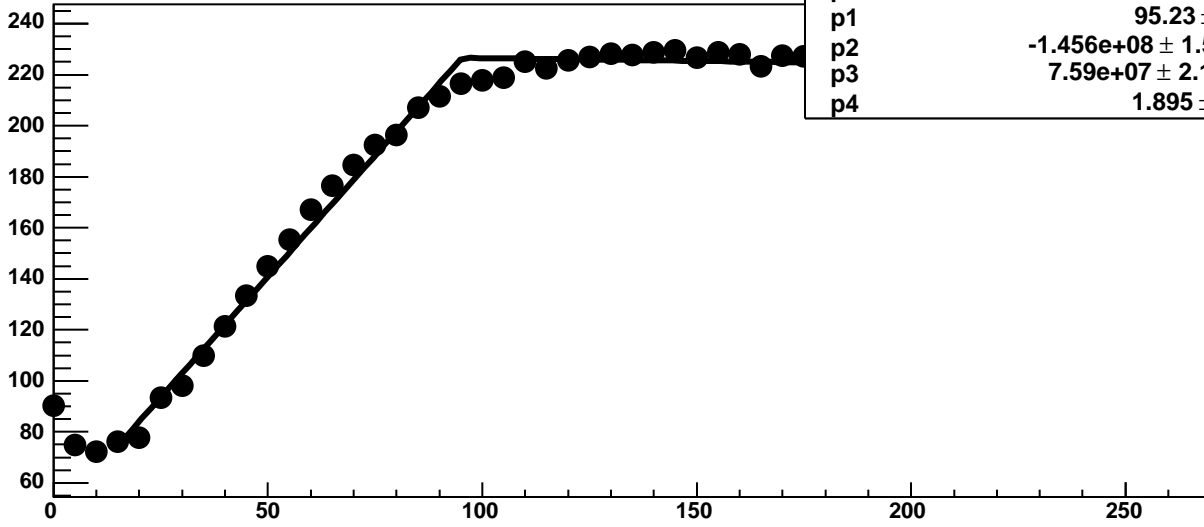
Chip 0, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold

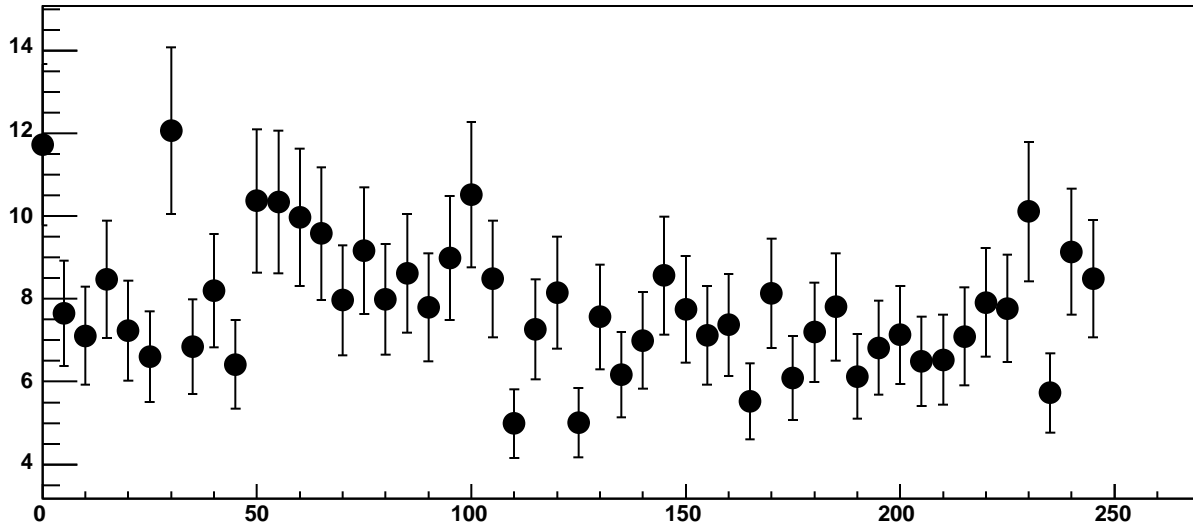


Chip 0, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

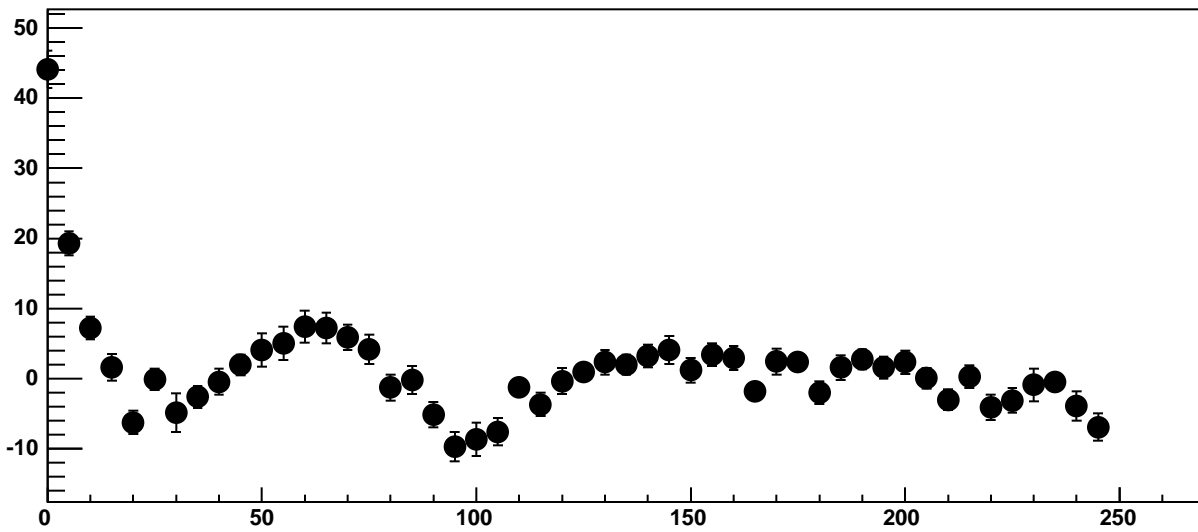


χ^2 / ndf	183.4 / 41
p0	226.6 ± 0.602
p1	95.23 ± 0.5689
p2	$-1.456\text{e}+08 \pm 1.562\text{e}+06$
p3	$7.59\text{e}+07 \pm 2.103\text{e}+05$
p4	1.895 ± 0.0184

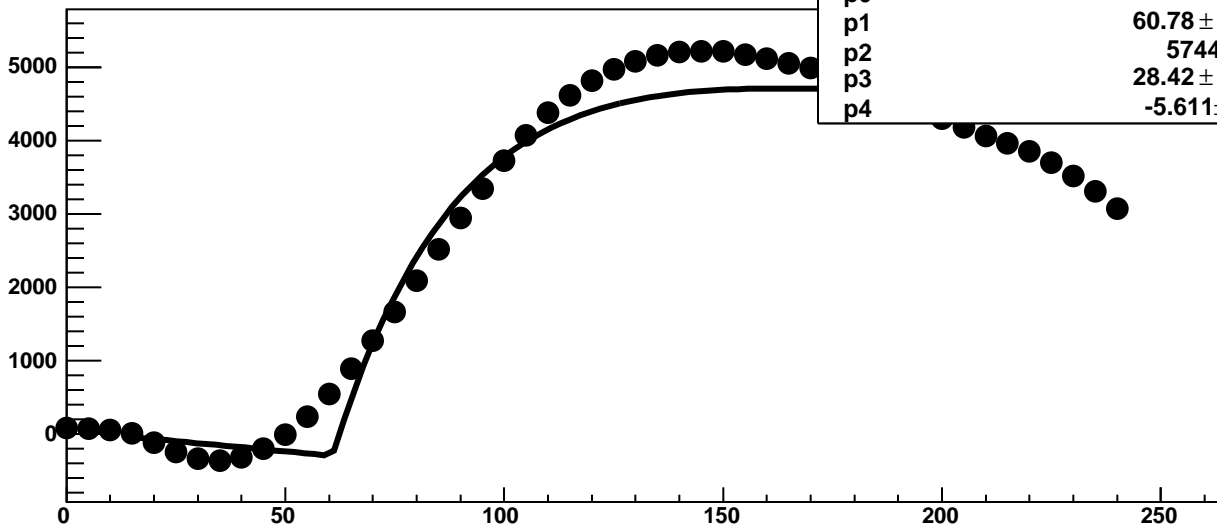
Chip 0, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

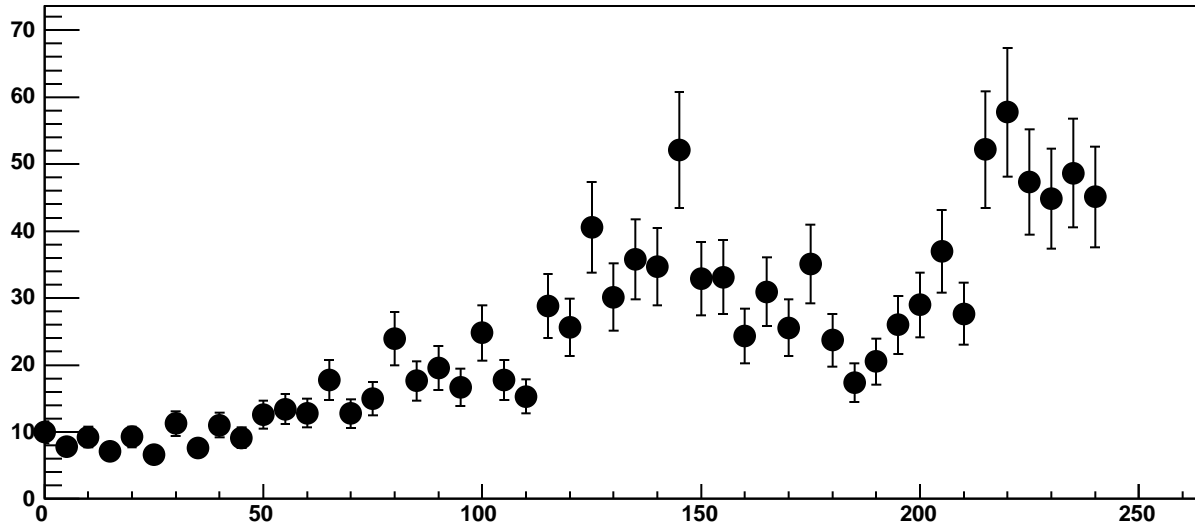


Chip 0, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold

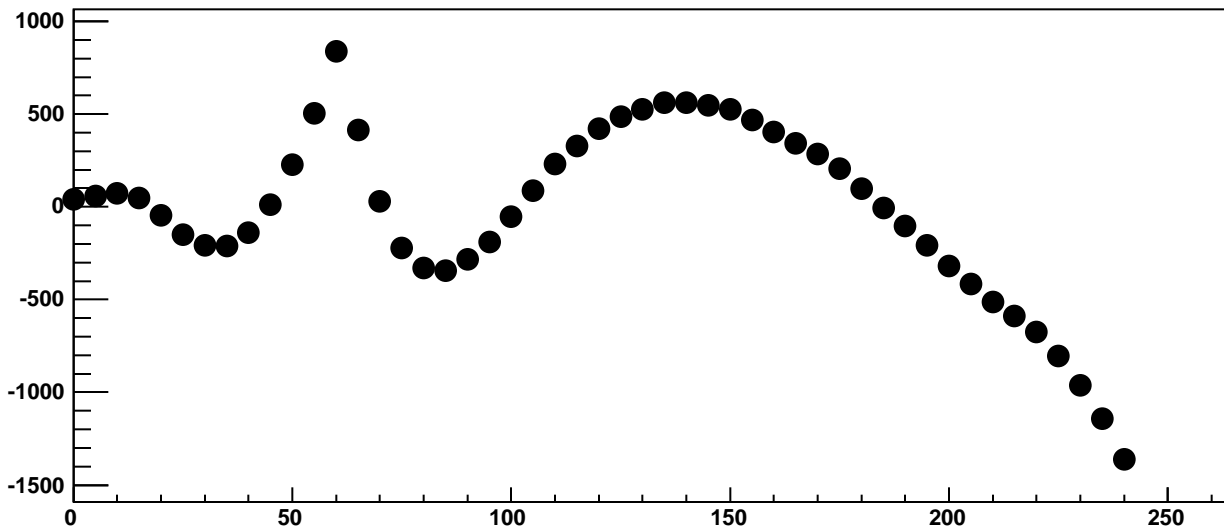


χ^2 / ndf	2.94e+05 / 41
p0	-296.7 ± 1.228
p1	60.78 ± 0.01985
p2	5744 ± 6.262
p3	28.42 ± 0.04609
p4	-5.611 ± 0.0377

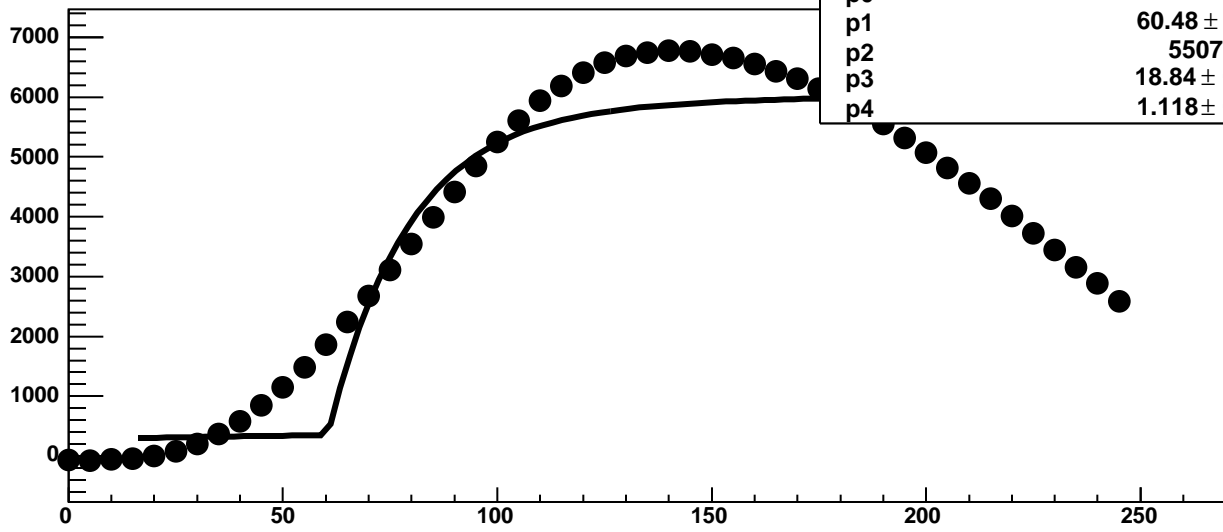
Chip 0, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold

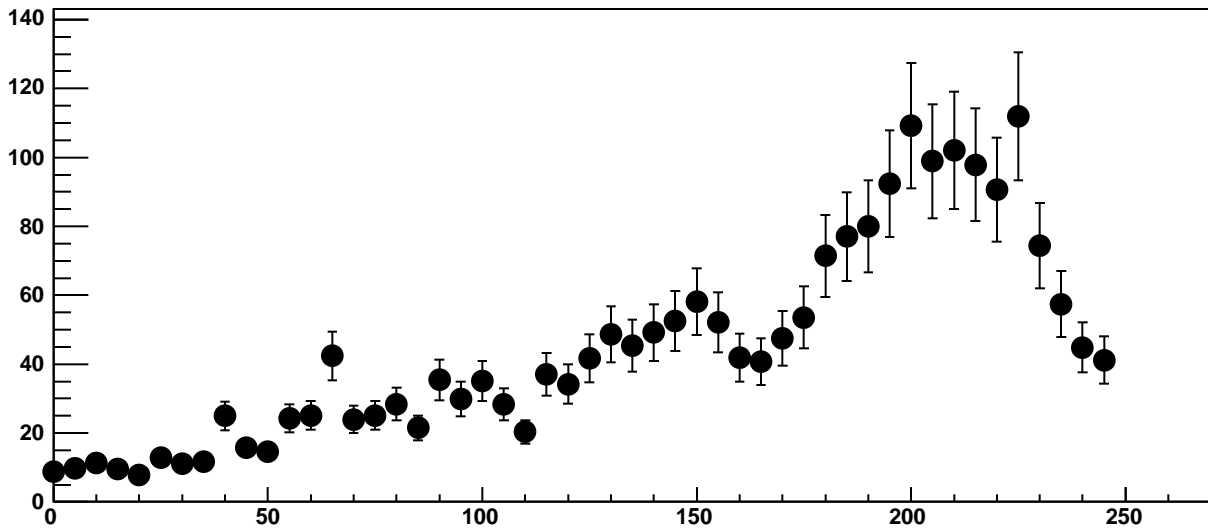


Chip 0, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold

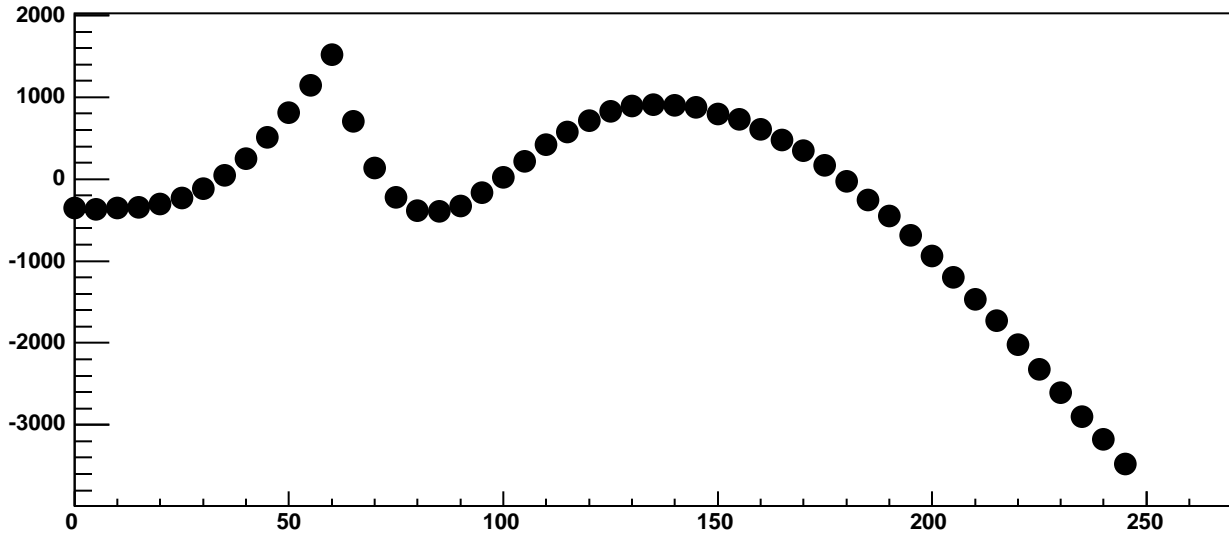


χ^2 / ndf	5.491e+05 / 41
p0	352.3 ± 1.737
p1	60.48 ± 0.03131
p2	5507 ± 6.677
p3	18.84 ± 0.04538
p4	1.118 ± 0.04675

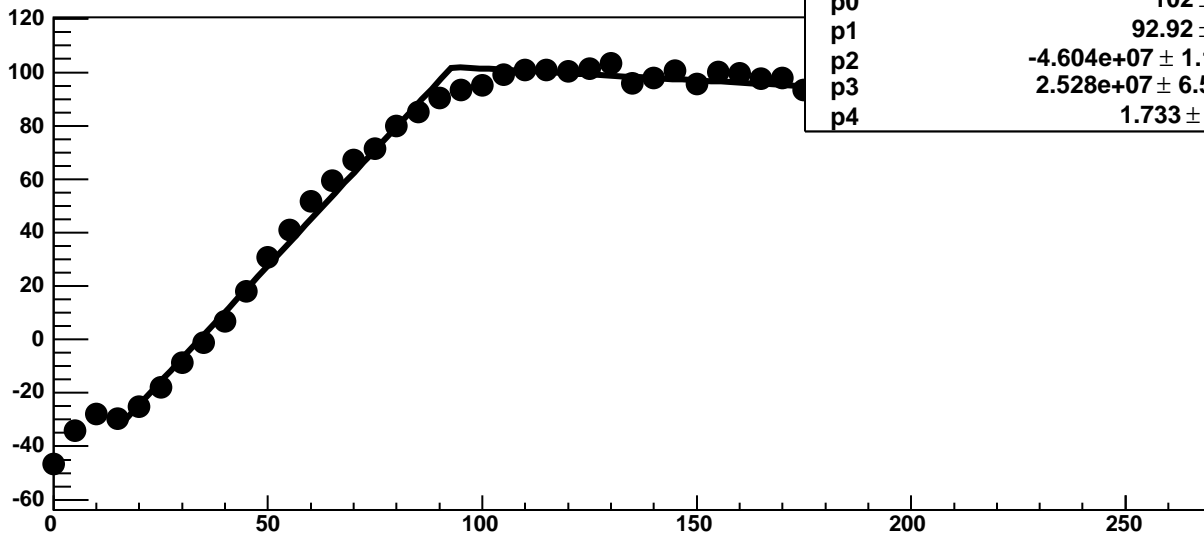
Chip 0, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold

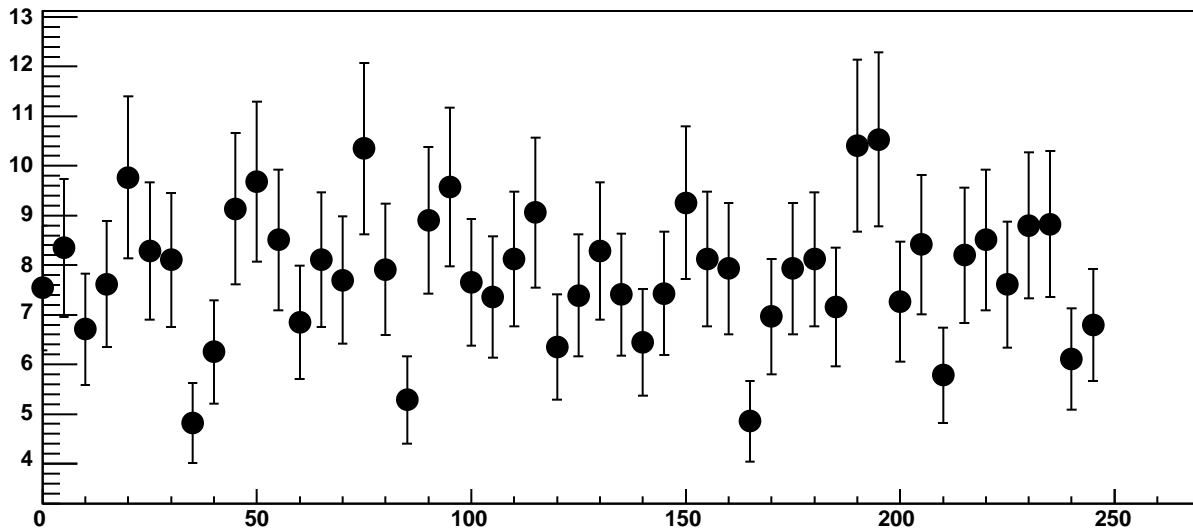


Chip 0, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

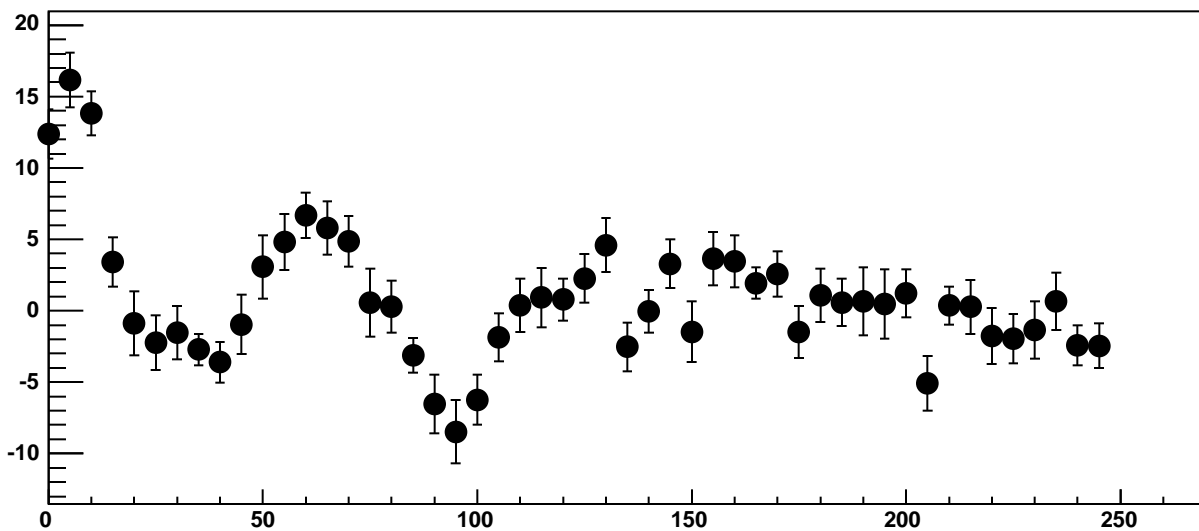


χ^2 / ndf	149.8 / 41
p0	102 ± 0.5944
p1	92.92 ± 0.5635
p2	$-4.604\text{e}+07 \pm 1.106\text{e}+06$
p3	$2.528\text{e}+07 \pm 6.522\text{e}+05$
p4	1.733 ± 0.01716

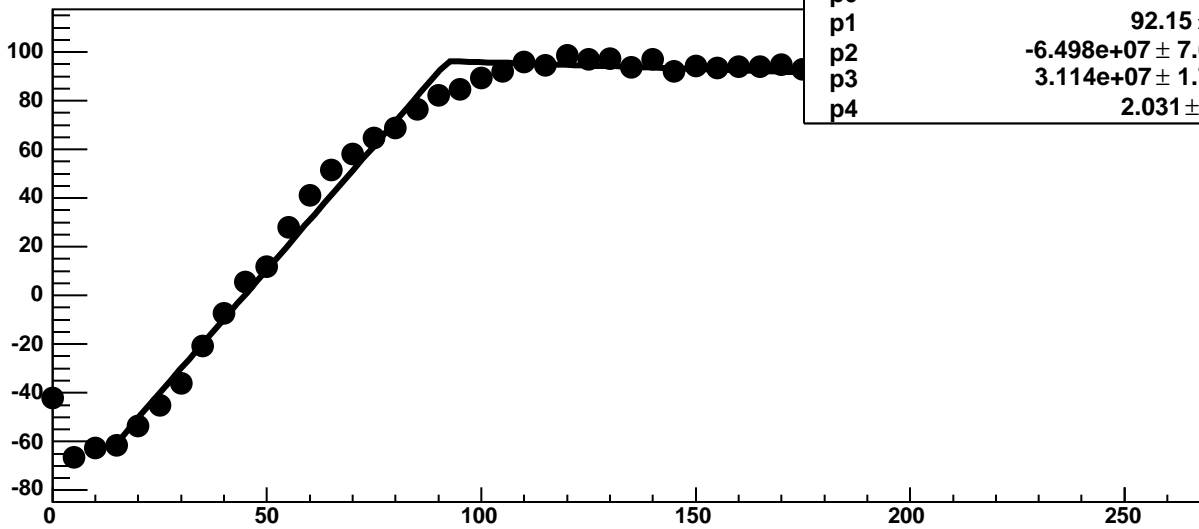
Chip 0, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

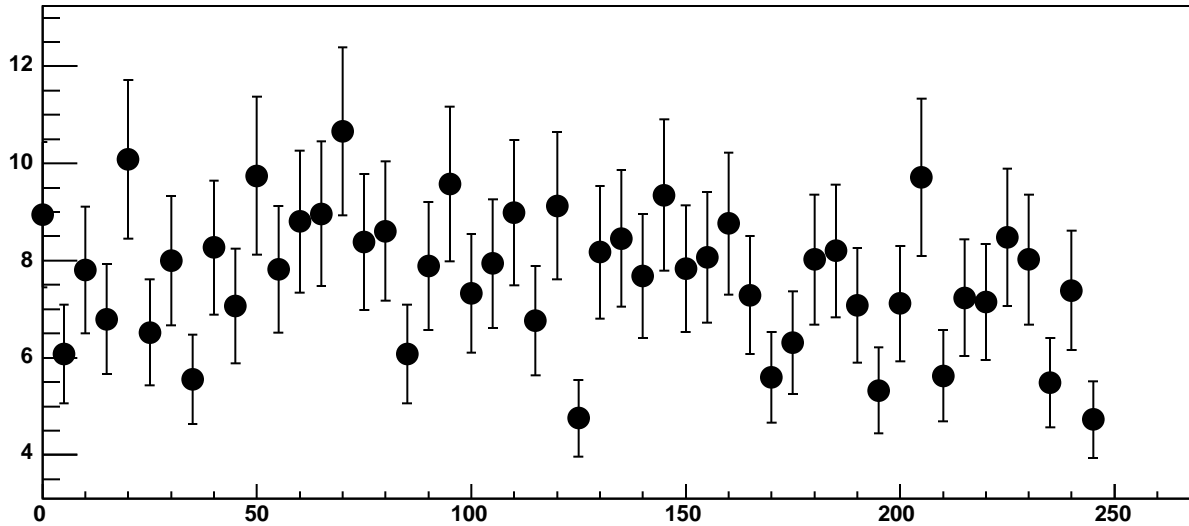


Chip 0, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

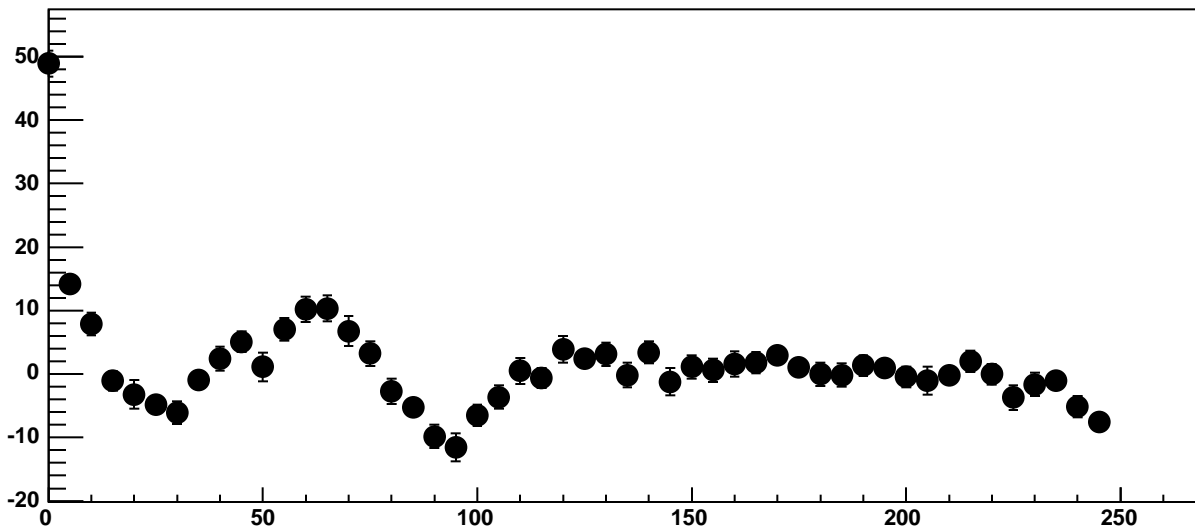


χ^2 / ndf	246.5 / 41
p0	96.29 ± 0.6165
p1	92.15 ± 0.5196
p2	$-6.498e+07 \pm 7.045e+05$
p3	$3.114e+07 \pm 1.705e+05$
p4	2.031 ± 0.01848

Chip 0, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold

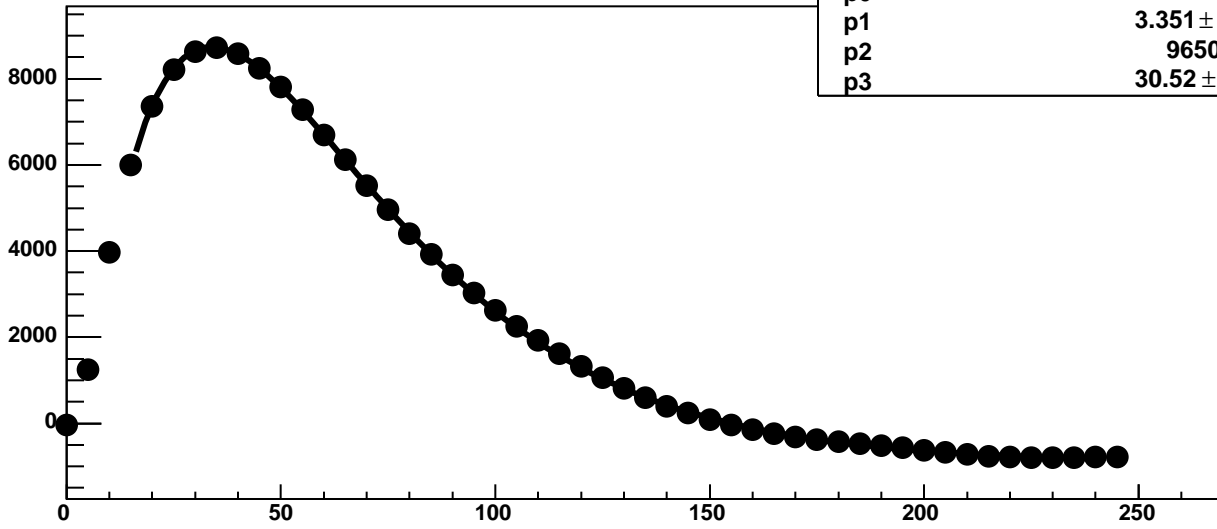


Chip 0, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

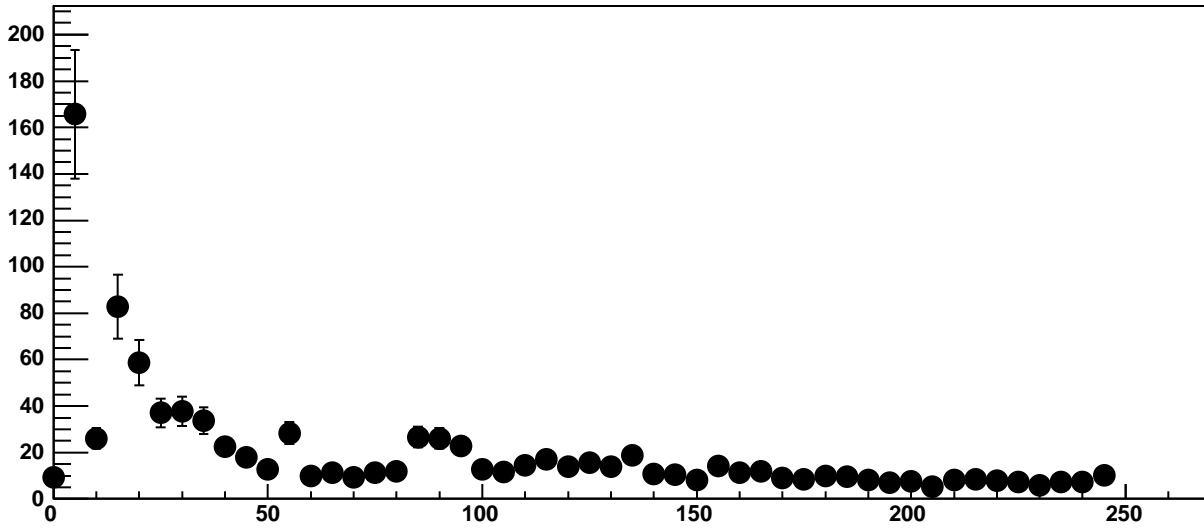


Chip 0, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold

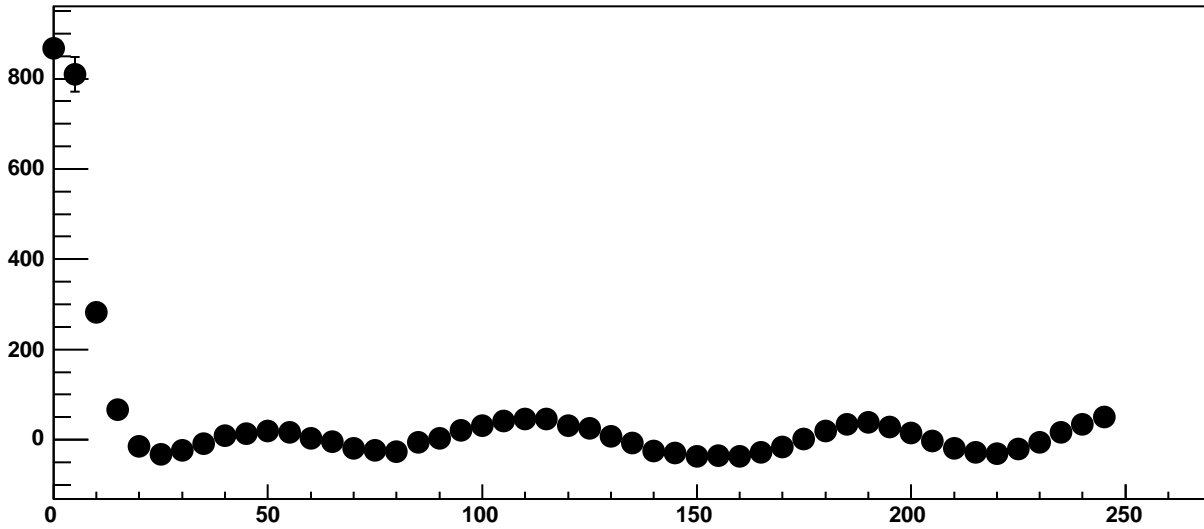
χ^2 / ndf	4722 / 42
p0	-909.1 ± 0.6269
p1	3.351 ± 0.02676
p2	9650 ± 2.389
p3	30.52 ± 0.01061



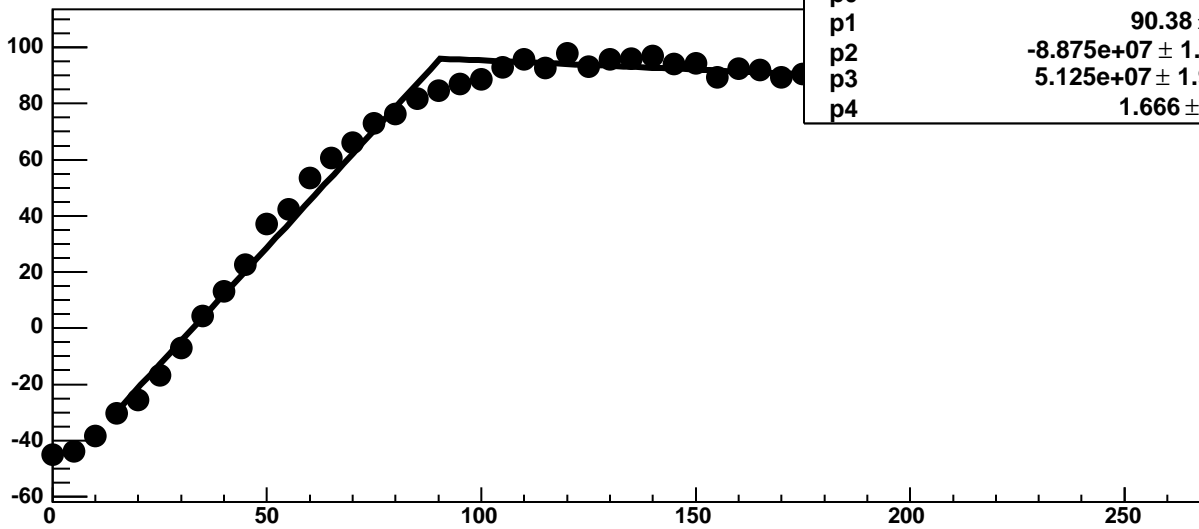
Chip 0, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold

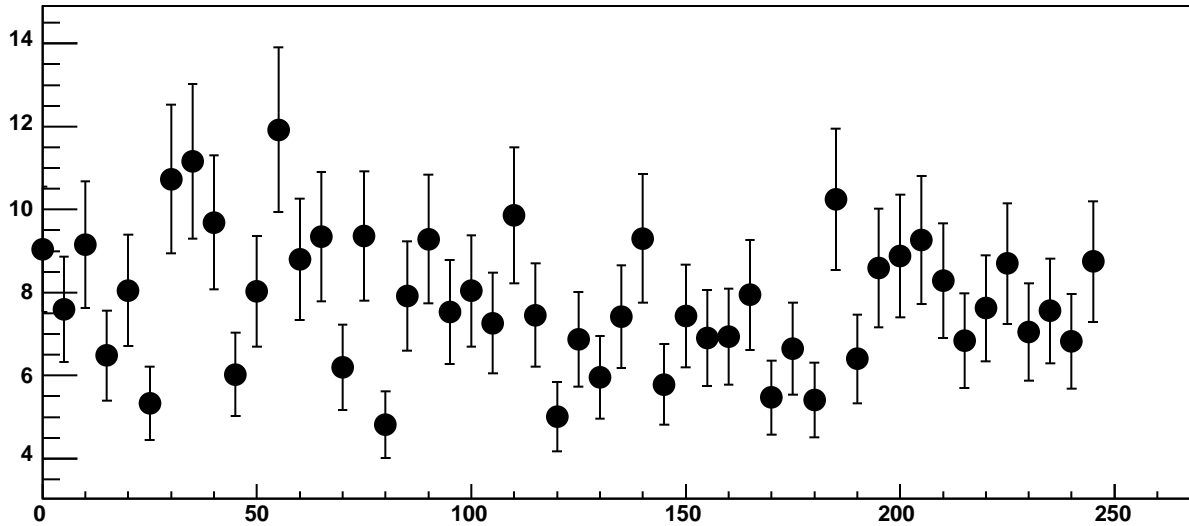


Chip 0, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

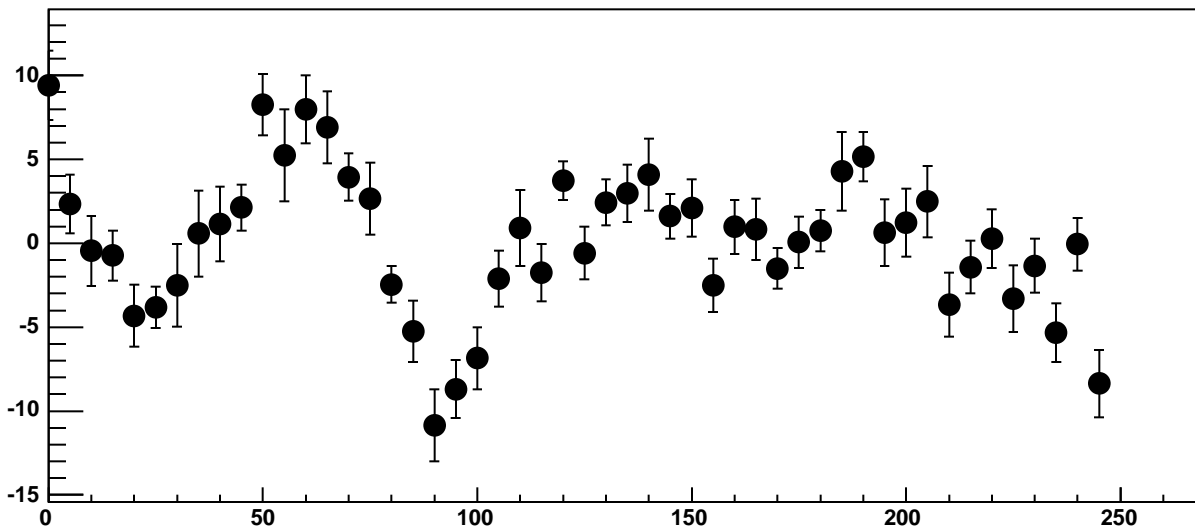


χ^2 / ndf	222.8 / 41
p0	96.01 ± 0.5881
p1	90.38 ± 0.5796
p2	$-8.875e+07 \pm 1.025e+06$
p3	$5.125e+07 \pm 1.922e+05$
p4	1.666 ± 0.01768

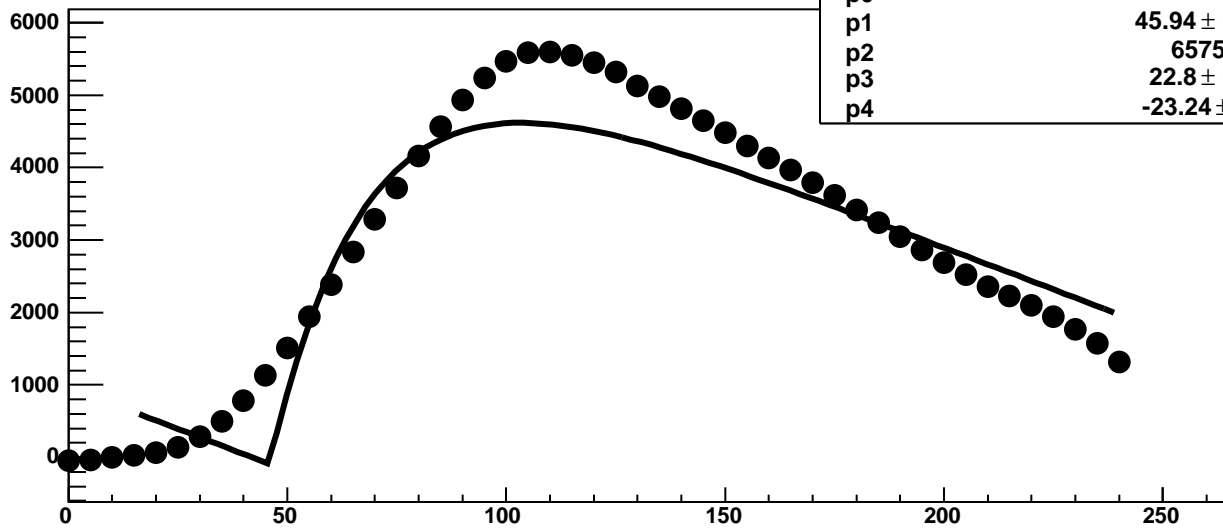
Chip 0, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

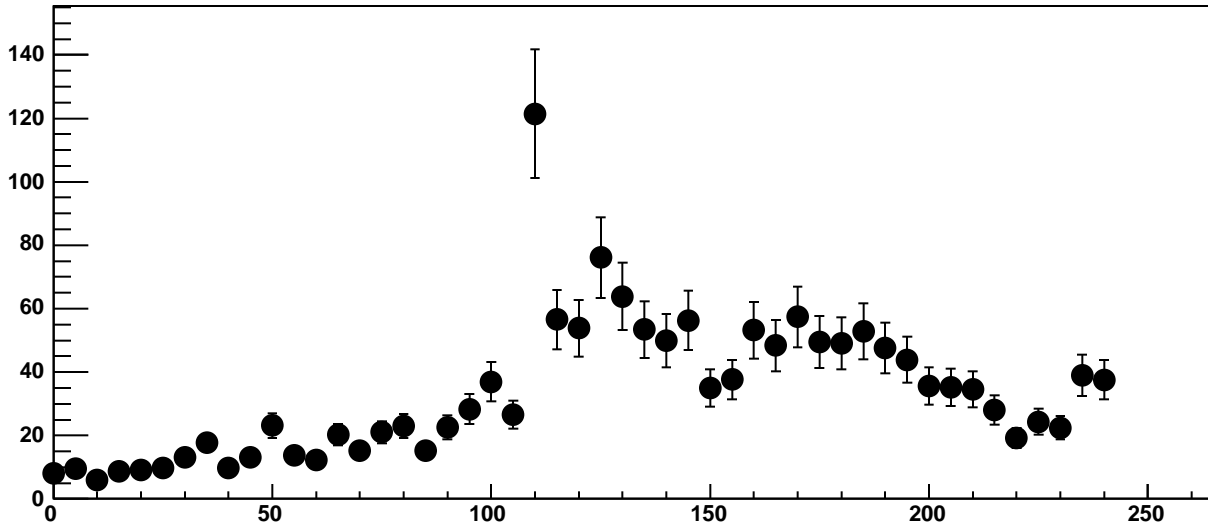


Chip 0, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold

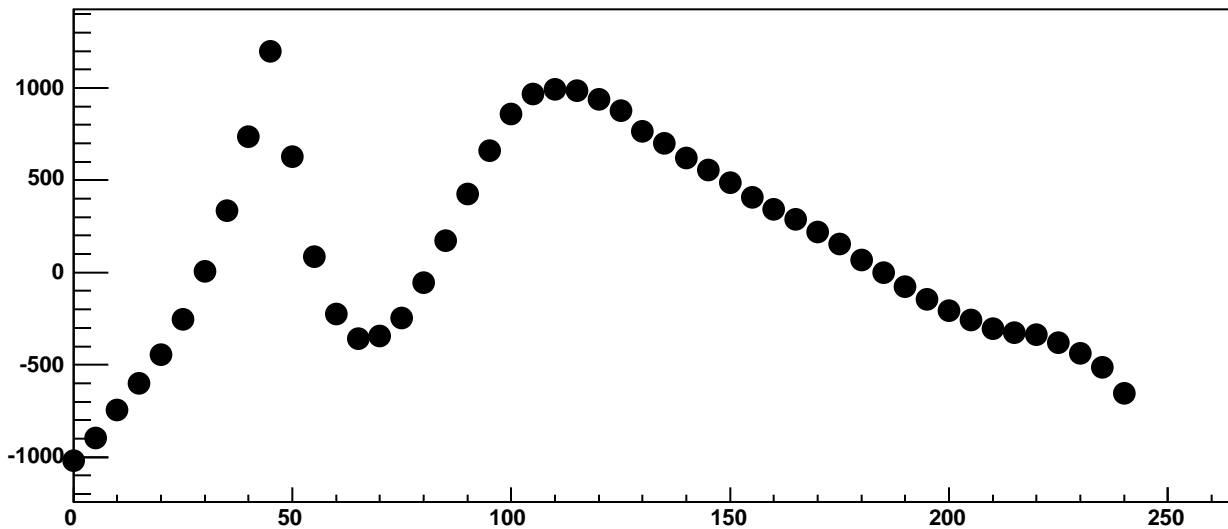


χ^2 / ndf	5.838e+05 / 41
p0	-92.42 ± 1.082
p1	45.94 ± 0.01732
p2	6575 ± 6.181
p3	22.8 ± 0.03778
p4	-23.24 ± 0.0357

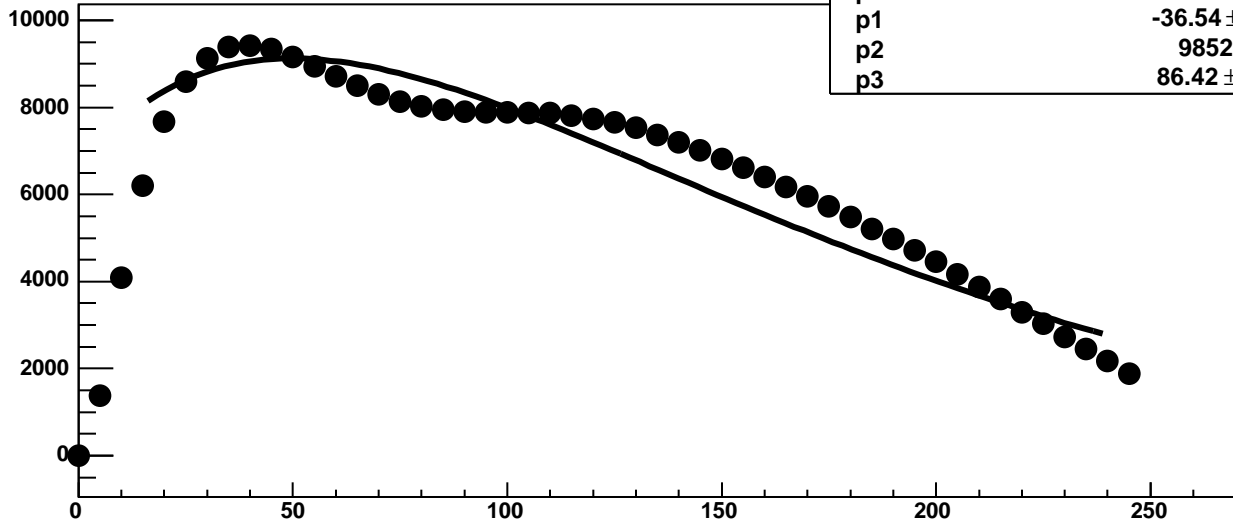
Chip 0, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold



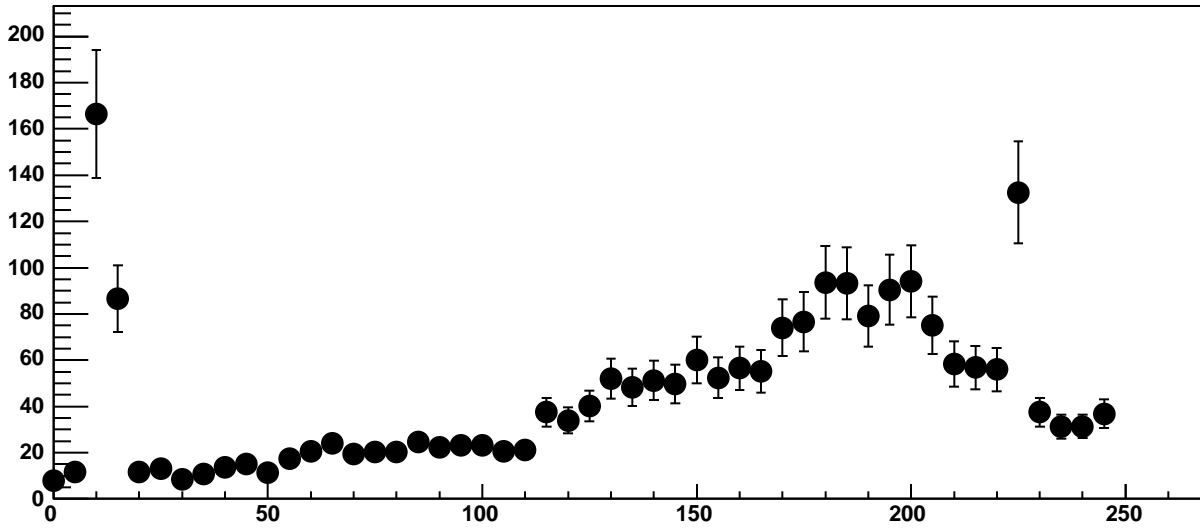
Chip 0, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold



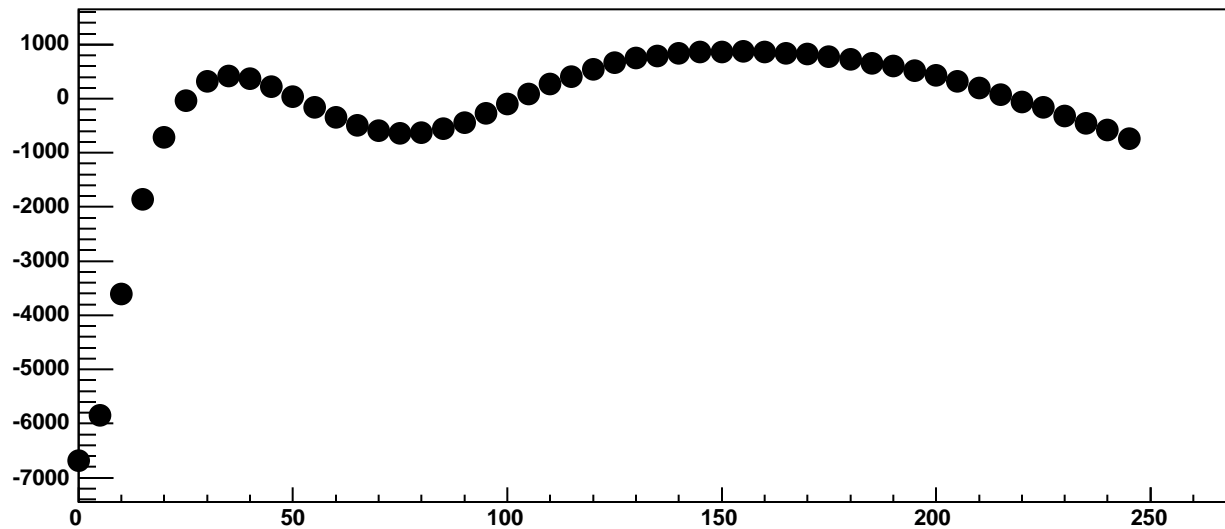
Chip 0, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold



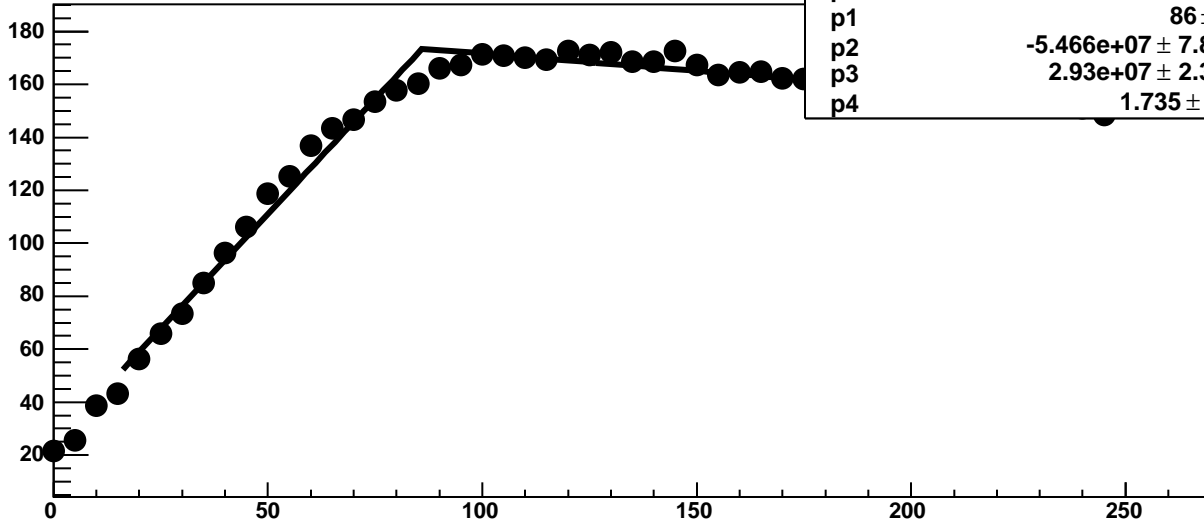
Chip 0, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold

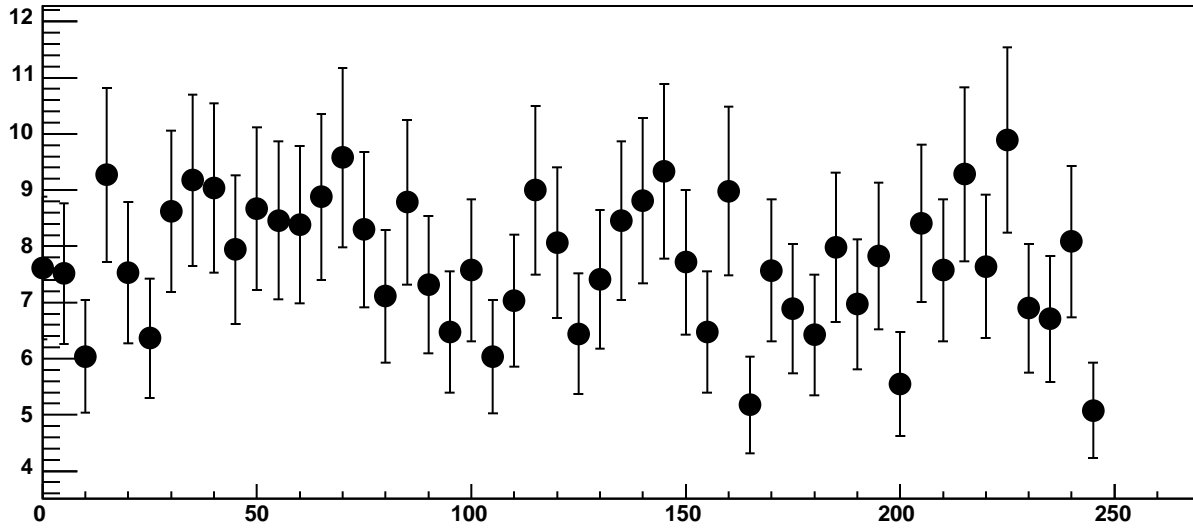


Chip 0, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

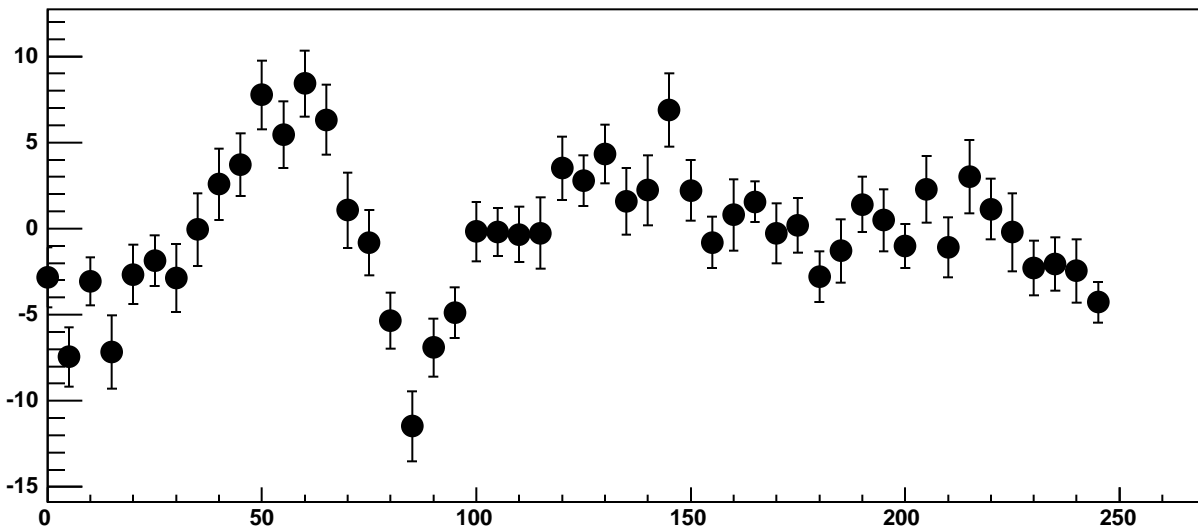


χ^2 / ndf	191.4 / 41
p0	173.5 ± 0.5666
p1	86 ± 0.6047
p2	$-5.466\text{e}+07 \pm 7.813\text{e}+05$
p3	$2.93\text{e}+07 \pm 2.364\text{e}+05$
p4	1.735 ± 0.02218

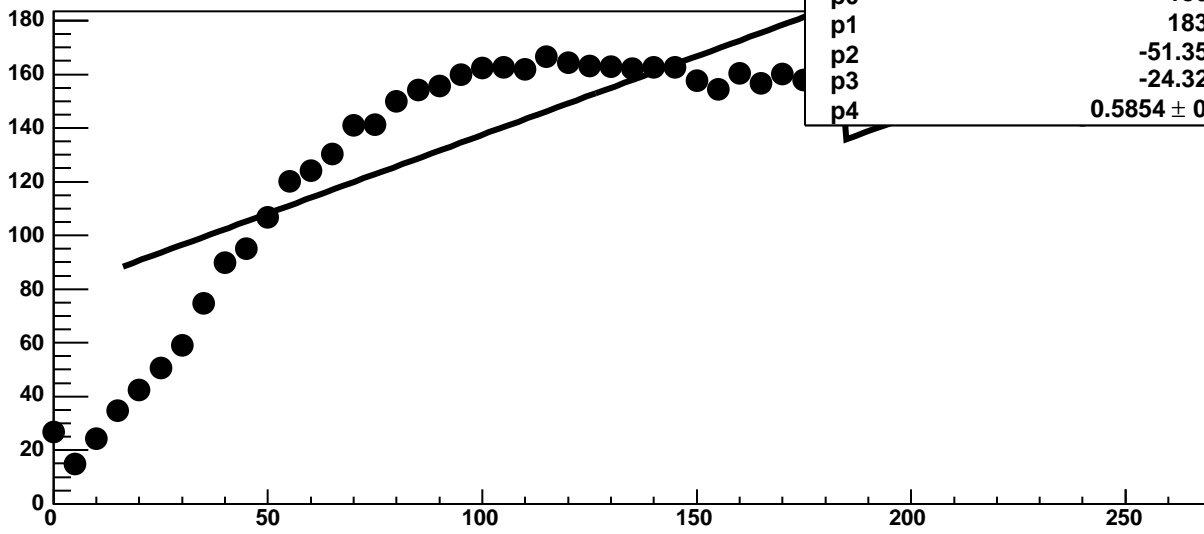
Chip 0, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



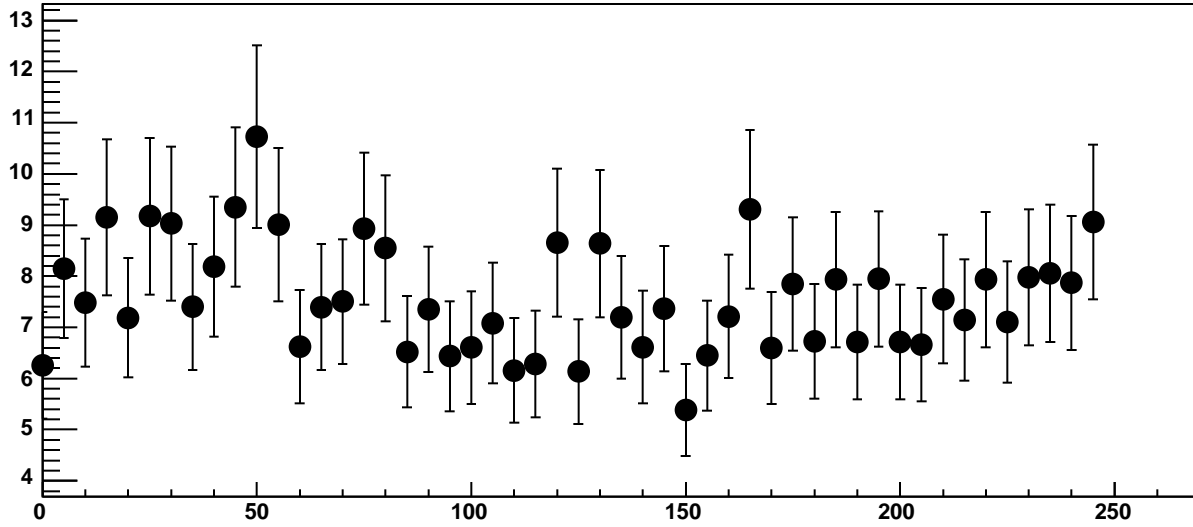
Chip 0, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold



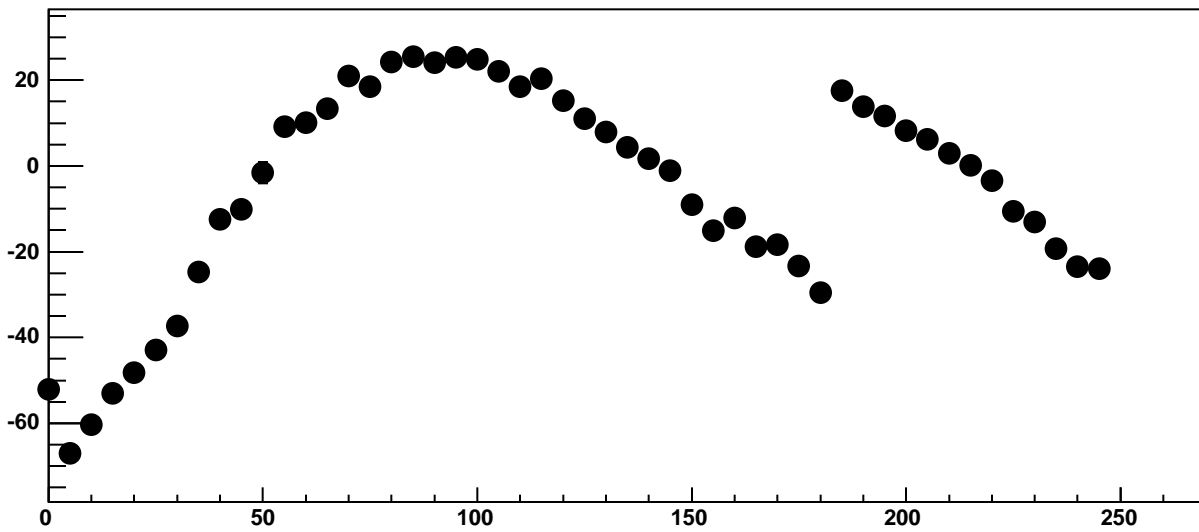
Chip 0, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold



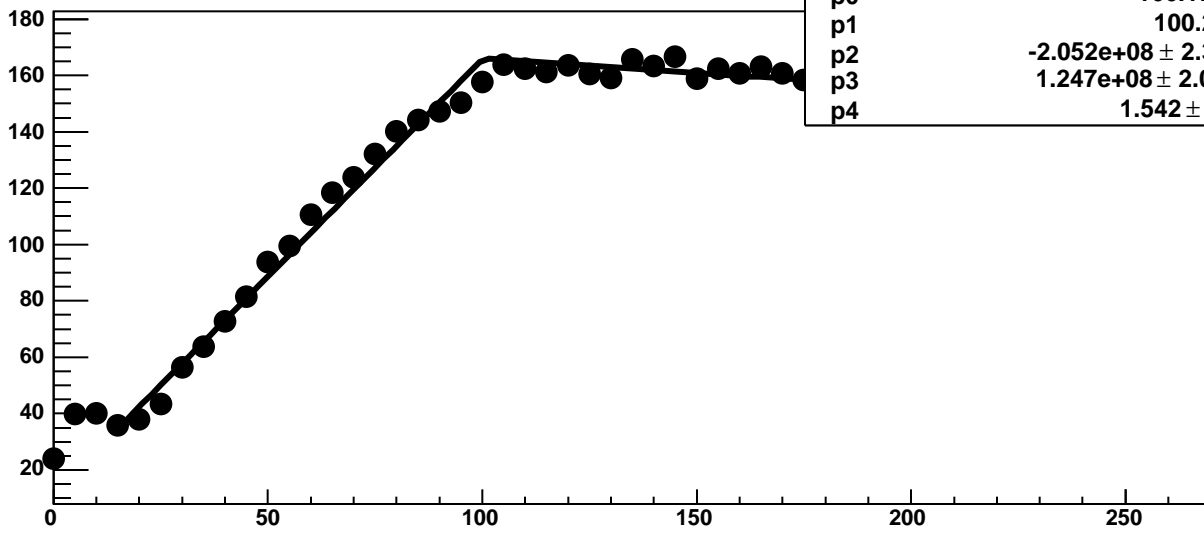
Chip 0, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

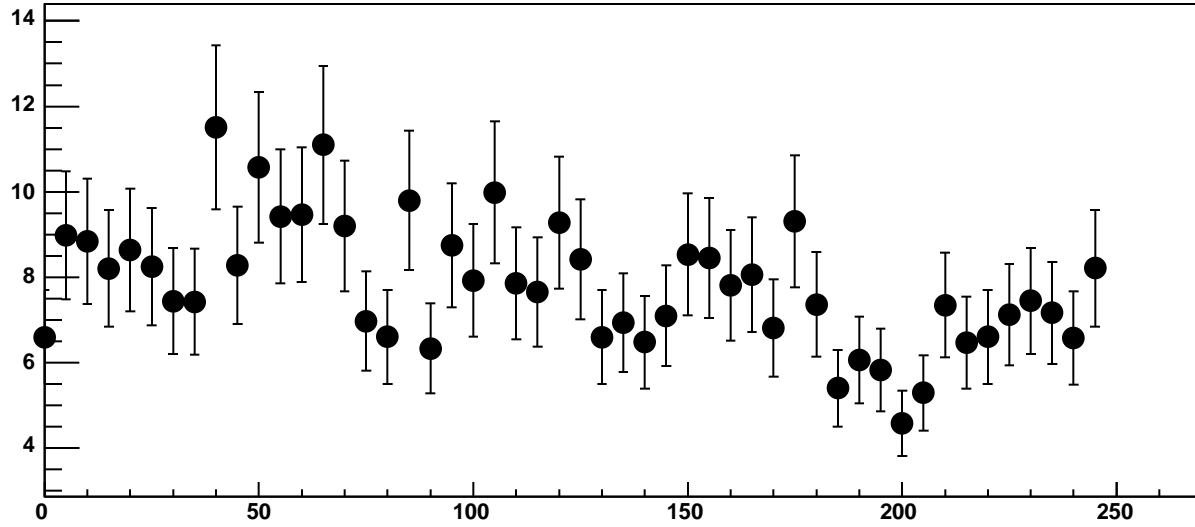


Chip 0, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold

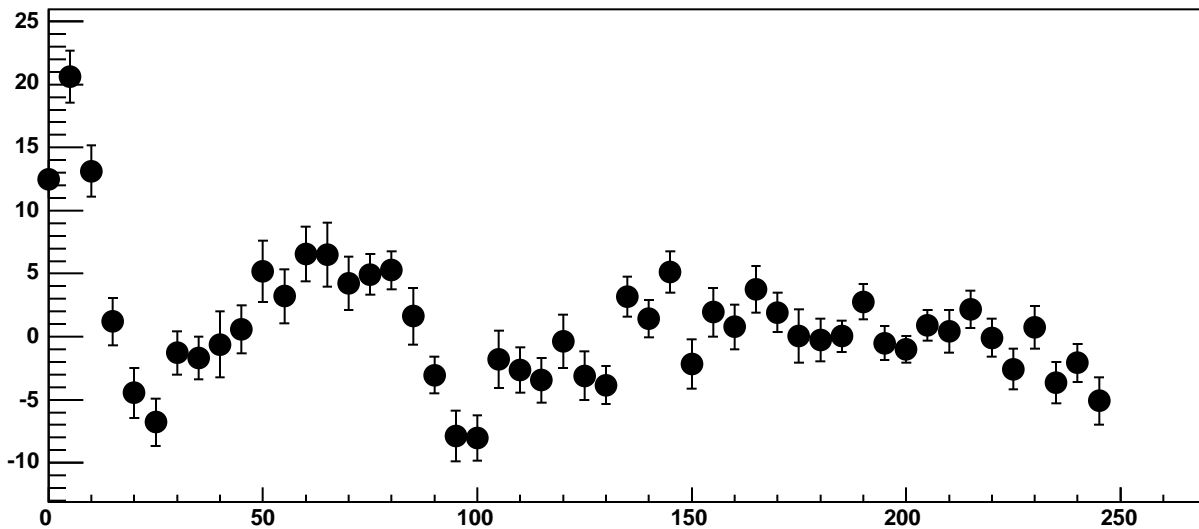


χ^2 / ndf	163.8 / 41
p0	166.1 ± 0.6523
p1	100.2 ± 0.66
p2	$-2.052\text{e}+08 \pm 2.308\text{e}+06$
p3	$1.247\text{e}+08 \pm 2.019\text{e}+05$
p4	1.542 ± 0.01679

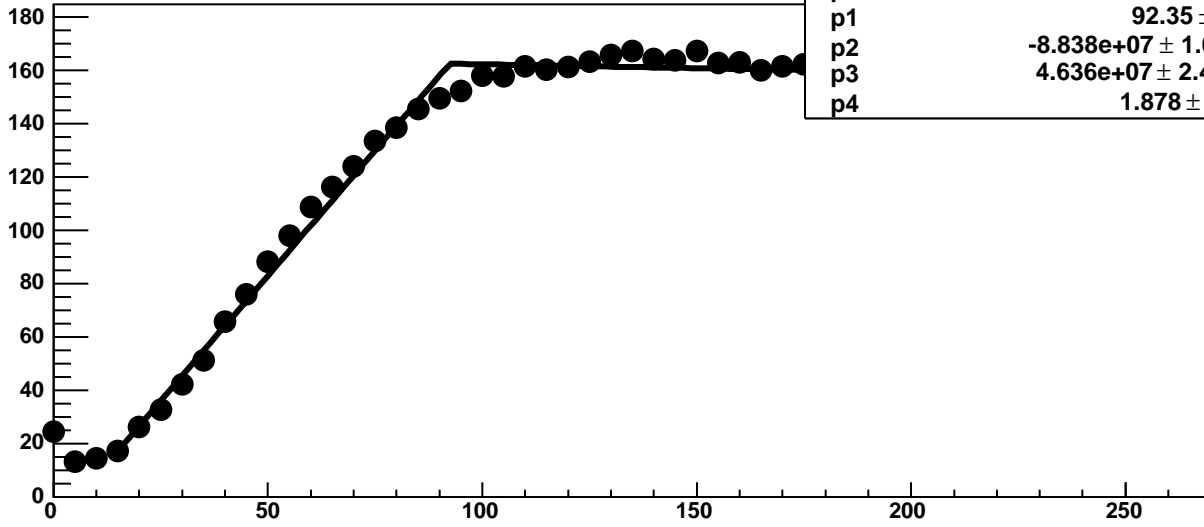
Chip 0, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold



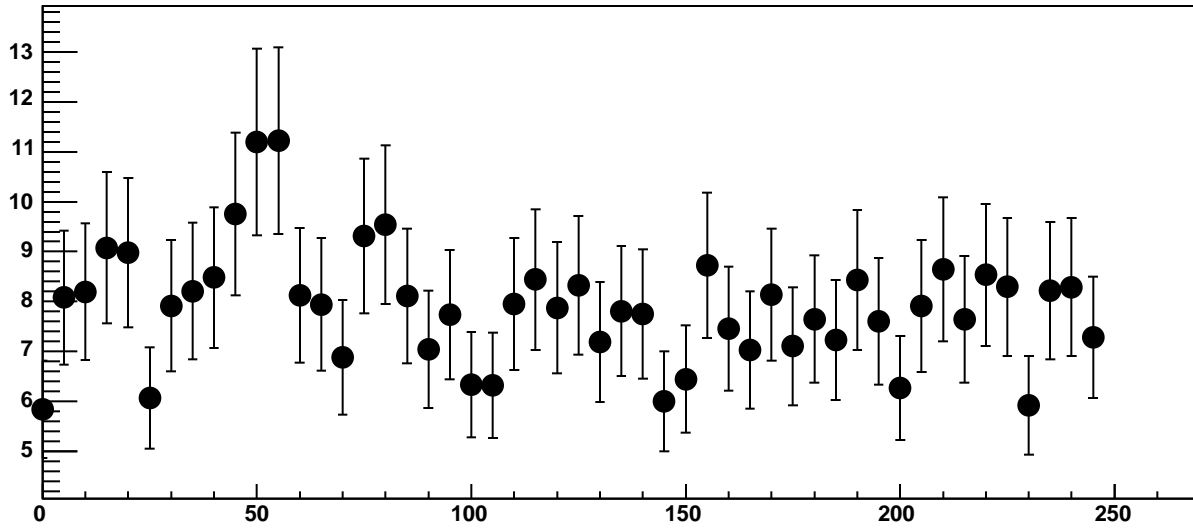
Chip 0, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold



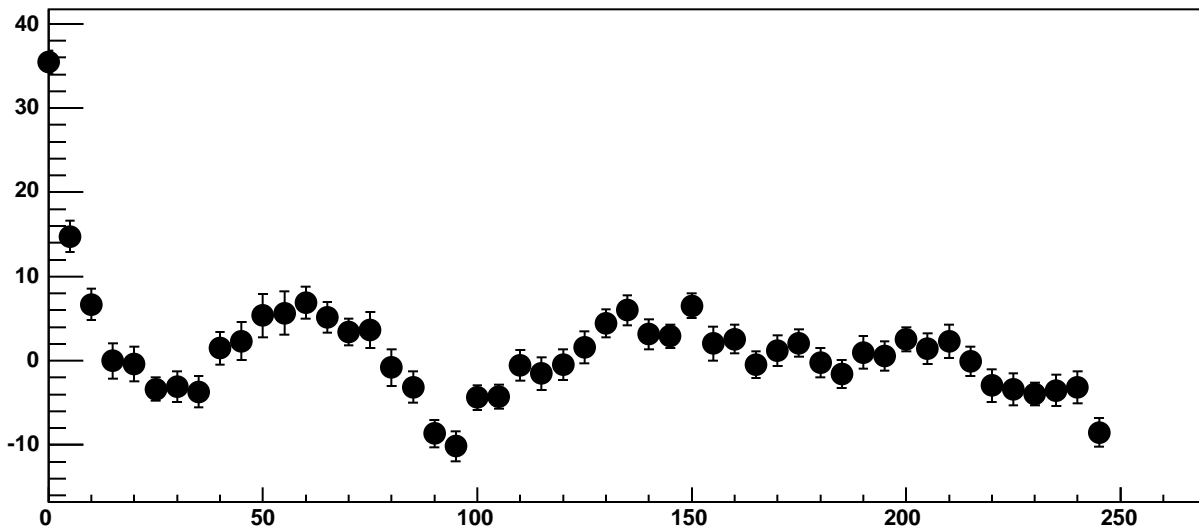
Chip 0, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold



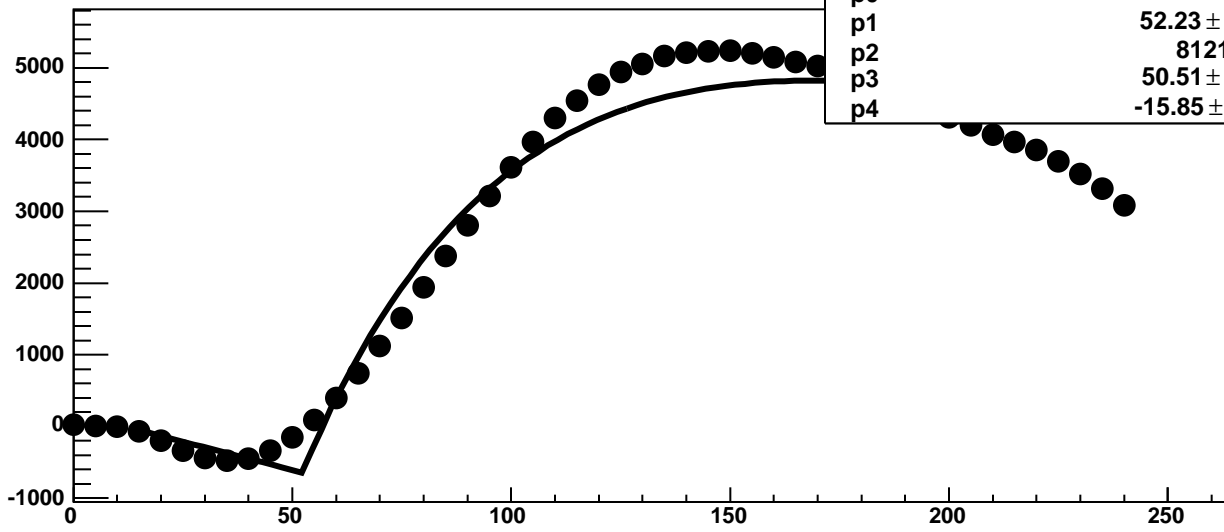
Chip 0, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

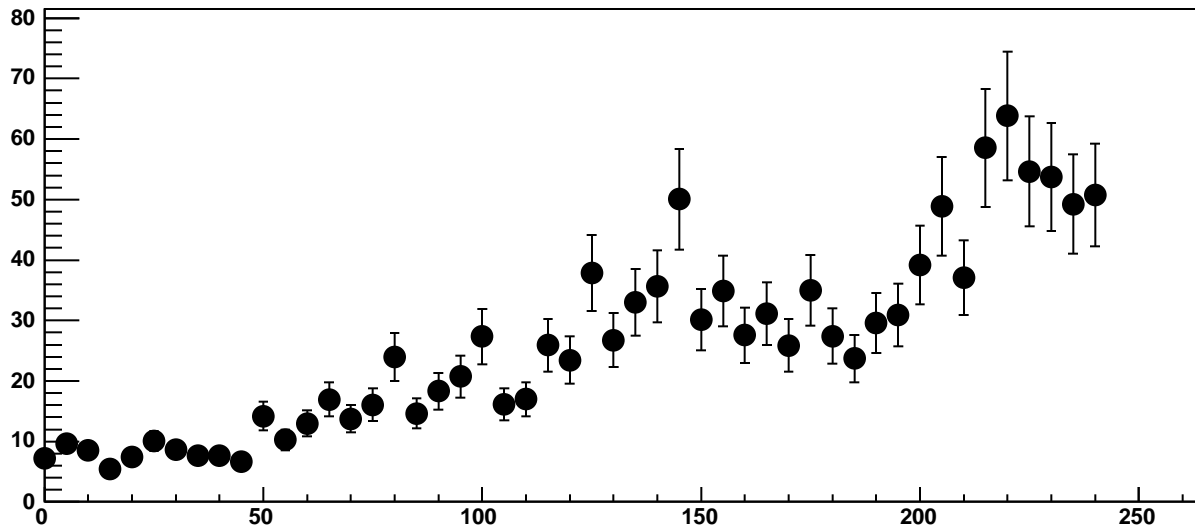


Chip 0, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold

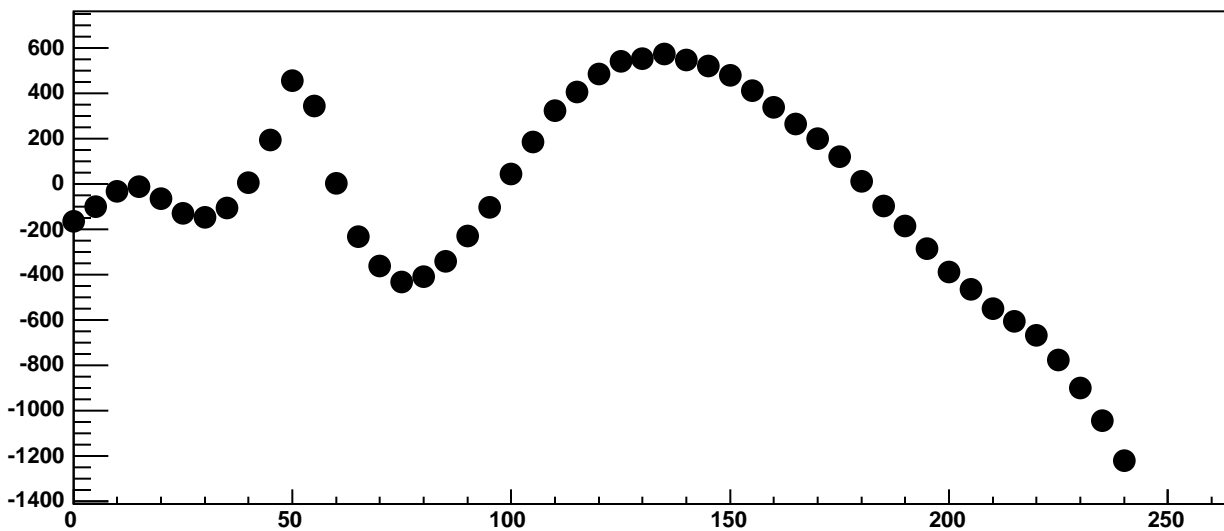


χ^2 / ndf	2.232e+05 / 41
p0	-642.2 ± 1.108
p1	52.23 ± 0.01529
p2	8121 ± 9.429
p3	50.51 ± 0.06536
p4	-15.85 ± 0.04621

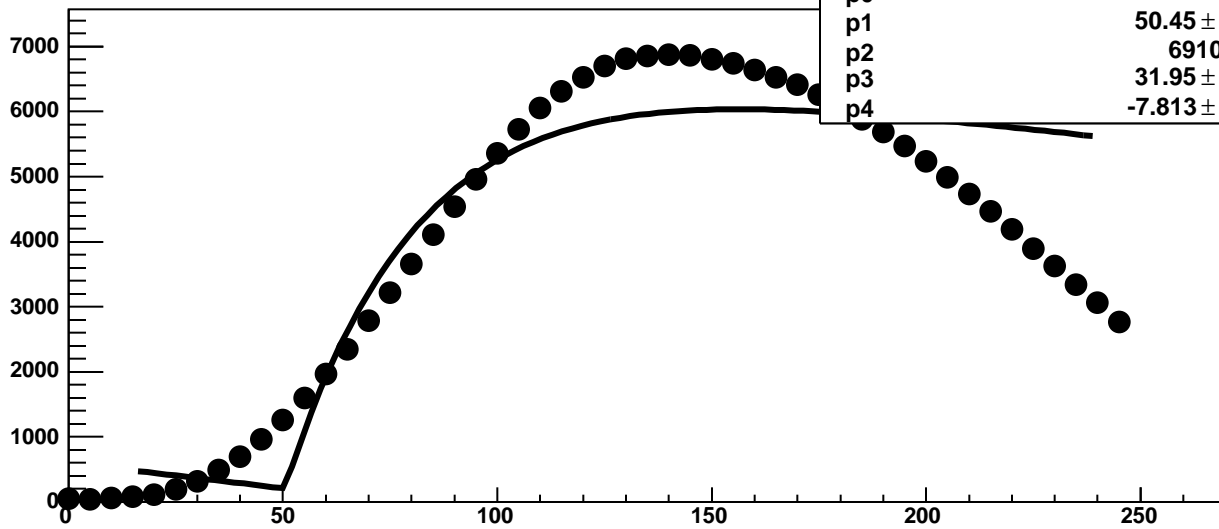
Chip 0, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

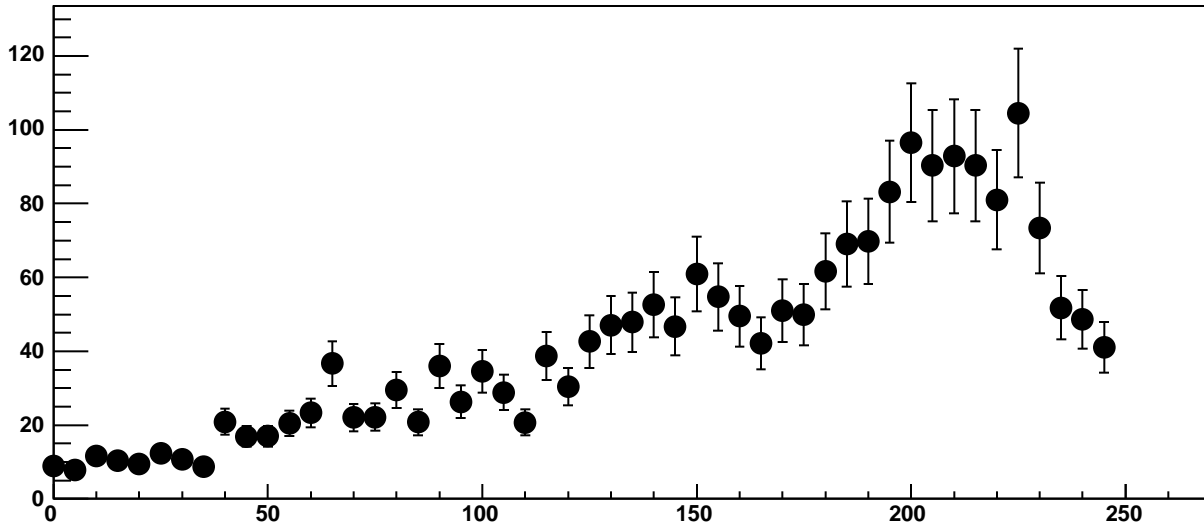


Chip 0, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

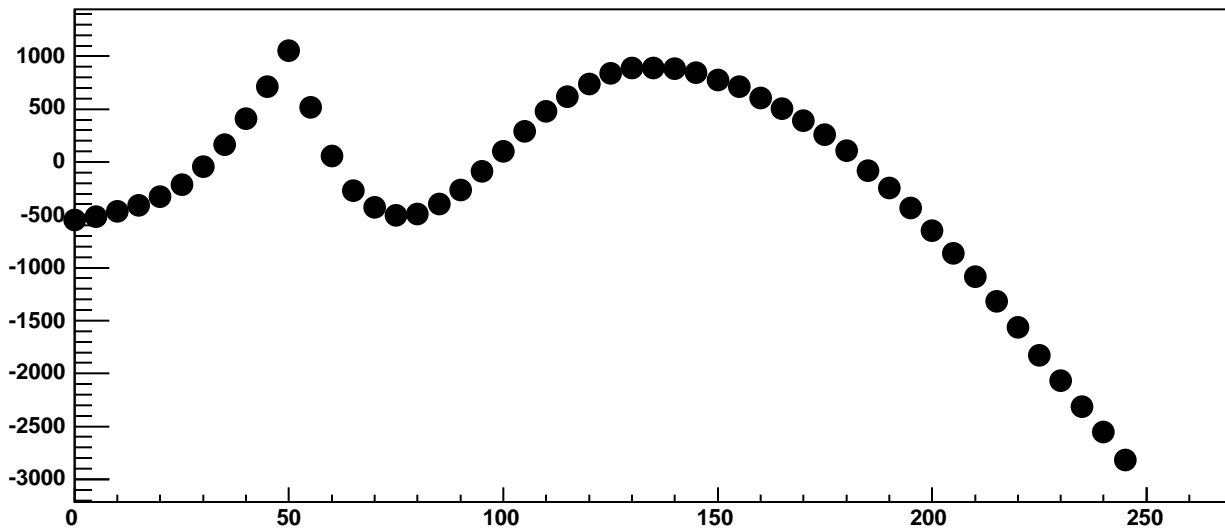


χ^2 / ndf	4.261e+05 / 41
p0	205.3 ± 1.525
p1	50.45 ± 0.02293
p2	6910 ± 9.413
p3	31.95 ± 0.05847
p4	-7.813 ± 0.05812

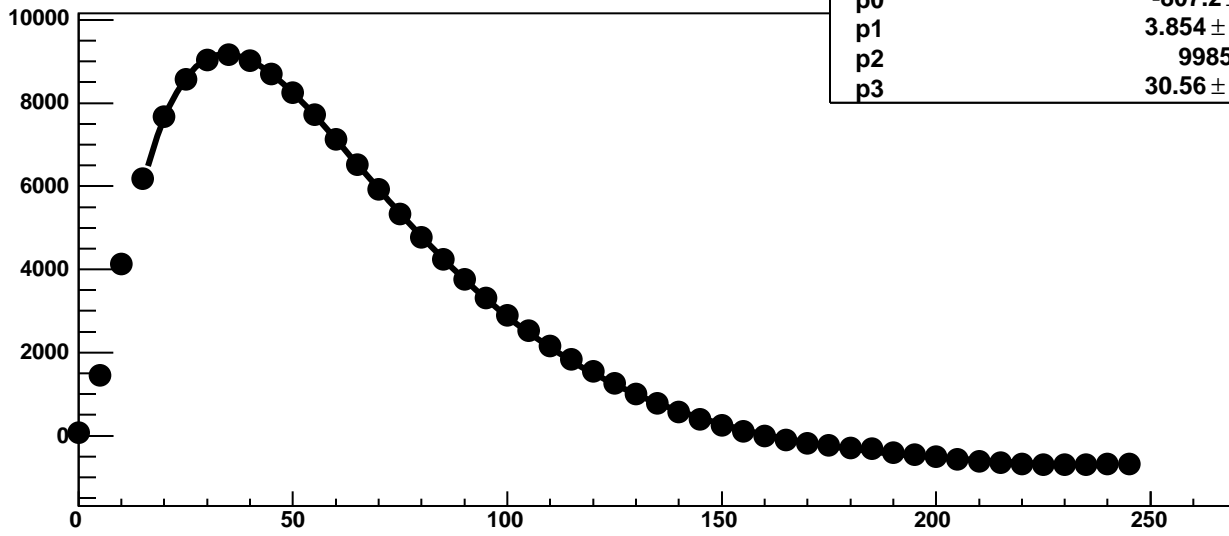
Chip 0, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

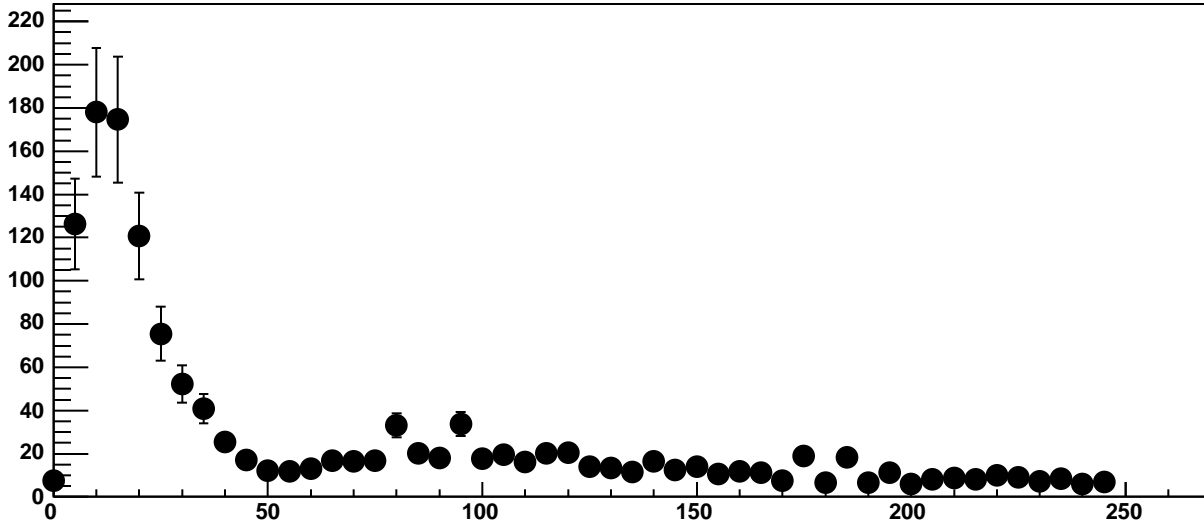


Chip 0, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold

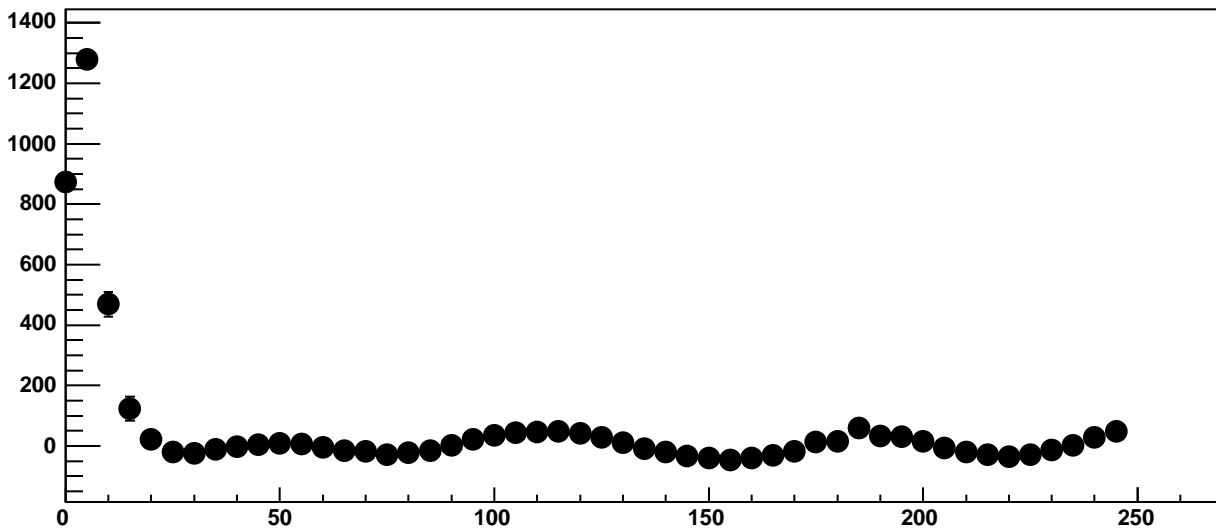


χ^2 / ndf	4258 / 42
p0	-807.2 ± 0.7262
p1	3.854 ± 0.04108
p2	9985 ± 3.516
p3	30.56 ± 0.01308

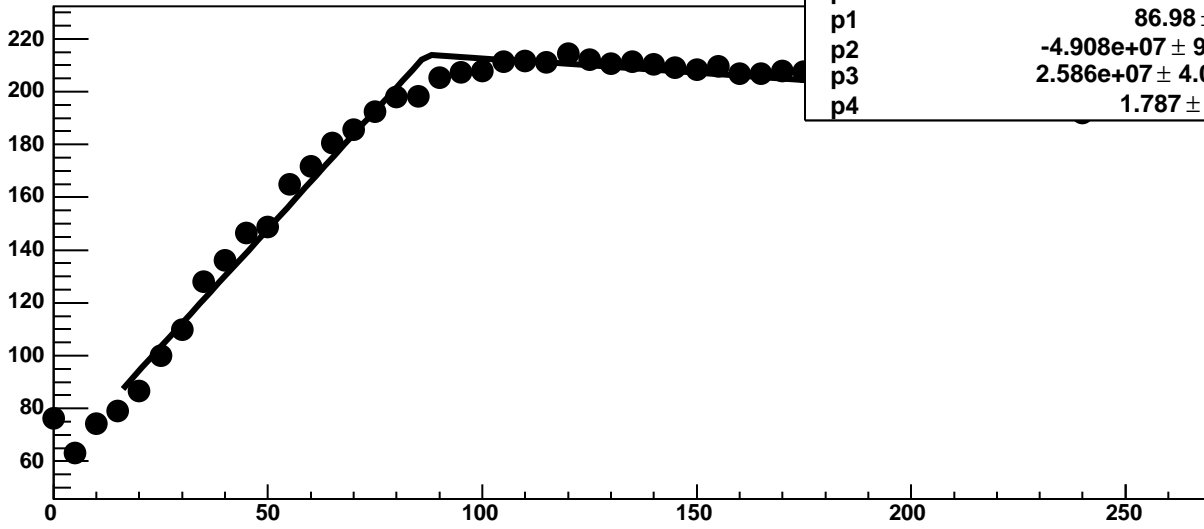
Chip 0, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold

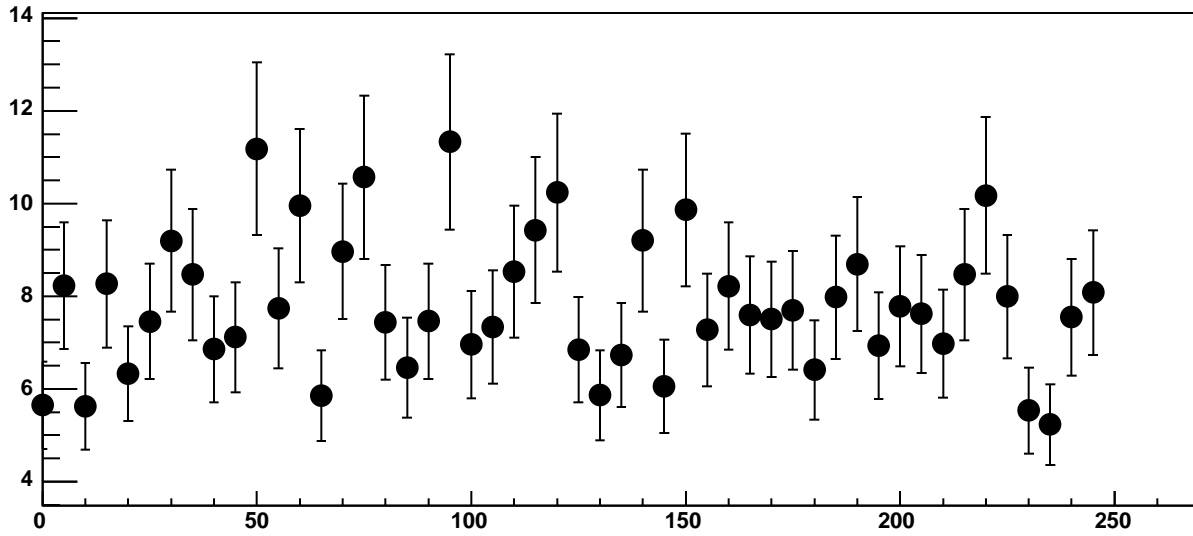


Chip 0, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

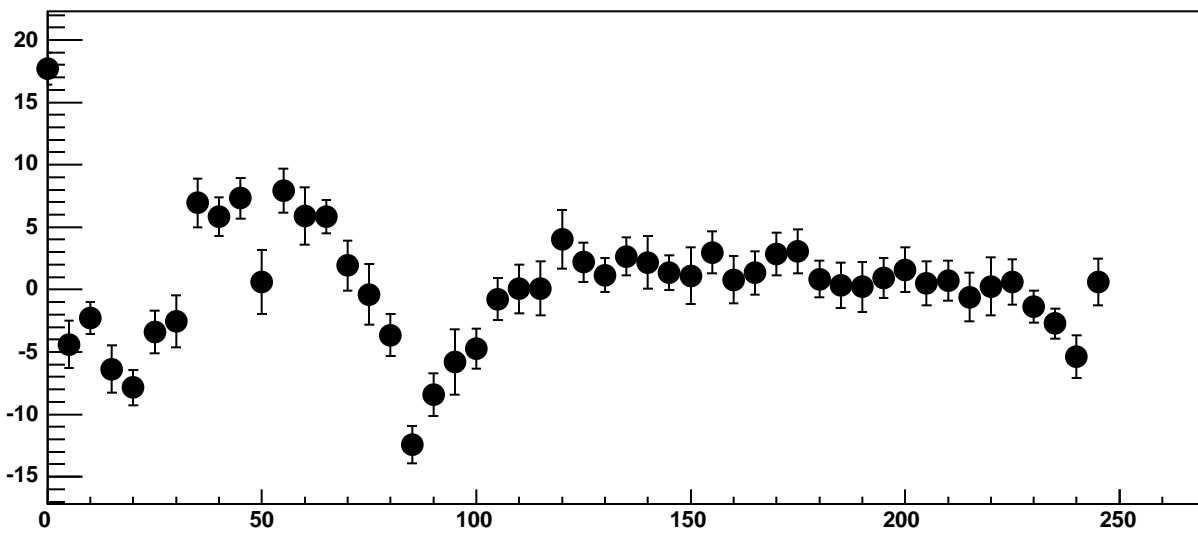


χ^2 / ndf	292.2 / 41
p0	214.1 ± 0.6044
p1	86.98 ± 0.5912
p2	$-4.908\text{e}+07 \pm 9.66\text{e}+05$
p3	$2.586\text{e}+07 \pm 4.071\text{e}+05$
p4	1.787 ± 0.02049

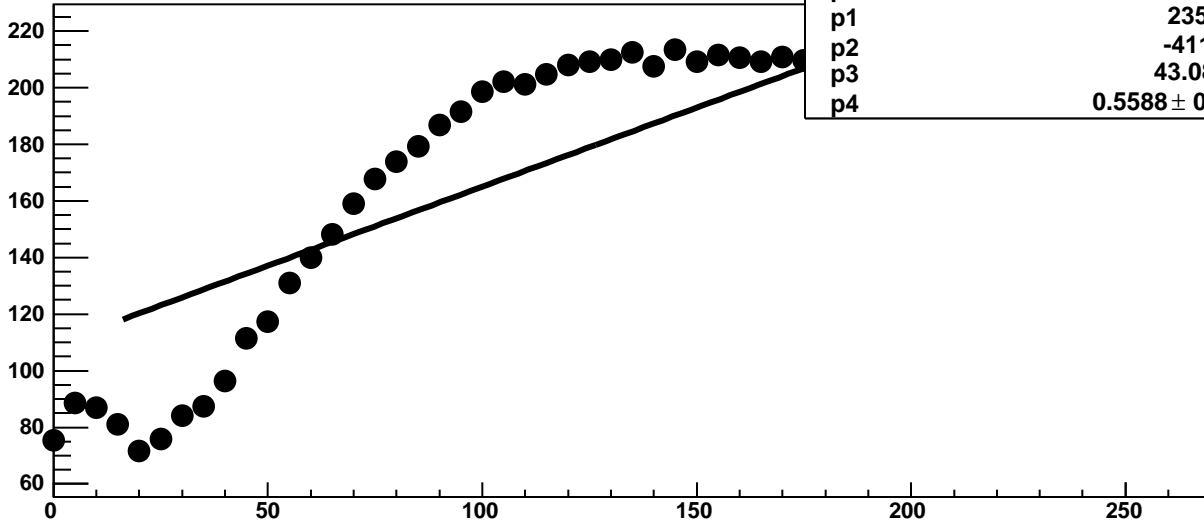
Chip 0, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

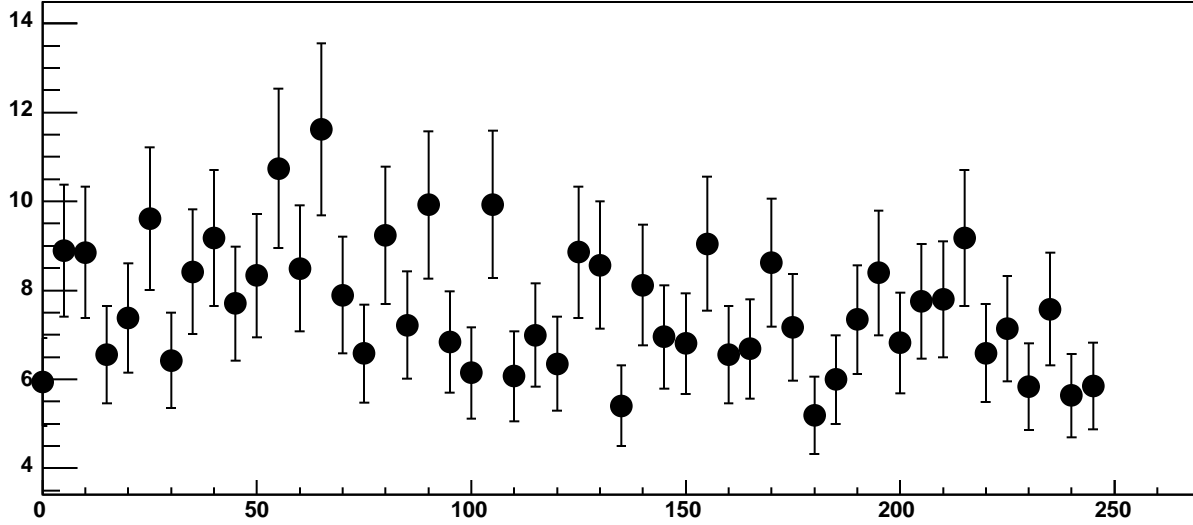


Chip 0, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

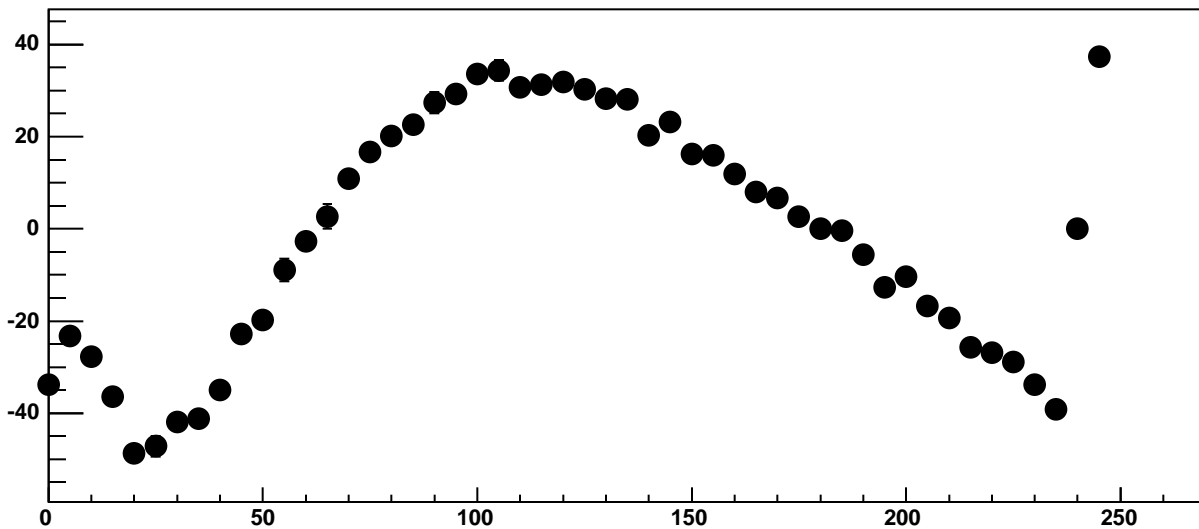


χ^2 / ndf	1.052e+04 / 41
p0	240.5 ± 1.926
p1	235 ± 3.345
p2	-411 ± 212.4
p3	43.08 ± 17.3
p4	0.5588 ± 0.003914

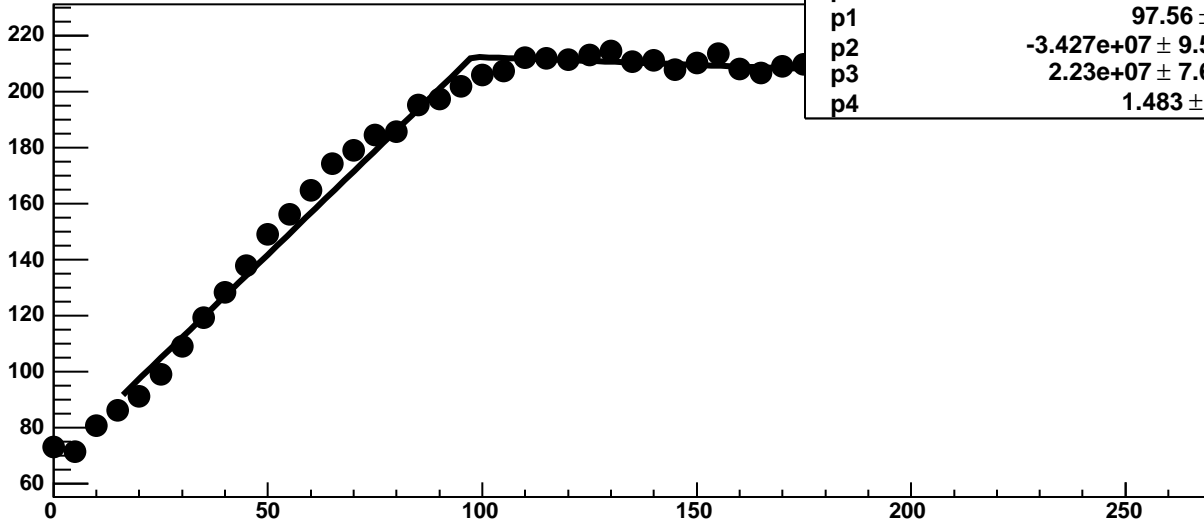
Chip 0, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

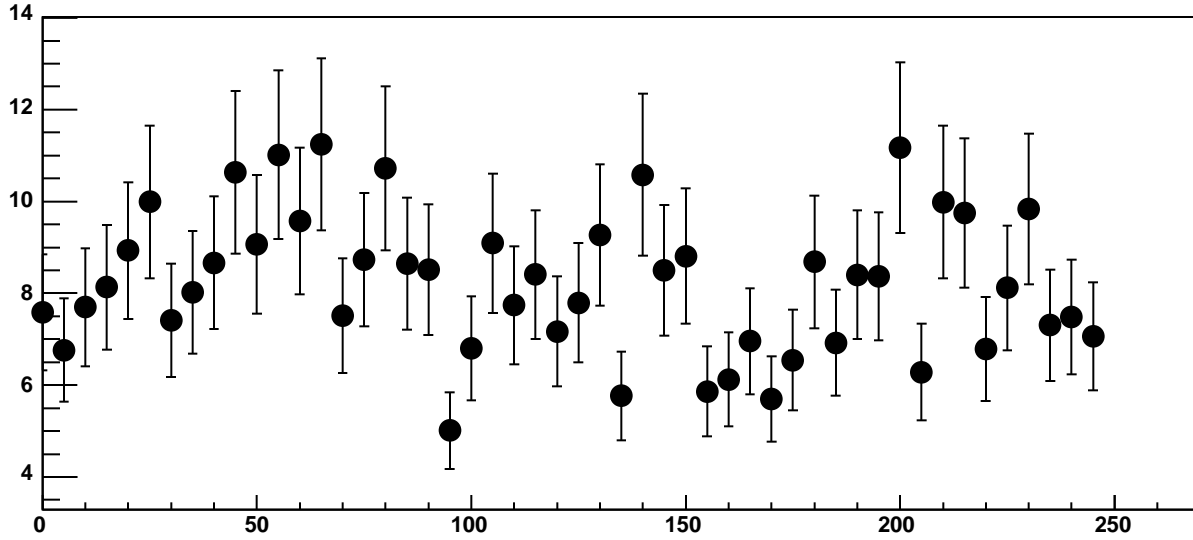


Chip 0, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold

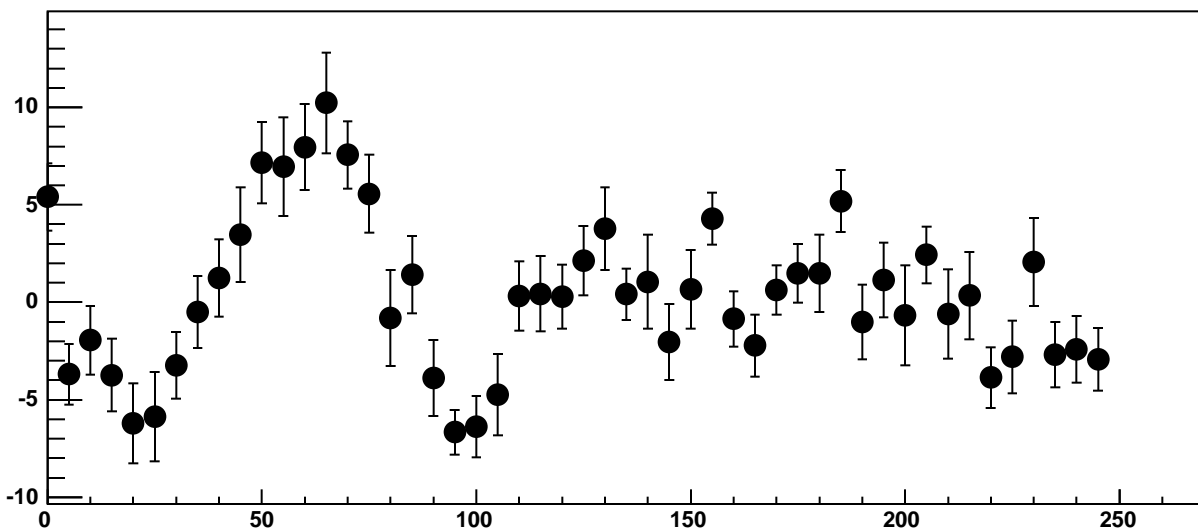


χ^2 / ndf	209.7 / 41
p0	212.5 ± 0.6103
p1	97.56 ± 0.6145
p2	$-3.427\text{e}+07 \pm 9.539\text{e}+05$
p3	$2.23\text{e}+07 \pm 7.654\text{e}+05$
p4	1.483 ± 0.01631

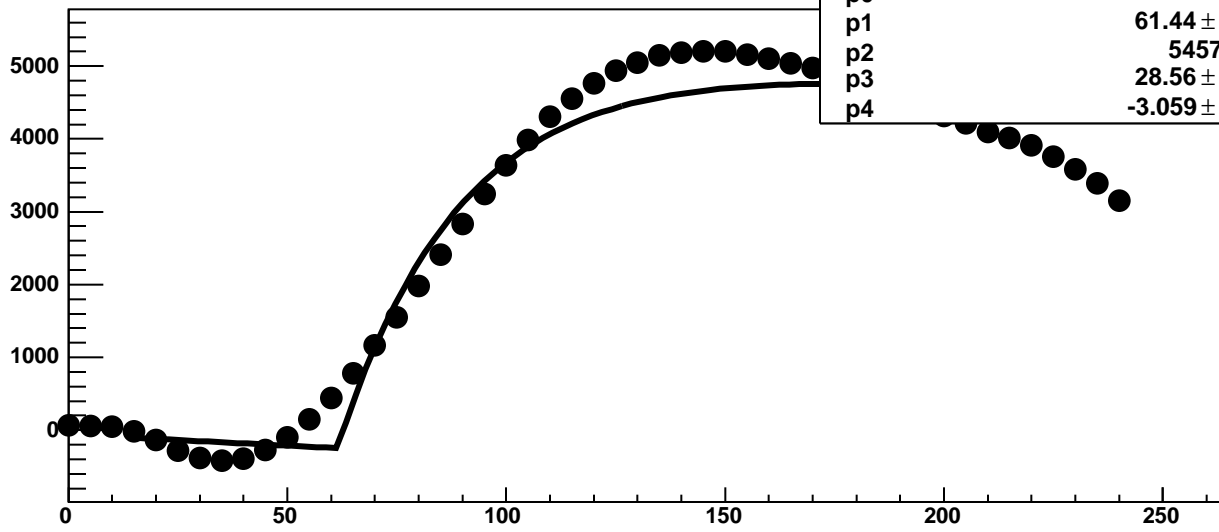
Chip 0, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold

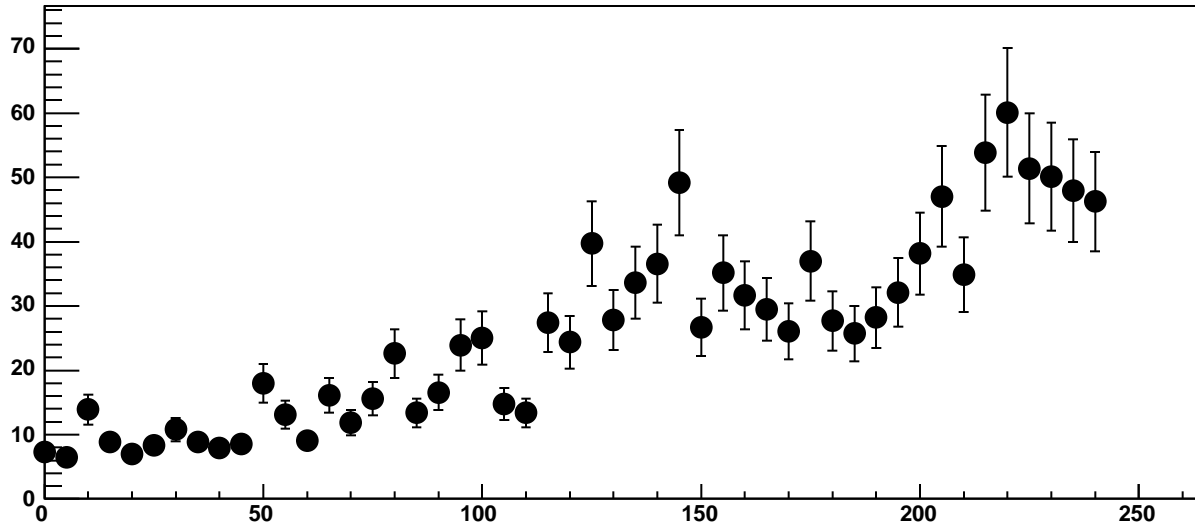


Chip 0, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

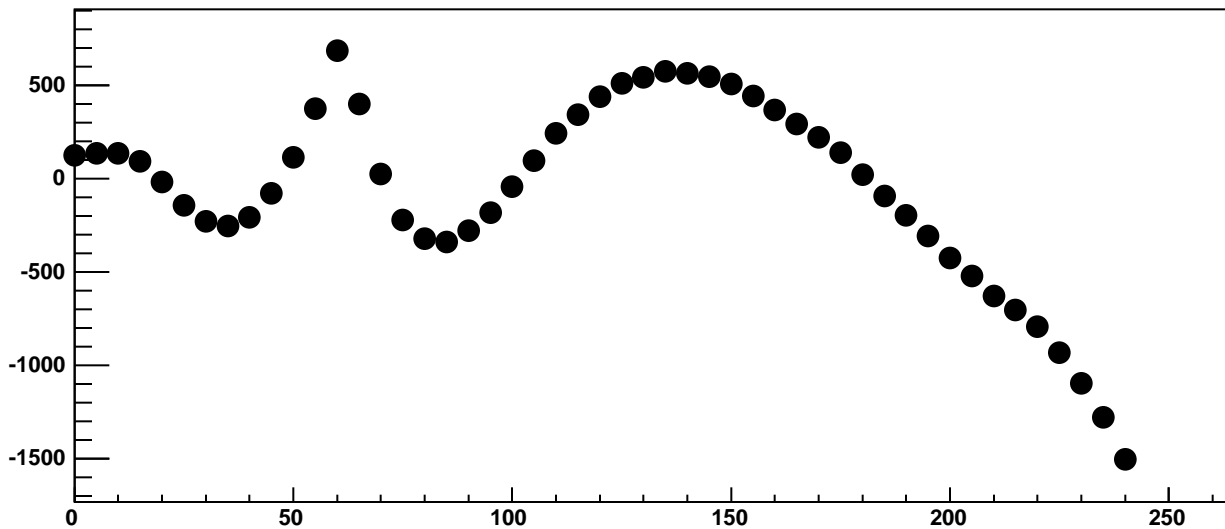


χ^2 / ndf	3.333e+05 / 41
p0	-247 ± 1.167
p1	61.44 ± 0.01848
p2	5457 ± 5.943
p3	28.56 ± 0.04574
p4	-3.059 ± 0.03663

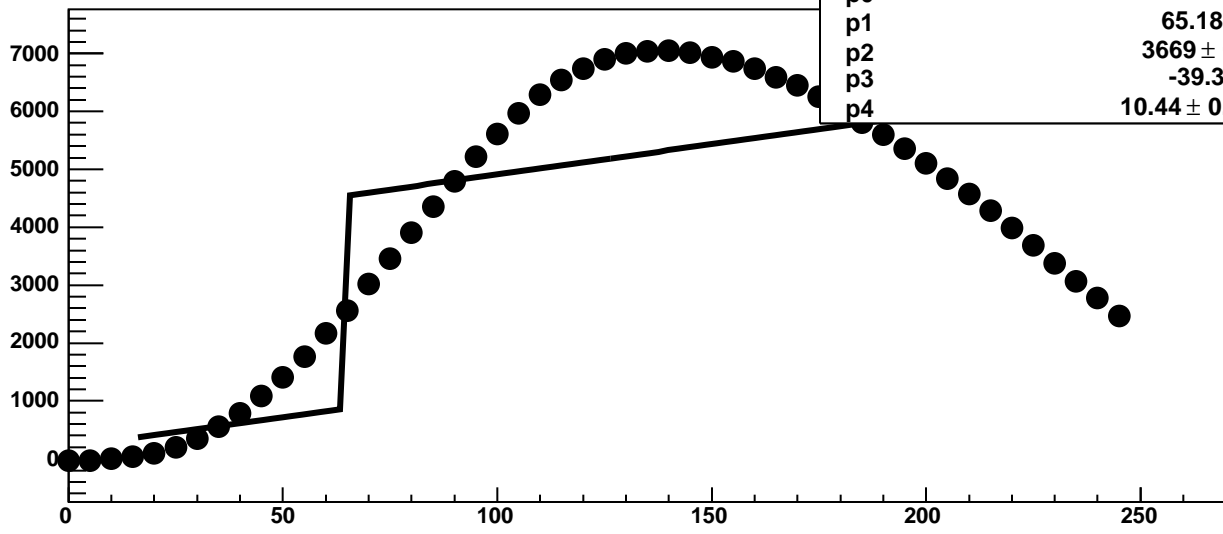
Chip 0, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold



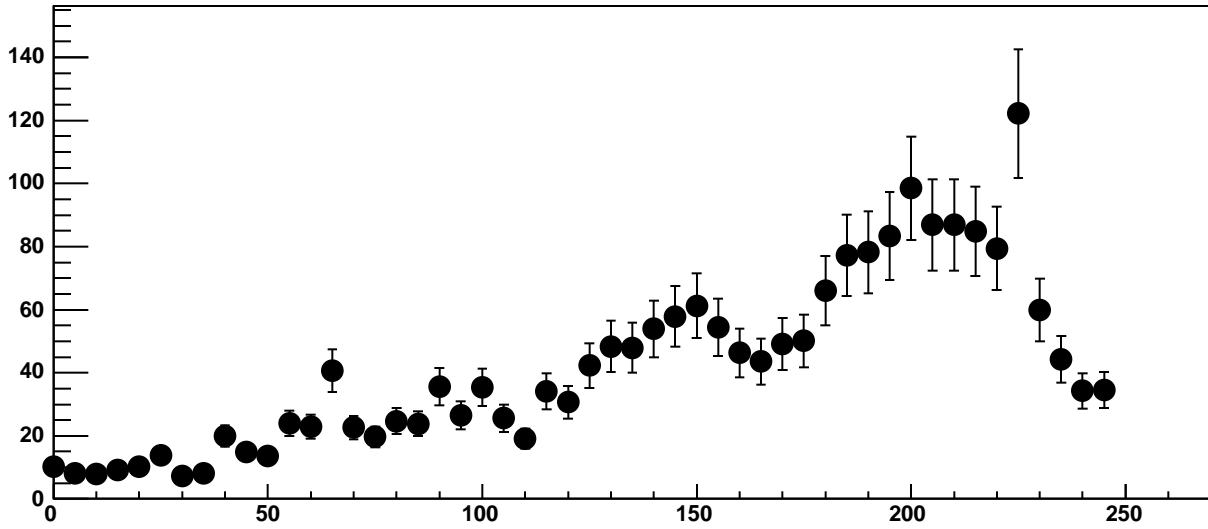
Chip 0, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold



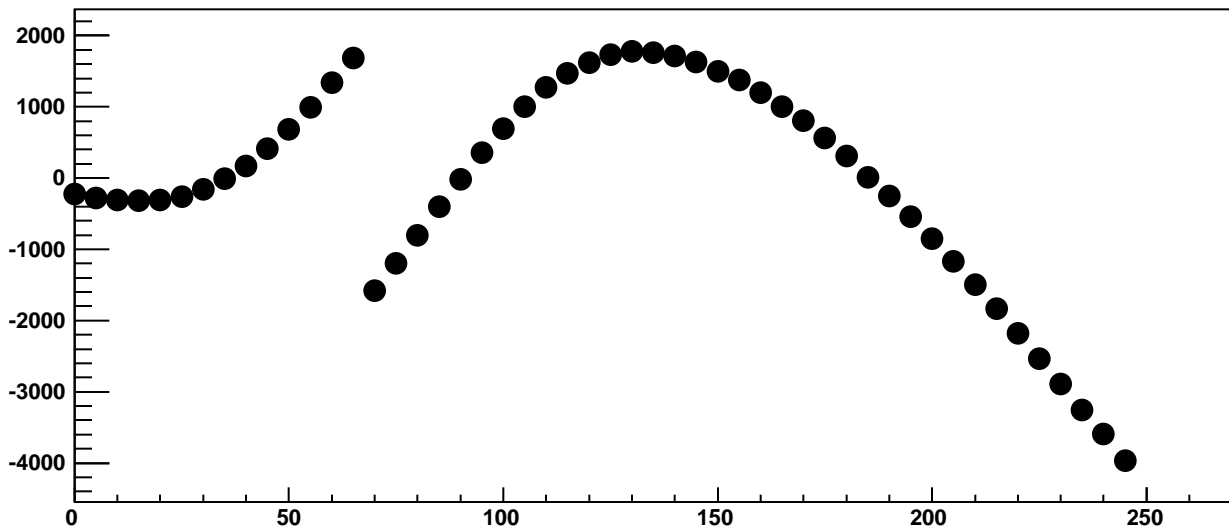
Chip 0, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold



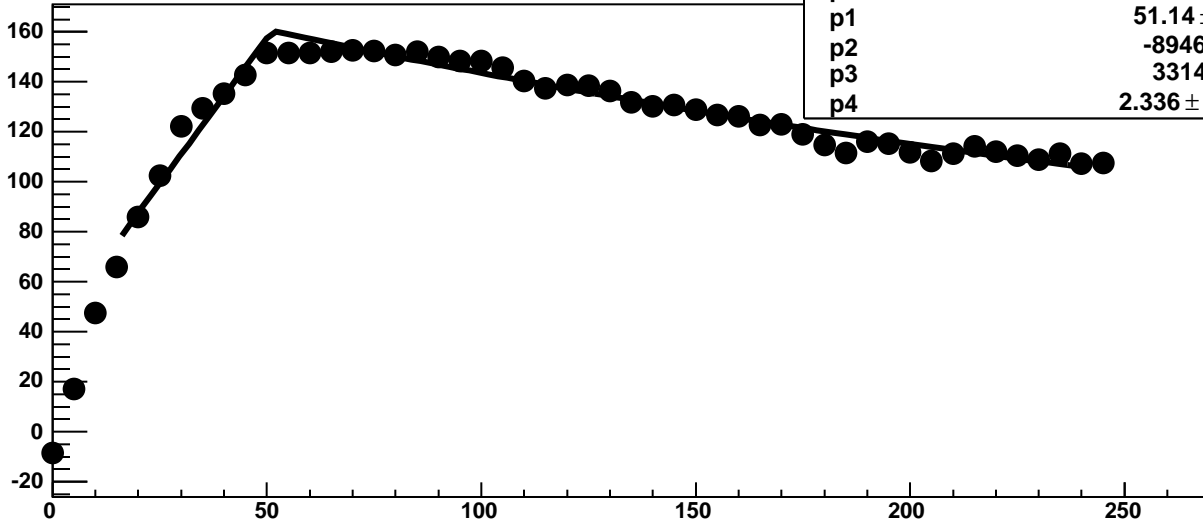
Chip 0, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold

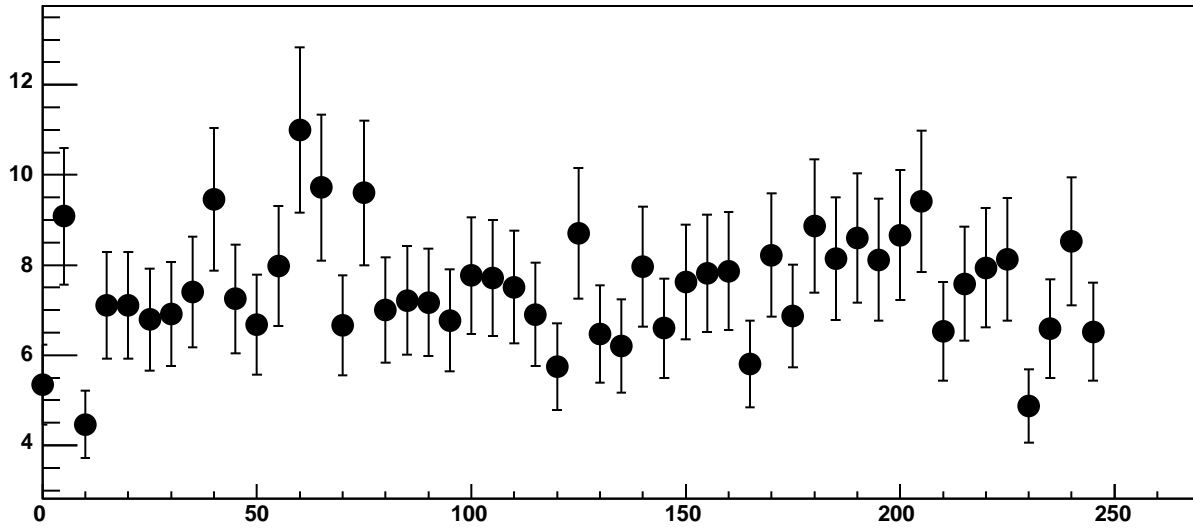


Chip 0, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold

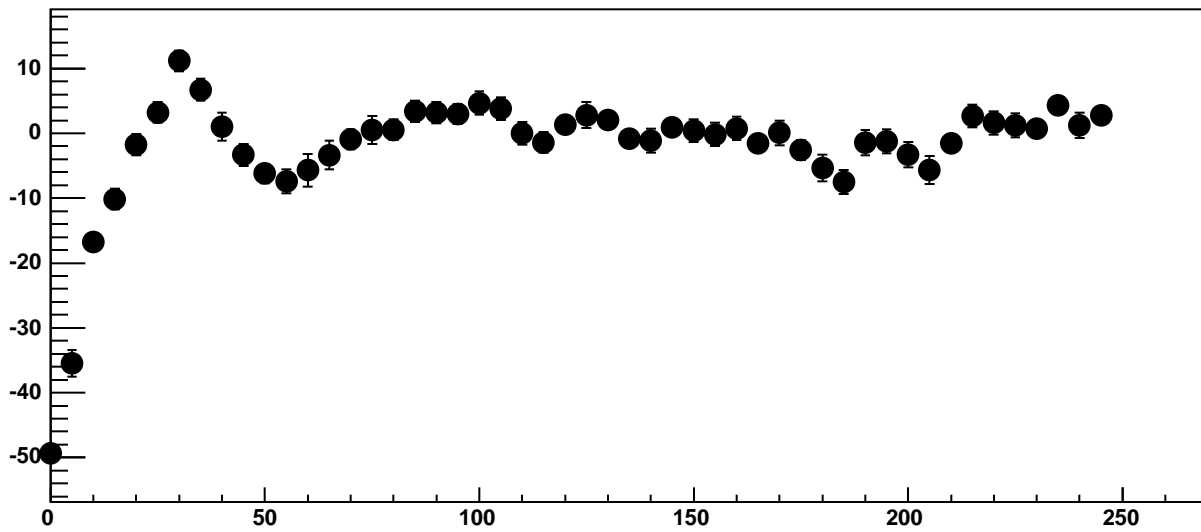


χ^2 / ndf	236 / 41
p0	160.4 ± 0.8239
p1	51.14 ± 0.5388
p2	-8946 ± 2273
p3	3314 ± 860.7
p4	2.336 ± 0.04993

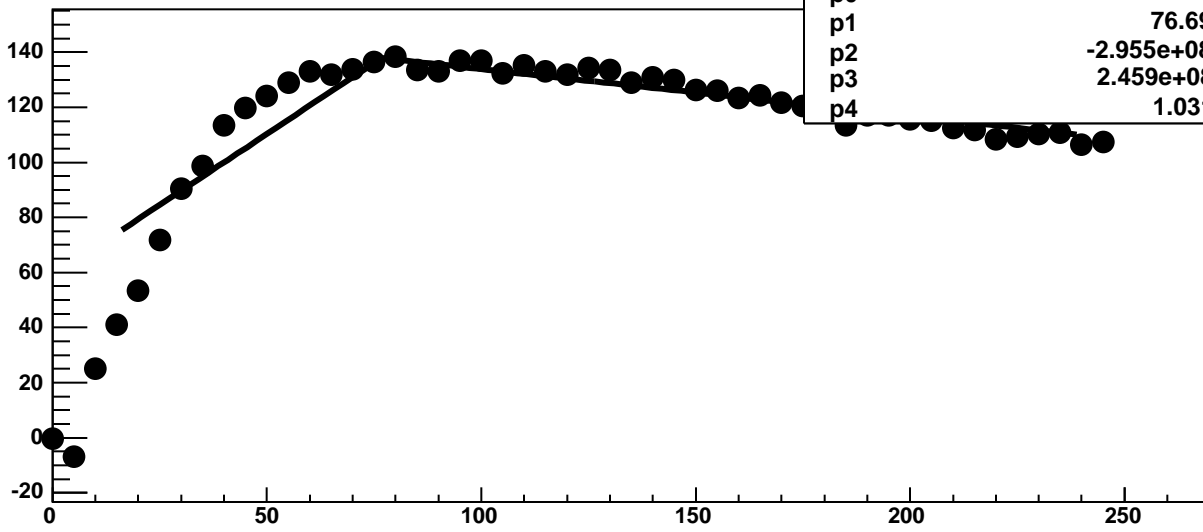
Chip 0, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold

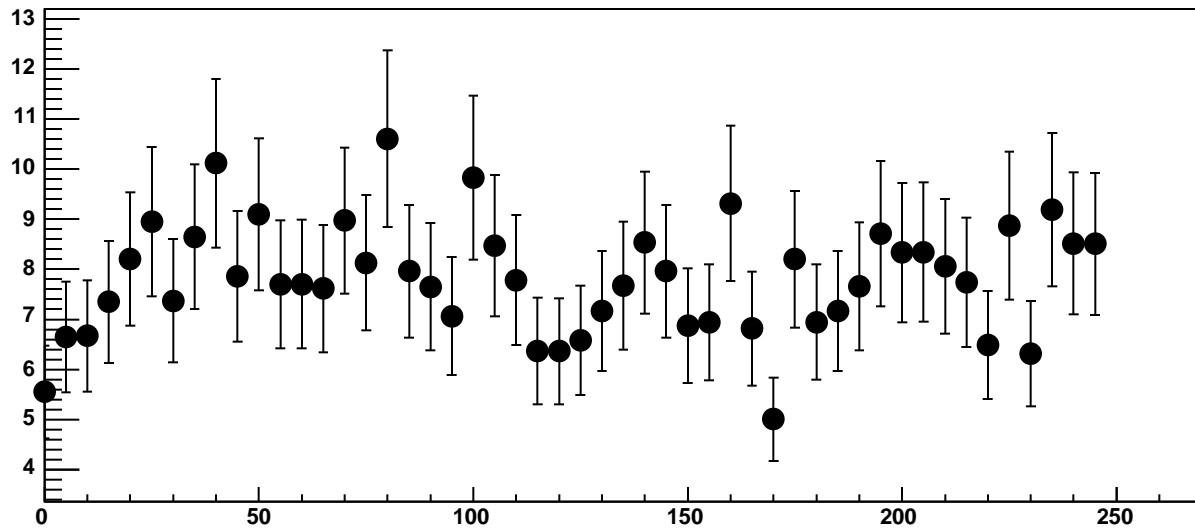


Chip 0, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

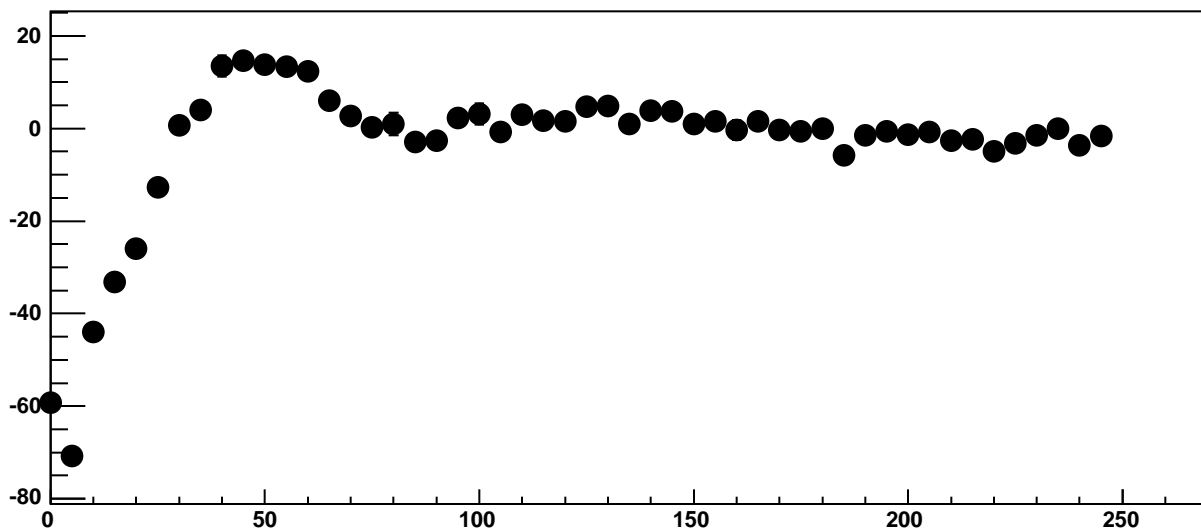


χ^2 / ndf	971.3 / 41
p0	137.8 ± nan
p1	76.69 ± nan
p2	-2.955e+08 ± nan
p3	2.459e+08 ± nan
p4	1.031 ± nan

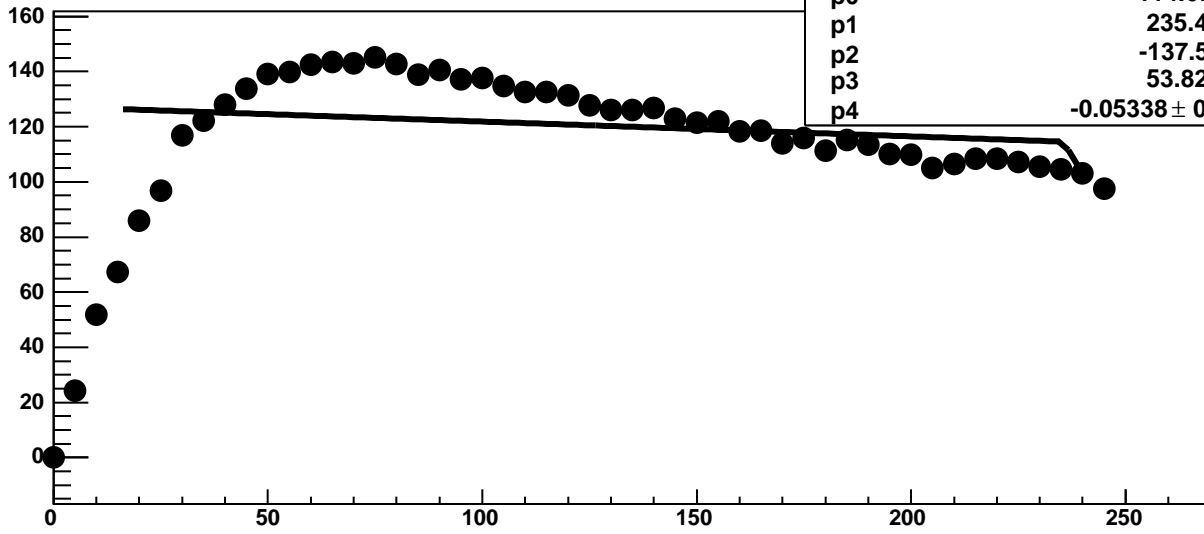
Chip 0, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

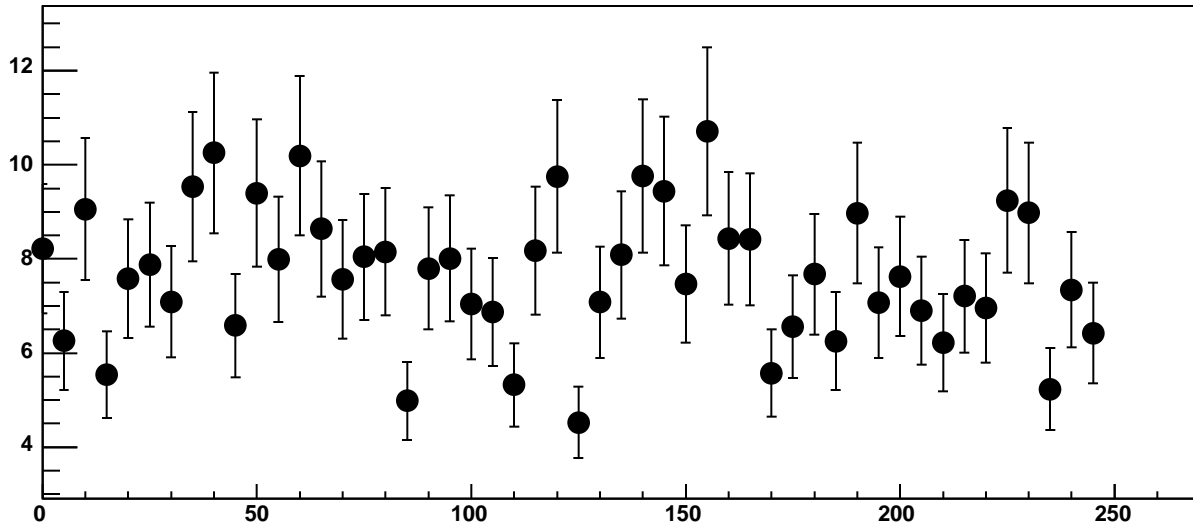


Chip 0, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

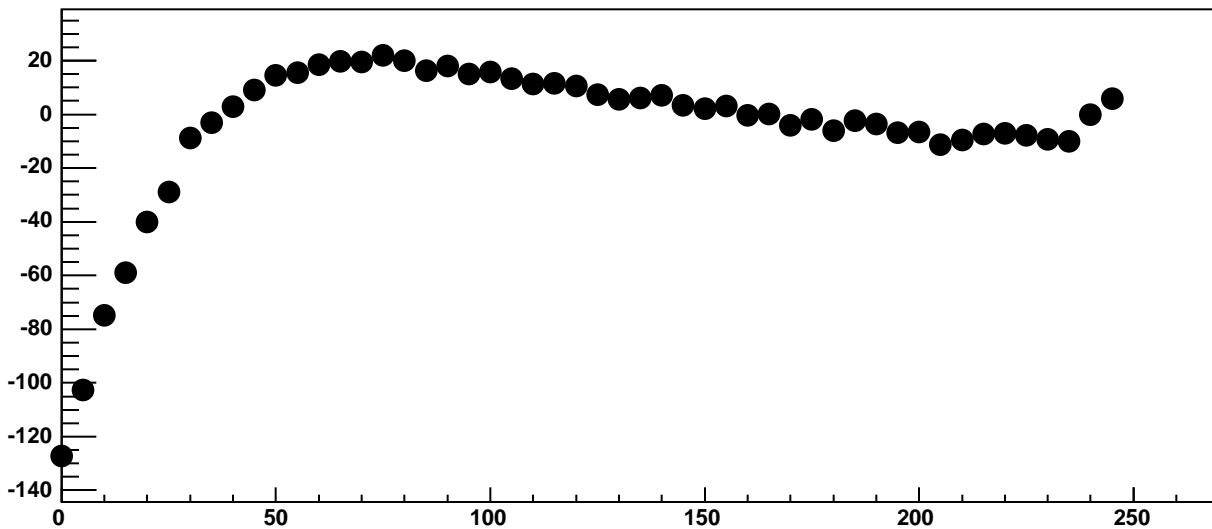


χ^2 / ndf	4750 / 41
p0	114.6 ± 0.5708
p1	235.4 ± 5.694
p2	-137.5 ± 159.5
p3	53.82 ± 53.93
p4	-0.05338 ± 0.003799

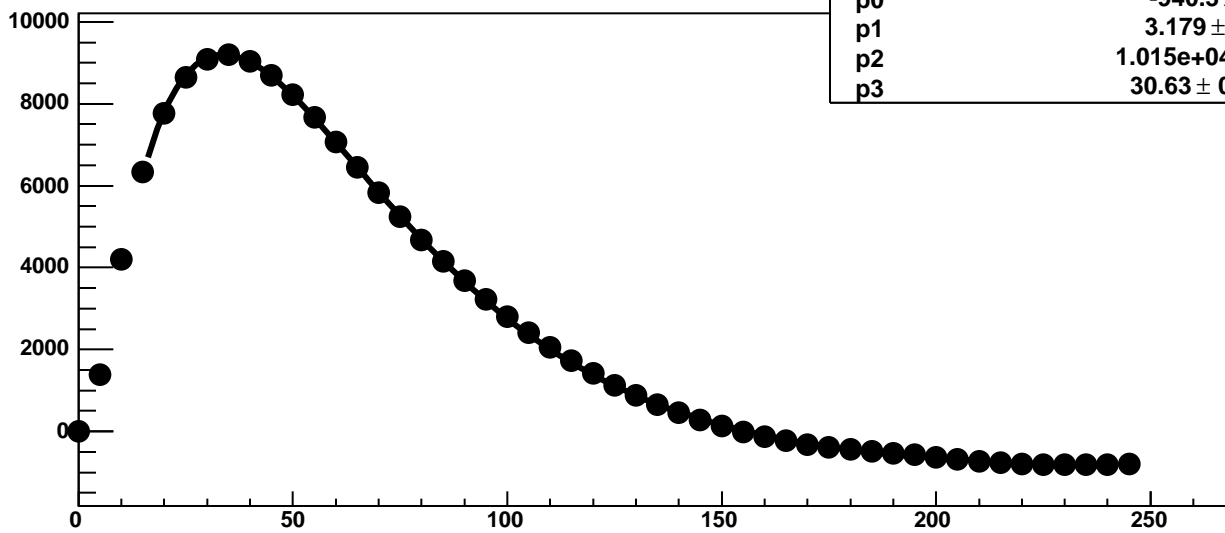
Chip 0, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

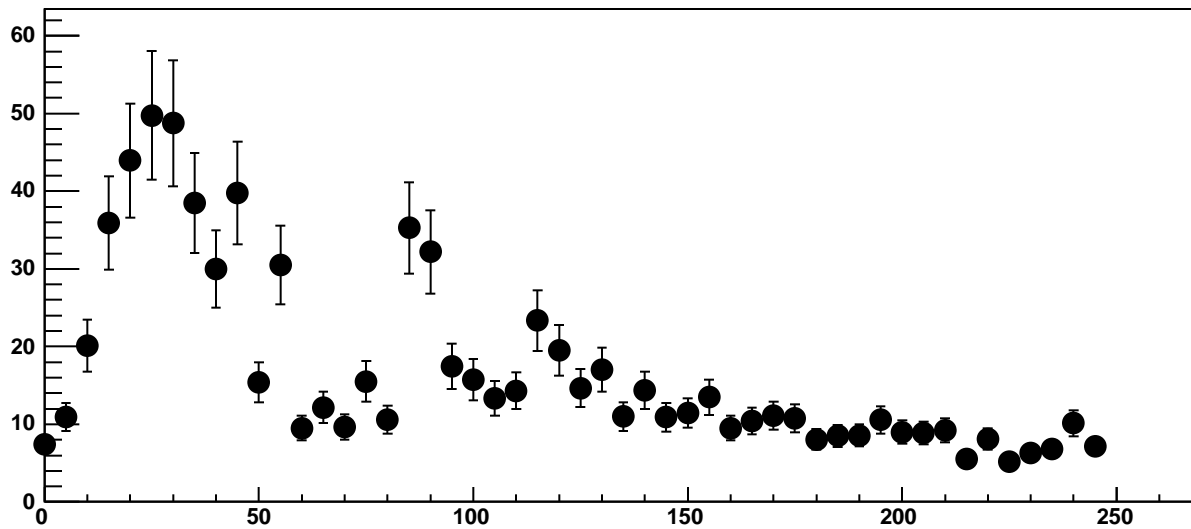


Chip 0, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold

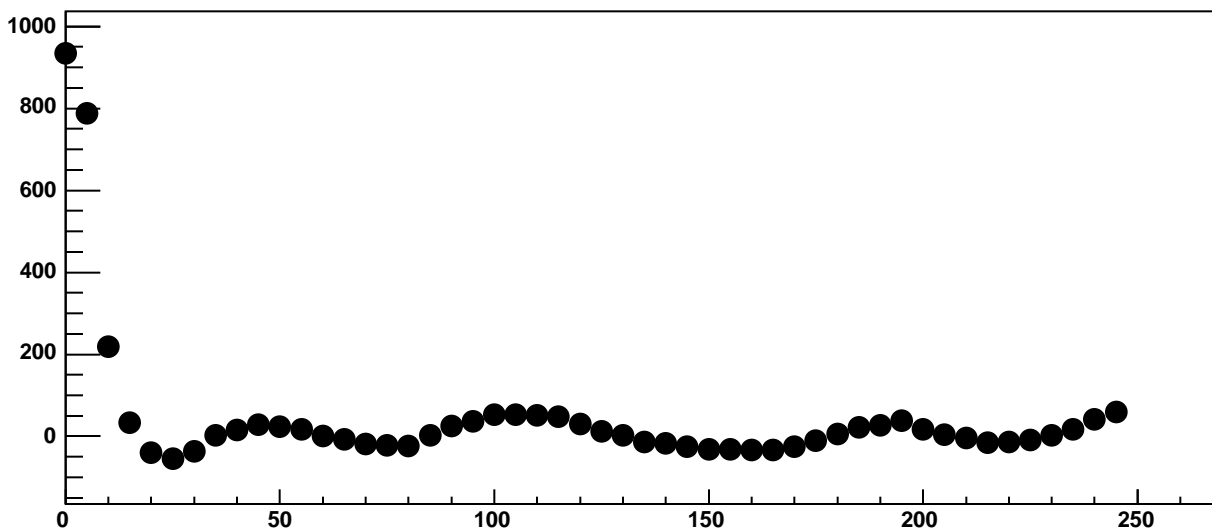


χ^2 / ndf	3621 / 42
p0	-940.3 ± 0.6354
p1	3.179 ± 0.01854
p2	$1.015\text{e}+04 \pm 2.263$
p3	30.63 ± 0.009734

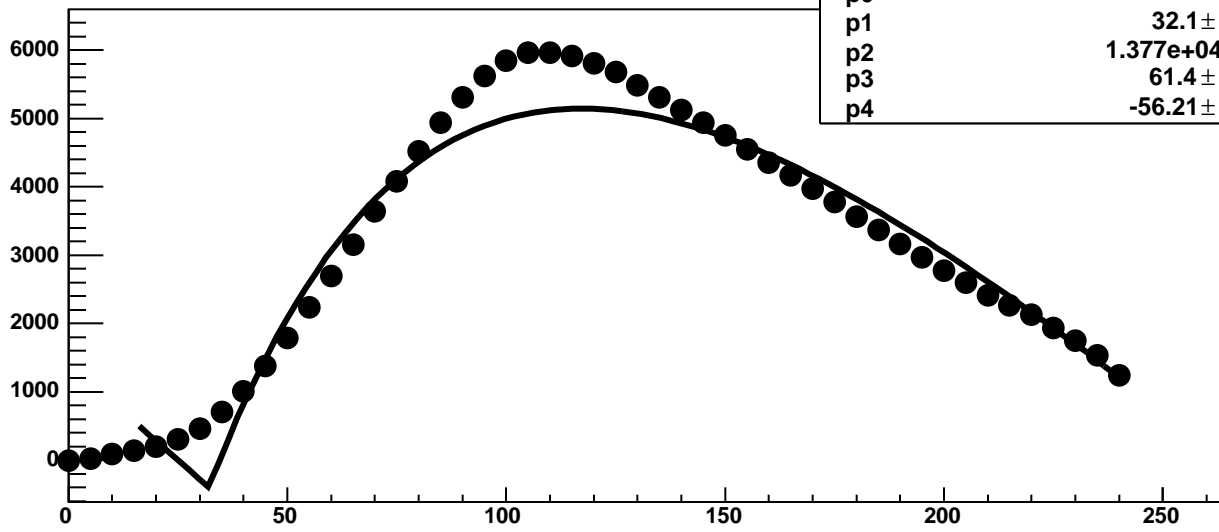
Chip 0, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold

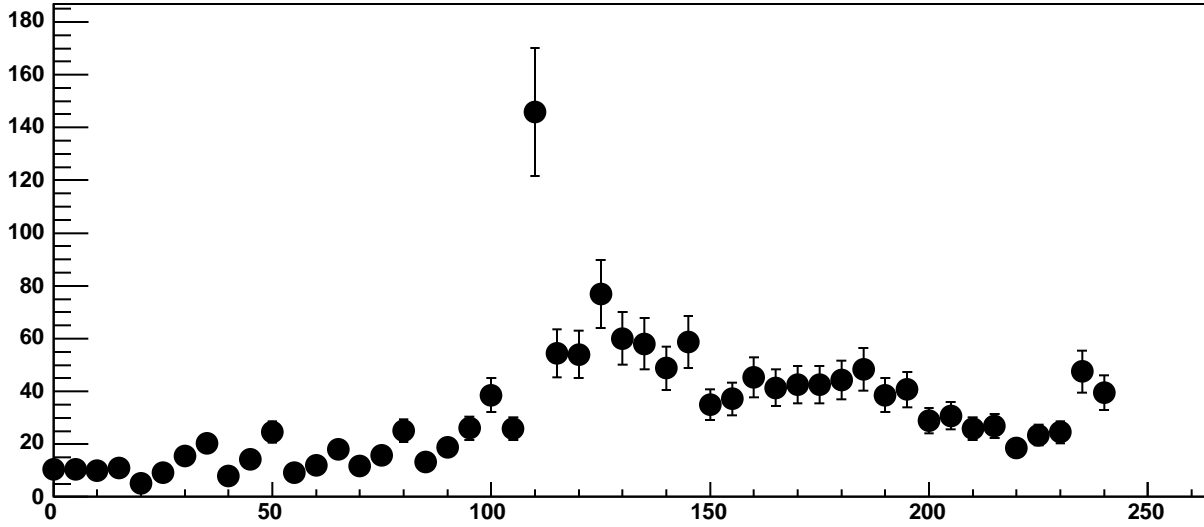


Chip 0, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

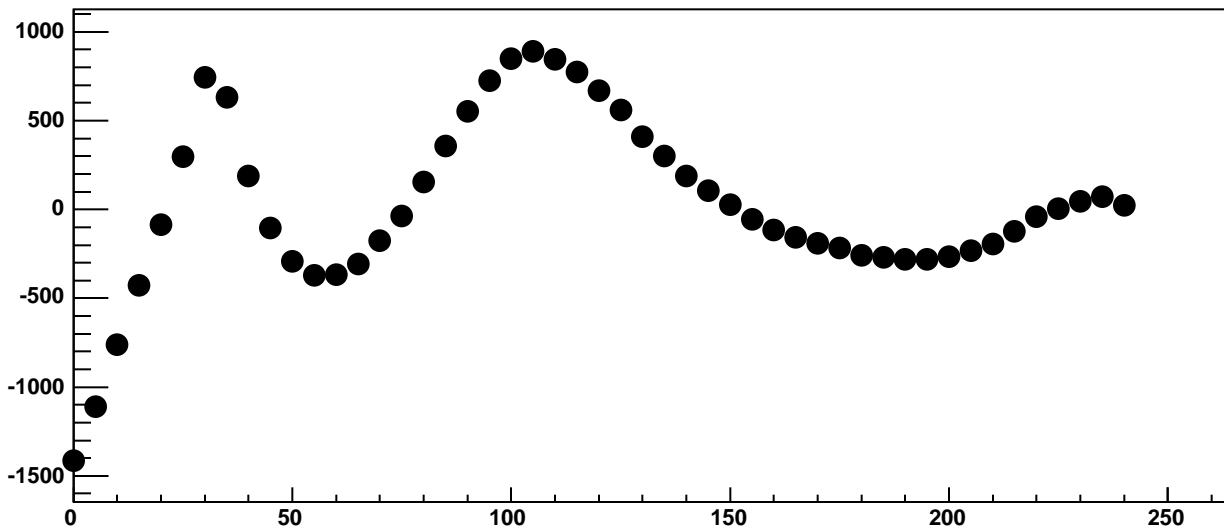


χ^2 / ndf	2.848e+05 / 41
p0	-397.1 ± 1.069
p1	32.1 ± 0.01287
p2	1.377e+04 ± 22.95
p3	61.4 ± 0.09702
p4	-56.21 ± 0.09835

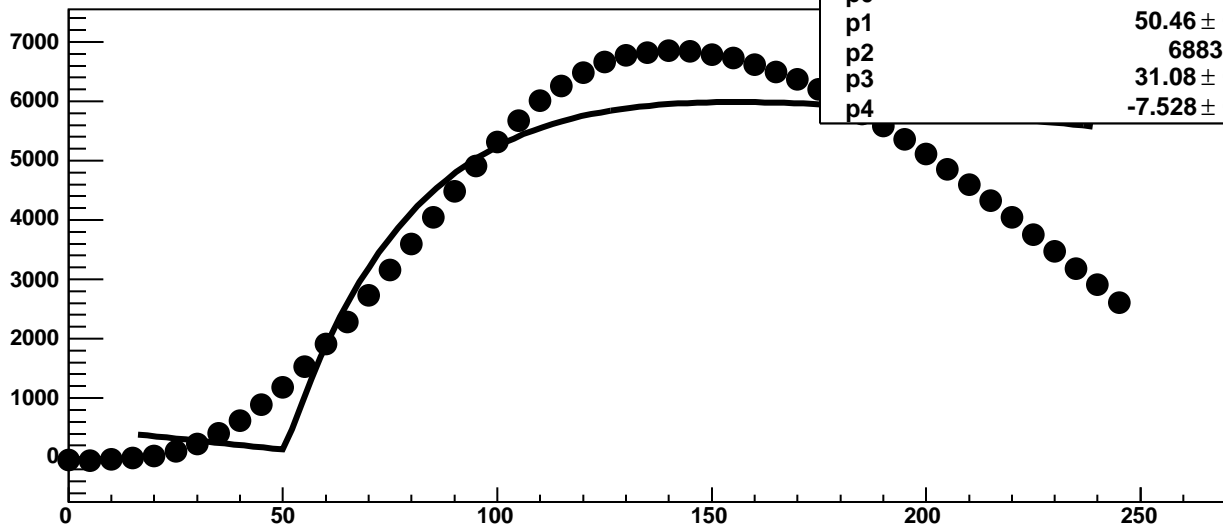
Chip 0, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold

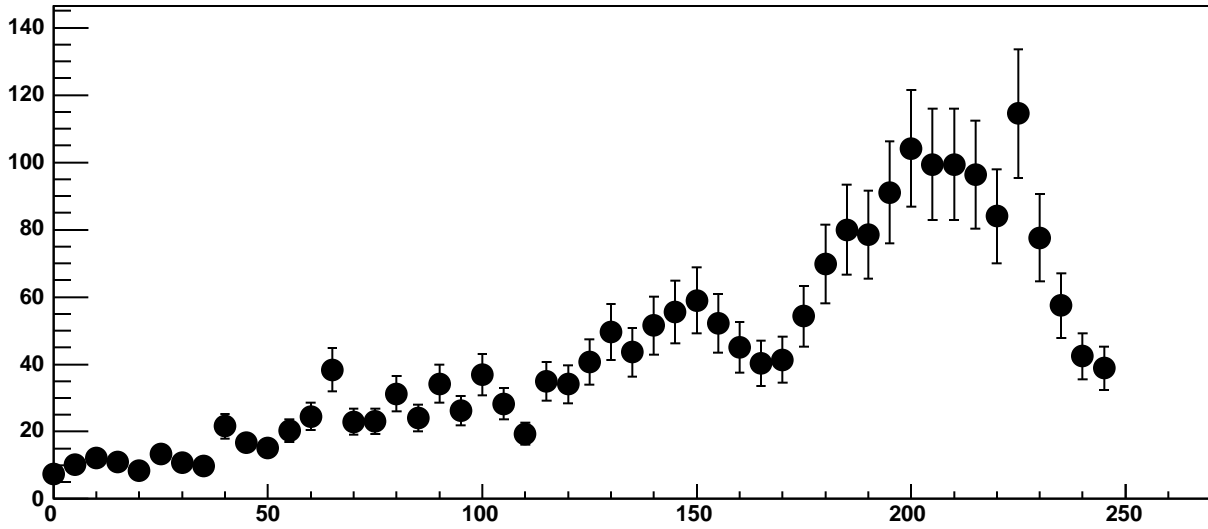


Chip 0, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

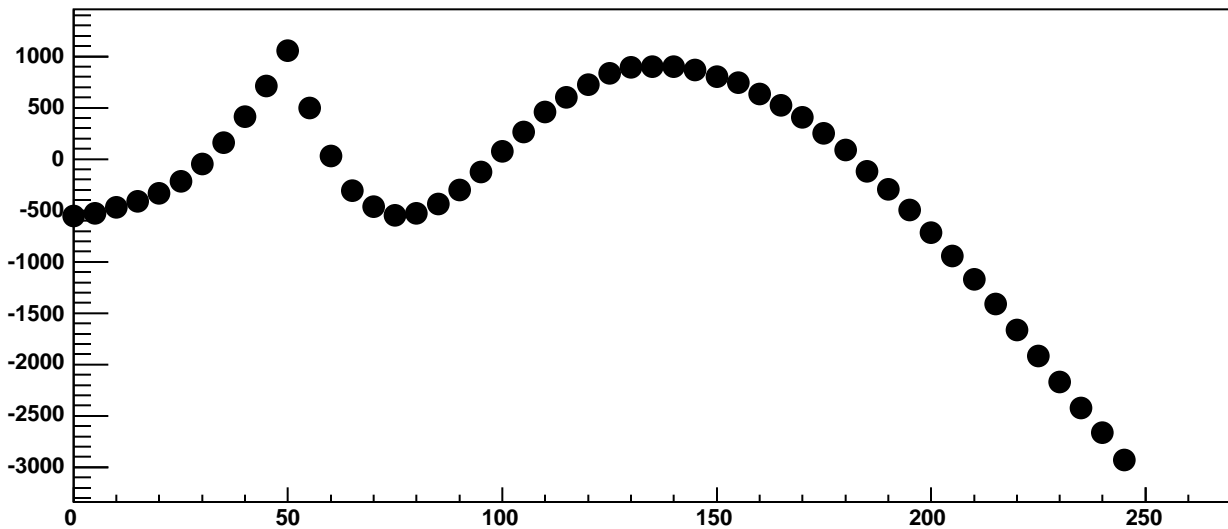


χ^2 / ndf	4.704e+05 / 41
p0	128.2 ± 1.521
p1	50.46 ± 0.02322
p2	6883 ± 9.087
p3	31.08 ± 0.05686
p4	-7.528 ± 0.05665

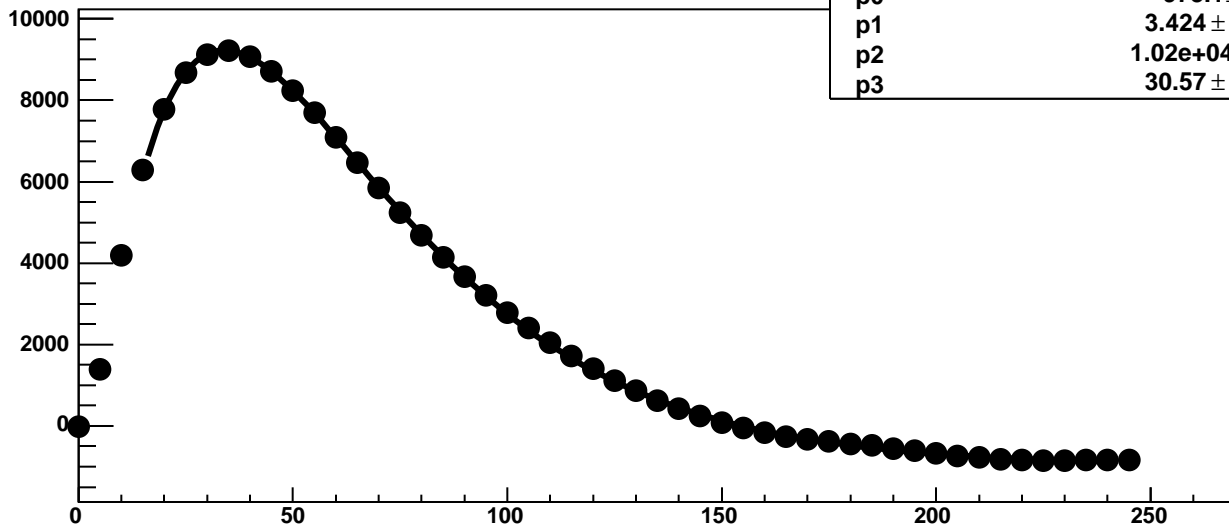
Chip 0, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

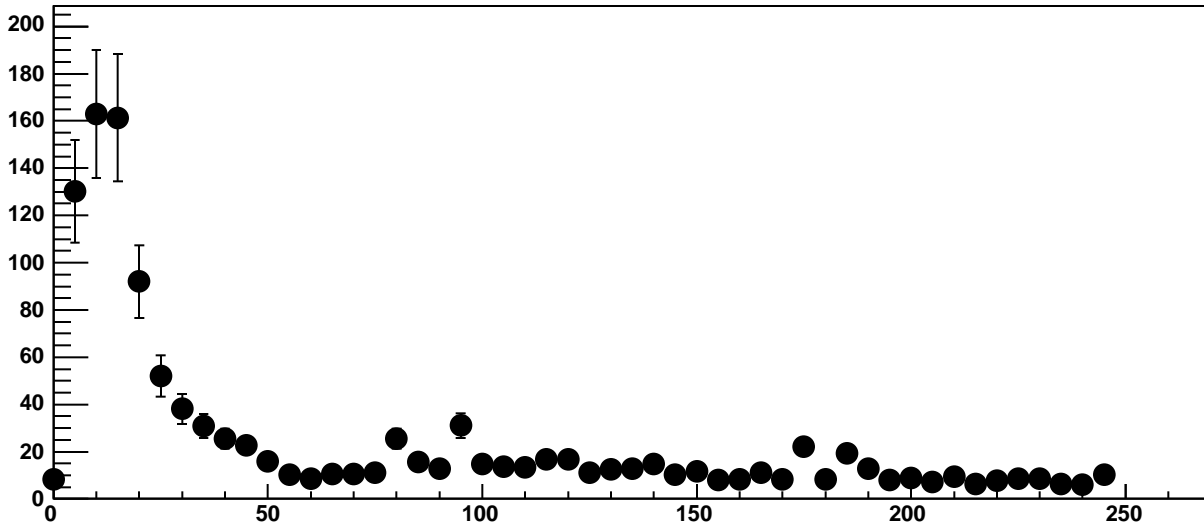


Chip 0, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold

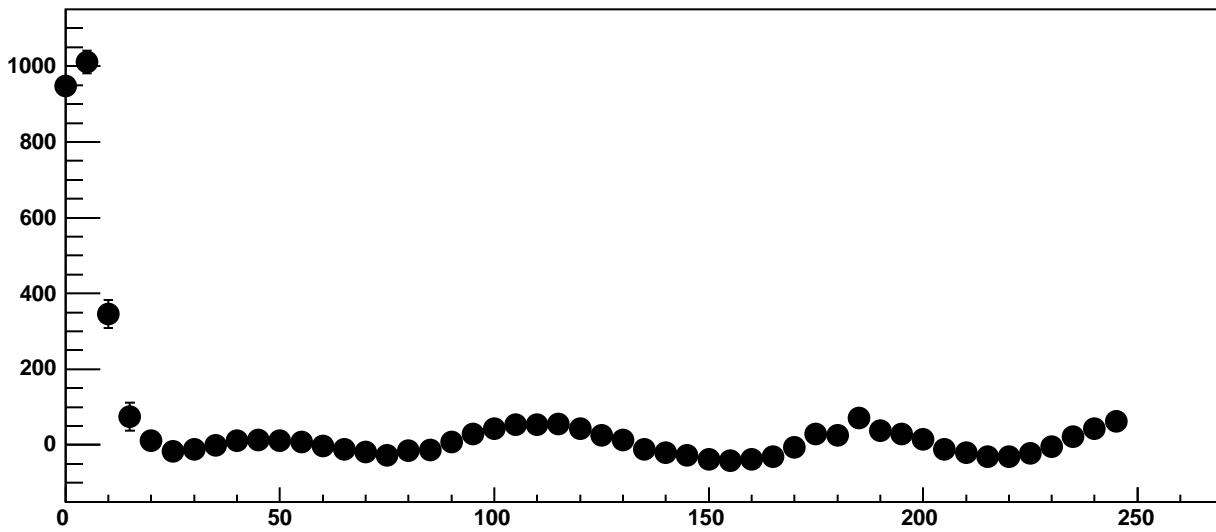


χ^2 / ndf	6079 / 42
p0	-973.1 ± 0.6748
p1	3.424 ± 0.03254
p2	1.02e+04 ± 3.014
p3	30.57 ± 0.01099

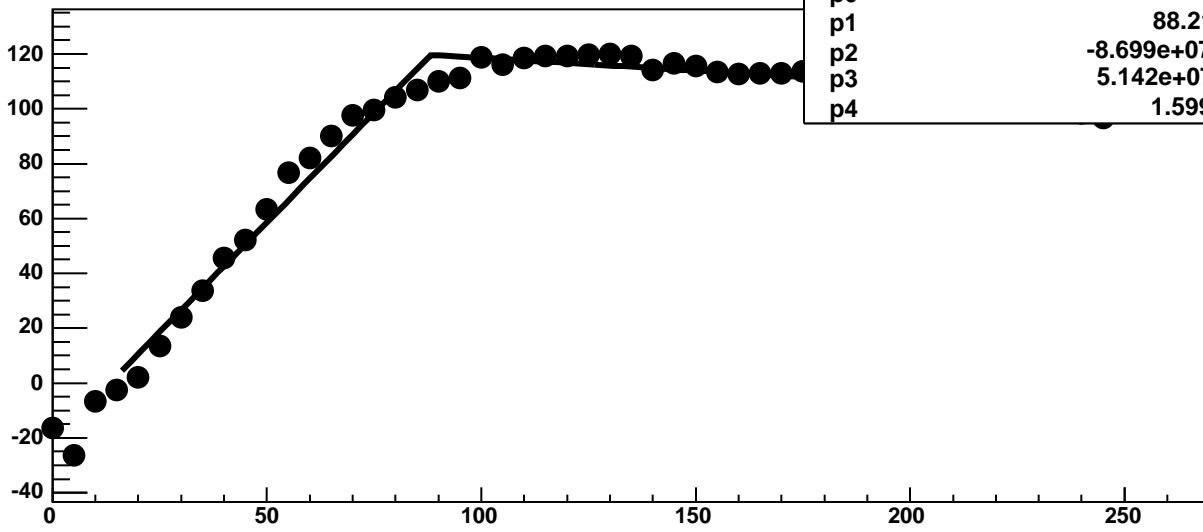
Chip 0, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold

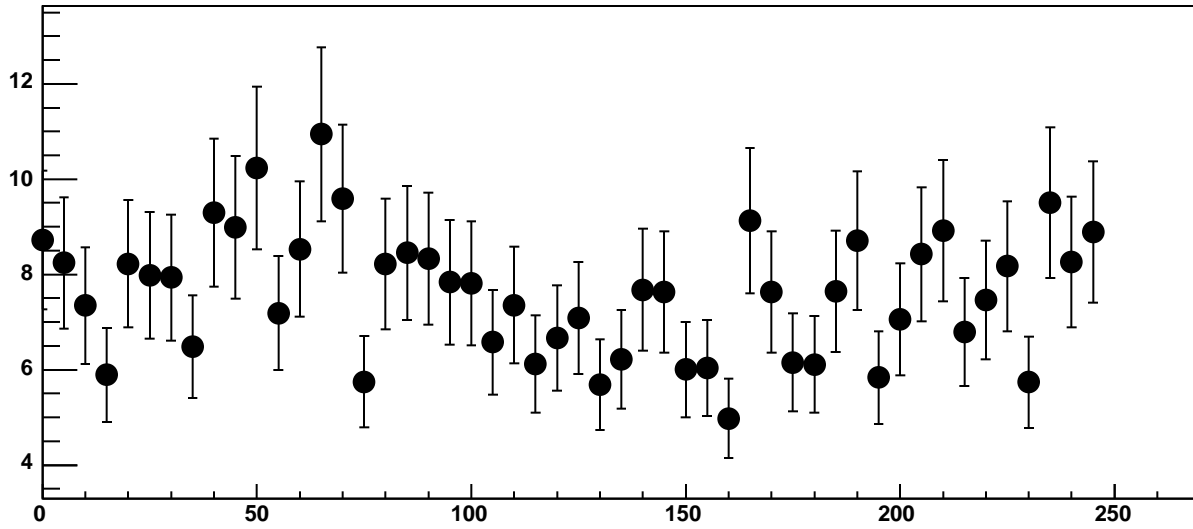


Chip 0, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

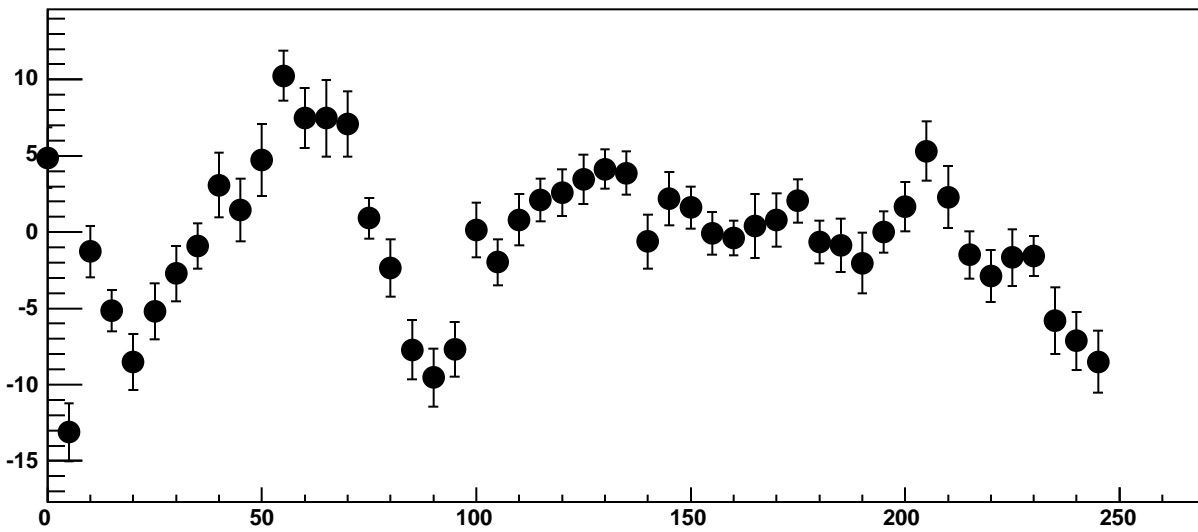


	χ^2 / ndf	260.3 / 41
p0	$119.7 \pm$	nan
p1	$88.21 \pm$	nan
p2	$-8.699e+07 \pm$	nan
p3	$5.142e+07 \pm$	nan
p4	$1.599 \pm$	nan

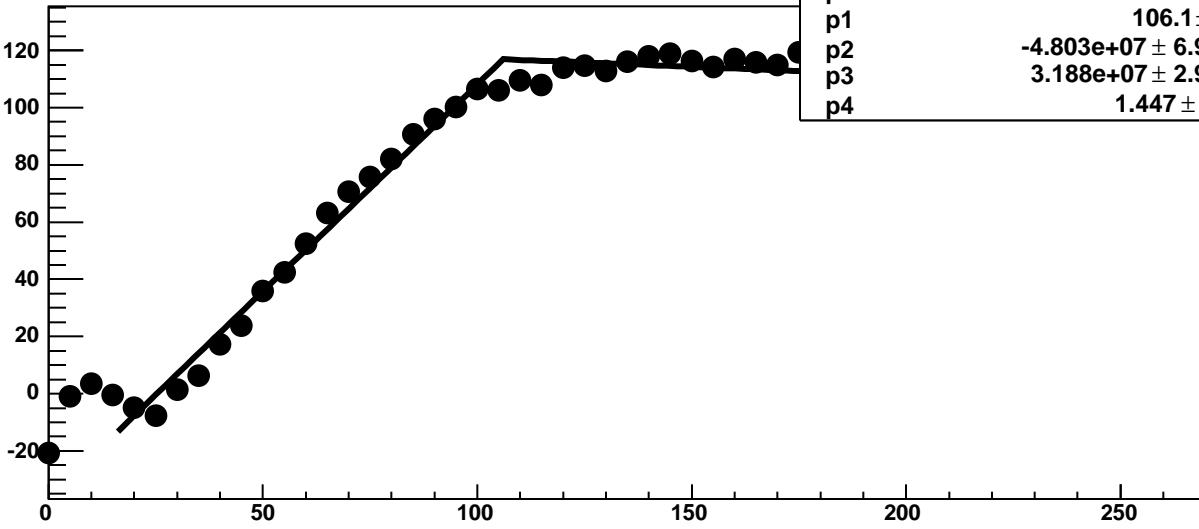
Chip 0, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

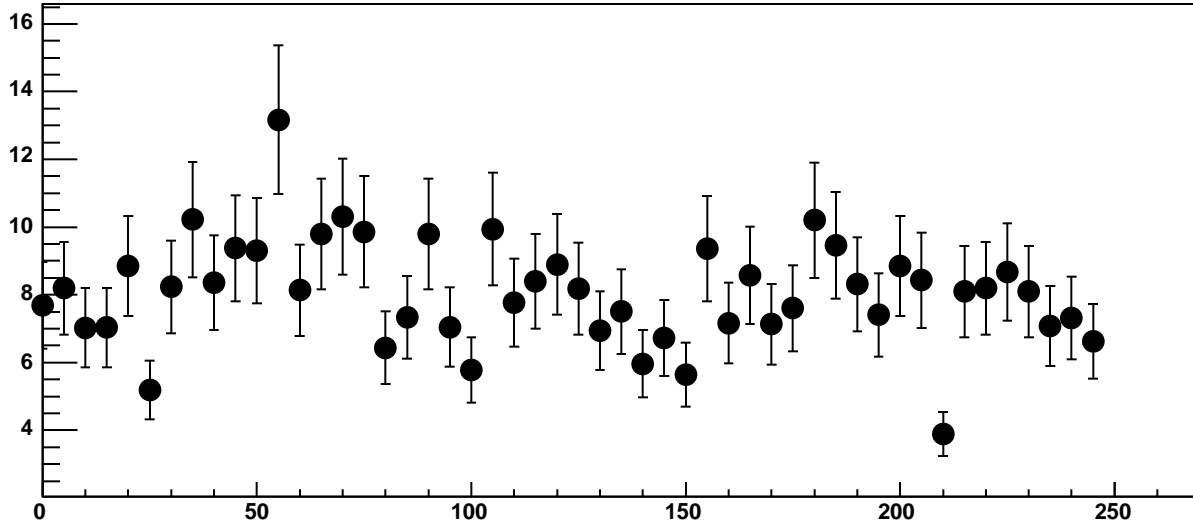


Chip 0, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

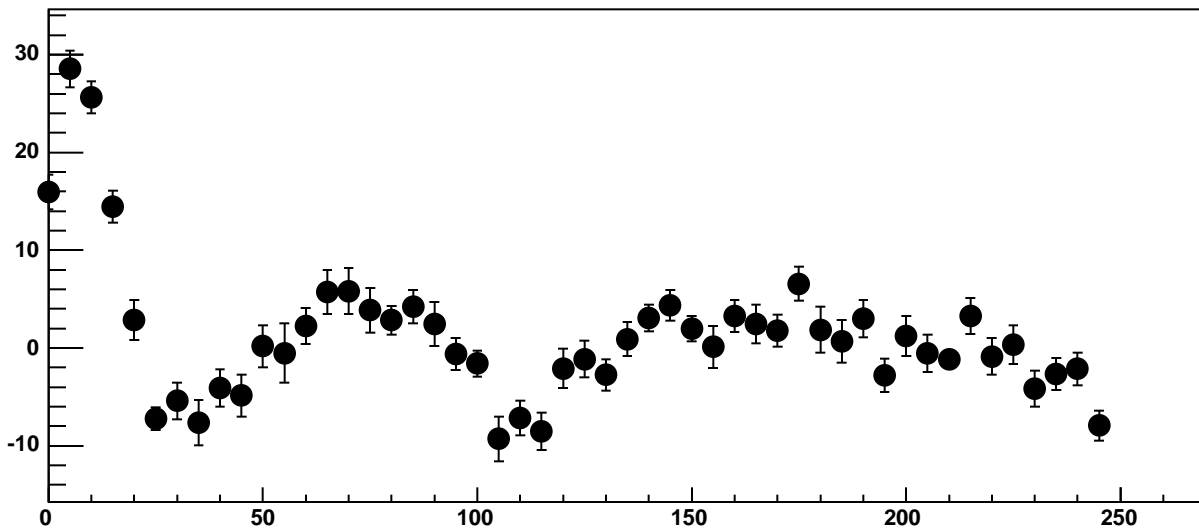


χ^2 / ndf	290.7 / 41
p0	116.9 ± 0.6483
p1	106.1 ± 0.6838
p2	$-4.803e+07 \pm 6.961e+05$
p3	$3.188e+07 \pm 2.939e+05$
p4	1.447 ± 0.01416

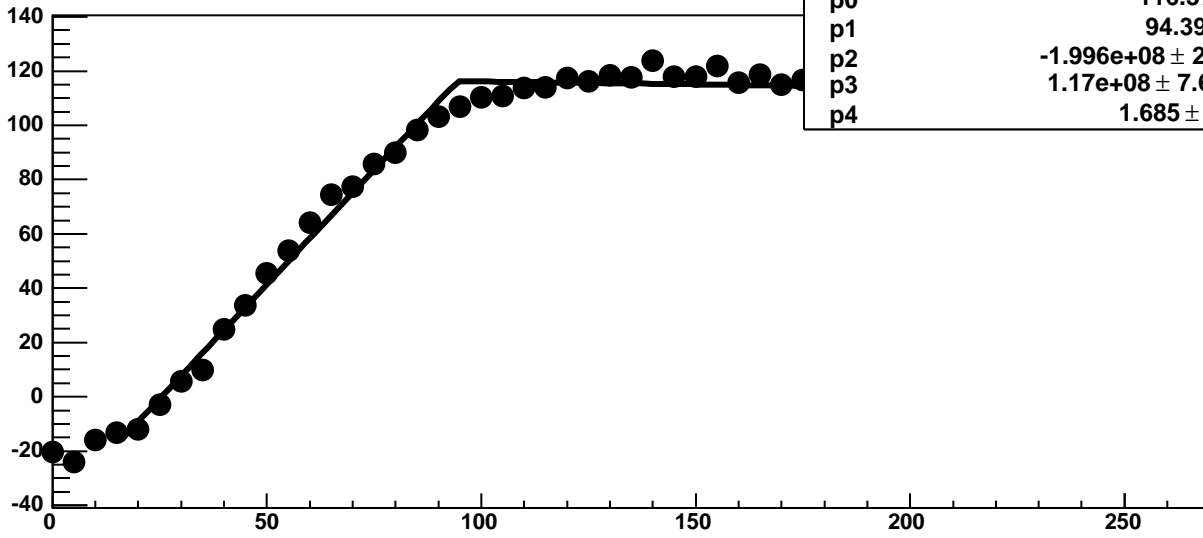
Chip 0, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

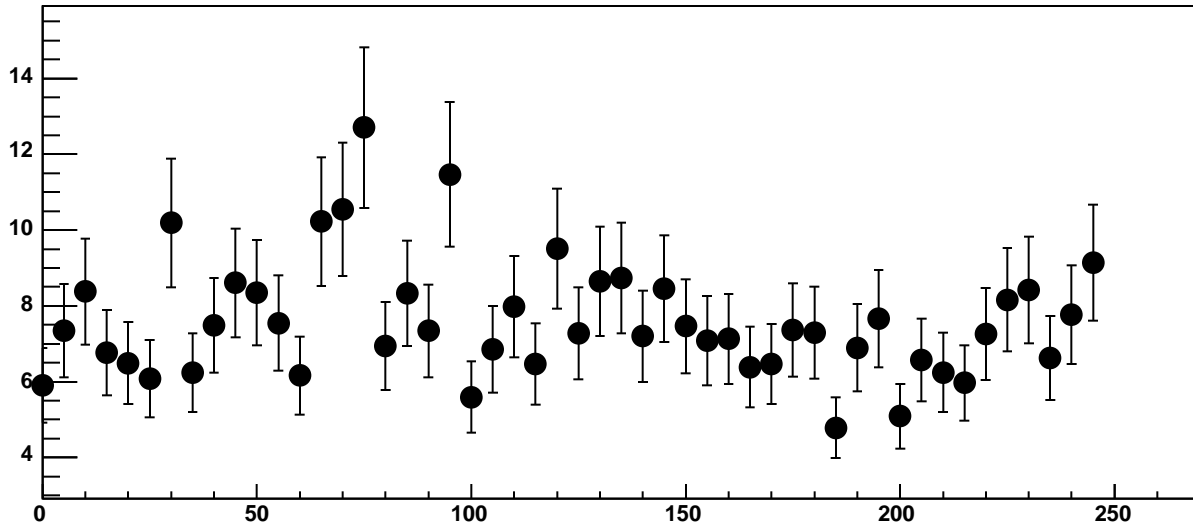


Chip 0, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold

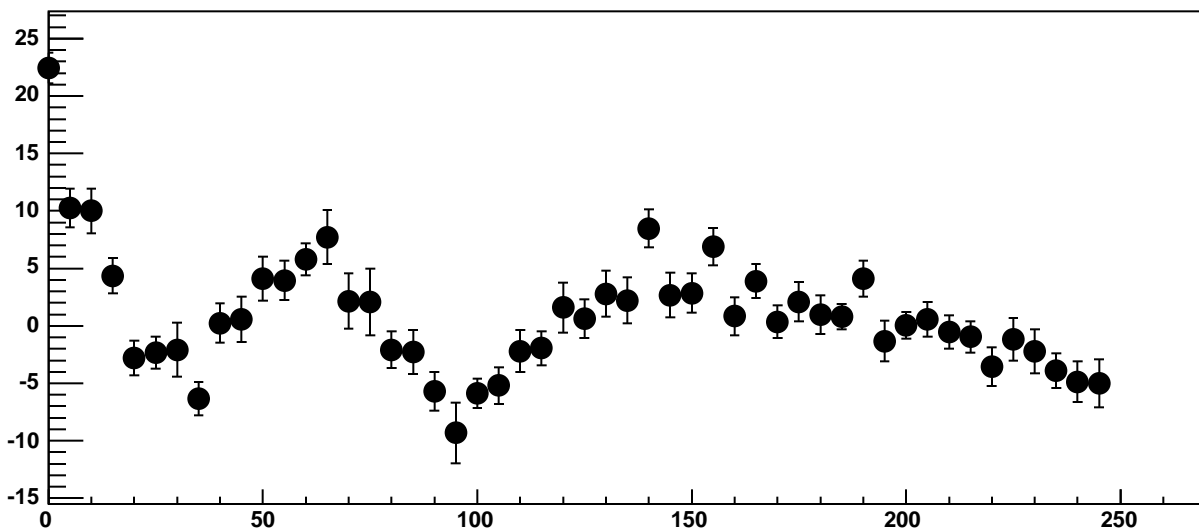


χ^2 / ndf	226.8 / 41
p0	116.3 ± 0.5831
p1	94.39 ± 0.637
p2	$-1.996\text{e}+08 \pm 2.71\text{e}+06$
p3	$1.17\text{e}+08 \pm 7.664\text{e}+05$
p4	1.685 ± 0.01804

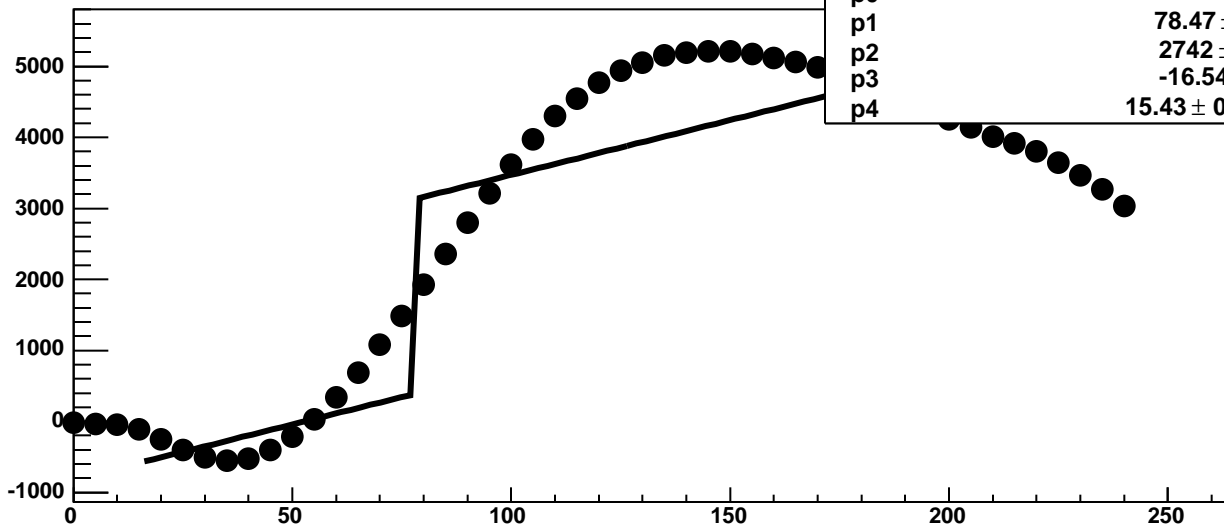
Chip 0, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold

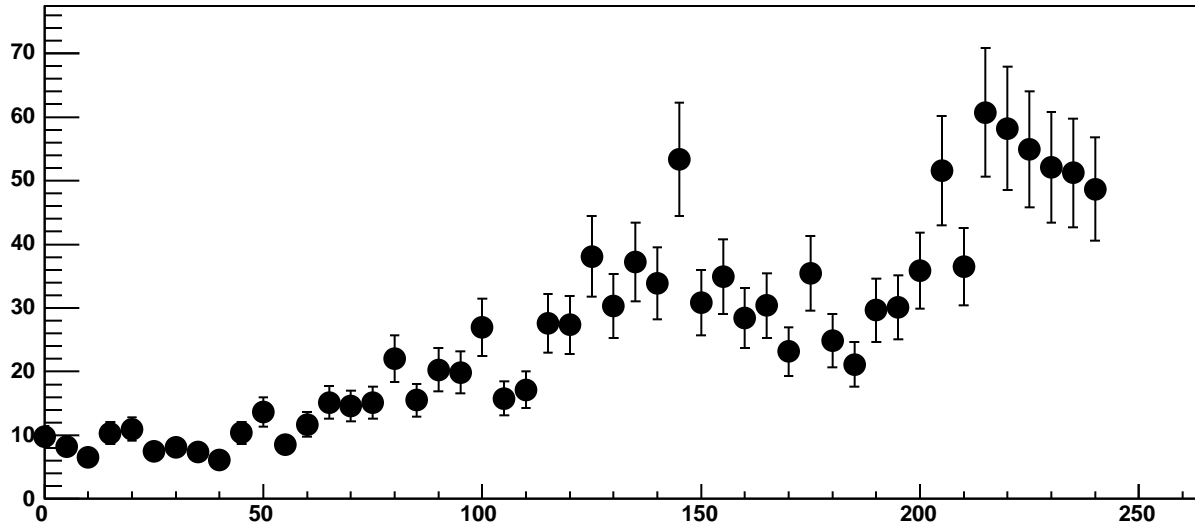


Chip 0, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

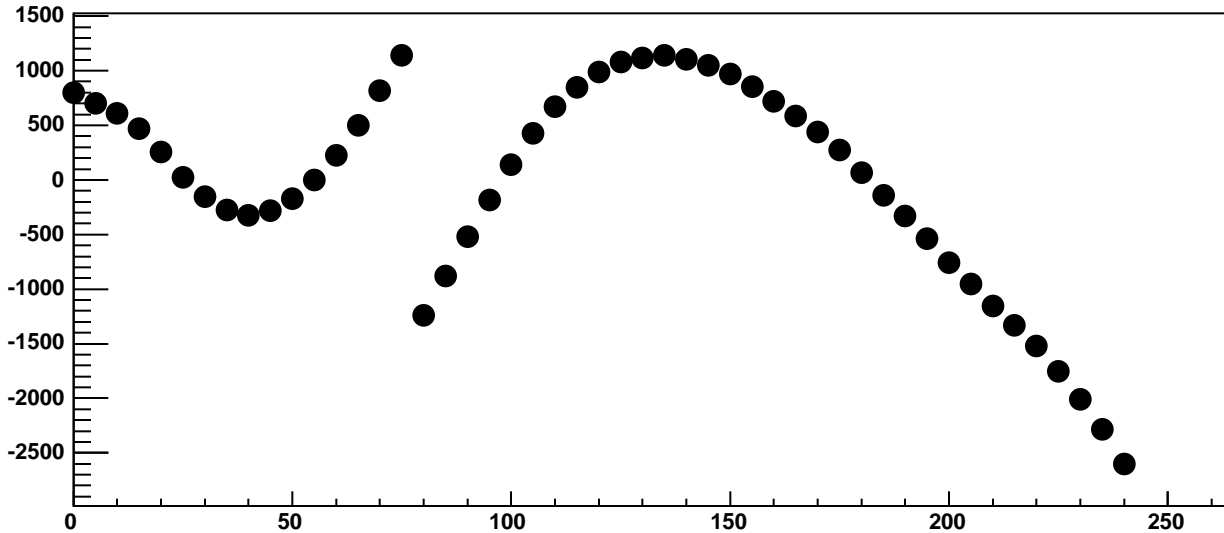


χ^2 / ndf	9.193e+05 / 41
p0	396.7 ± 0.1372
p1	78.47 ± 0.1077
p2	2742 ± 0.1726
p3	-16.54 ± 4.518
p4	15.43 ± 0.002029

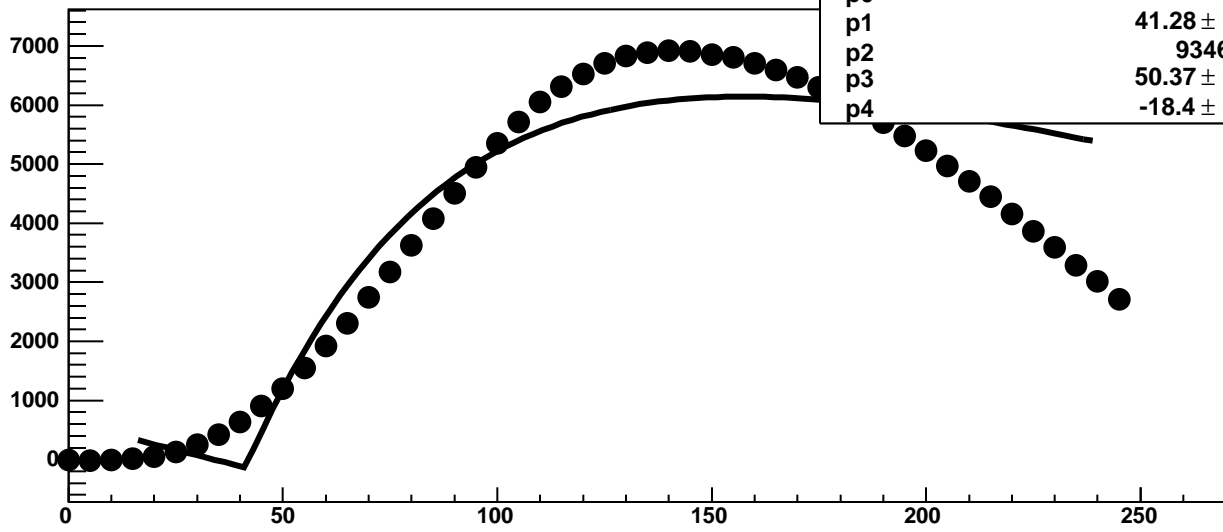
Chip 0, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold



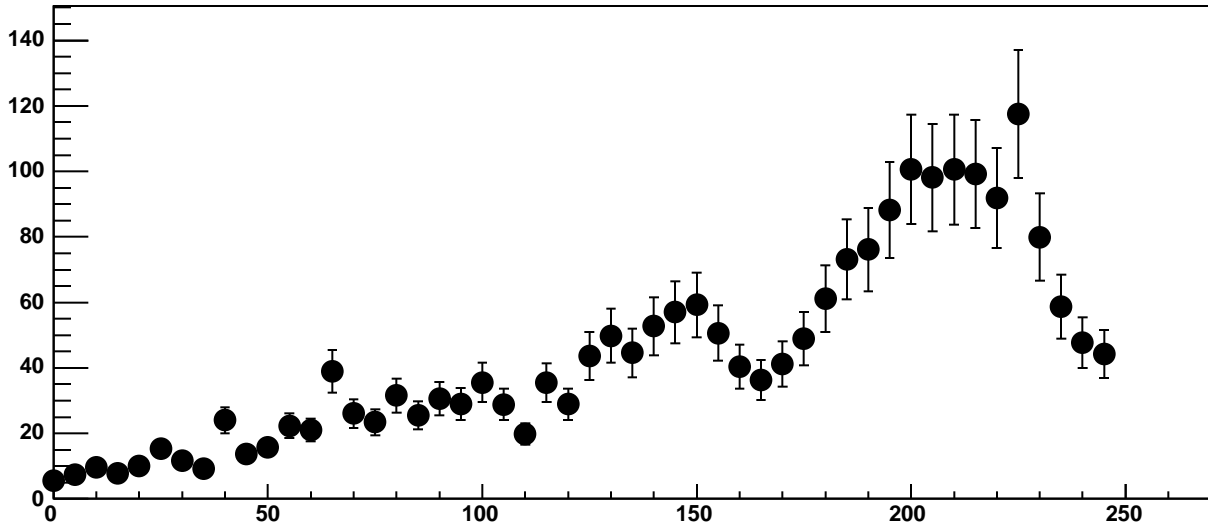
Chip 0, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold



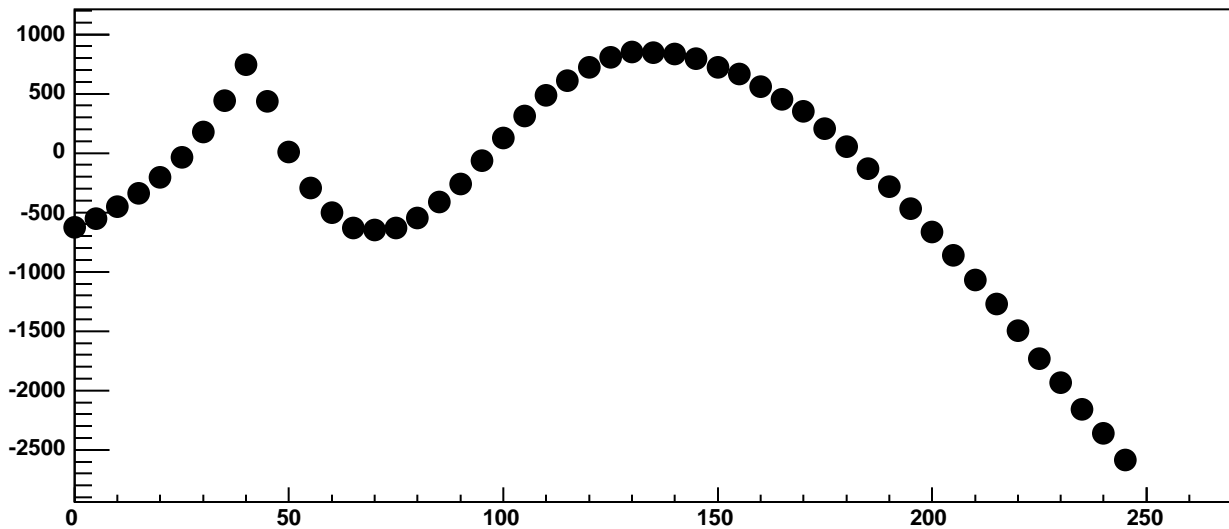
Chip 0, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold



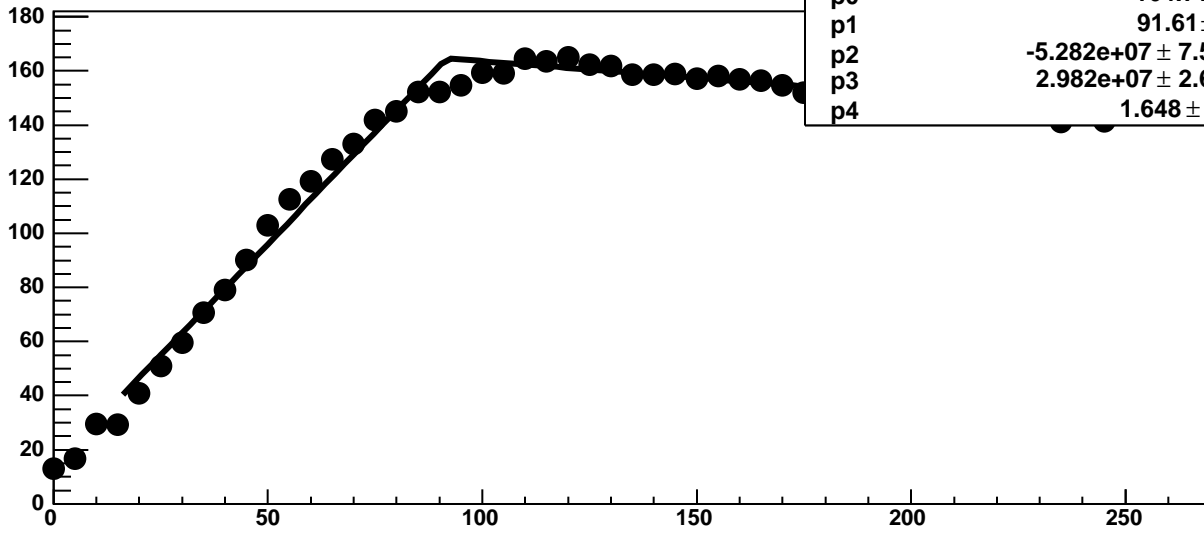
Chip 0, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold

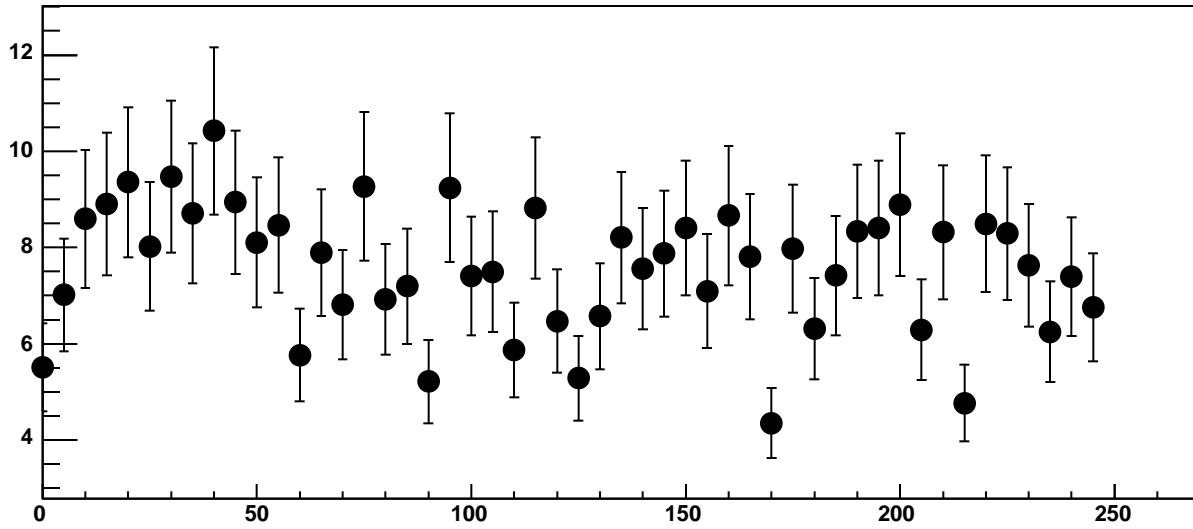


Chip 0, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

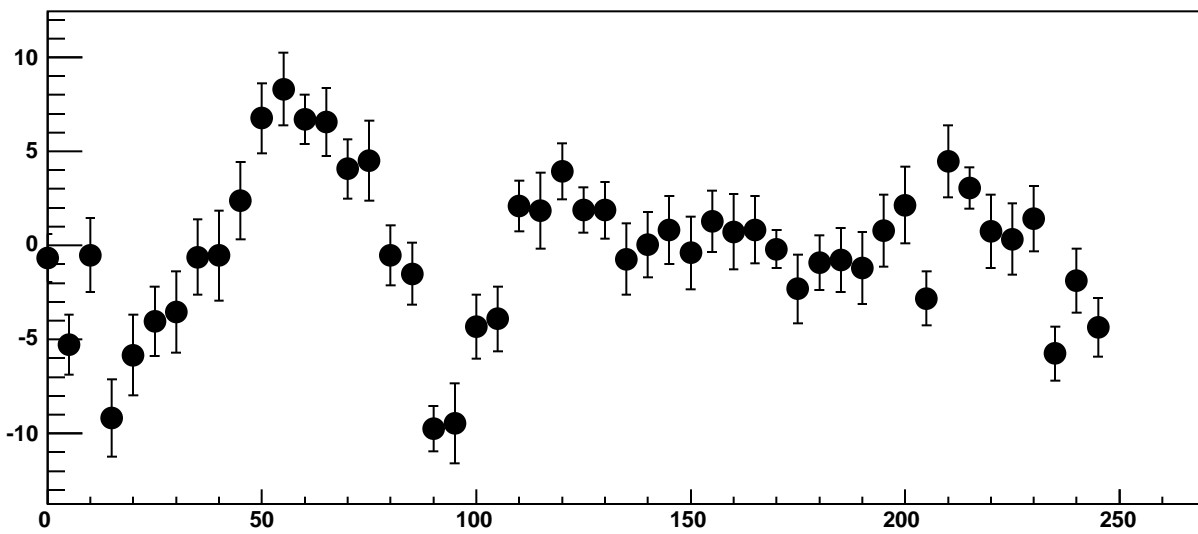


χ^2 / ndf	271.6 / 41
p0	164.7 ± 0.5627
p1	91.61 ± 0.5472
p2	$-5.282e+07 \pm 7.523e+05$
p3	$2.982e+07 \pm 2.614e+05$
p4	1.648 ± 0.01886

Chip 0, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold

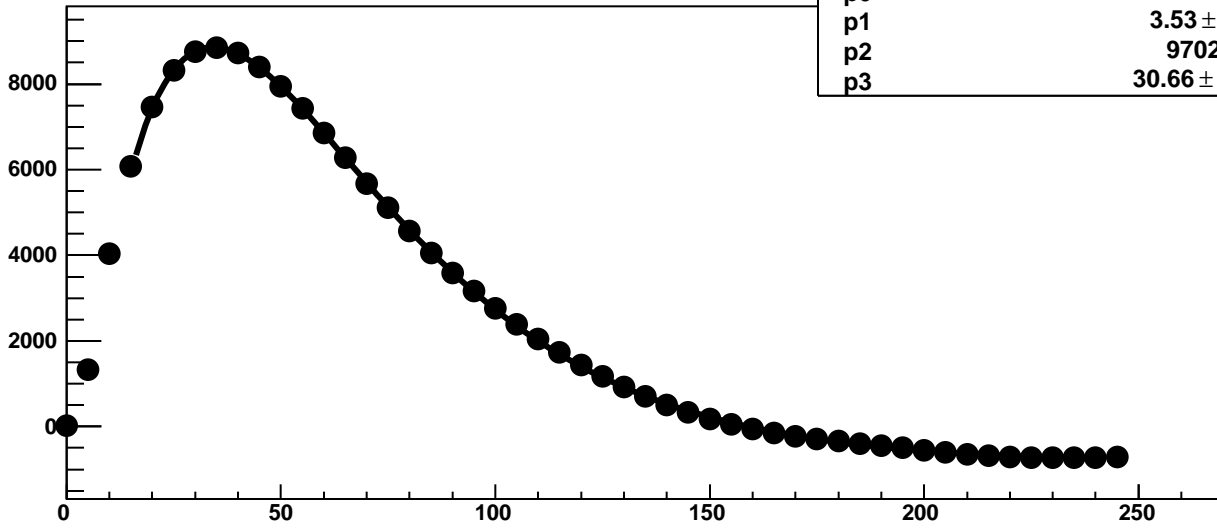


Chip 0, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

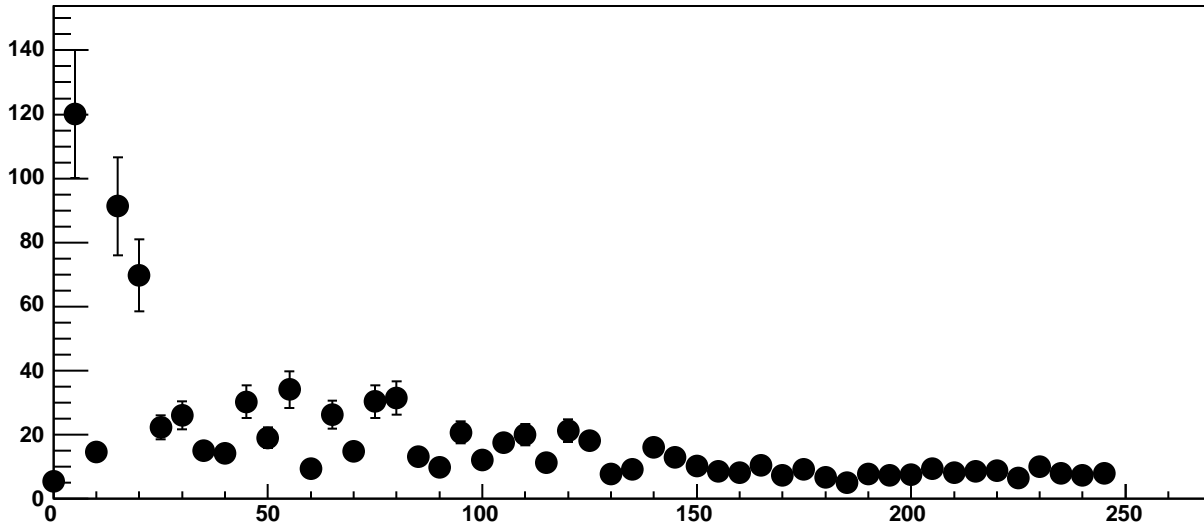


Chip 0, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold

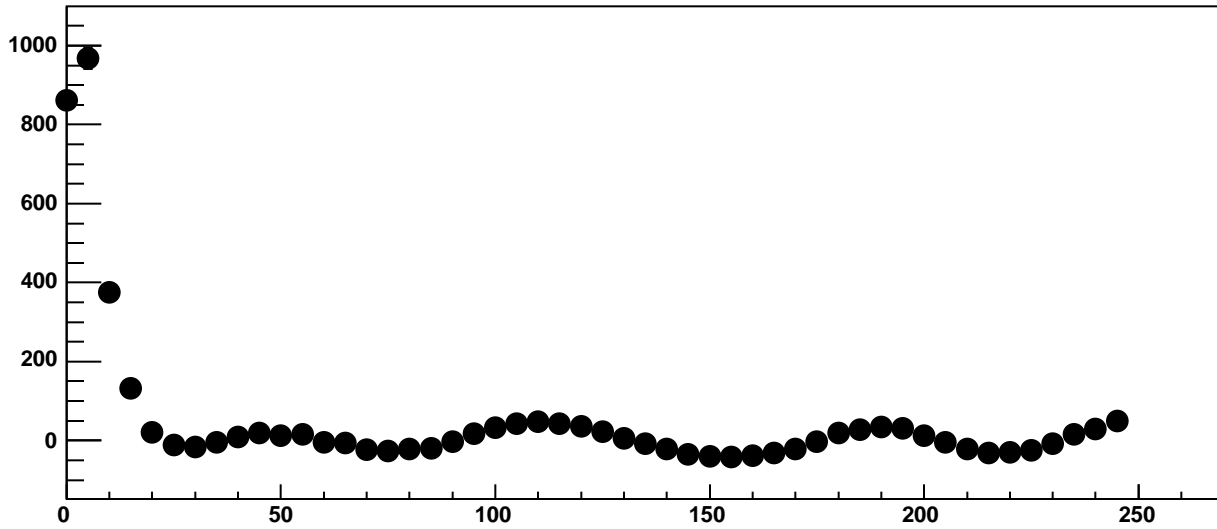
χ^2 / ndf	5384 / 42
p0	-839.5 ± 0.6658
p1	3.53 ± 0.02481
p2	9702 ± 1.793
p3	30.66 ± 0.01044



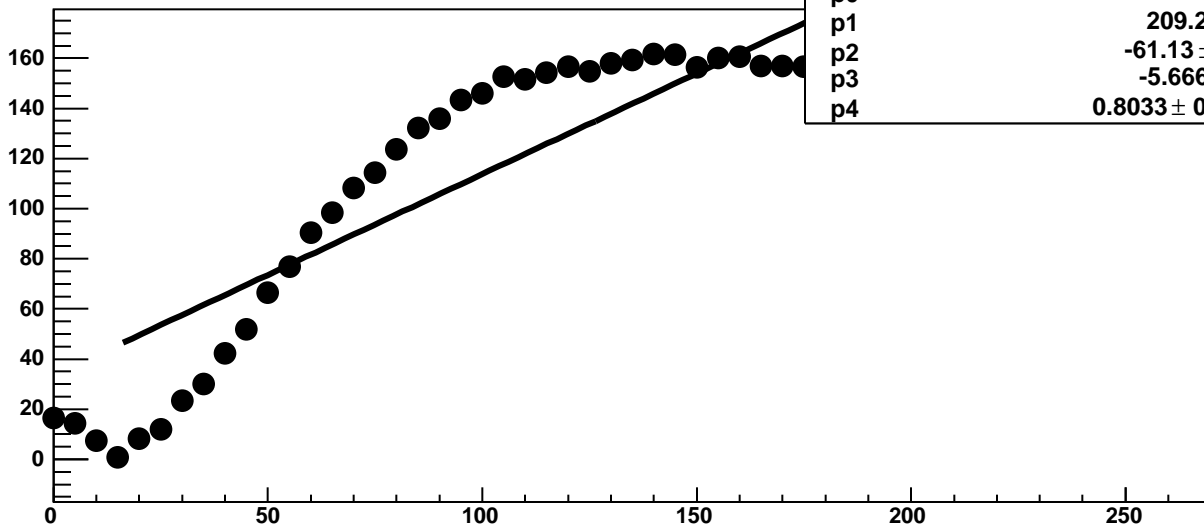
Chip 0, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold

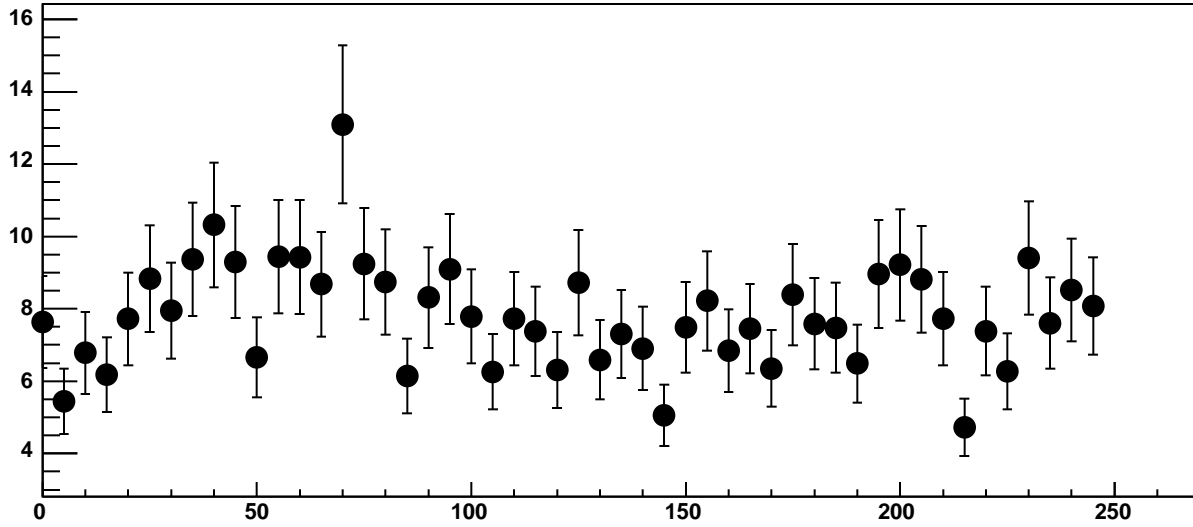


Chip 0, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

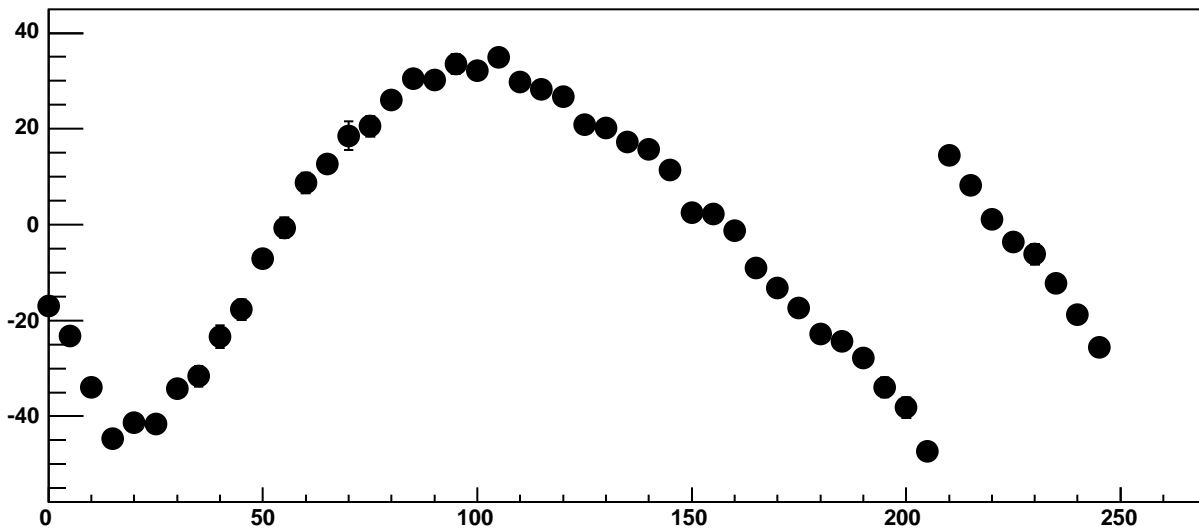


χ^2 / ndf	8806 / 41
p0	201.6 ± 0.5113
p1	209.2 ± 2.689
p2	-61.13 ± 0.9986
p3	-5.666 ± 7.393
p4	0.8033 ± 0.002403

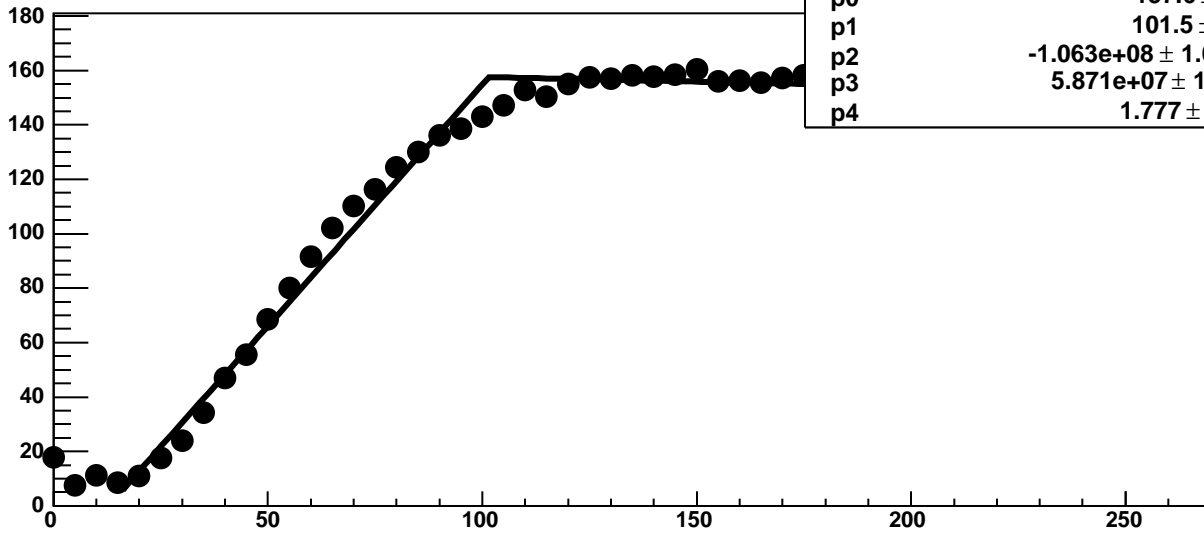
Chip 0, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

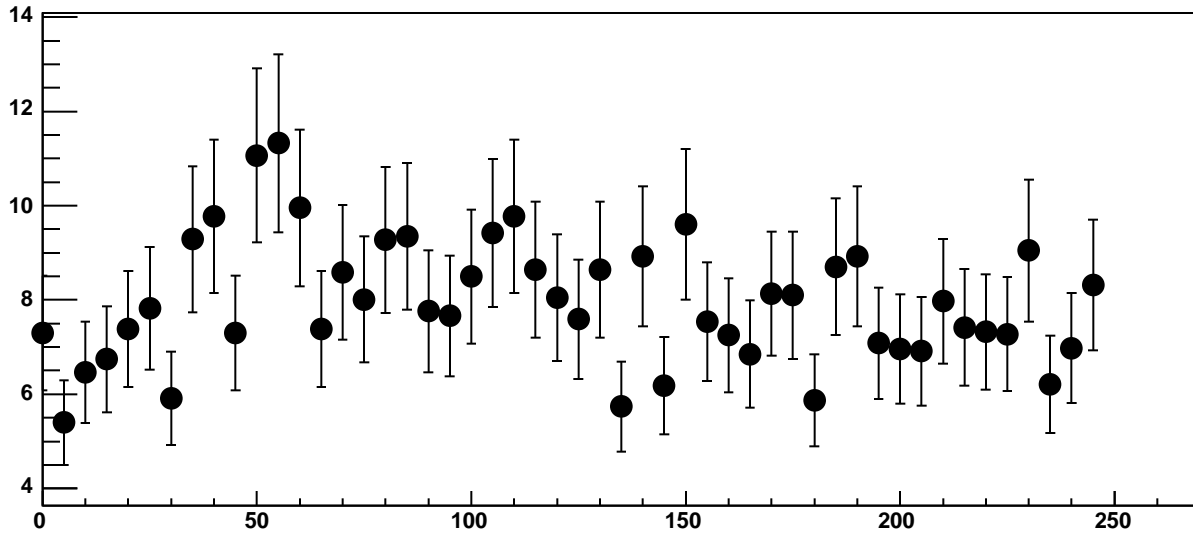


Chip 0, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

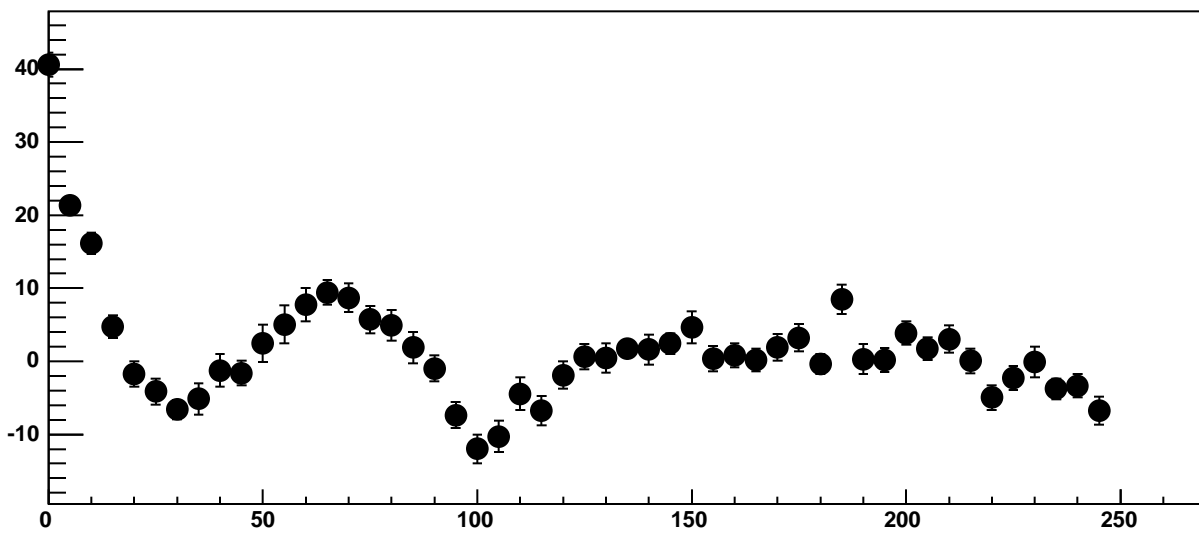


χ^2 / ndf	287.8 / 41
p0	157.6 ± 0.6748
p1	101.5 ± 0.5916
p2	$-1.063\text{e}+08 \pm 1.078\text{e}+06$
p3	$5.871\text{e}+07 \pm 1.31\text{e}+05$
p4	1.777 ± 0.01589

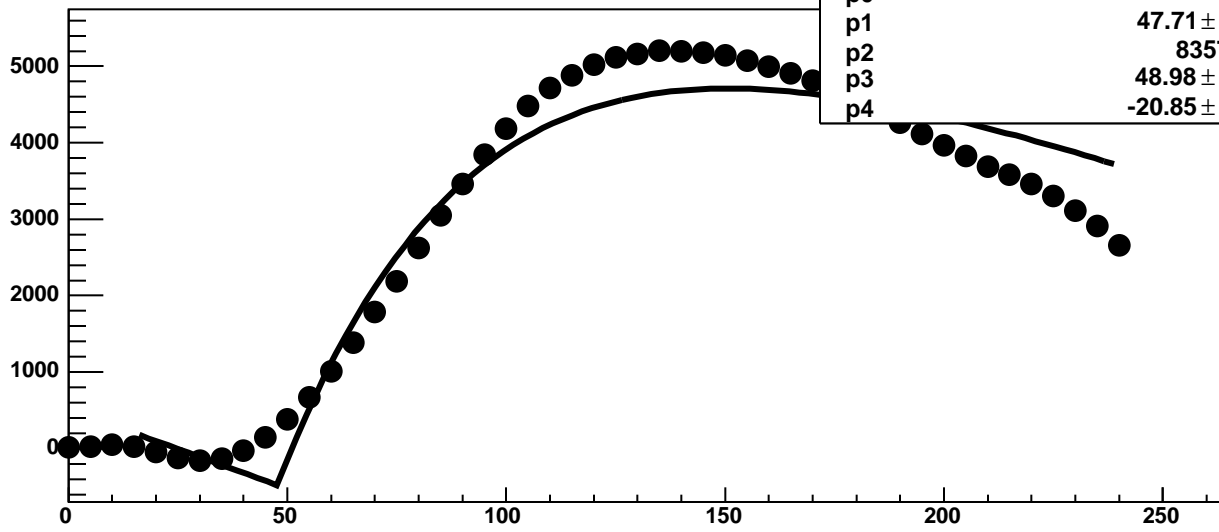
Chip 0, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

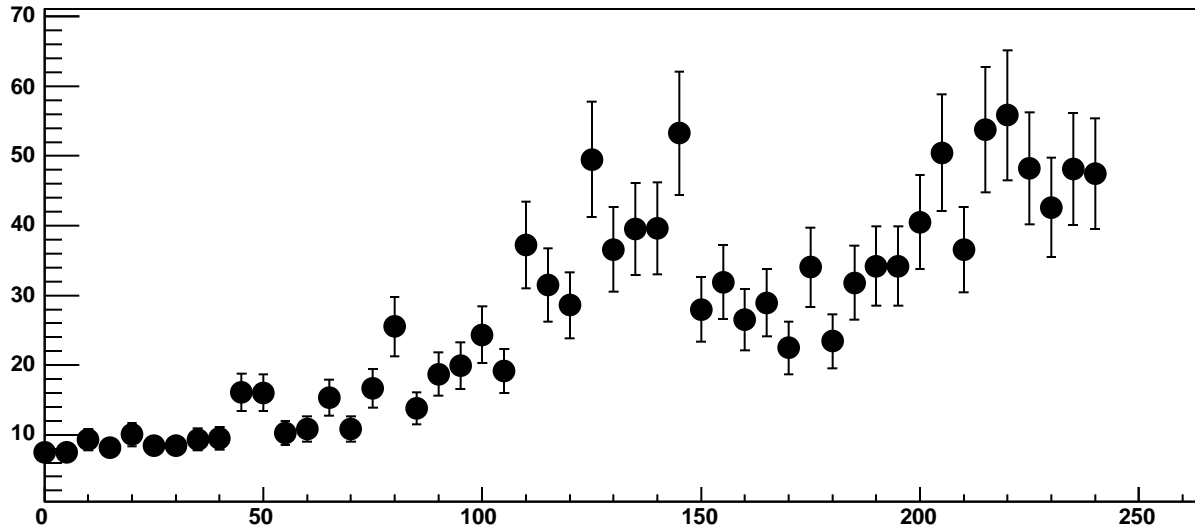


Chip 0, Channel 15, Enable 5, DAC=4000, ADC Mean vs Hold

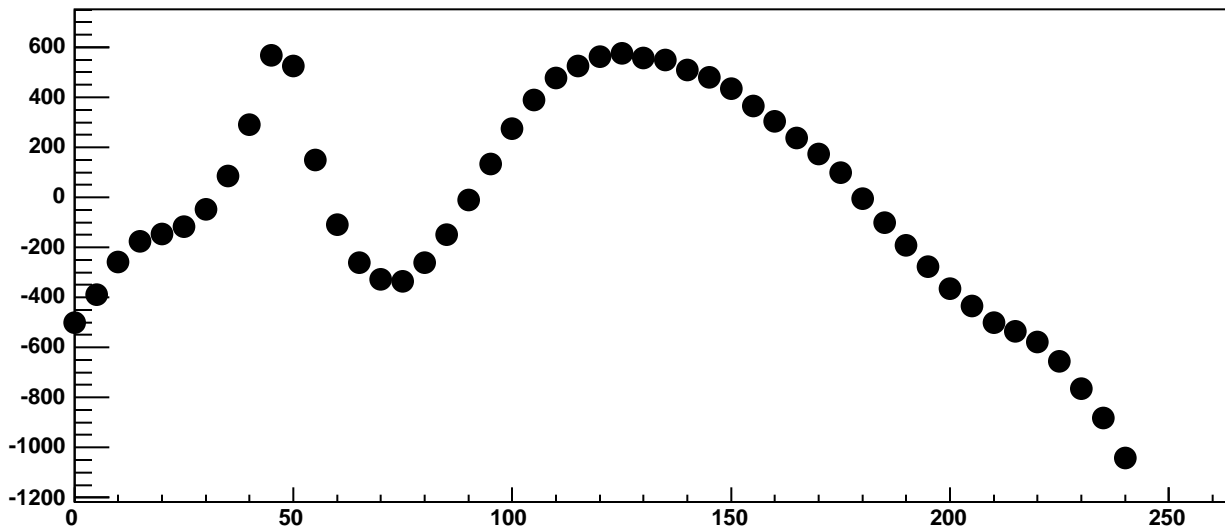


χ^2 / ndf	2.129e+05 / 41
p0	-480.3 ± 1.313
p1	47.71 ± 0.01639
p2	8357 ± 12.3
p3	48.98 ± 0.07324
p4	-20.85 ± 0.06236

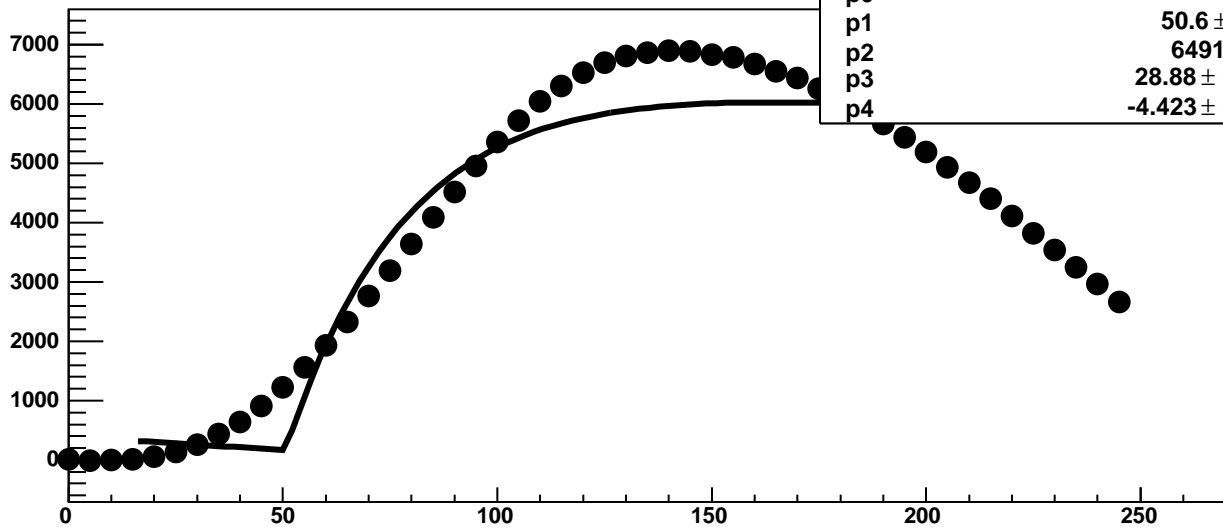
Chip 0, Channel 15, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 15, Enable 5, DAC=4000, ADC Residuals vs Hold

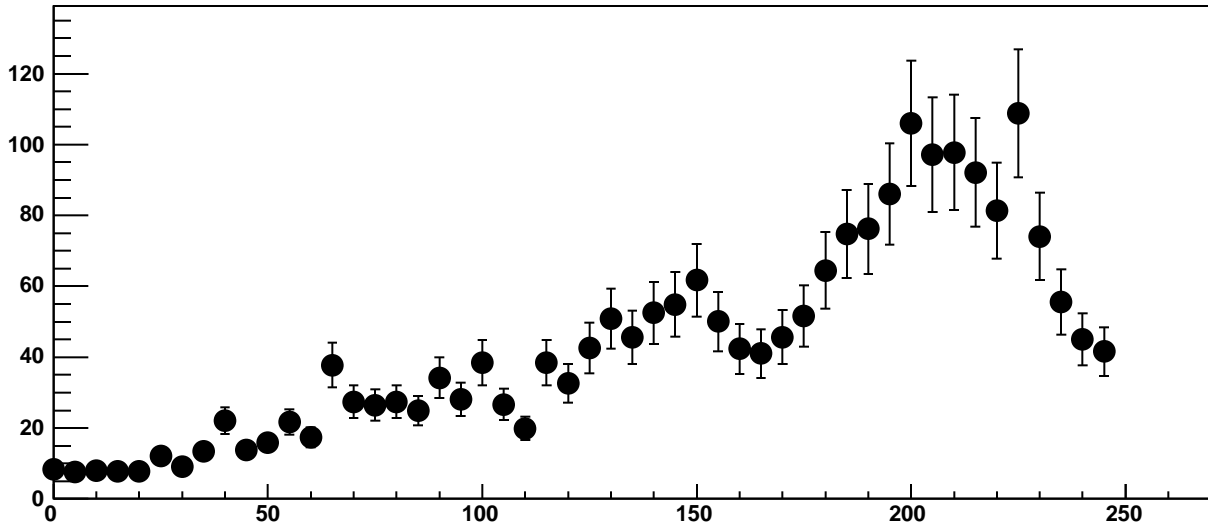


Chip 0, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

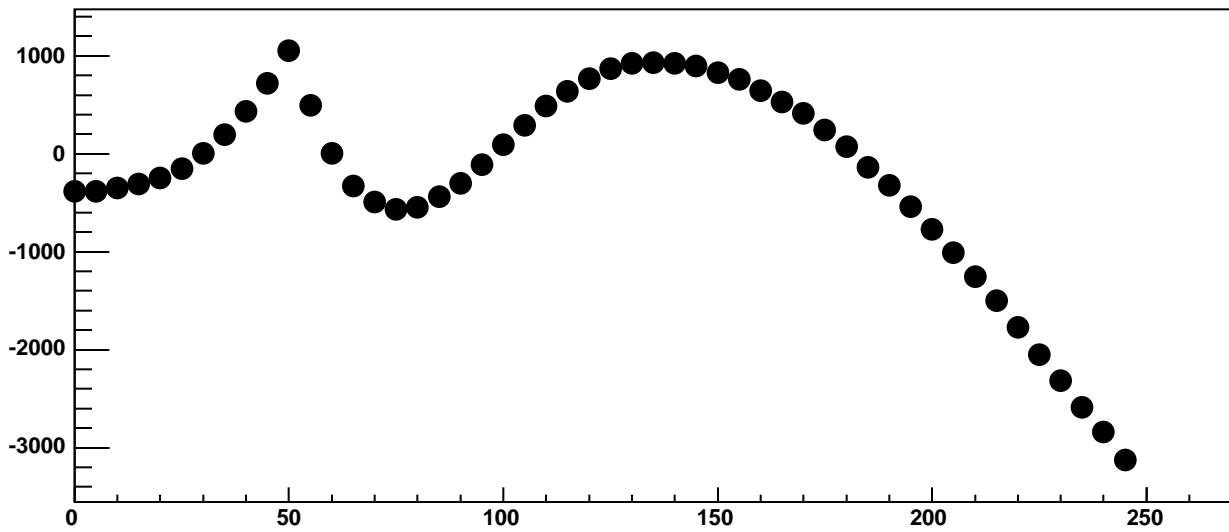


χ^2 / ndf	4.867e+05 / 41
p0	165.7 ± 1.511
p1	50.6 ± 0.0224
p2	6491 ± 8.324
p3	28.88 ± 0.05412
p4	-4.423 ± 0.05263

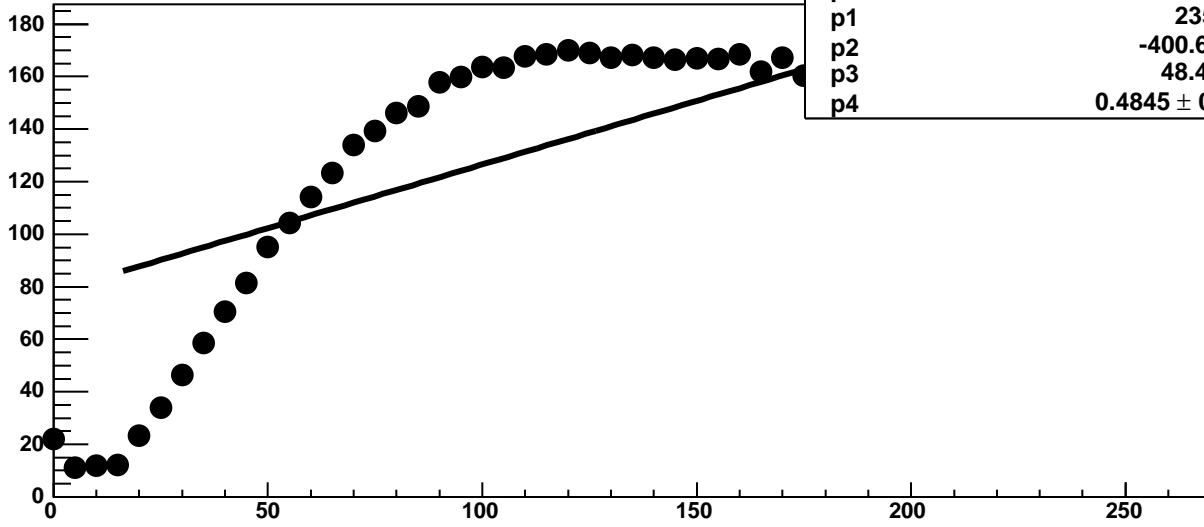
Chip 0, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

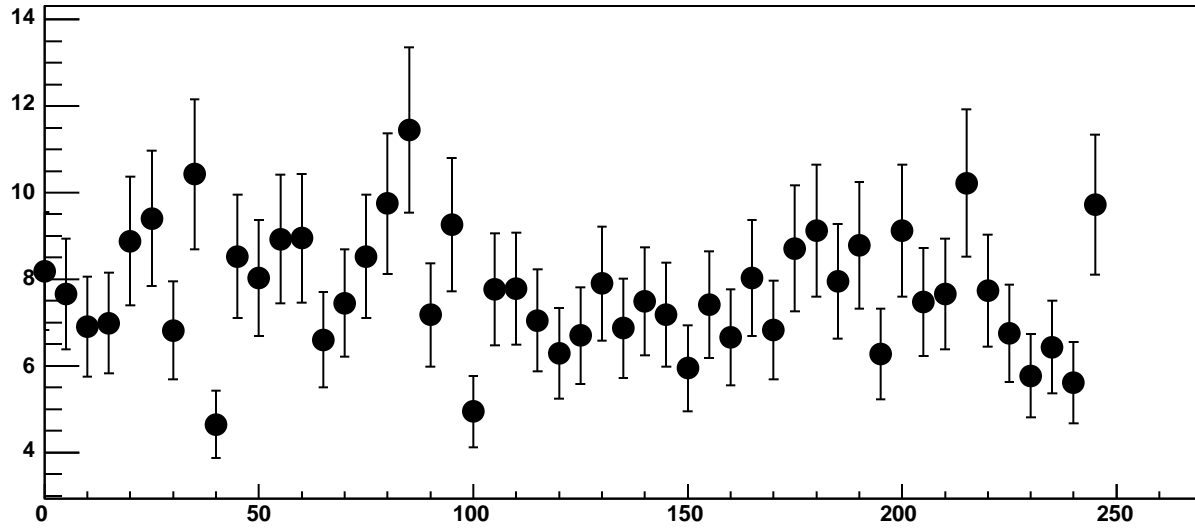


Chip 0, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold

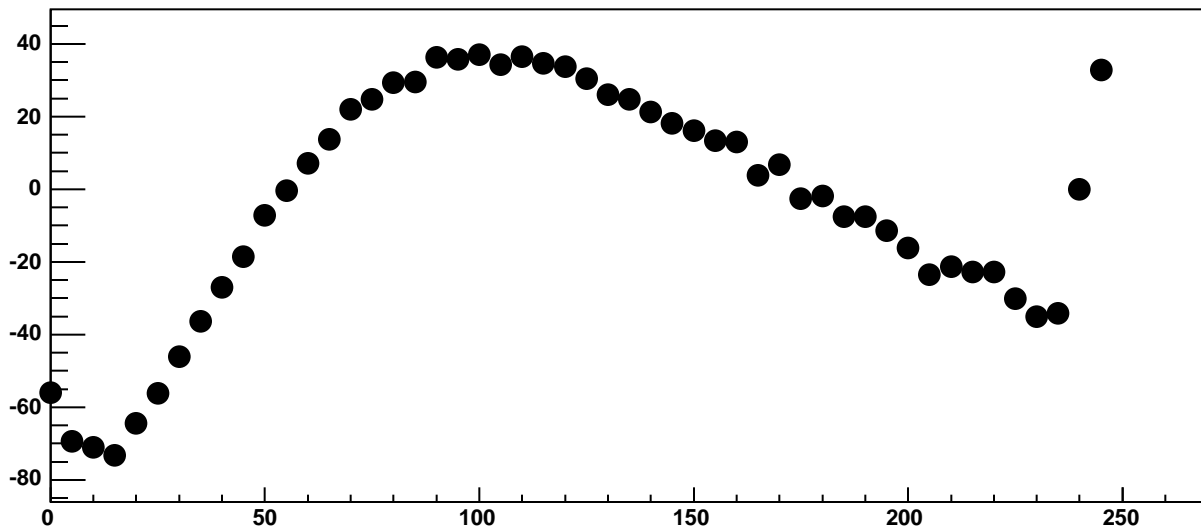


χ^2 / ndf	1.374e+04 / 41
p0	192 ± 1.202
p1	235 ± 2.28
p2	-400.6 ± 167.8
p3	48.4 ± 17.62
p4	0.4845 ± 0.003881

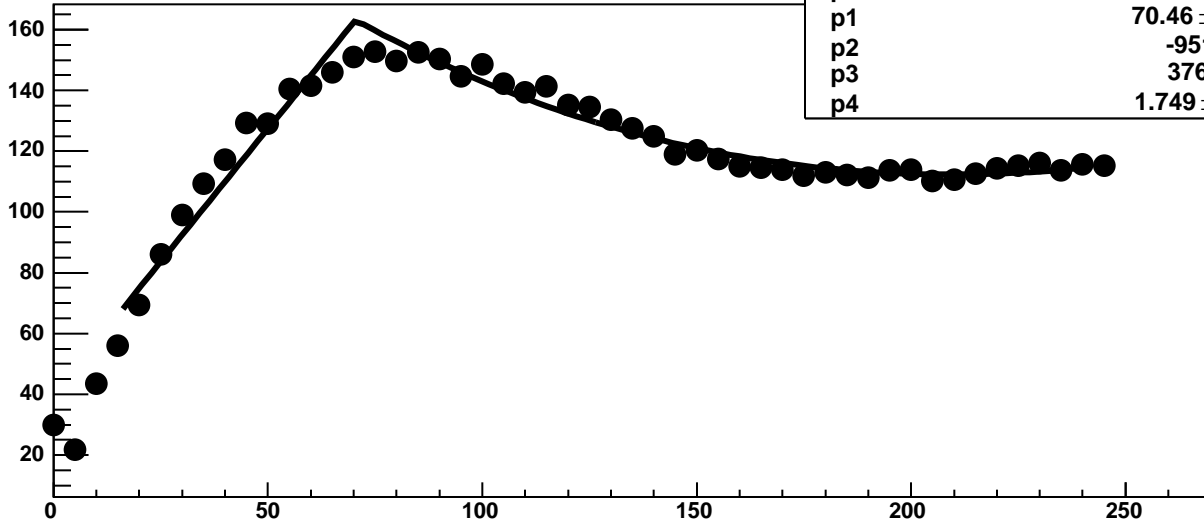
Chip 0, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold

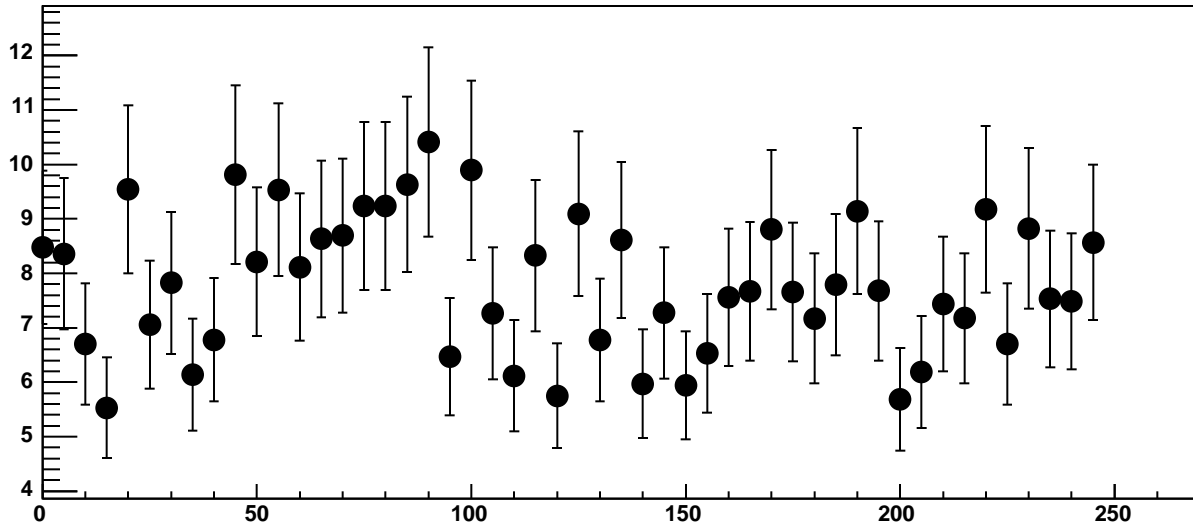


Chip 0, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold

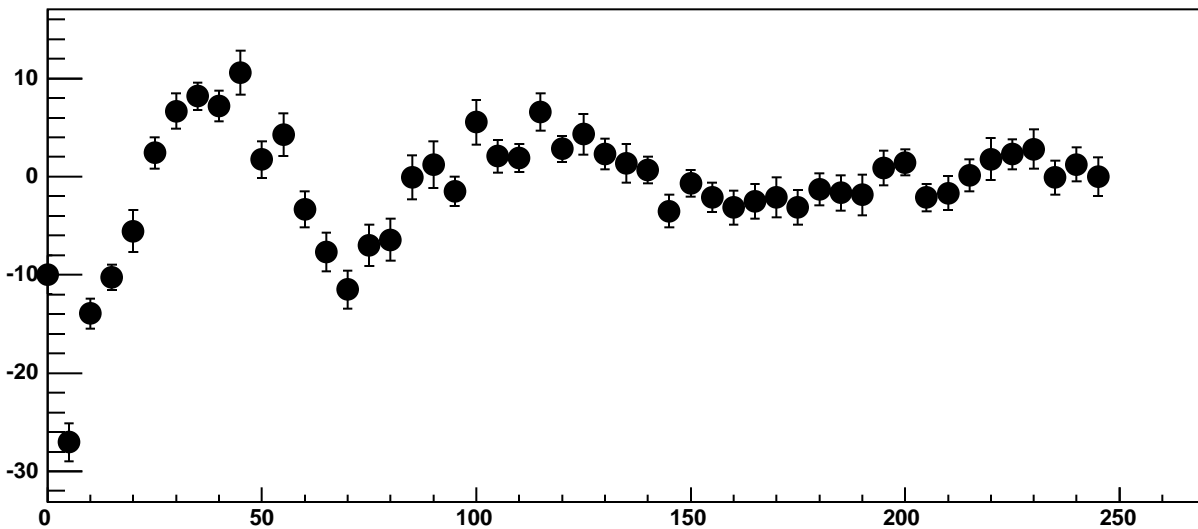


χ^2 / ndf	305.9 / 41
p0	163.2 ± 0.8342
p1	70.46 ± 0.6066
p2	-951 ± 47.61
p3	376 ± 20.84
p4	1.749 ± 0.0289

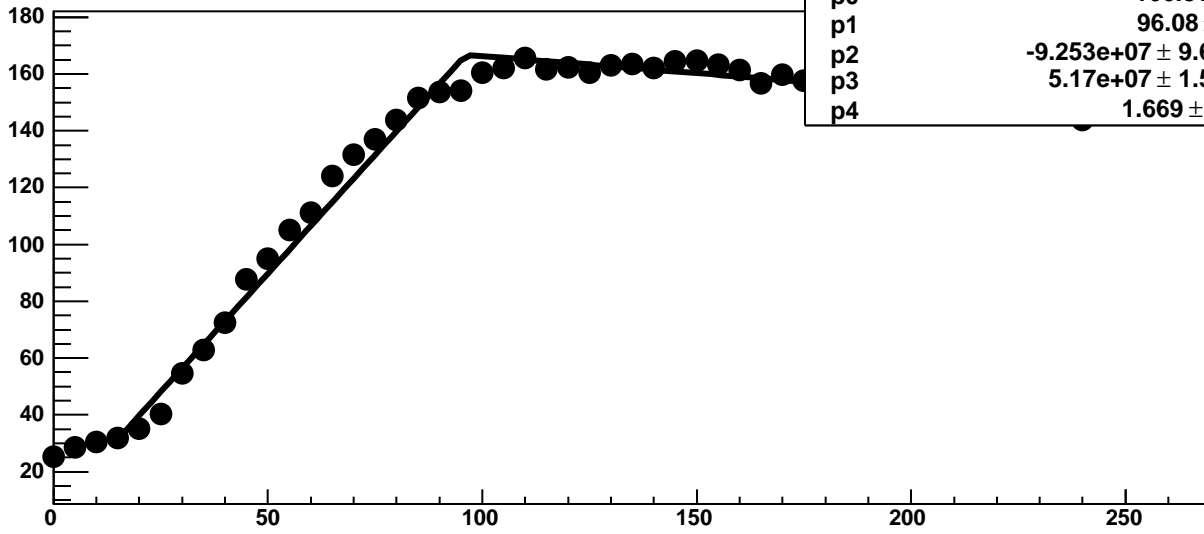
Chip 0, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold

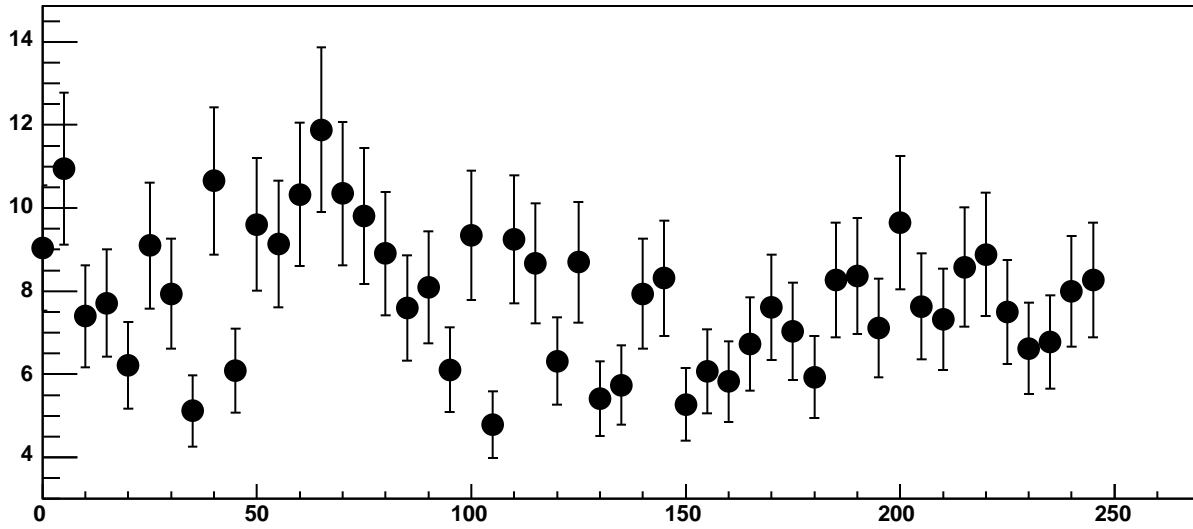


Chip 0, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

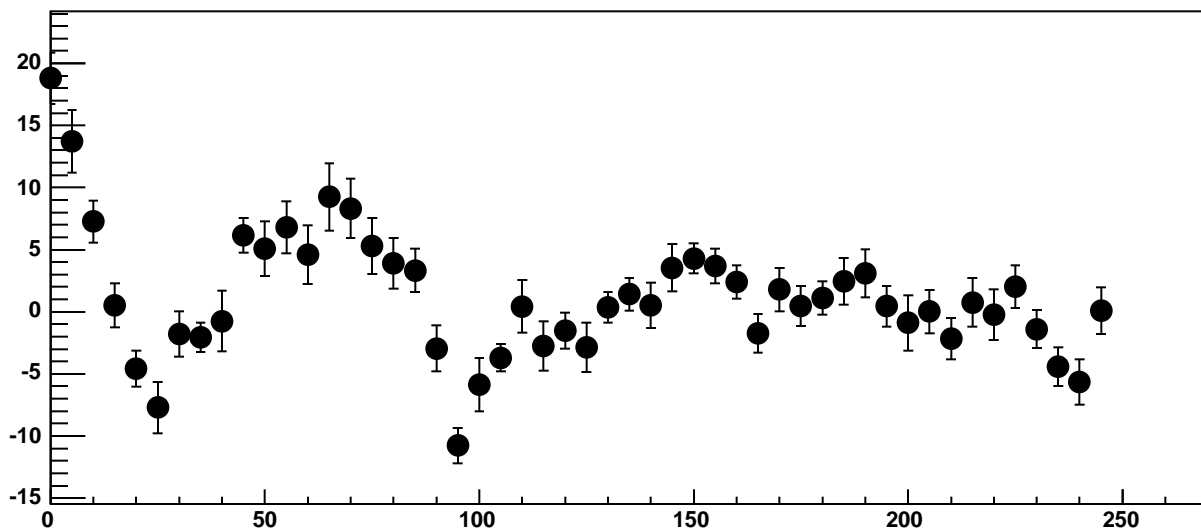


χ^2 / ndf	246.5 / 41
p0	166.8 ± 0.5509
p1	96.08 ± 0.5741
p2	$-9.253\text{e}+07 \pm 9.663\text{e}+05$
p3	$5.17\text{e}+07 \pm 1.533\text{e}+05$
p4	1.669 ± 0.01661

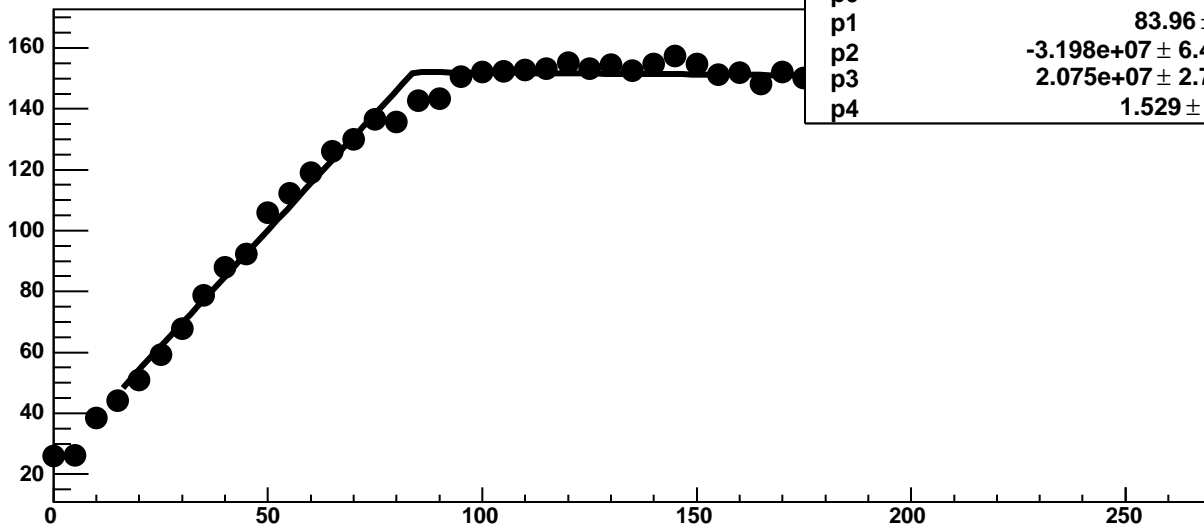
Chip 0, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

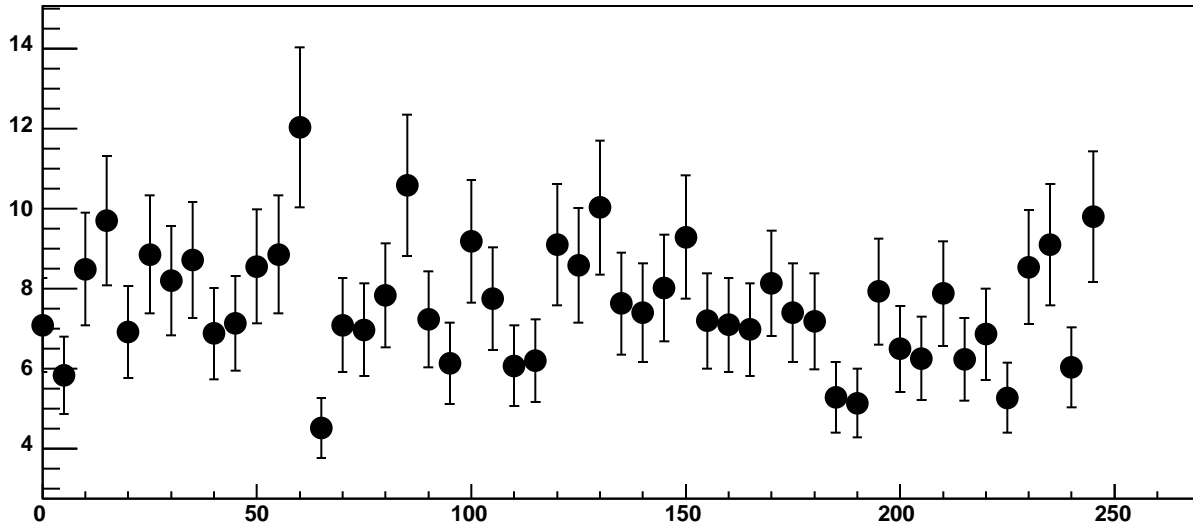


Chip 0, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

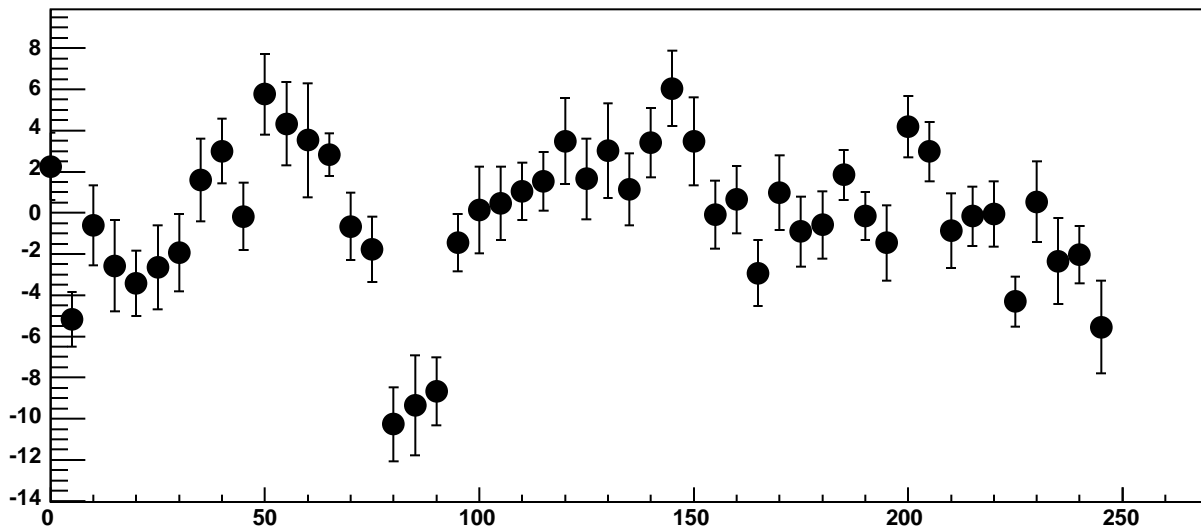


χ^2 / ndf	173.3 / 41
p0	152.1 ± 0.5967
p1	83.96 ± 0.6993
p2	$-3.198\text{e}+07 \pm 6.493\text{e}+05$
p3	$2.075\text{e}+07 \pm 2.729\text{e}+05$
p4	1.529 ± 0.02307

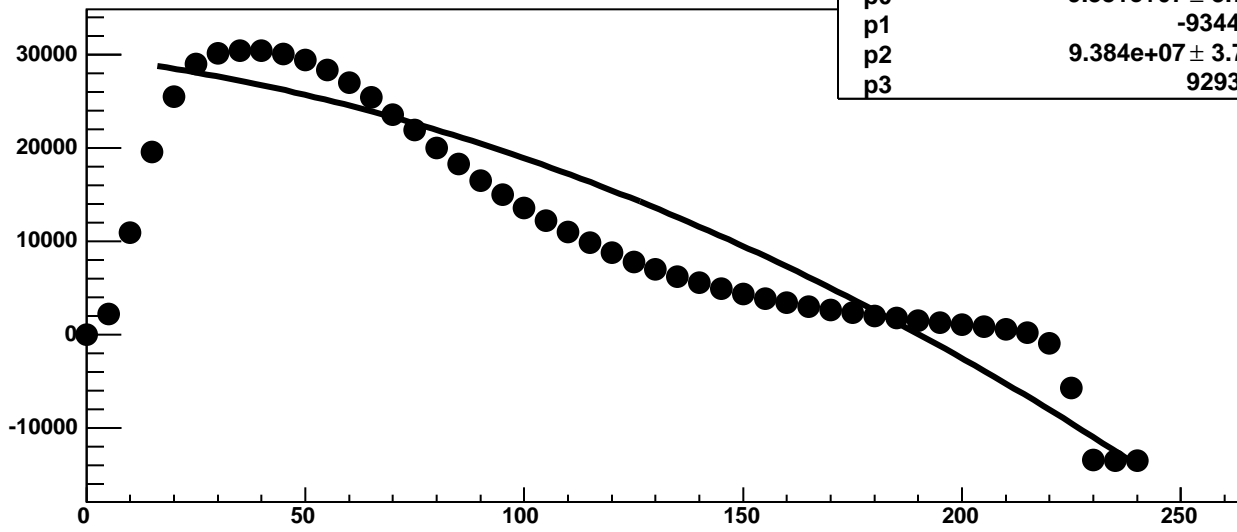
Chip 0, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

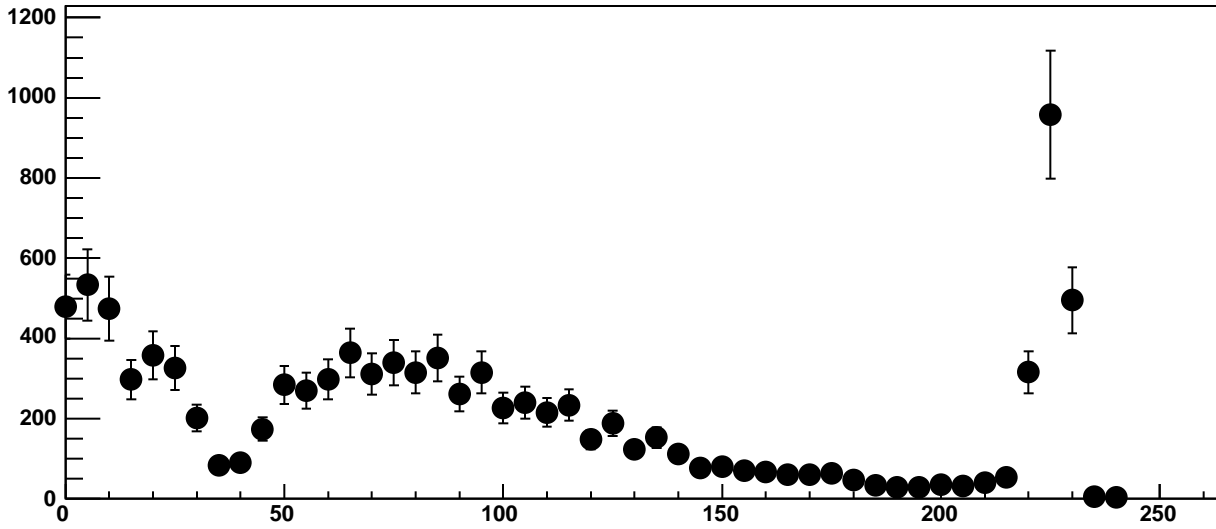


Chip 0, Channel 16, Enable 5!, DAC=4000, ADC Mean vs Hold

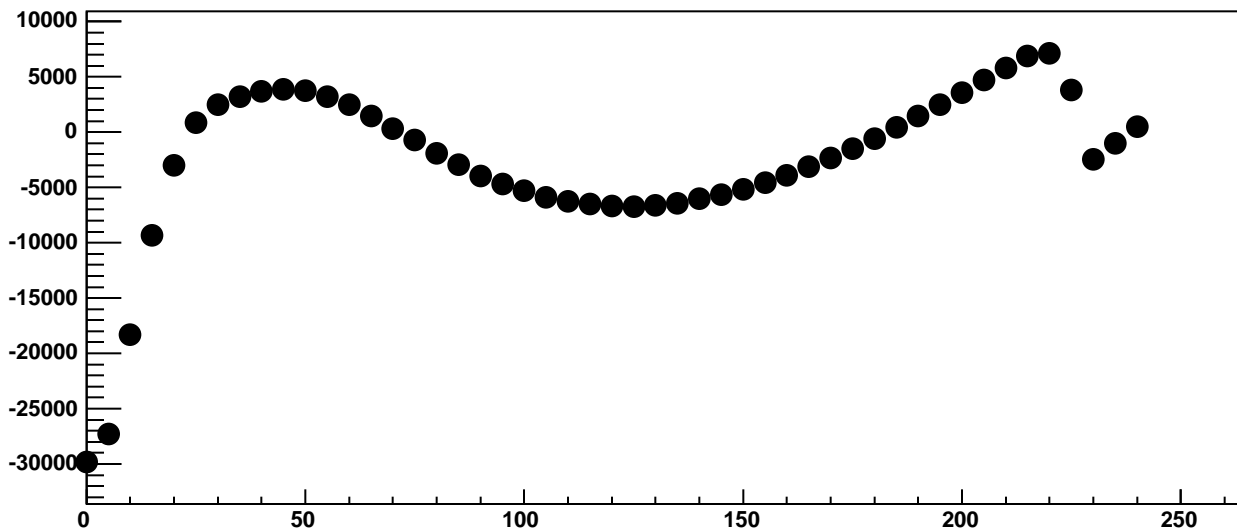


χ^2 / ndf	3.127e+06 / 42
p0	-9.381e+07 ± 3.782e+05
p1	-9344 ± 20.16
p2	9.384e+07 ± 3.782e+05
p3	9293 ± 20.09

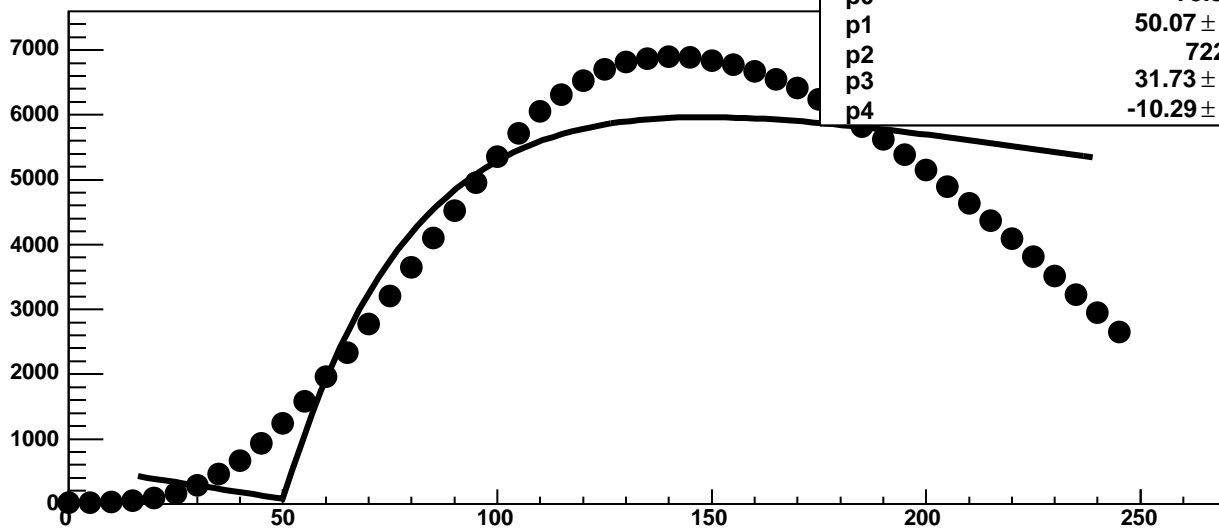
Chip 0, Channel 16, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 16, Enable 5!, DAC=4000, ADC Residuals vs Hold

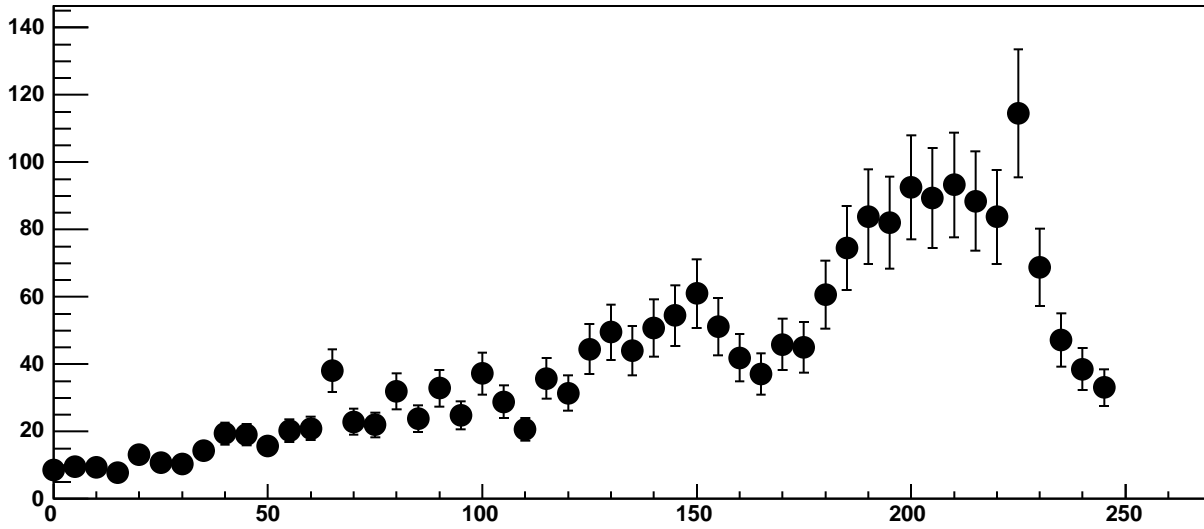


Chip 0, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

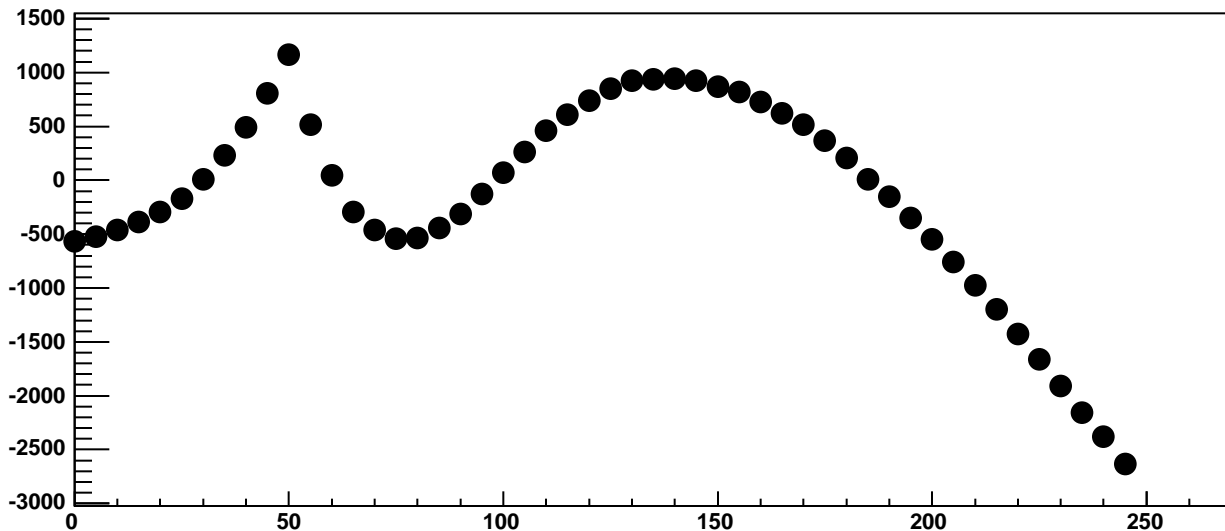


χ^2 / ndf	4.95e+05 / 41
p0	75.8 ± 1.548
p1	50.07 ± 0.02226
p2	7226 ± 9.1
p3	31.73 ± 0.05496
p4	-10.29 ± 0.05477

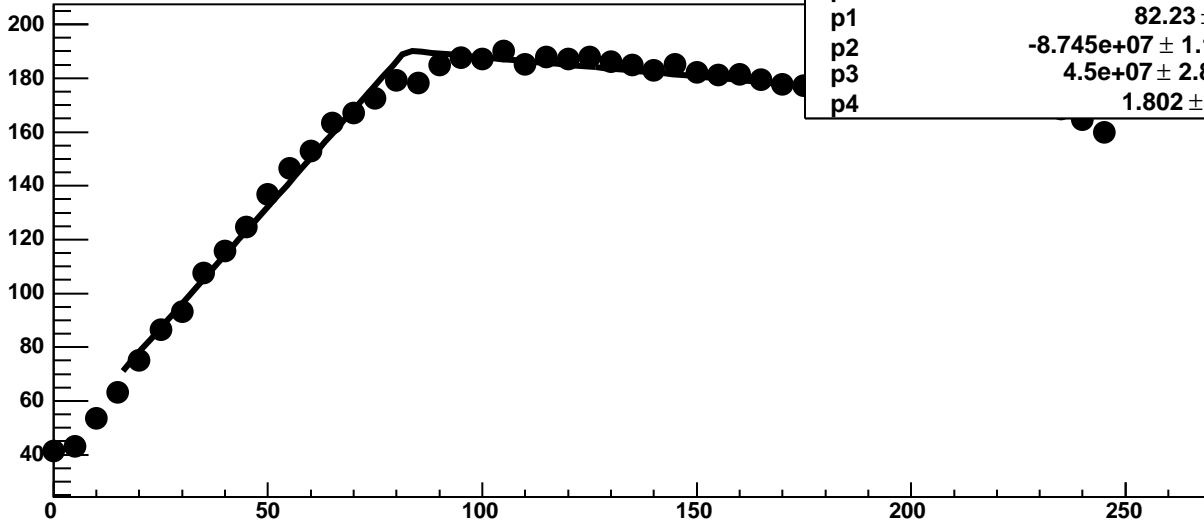
Chip 0, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

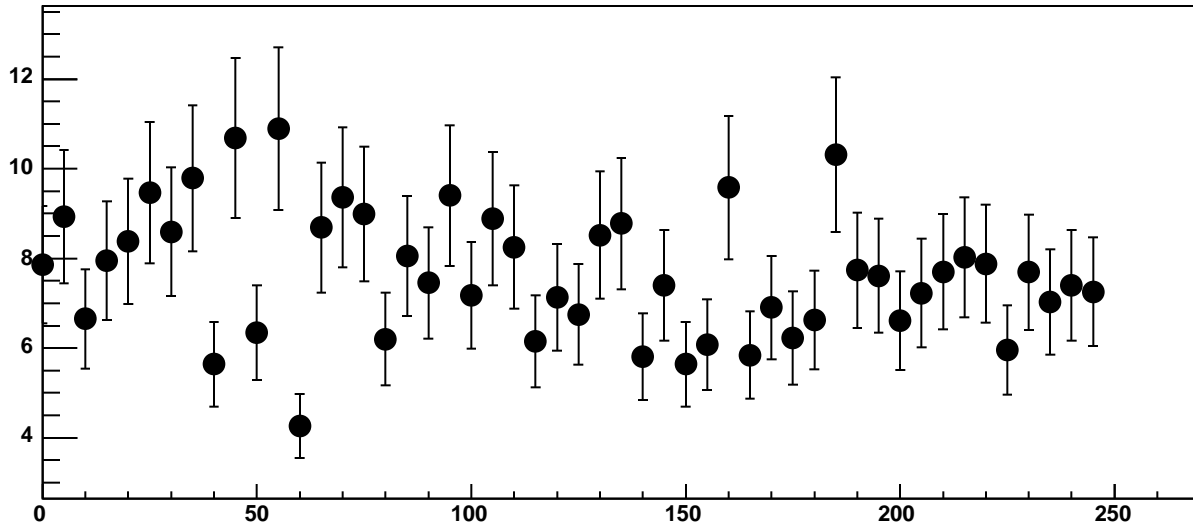


Chip 0, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

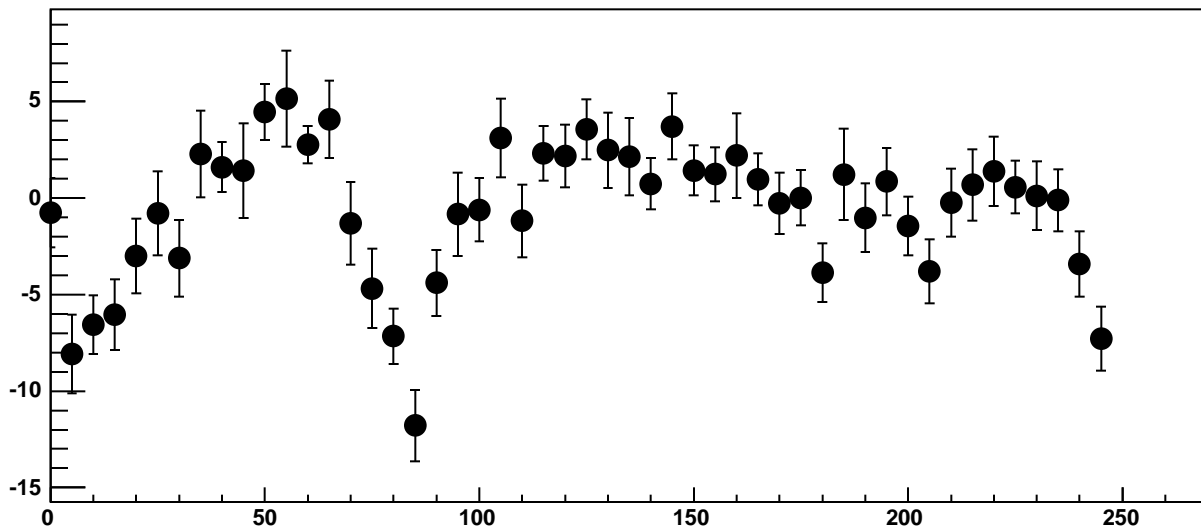


χ^2 / ndf	165.5 / 41
p0	190.3 ± 0.571
p1	82.23 ± 0.5547
p2	$-8.745e+07 \pm 1.162e+06$
p3	$4.5e+07 \pm 2.869e+05$
p4	1.802 ± 0.02361

Chip 0, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold

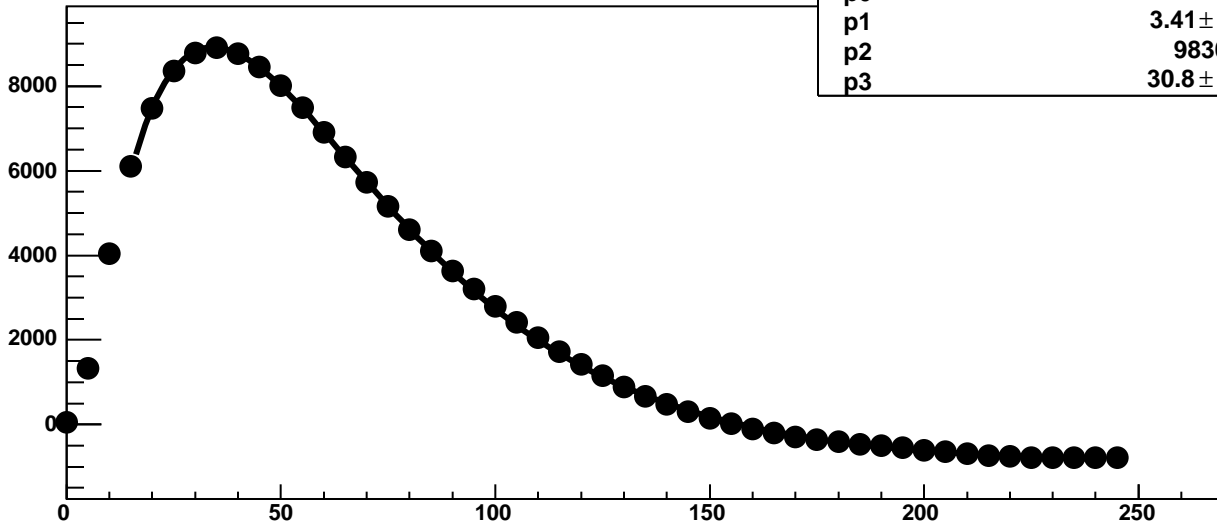


Chip 0, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

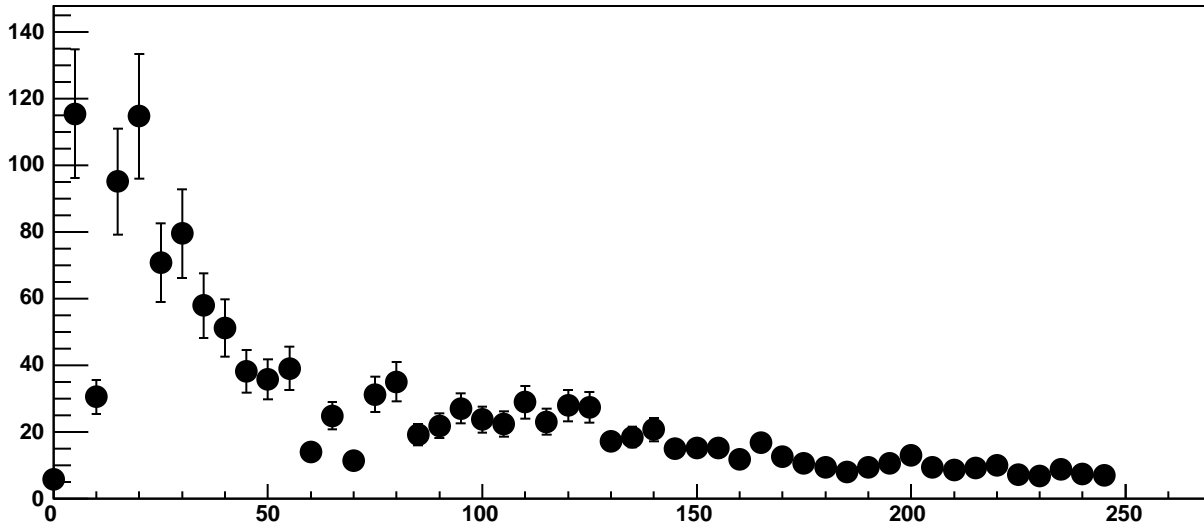


Chip 0, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold

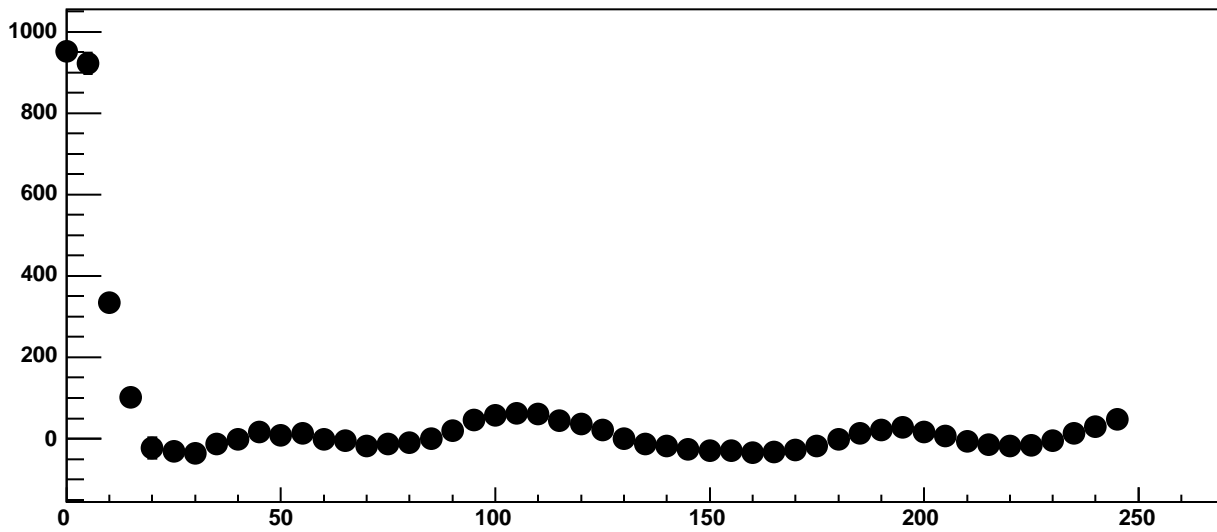
χ^2 / ndf	2103 / 42
p0	-906.8 ± 0.8227
p1	3.41 ± 0.03953
p2	9830 ± 4.14
p3	30.8 ± 0.01547



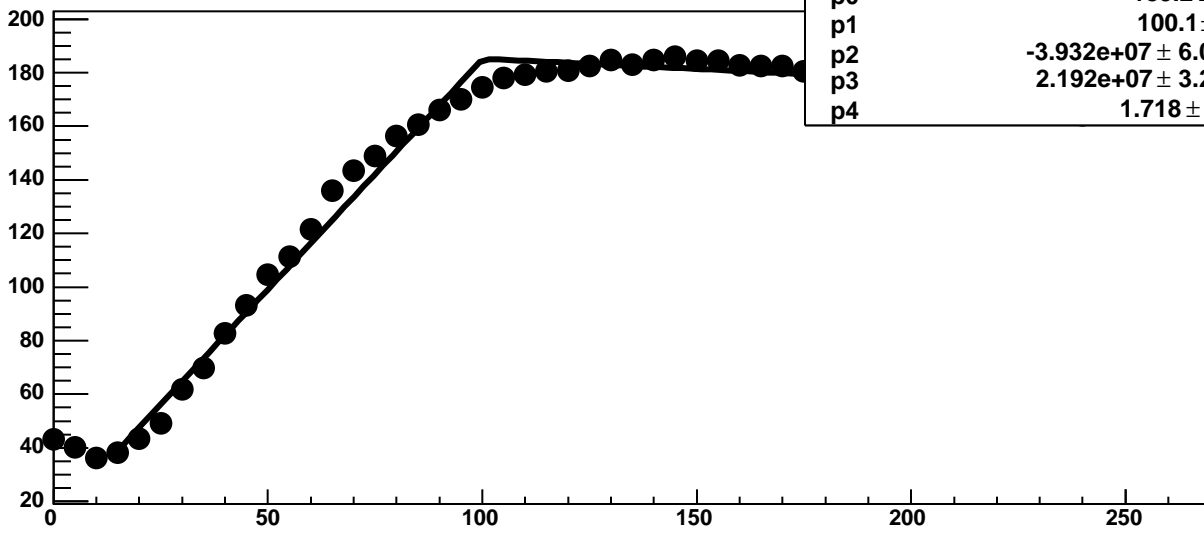
Chip 0, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold

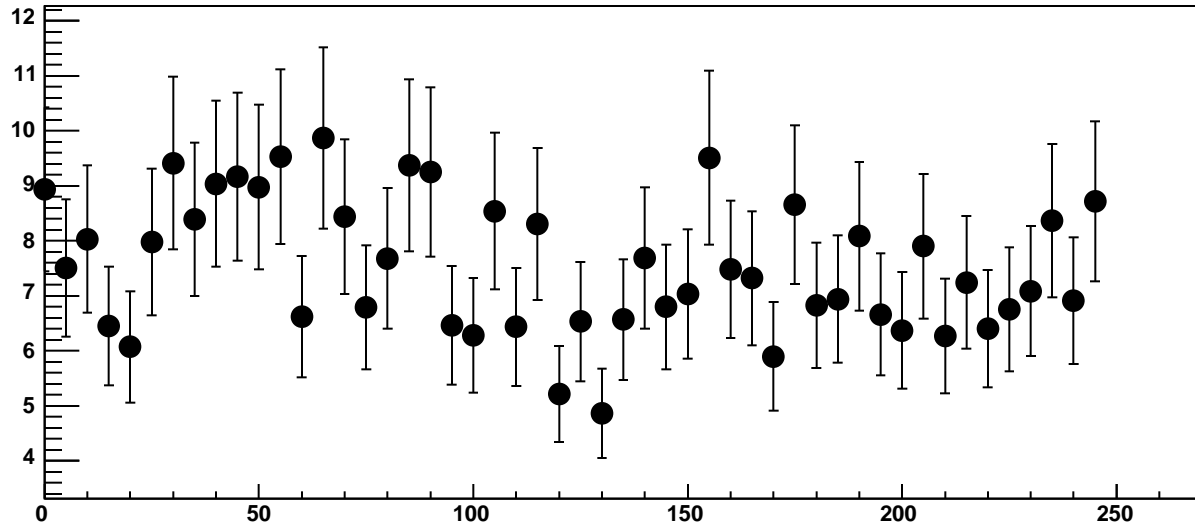


Chip 0, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold

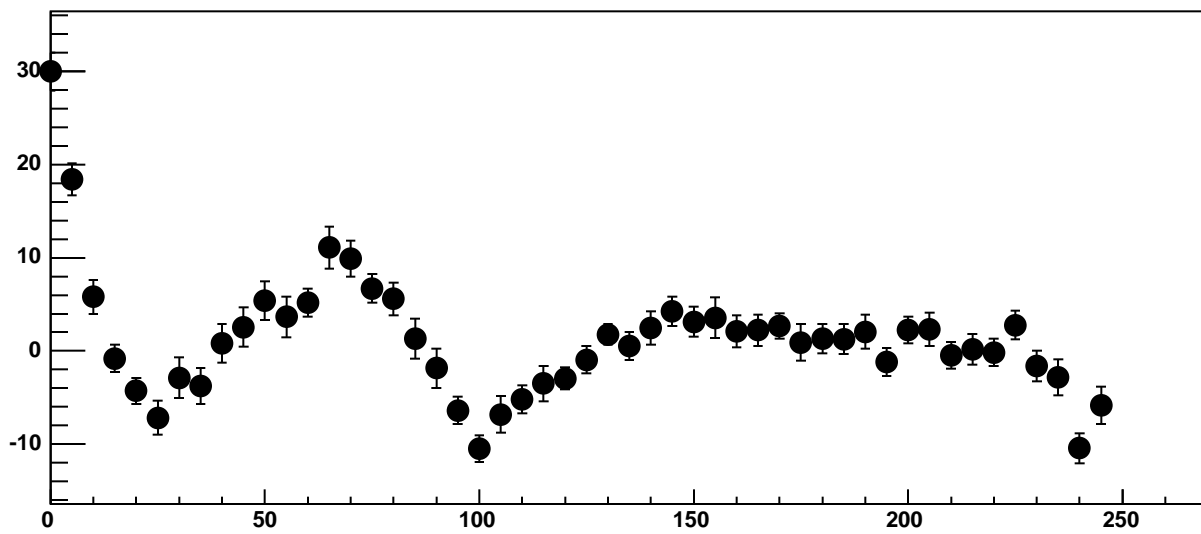


χ^2 / ndf	321.9 / 41
p0	185.2 ± 0.5714
p1	100.1 ± 0.5442
p2	$-3.932e+07 \pm 6.035e+05$
p3	$2.192e+07 \pm 3.227e+05$
p4	1.718 ± 0.01598

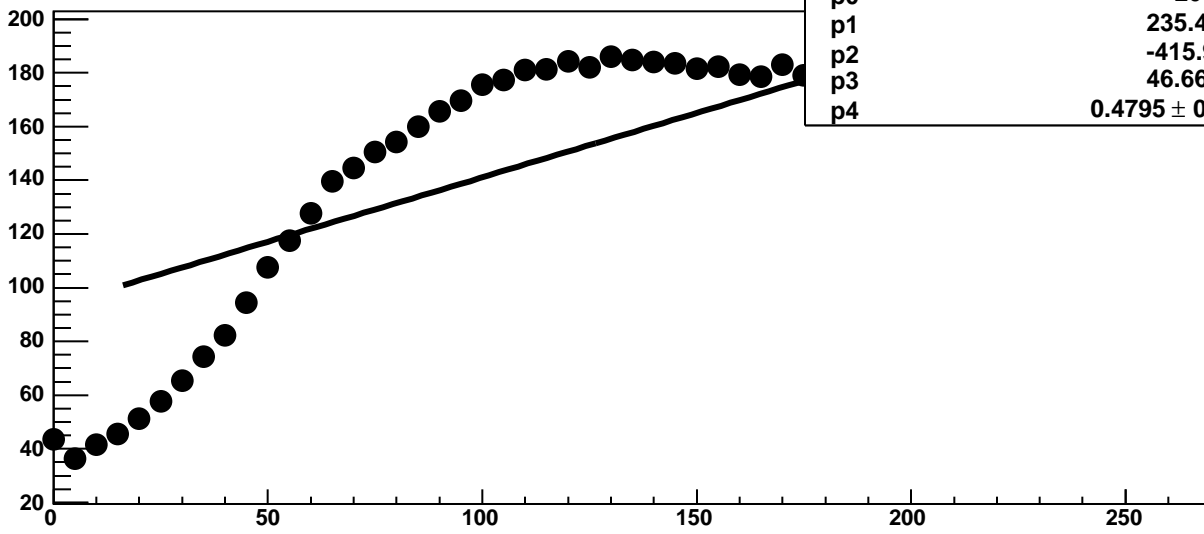
Chip 0, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold

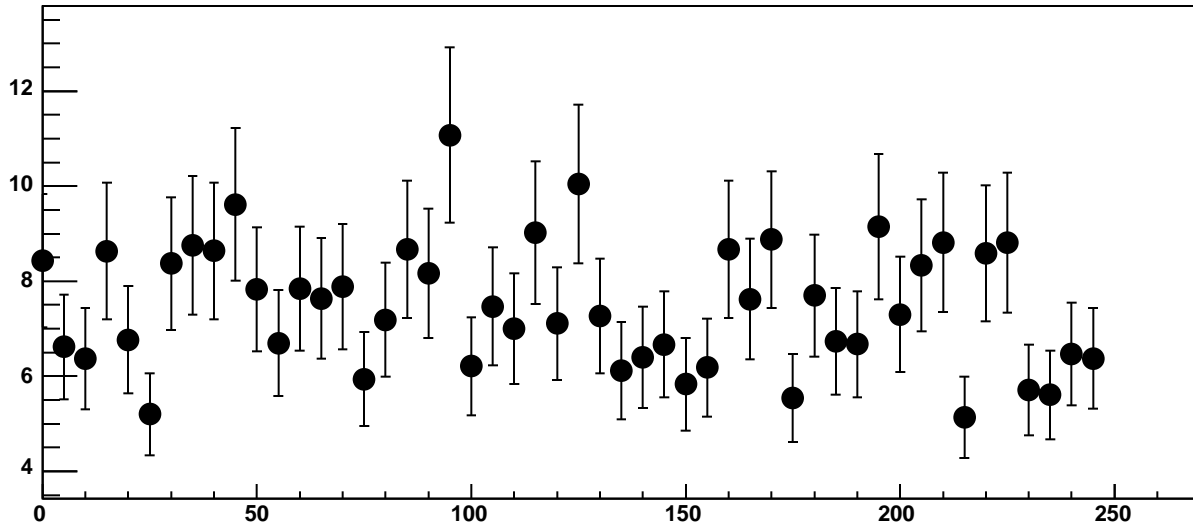


Chip 0, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold

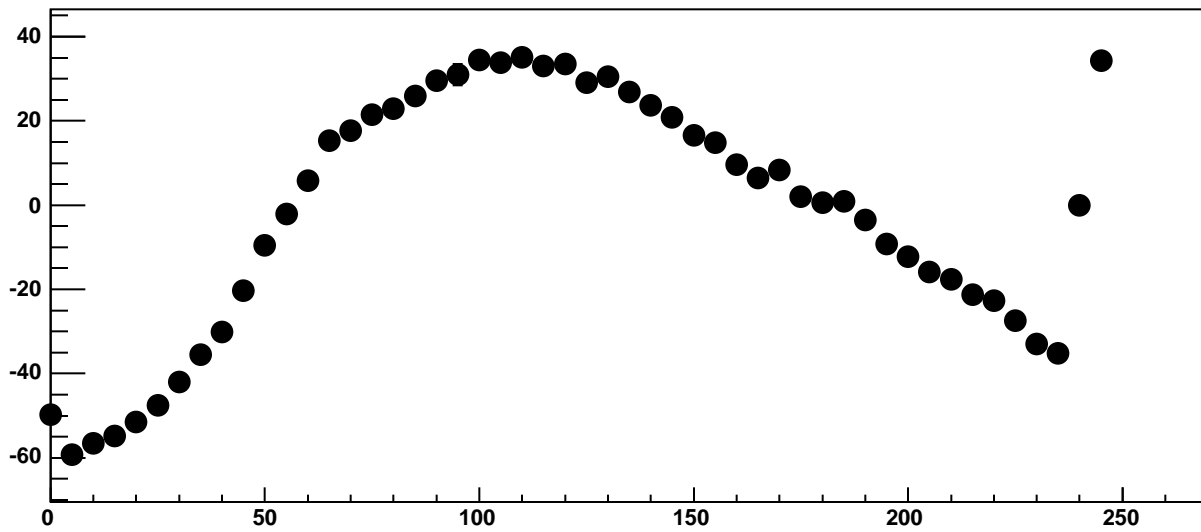


χ^2 / ndf	1.178e+04 / 41
p0	206 ± 1.22
p1	235.4 ± 2.364
p2	-415.9 ± 194
p3	46.66 ± 18.86
p4	0.4795 ± 0.003727

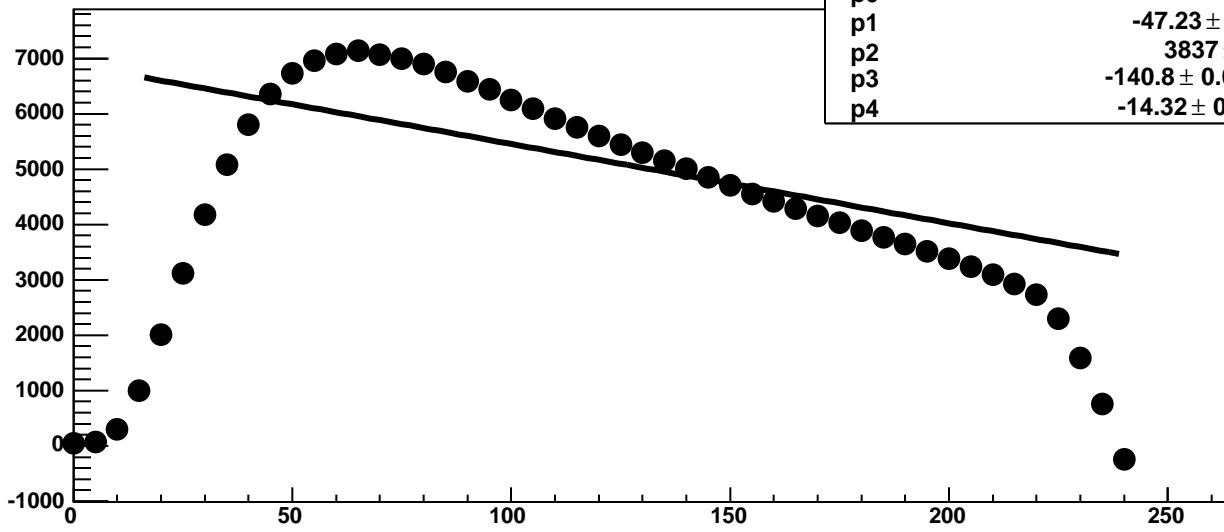
Chip 0, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

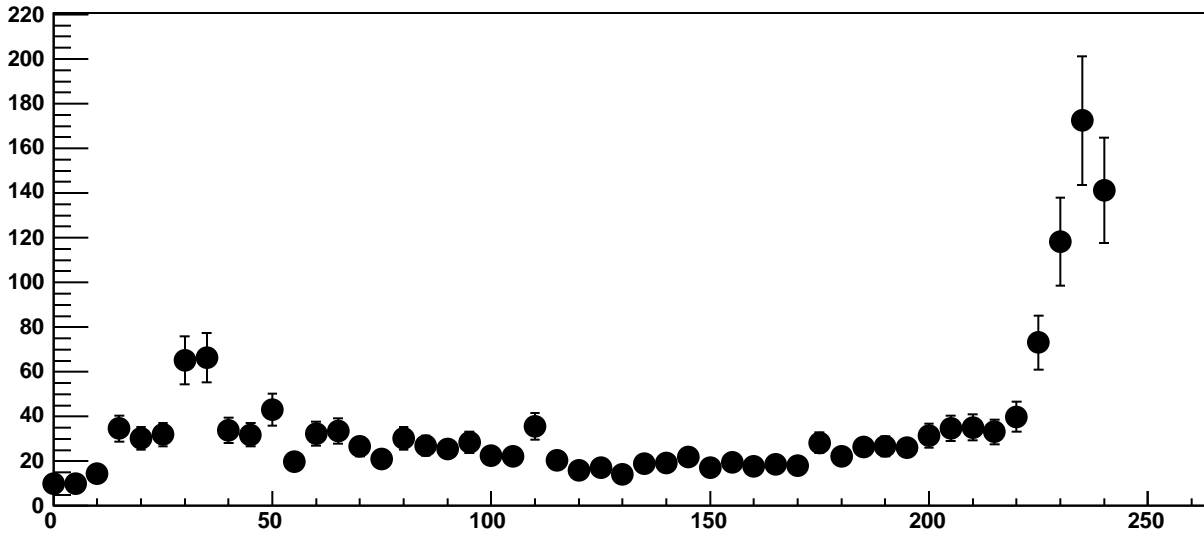


Chip 0, Channel 17, Enable 5, DAC=4000, ADC Mean vs Hold

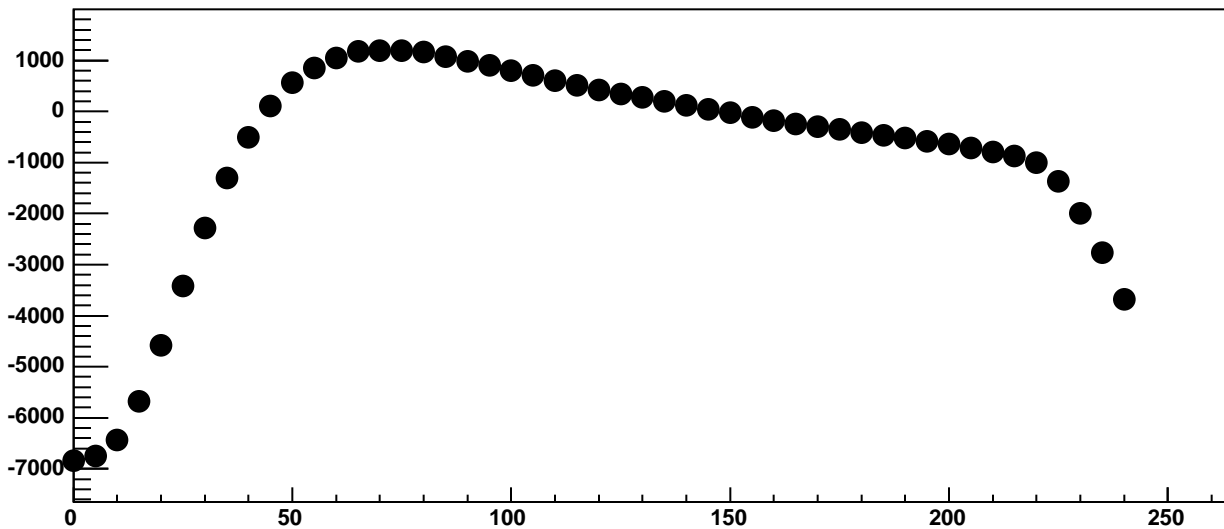


χ^2 / ndf	1.697e+06 / 41
p0	3725 ± 0.8918
p1	-47.23 ± 0.06232
p2	3837 ± 0.8921
p3	-140.8 ± 0.0005825
p4	-14.32 ± 0.005416

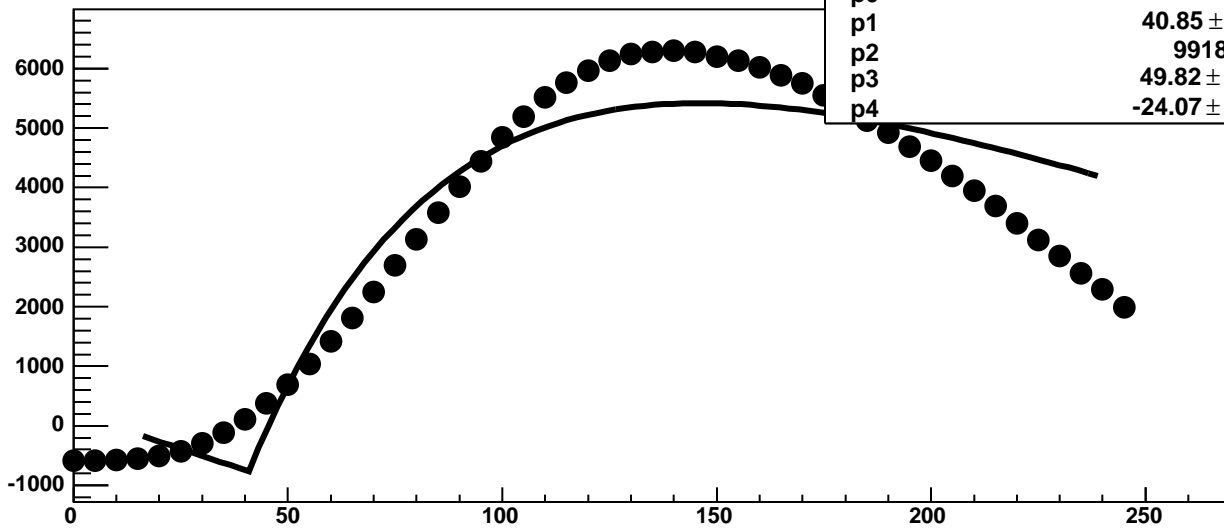
Chip 0, Channel 17, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 0, Channel 17, Enable 5, DAC=4000, ADC Residuals vs Hold

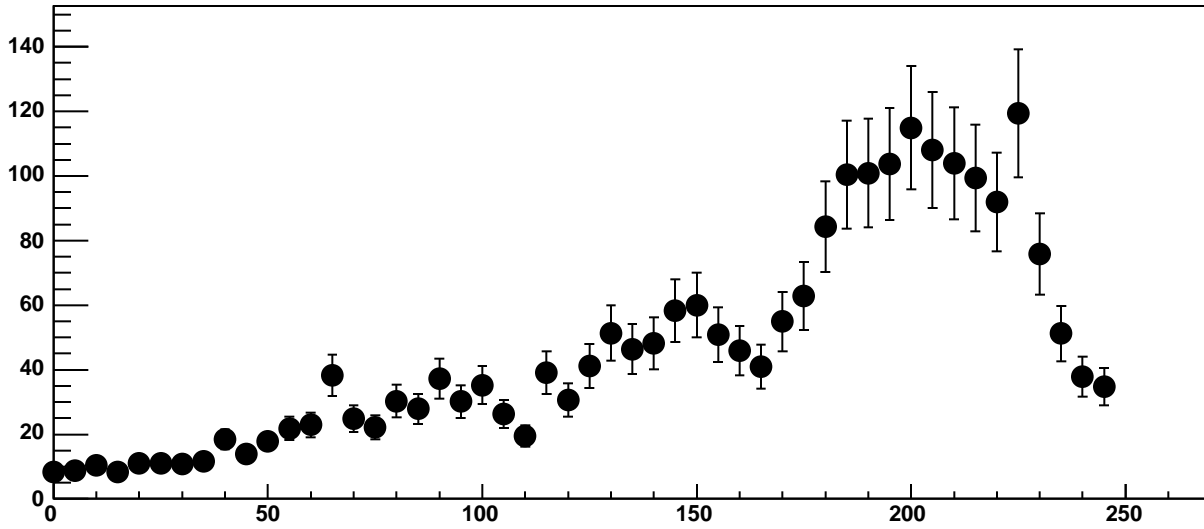


Chip 1, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold

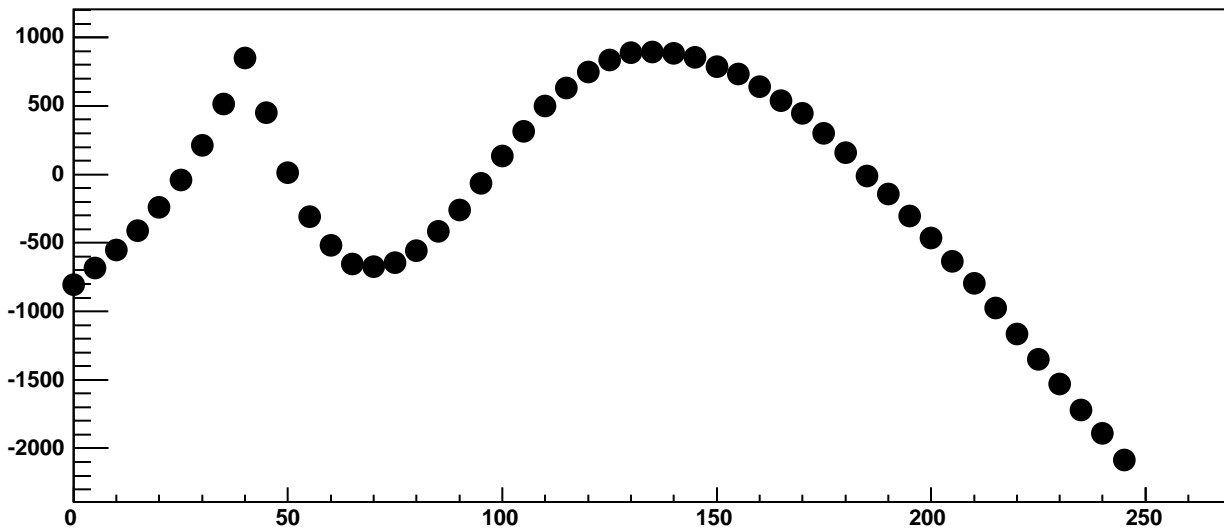


χ^2 / ndf	3.878e+05 / 41
p0	-765.2 ± 1.457
p1	40.85 ± 0.01751
p2	9918 ± 15.15
p3	49.82 ± 0.08106
p4	-24.07 ± 0.07659

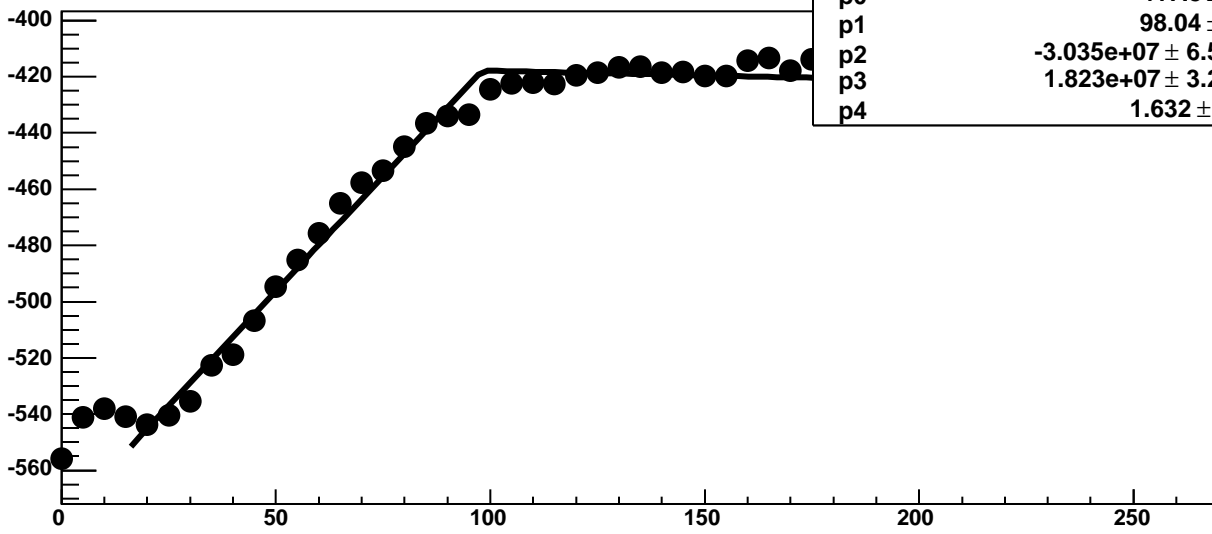
Chip 1, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold

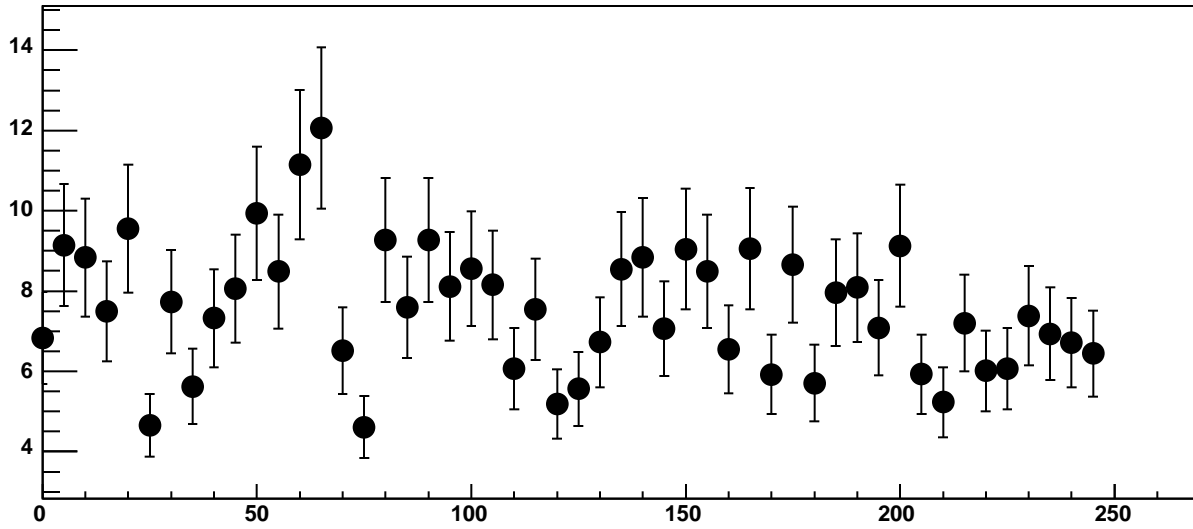


Chip 1, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

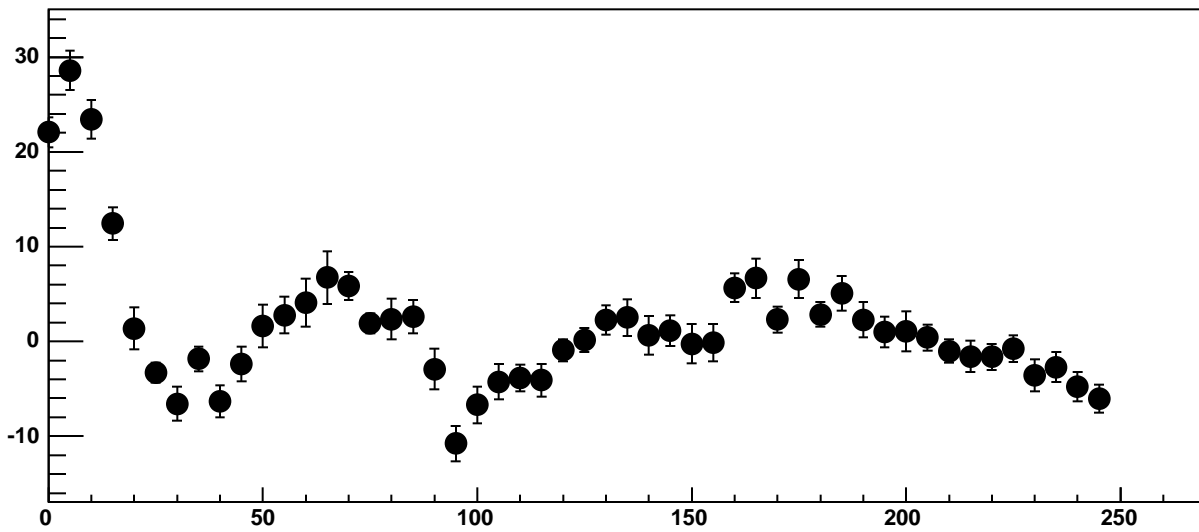


χ^2 / ndf	270.3 / 41
p0	-417.8 ± 0.5775
p1	98.04 ± 0.6186
p2	$-3.035e+07 \pm 6.589e+05$
p3	$1.823e+07 \pm 3.231e+05$
p4	1.632 ± 0.01631

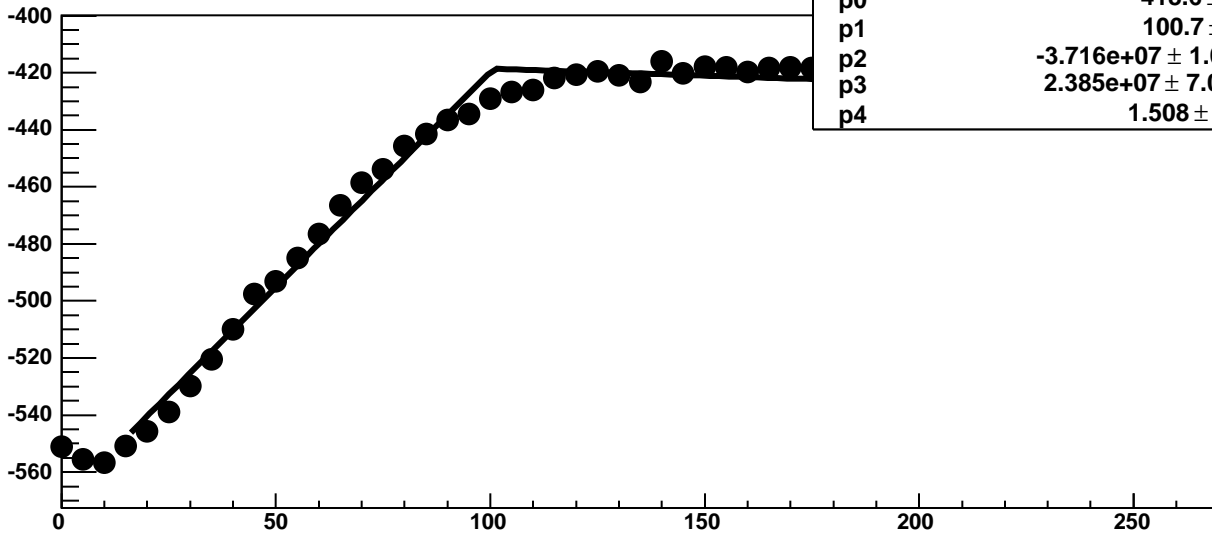
Chip 1, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold

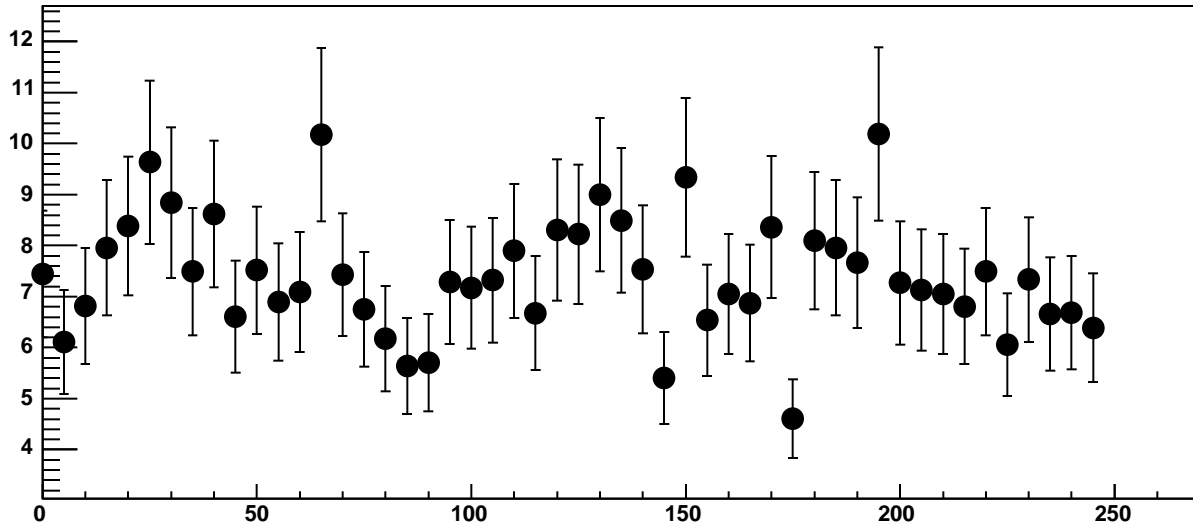


Chip 1, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

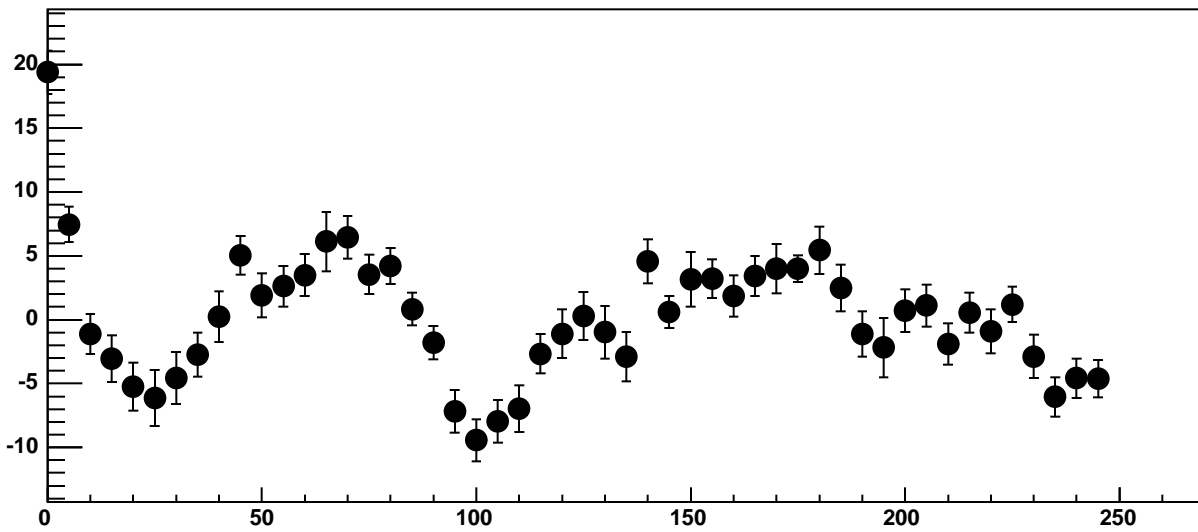


χ^2 / ndf	259.2 / 41
p0	-418.6 ± 0.6205
p1	100.7 ± 0.5958
p2	$-3.716\text{e}+07 \pm 1.079\text{e}+06$
p3	$2.385\text{e}+07 \pm 7.026\text{e}+05$
p4	1.508 ± 0.01443

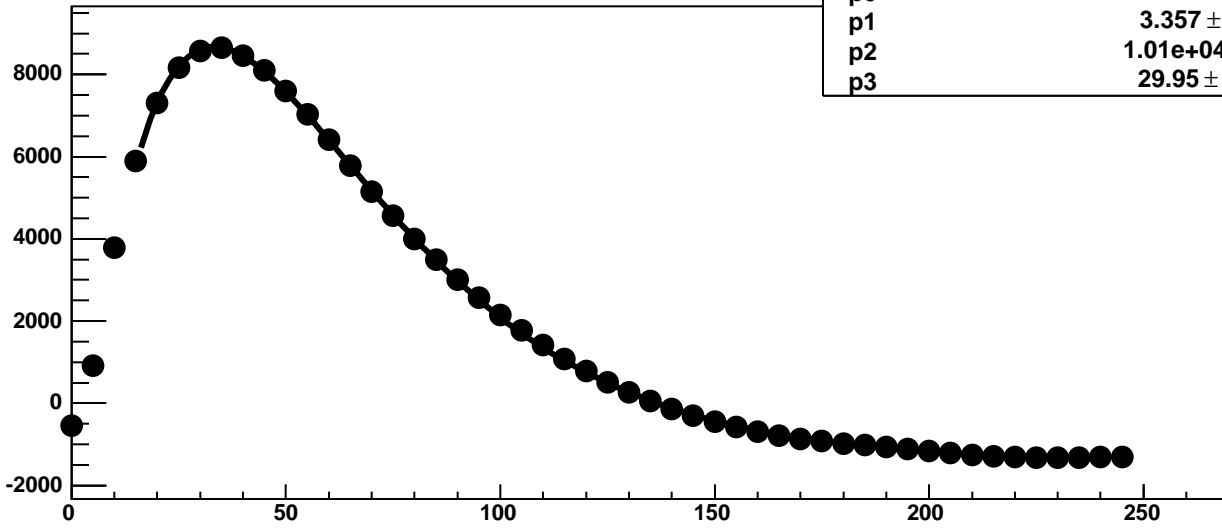
Chip 1, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

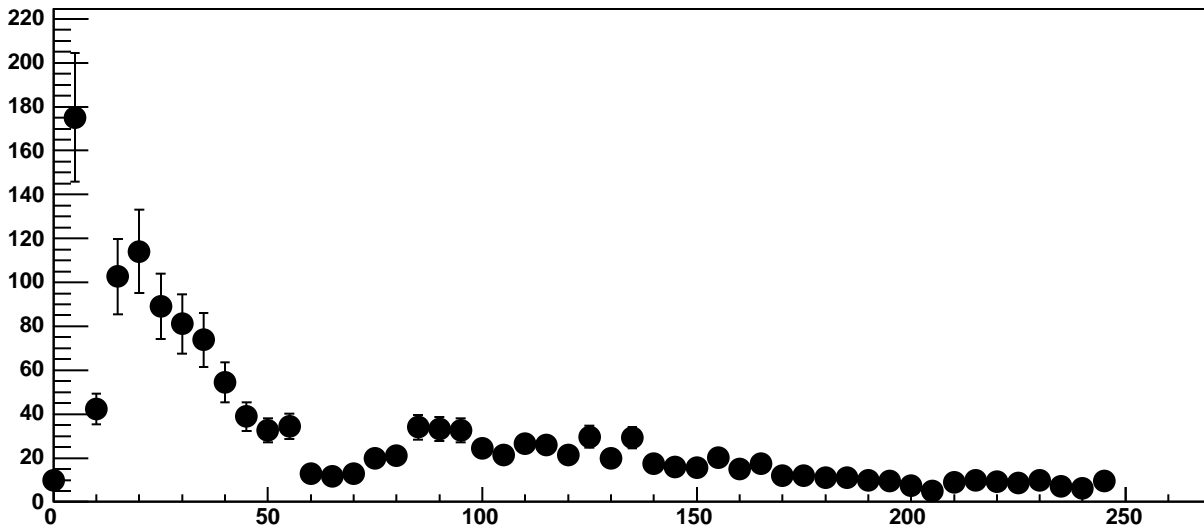


Chip 1, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold

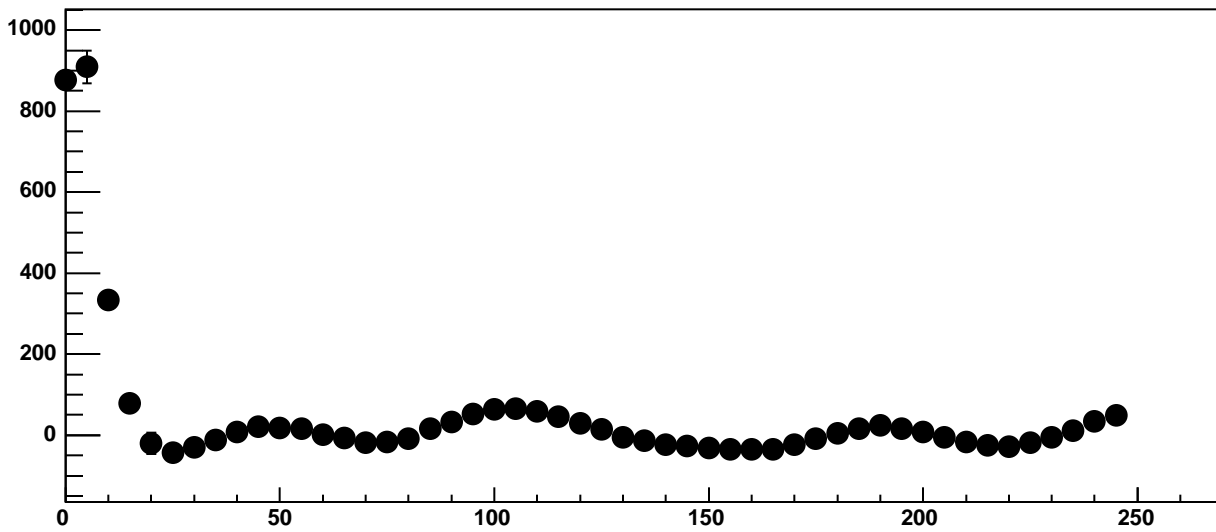


χ^2 / ndf	2462 / 42
p0	-1425 ± 0.7417
p1	3.357 ± 0.04119
p2	1.01e+04 ± 4.633
p3	29.95 ± 0.01539

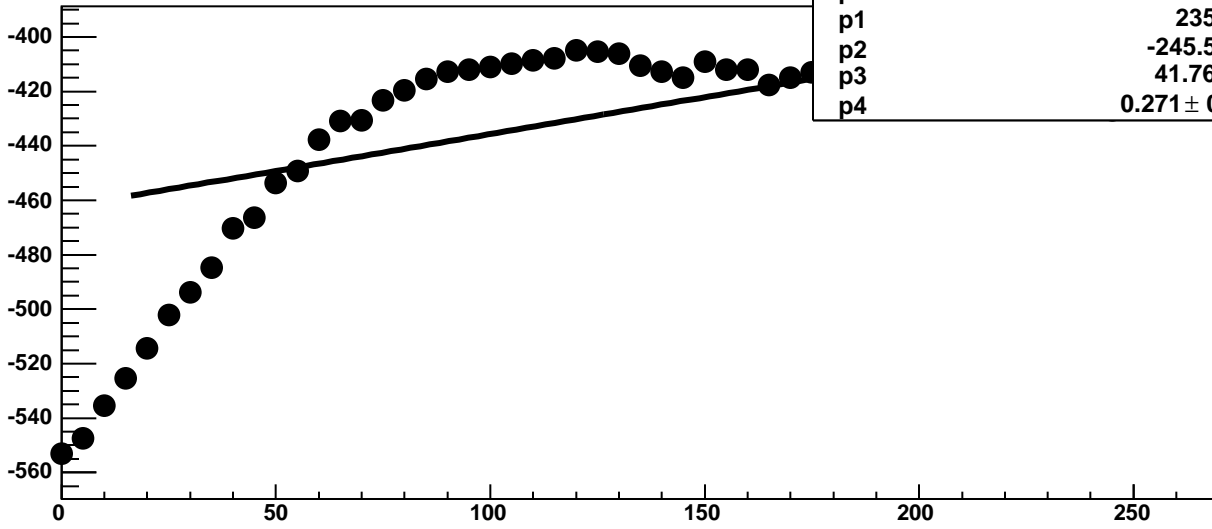
Chip 1, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold

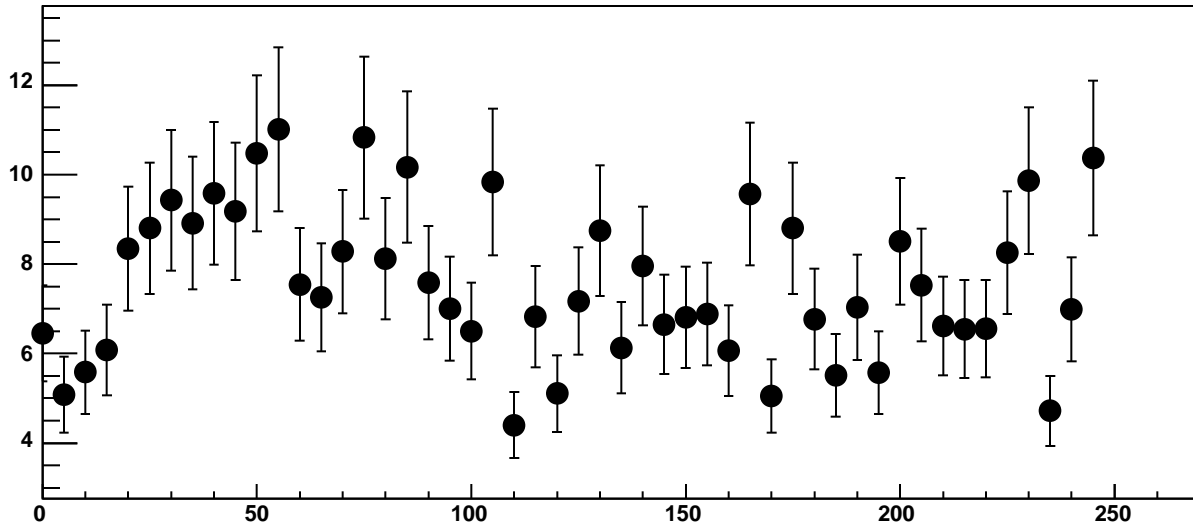


Chip 1, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

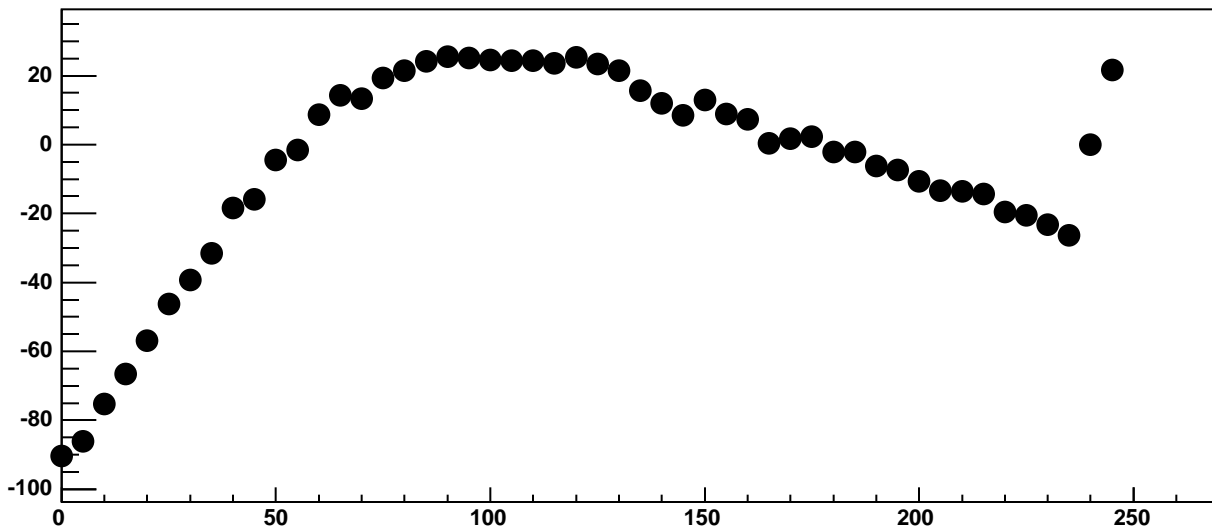


χ^2 / ndf	8910 / 41
p0	-399.1 ± 1.028
p1	235 ± 3.425
p2	-245.5 ± 152.4
p3	41.76 ± 22.78
p4	0.271 ± 0.003891

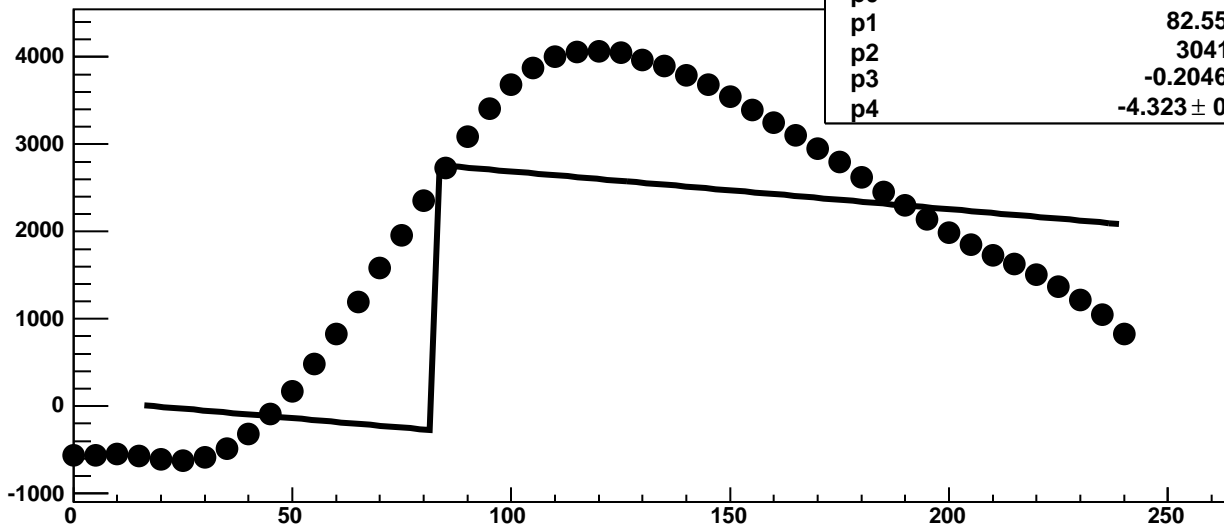
Chip 1, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



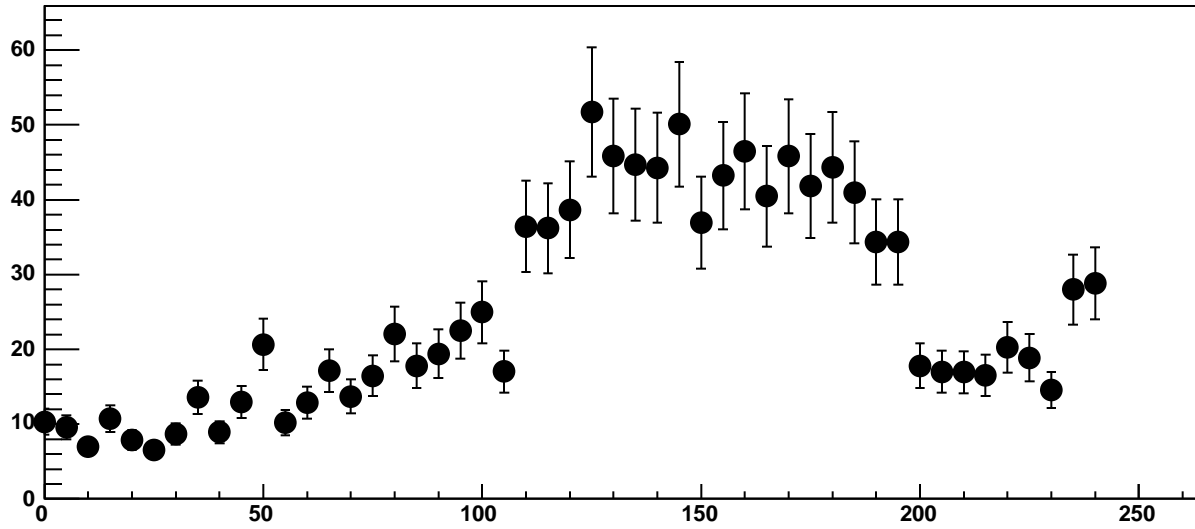
Chip 1, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold



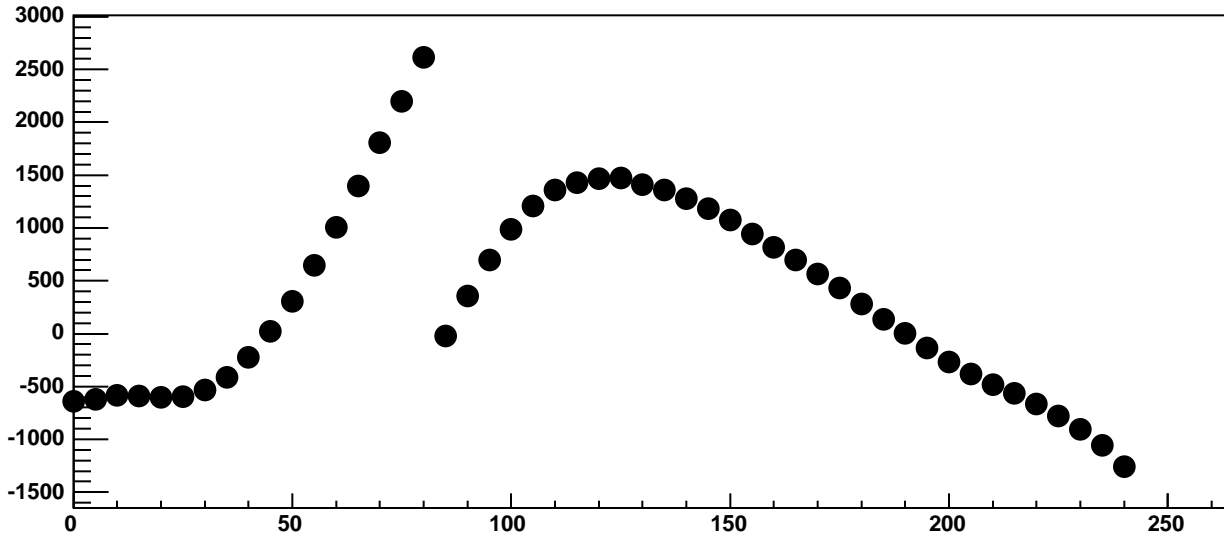
Chip 1, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold



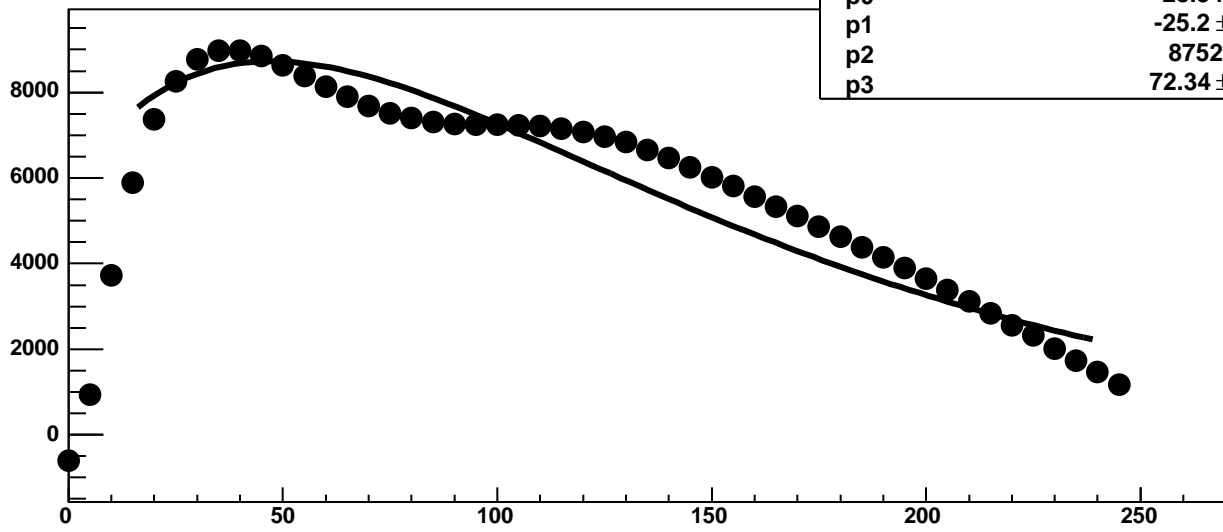
Chip 1, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold

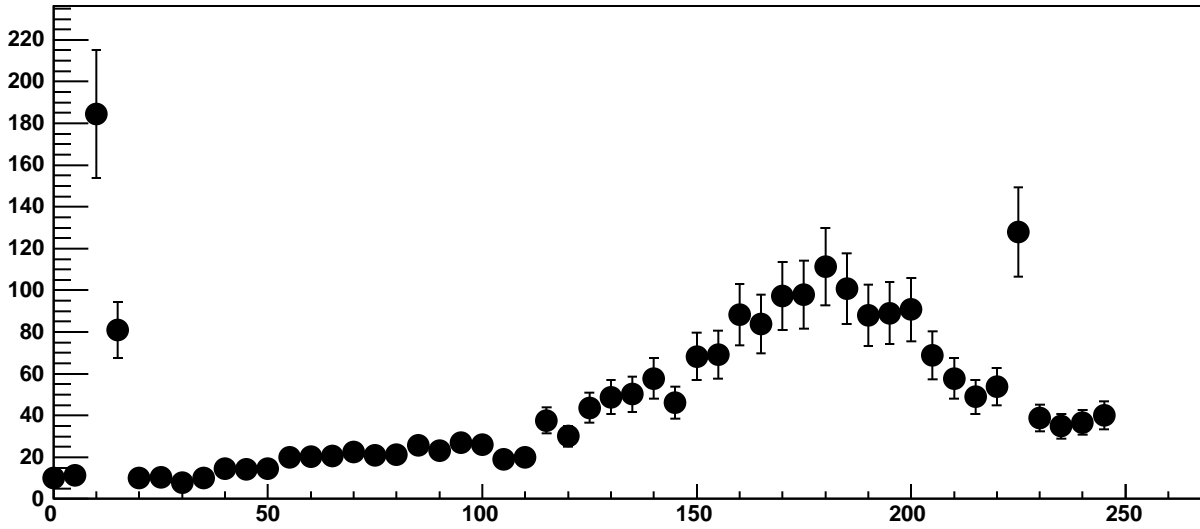


Chip 1, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold

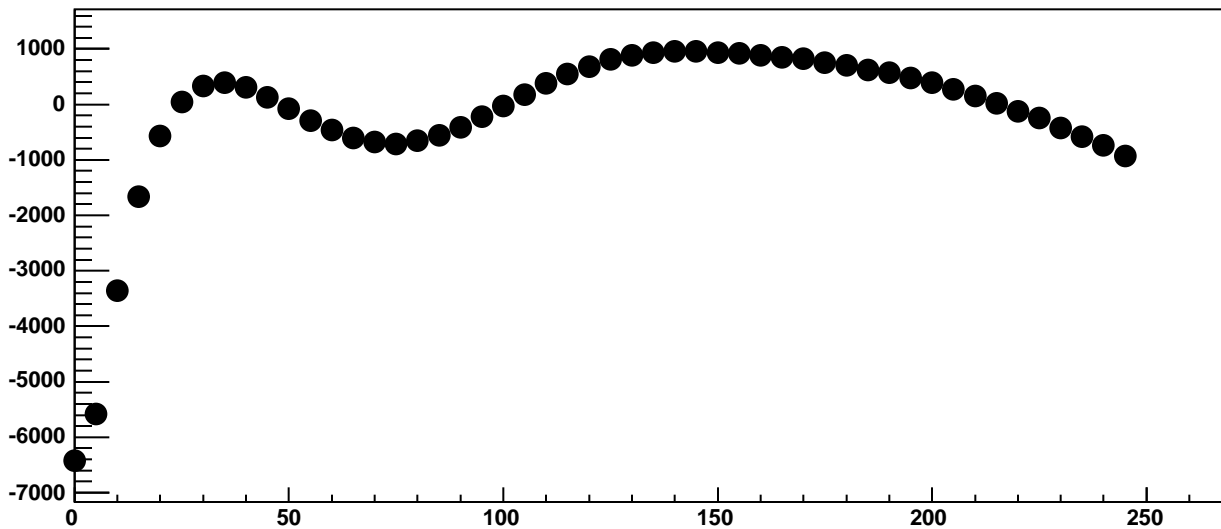


χ^2 / ndf	3.368e+05 / 42
p0	-28.34 ± 16.19
p1	-25.2 ± 0.1442
p2	8752 ± 15.45
p3	72.34 ± 0.1443

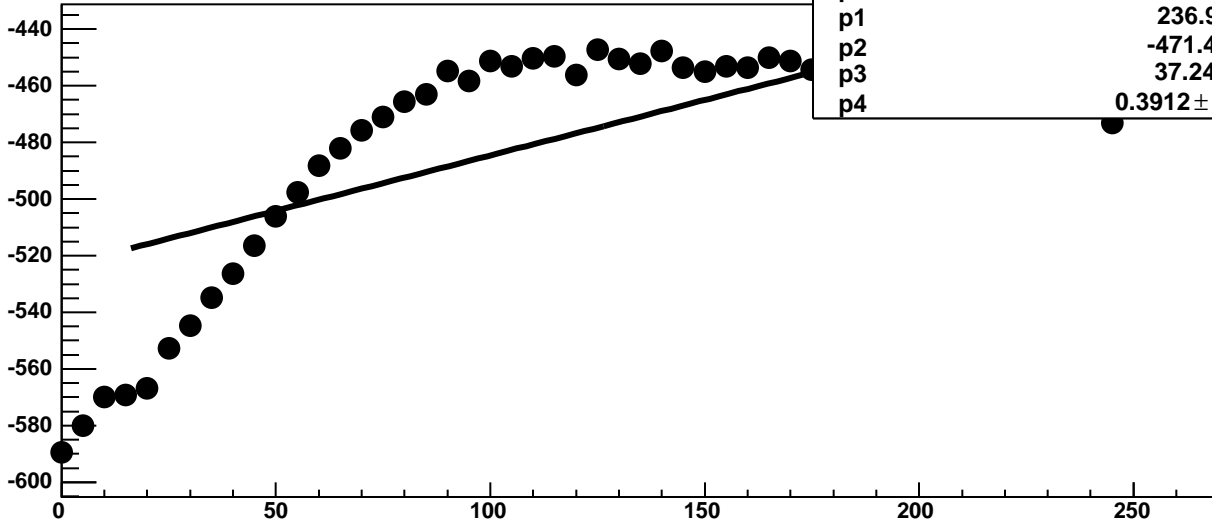
Chip 1, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold

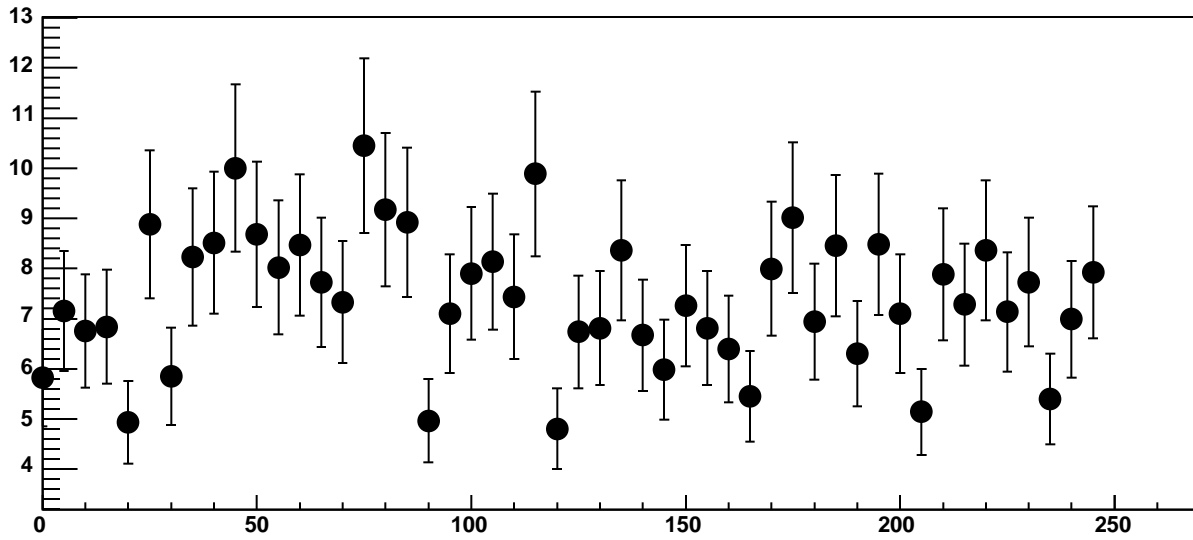


Chip 1, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

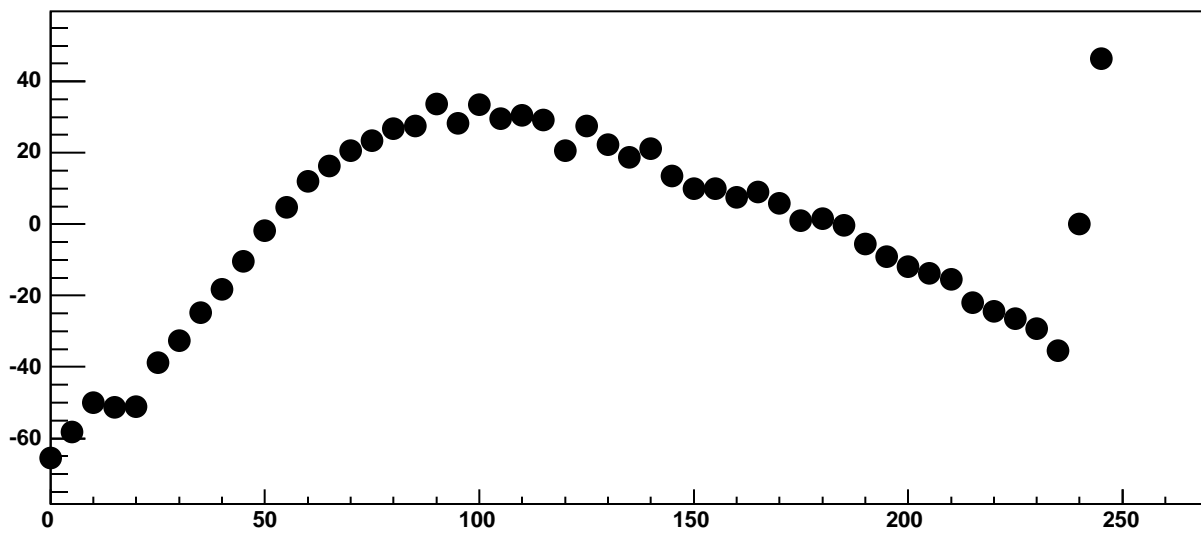


χ^2 / ndf	1.071e+04 / 41
p0	-431 ± 0.796
p1	236.9 ± 1.661
p2	-471.4 ± 239.8
p3	37.24 ± 16.29
p4	0.3912 ± 0.00368

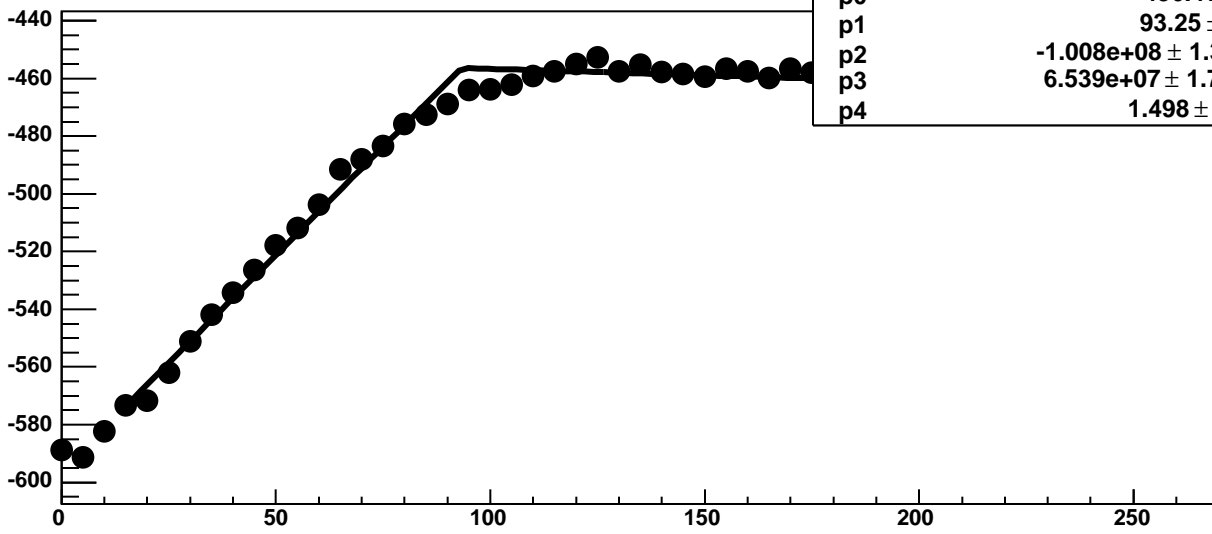
Chip 1, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

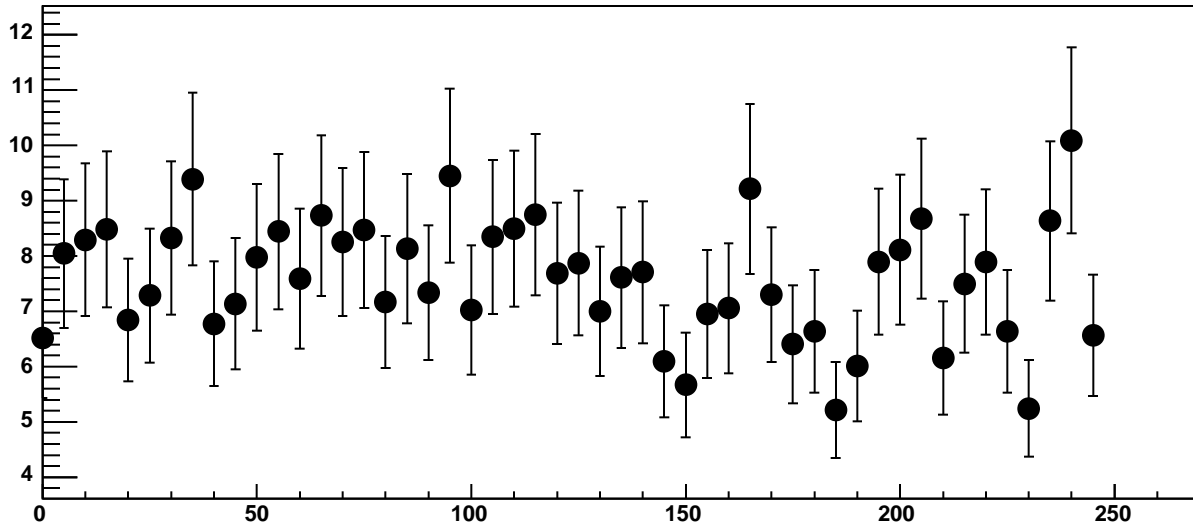


Chip 1, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

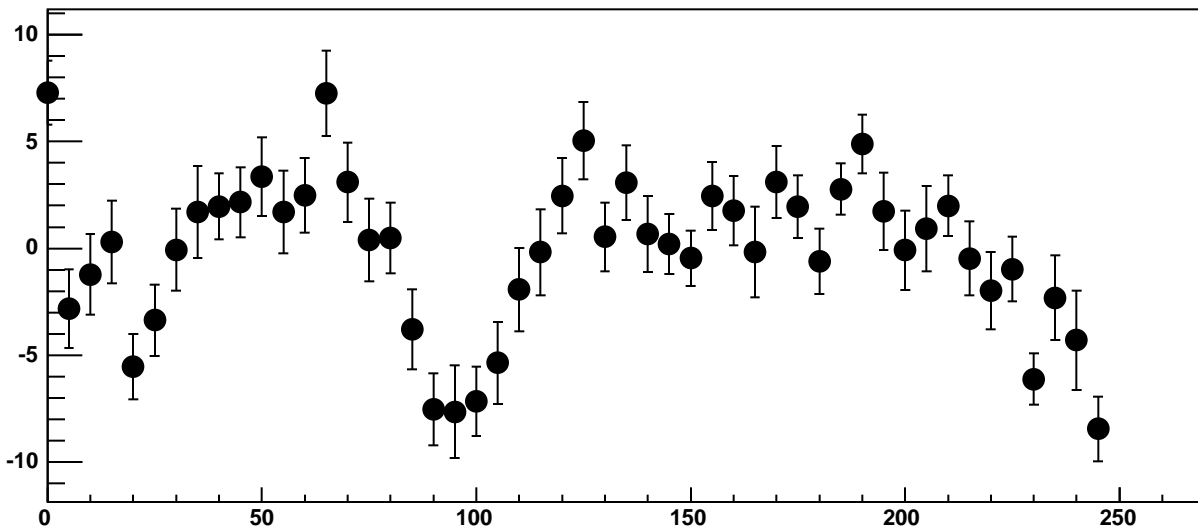


χ^2 / ndf	184.3 / 41
p0	-456.4 ± 0.6229
p1	93.25 ± 0.7165
p2	$-1.008\text{e}+08 \pm 1.353\text{e}+06$
p3	$6.539\text{e}+07 \pm 1.725\text{e}+05$
p4	1.498 ± 0.01905

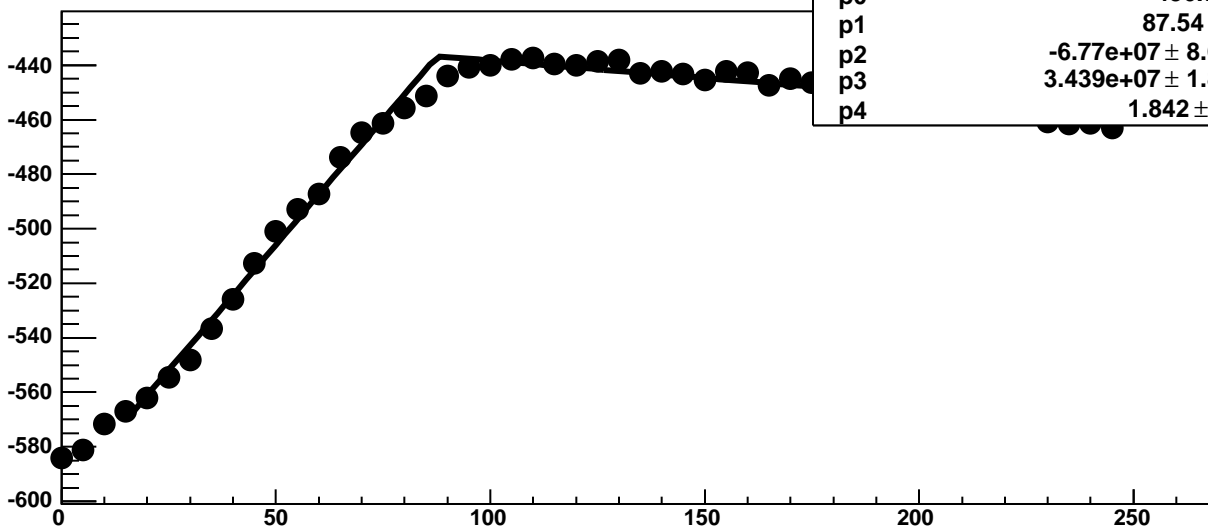
Chip 1, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

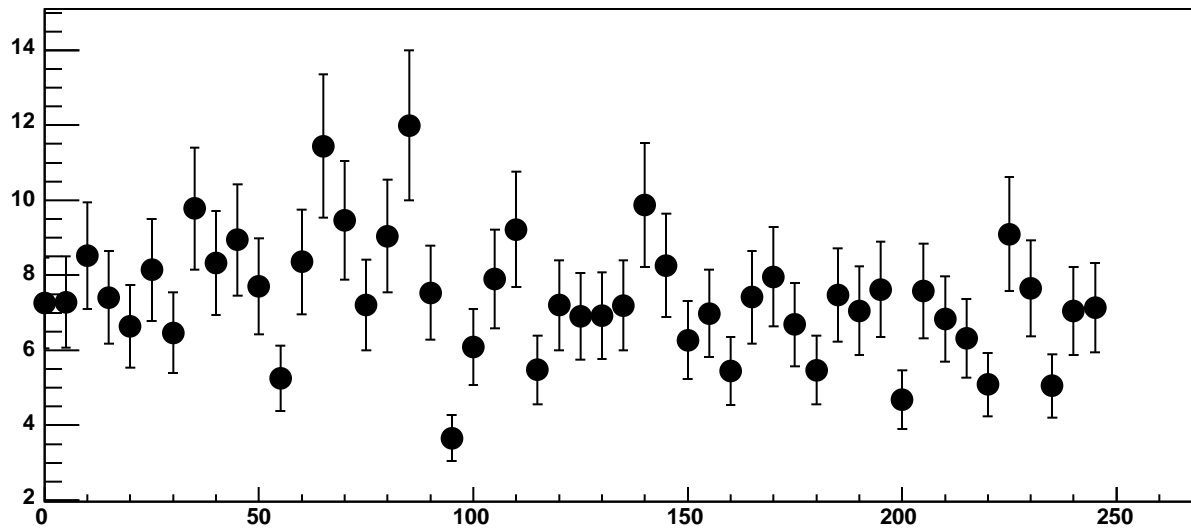


Chip 1, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold

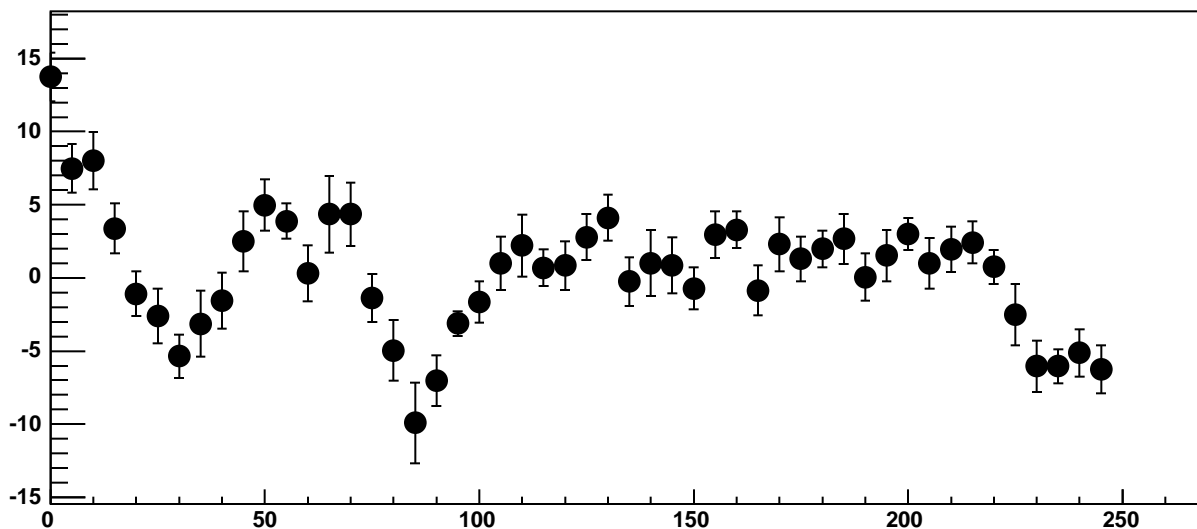


χ^2 / ndf	194 / 41
p0	-436.7 ± 0.476
p1	87.54 ± 0.5829
p2	$-6.77\text{e}+07 \pm 8.627\text{e}+05$
p3	$3.439\text{e}+07 \pm 1.821\text{e}+05$
p4	1.842 ± 0.02255

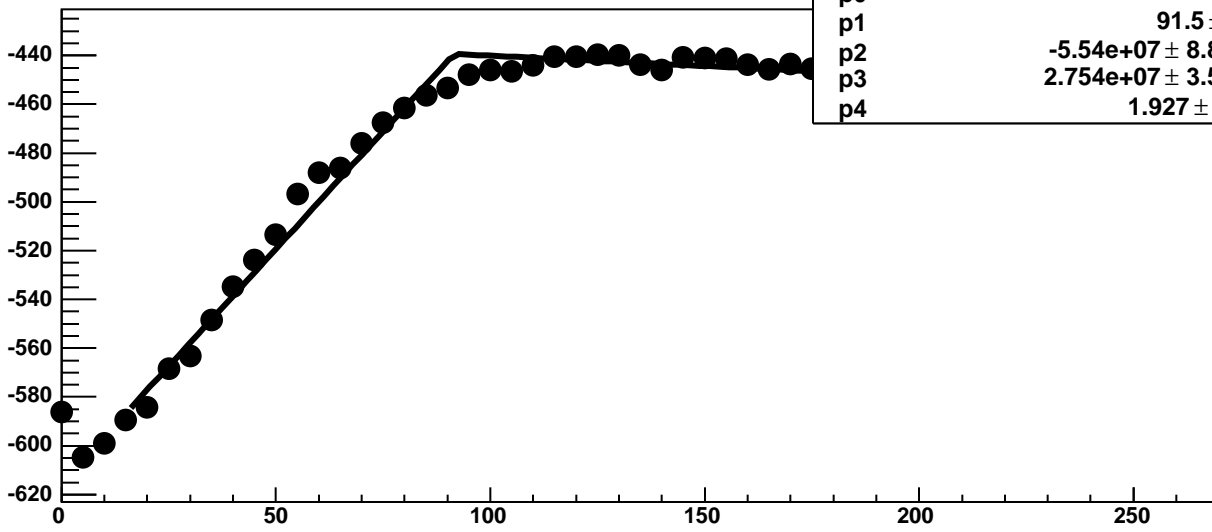
Chip 1, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold

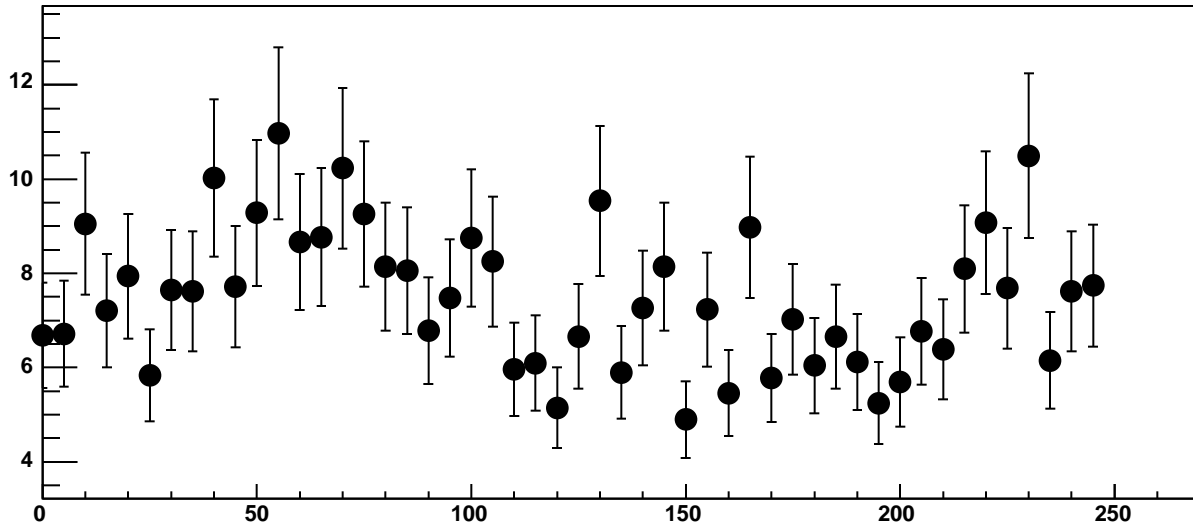


Chip 1, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

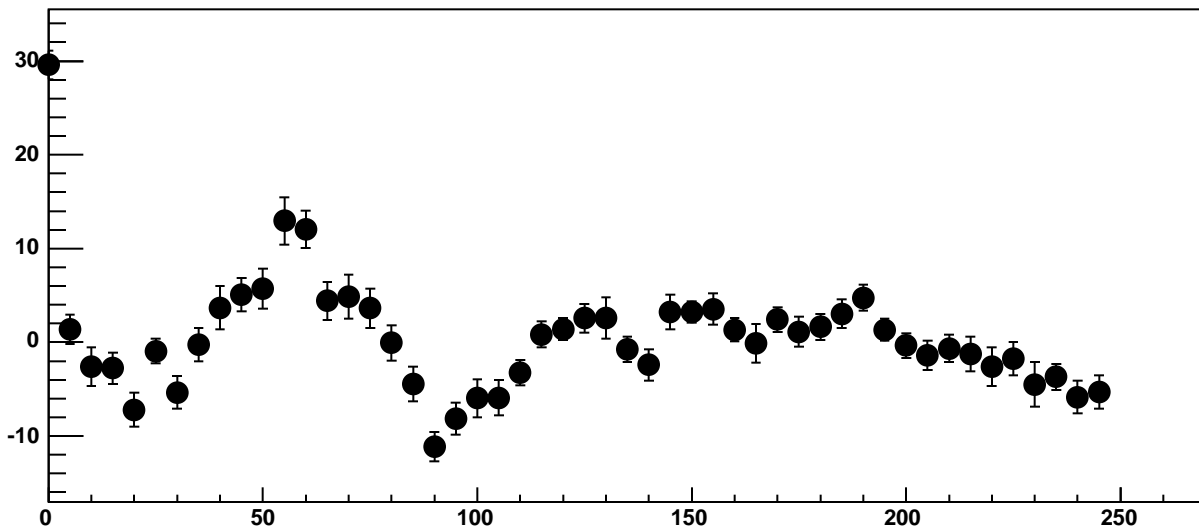


χ^2 / ndf	297.9 / 41
p0	-439.4 ± 0.5586
p1	91.5 ± 0.5313
p2	$-5.54\text{e}+07 \pm 8.887\text{e}+05$
p3	$2.754\text{e}+07 \pm 3.506\text{e}+05$
p4	1.927 ± 0.01865

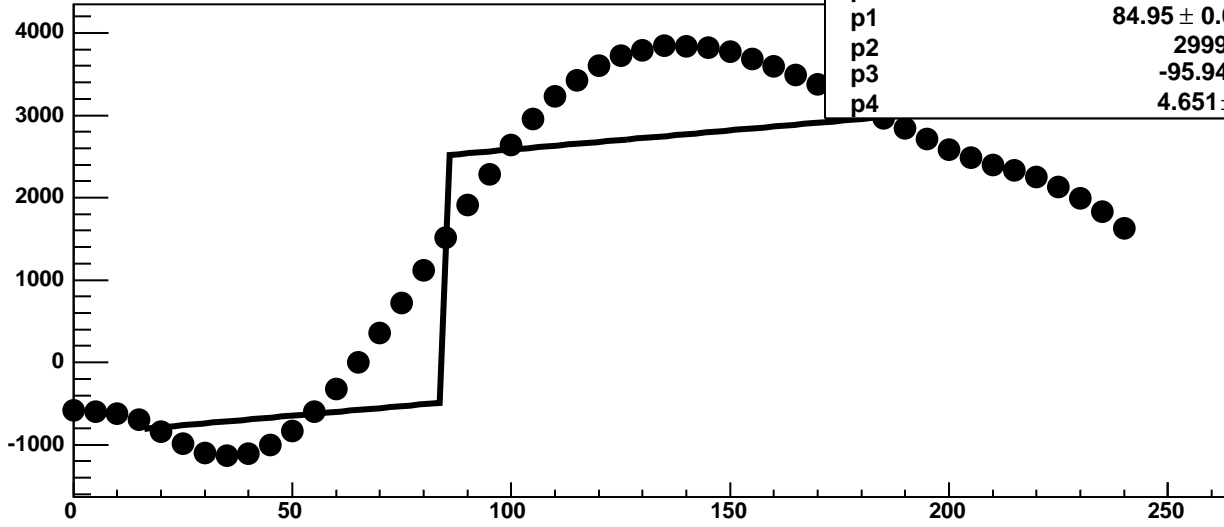
Chip 1, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

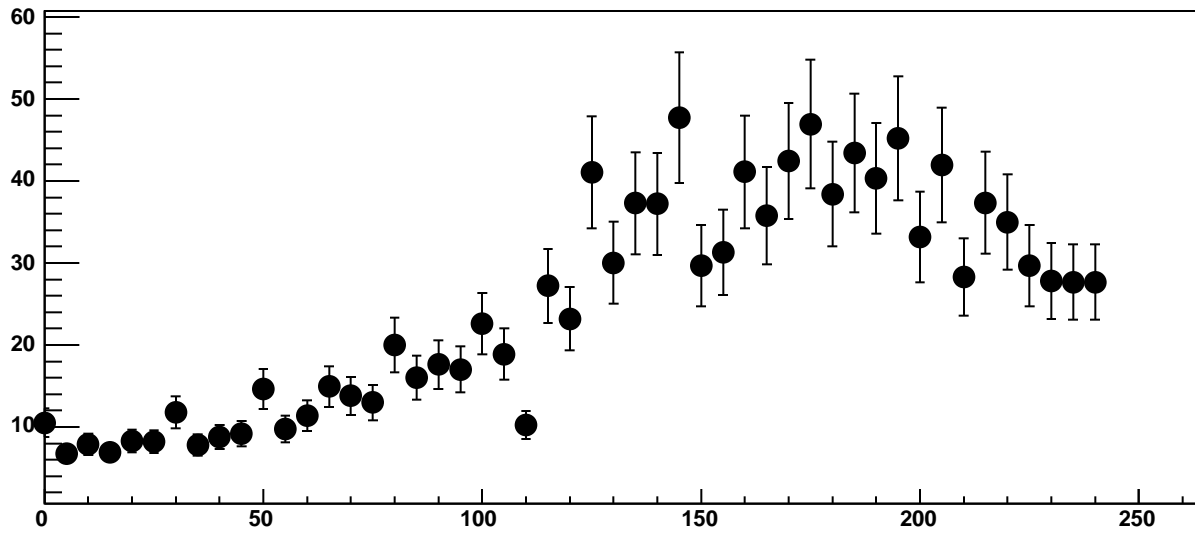


Chip 1, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

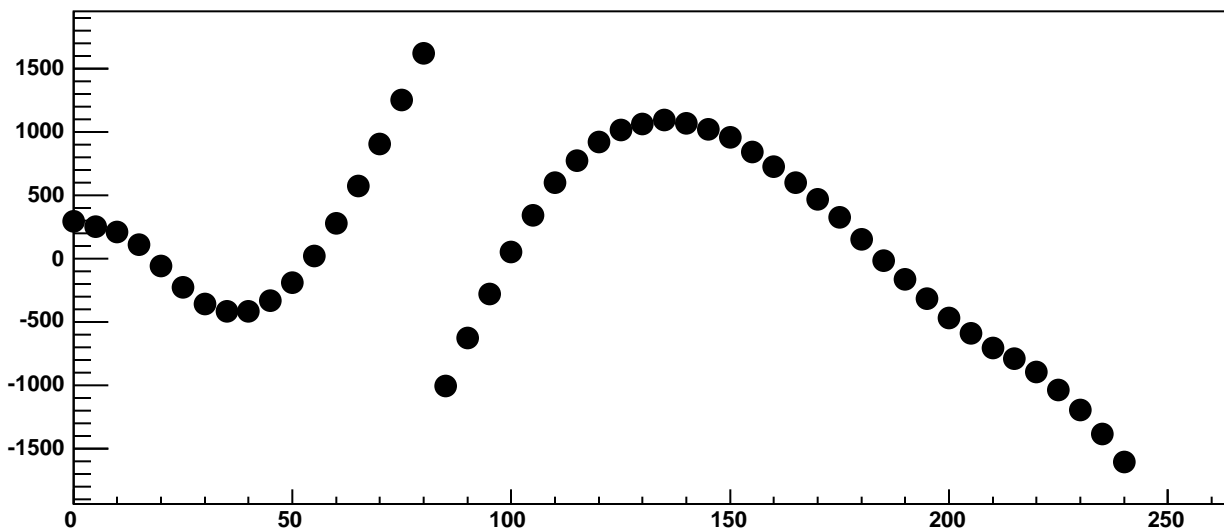


χ^2 / ndf	1.141e+06 / 41
p0	-484.1 ± 6.868
p1	84.95 ± 0.0006545
p2	2999 ± 7.283
p3	-95.94 ± 2.464
p4	4.651 ± 0.1038

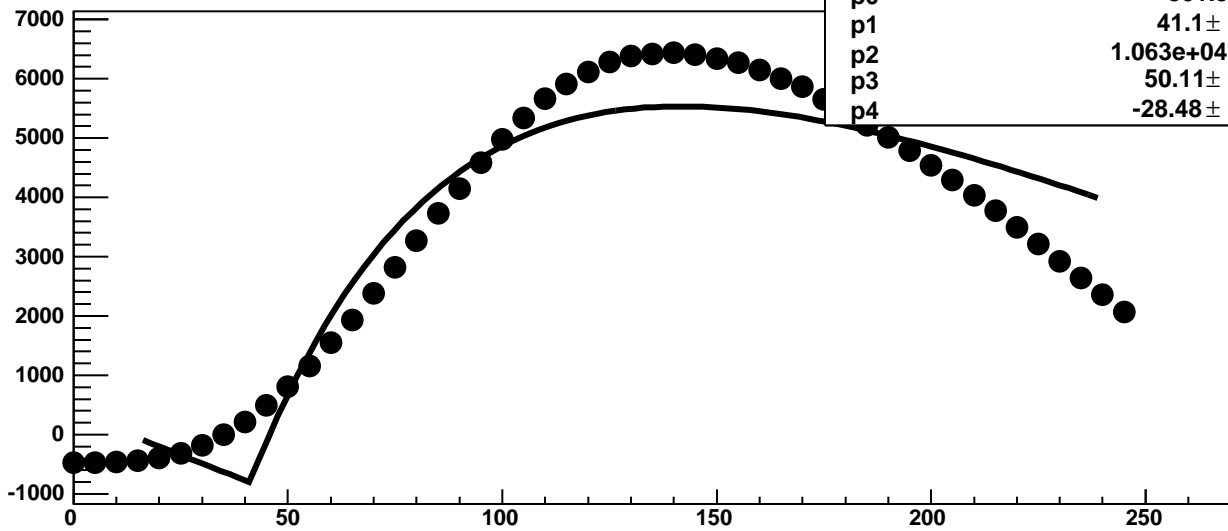
Chip 1, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



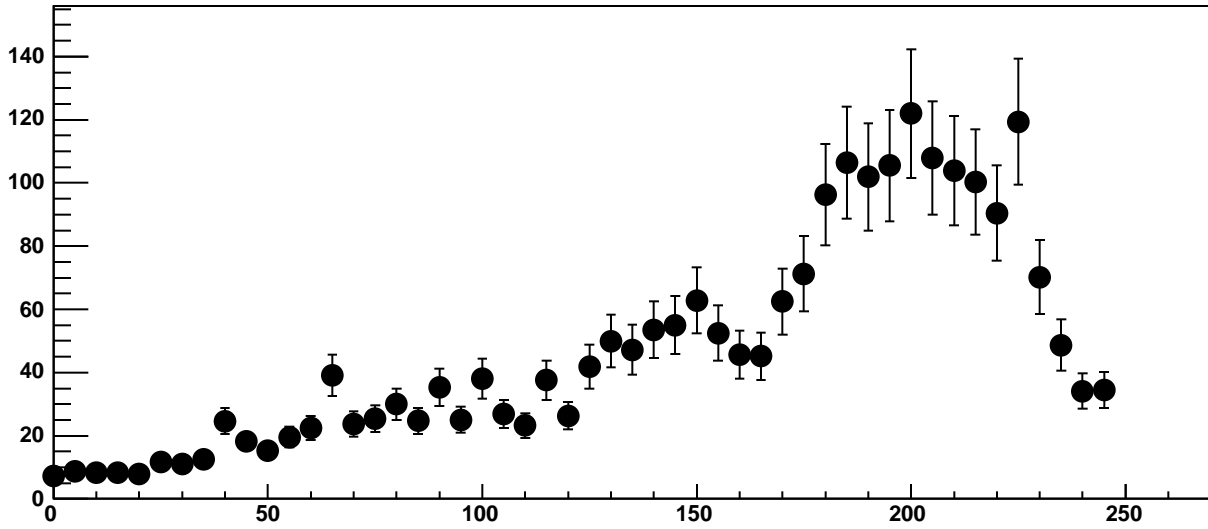
Chip 1, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold



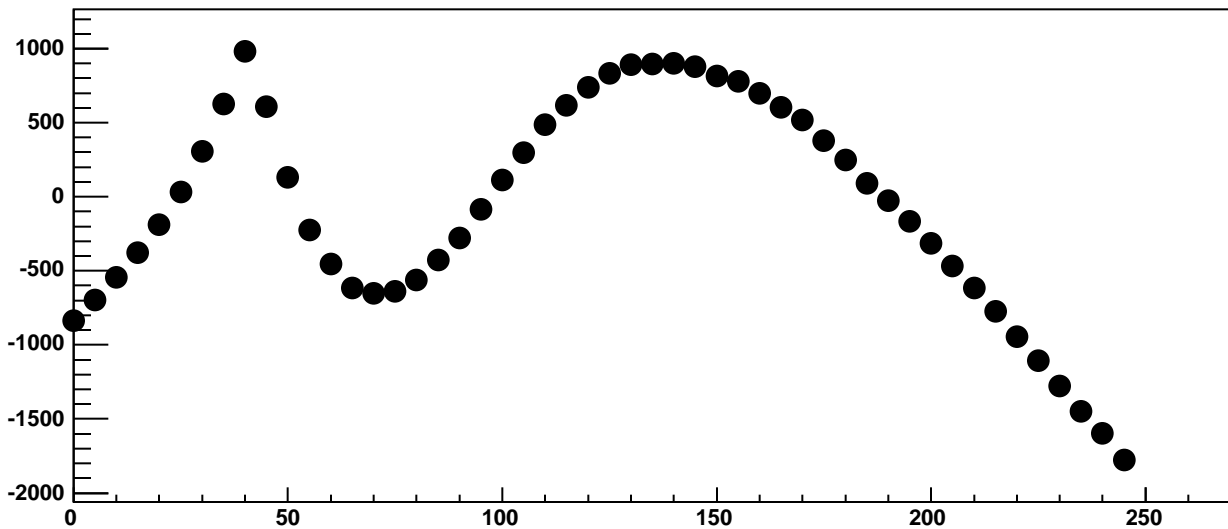
Chip 1, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold



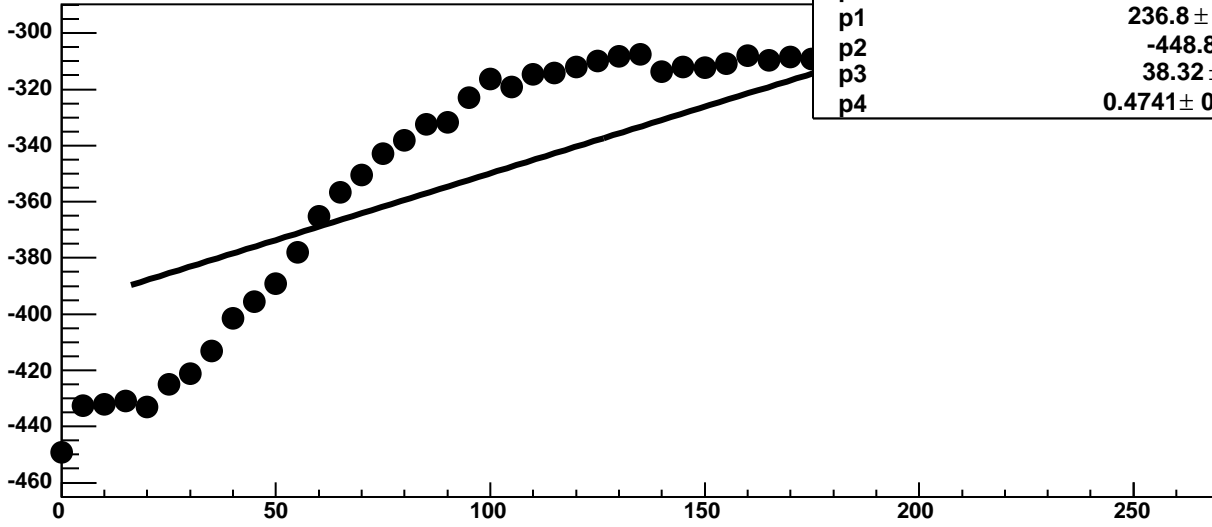
Chip 1, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold

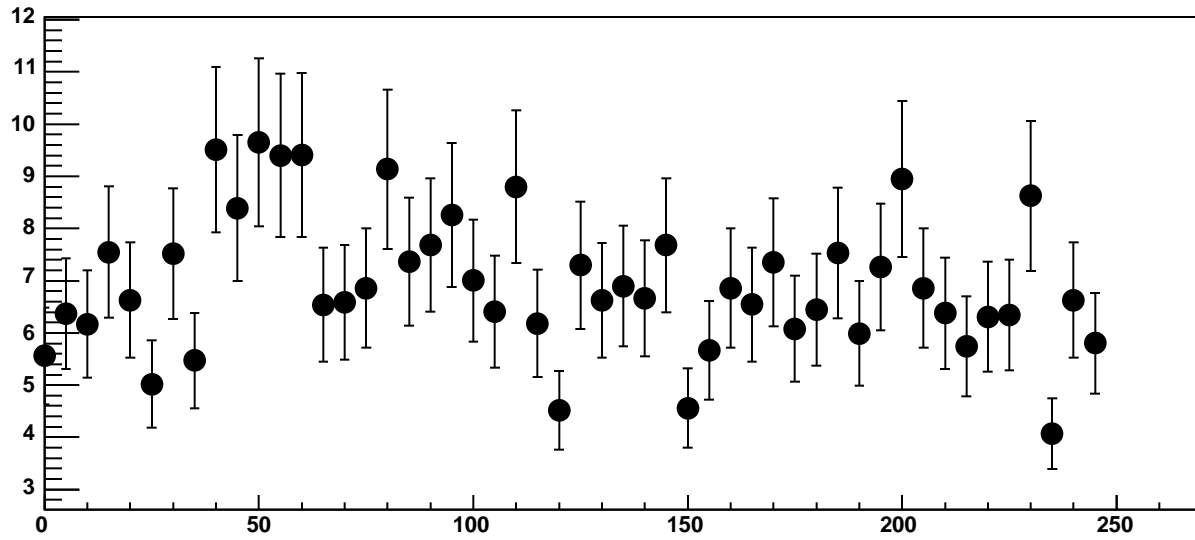


Chip 1, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

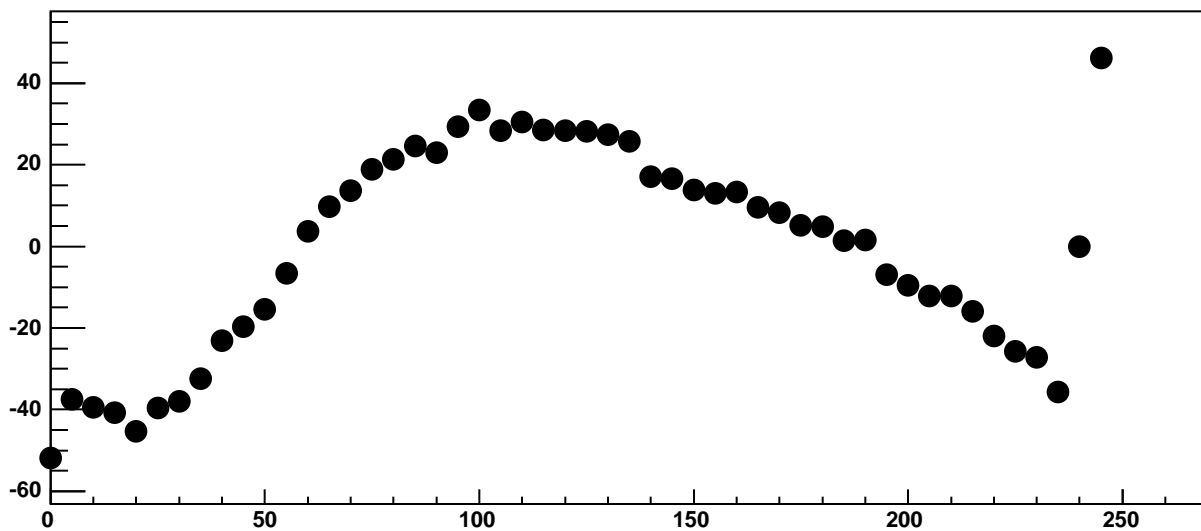


χ^2 / ndf	1.126e+04 / 41
p0	-285.1 ± 0.4216
p1	236.8 ± 0.01005
p2	-448.8 ± 19.29
p3	38.32 ± 0.1102
p4	0.4741 ± 0.003428

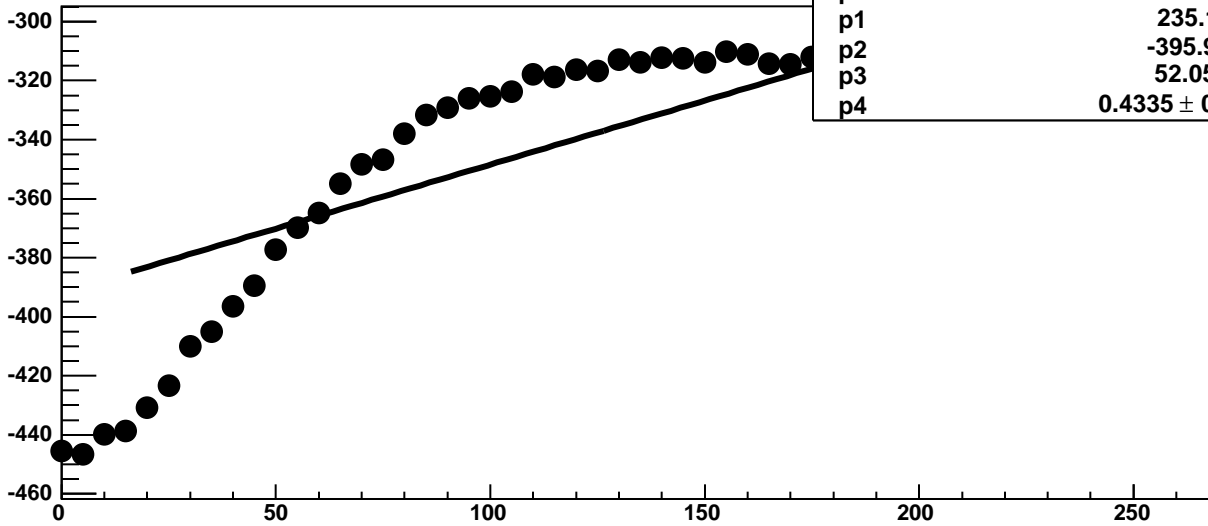
Chip 1, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold

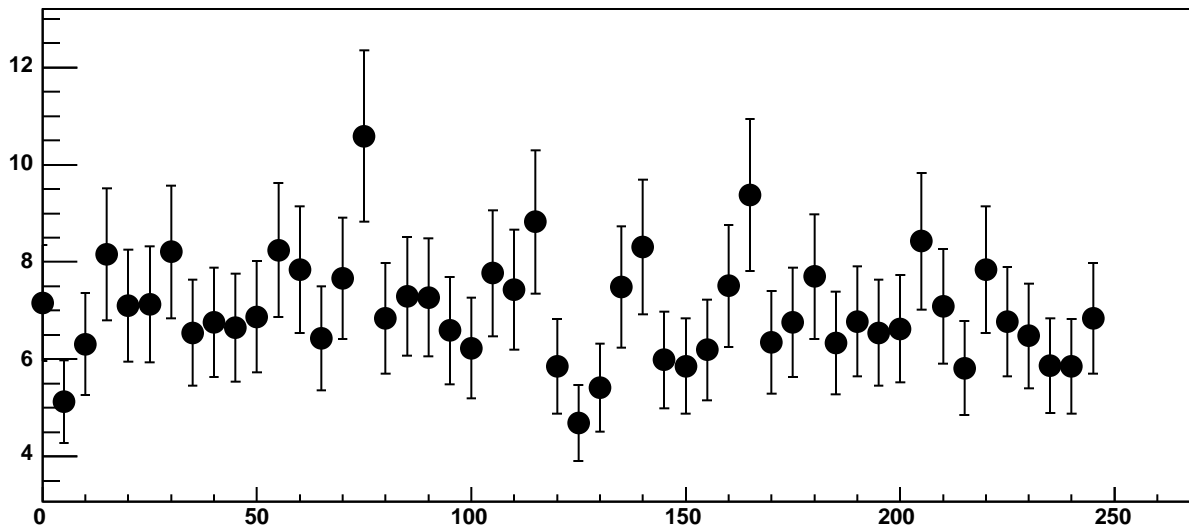


Chip 1, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

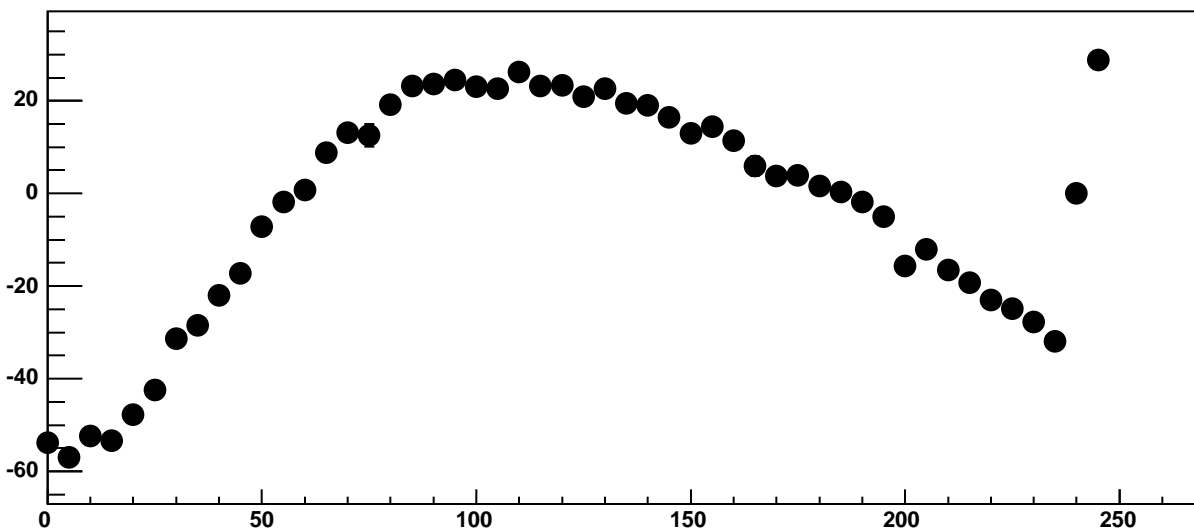


χ^2 / ndf	8543 / 41
p0	-289.9 ± 1.251
p1	235.1 ± 2.703
p2	-395.9 ± 199.9
p3	52.05 ± 22.69
p4	0.4335 ± 0.003664

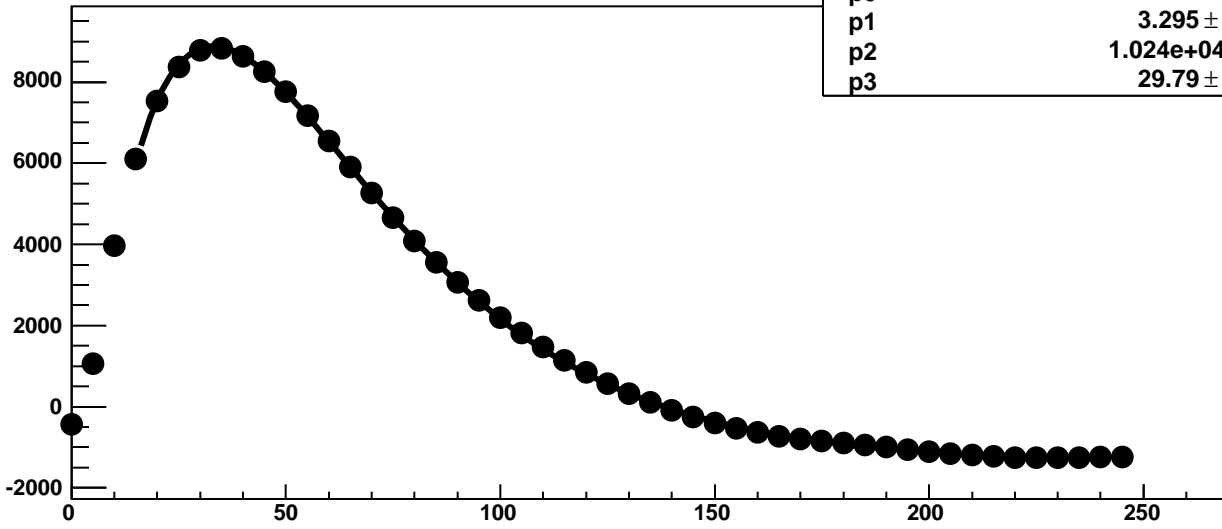
Chip 1, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

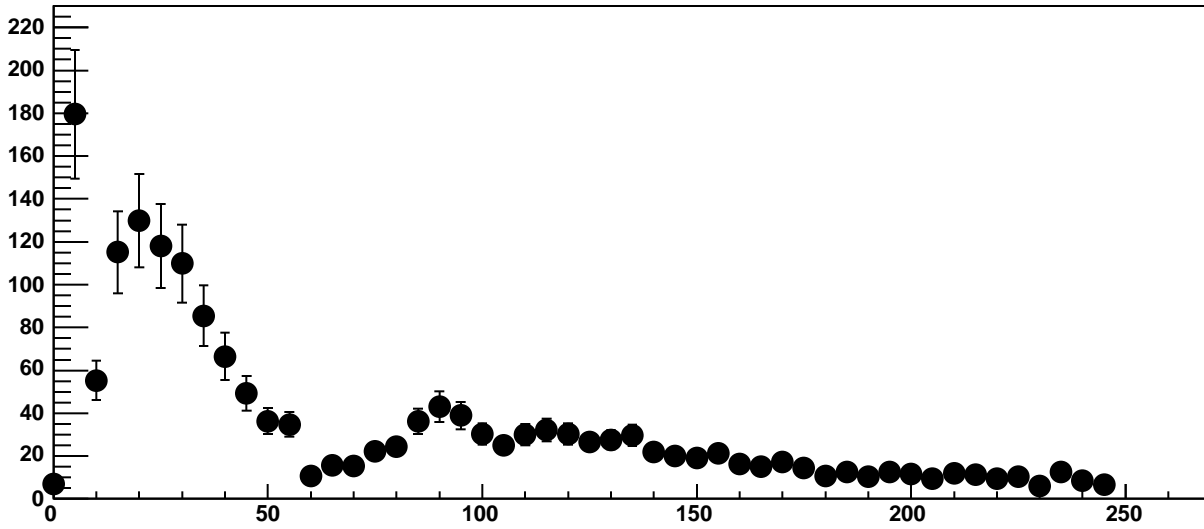


Chip 1, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold

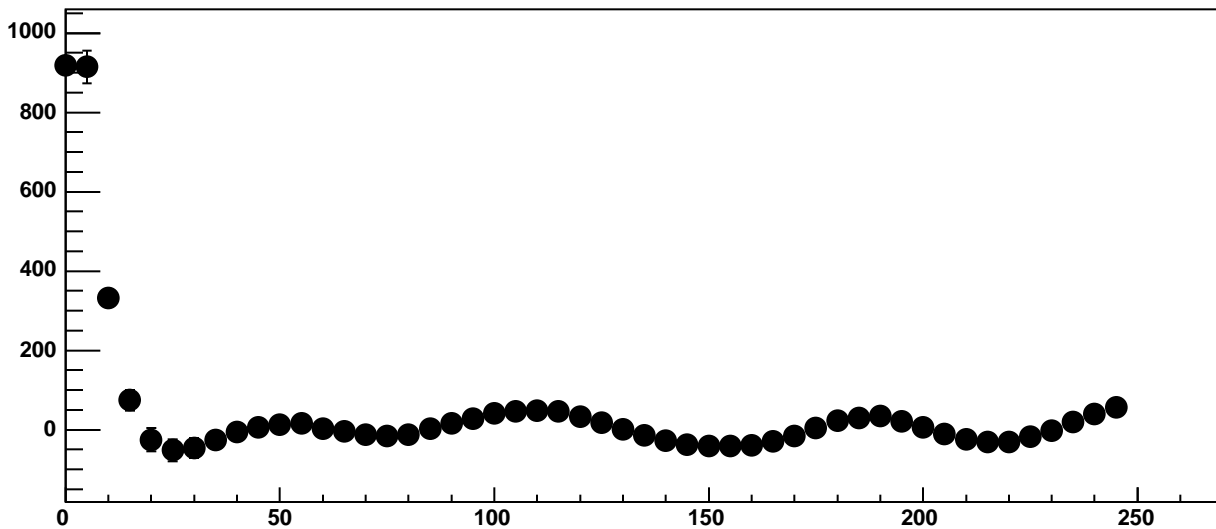


χ^2 / ndf	2166 / 42
p0	-1360 ± 0.8855
p1	3.295 ± 0.04712
p2	1.024e+04 ± 5.487
p3	29.79 ± 0.01748

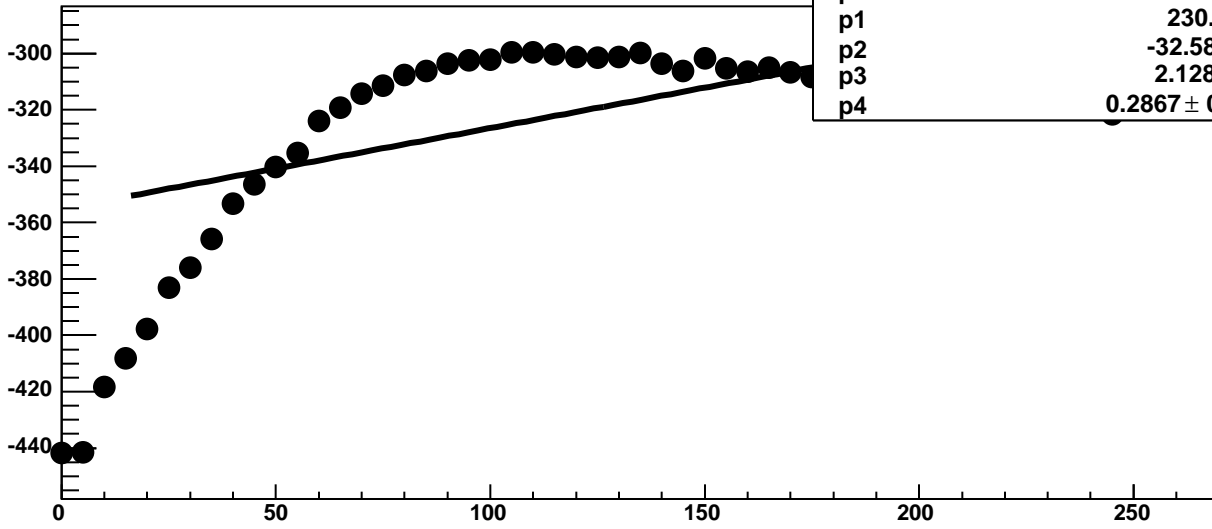
Chip 1, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold

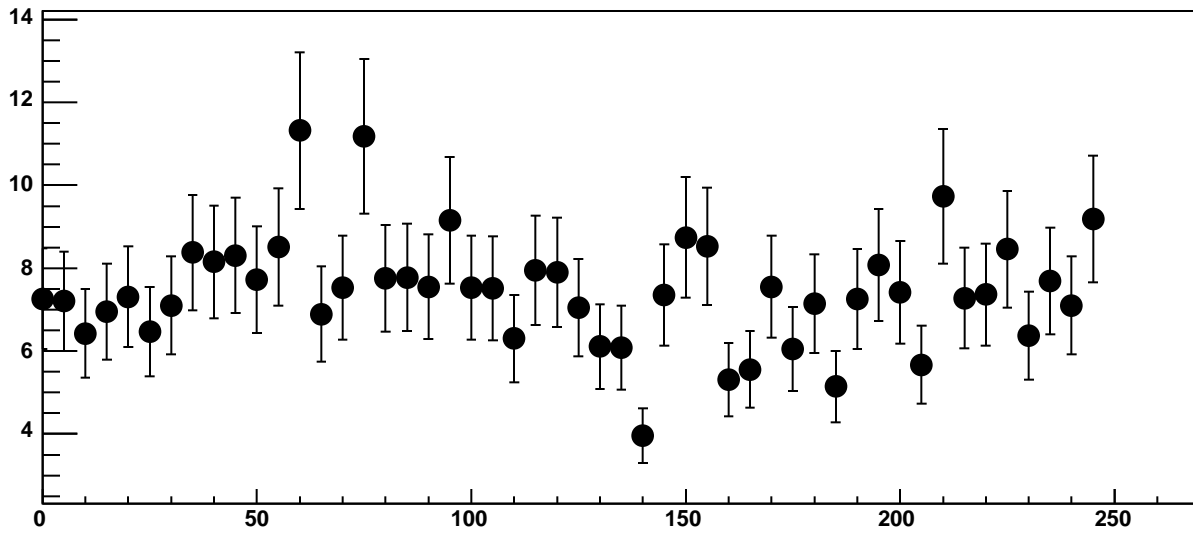


Chip 1, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold

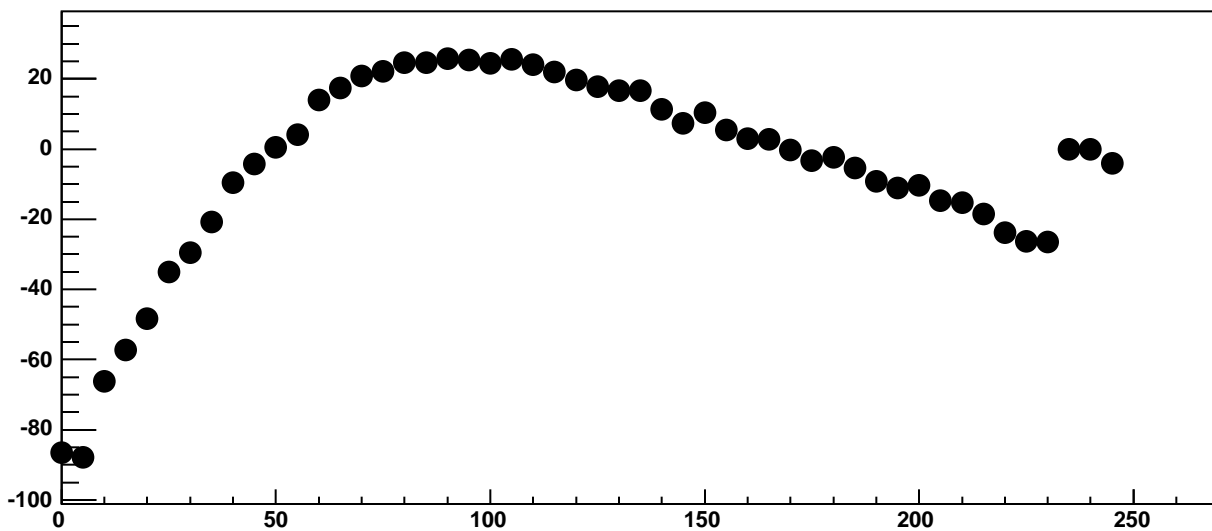


χ^2 / ndf	6991 / 41
p0	-289.2 ± 2.642
p1	230.1 ± 9.51
p2	-32.58 ± 2.824
p3	2.128 ± 4.697
p4	0.2867 ± 0.003911

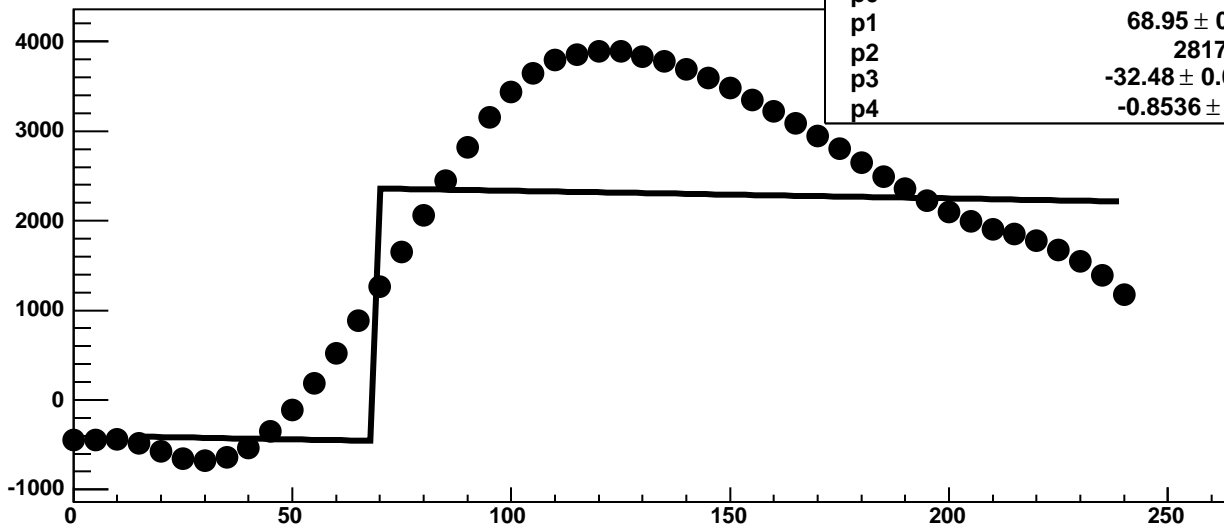
Chip 1, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

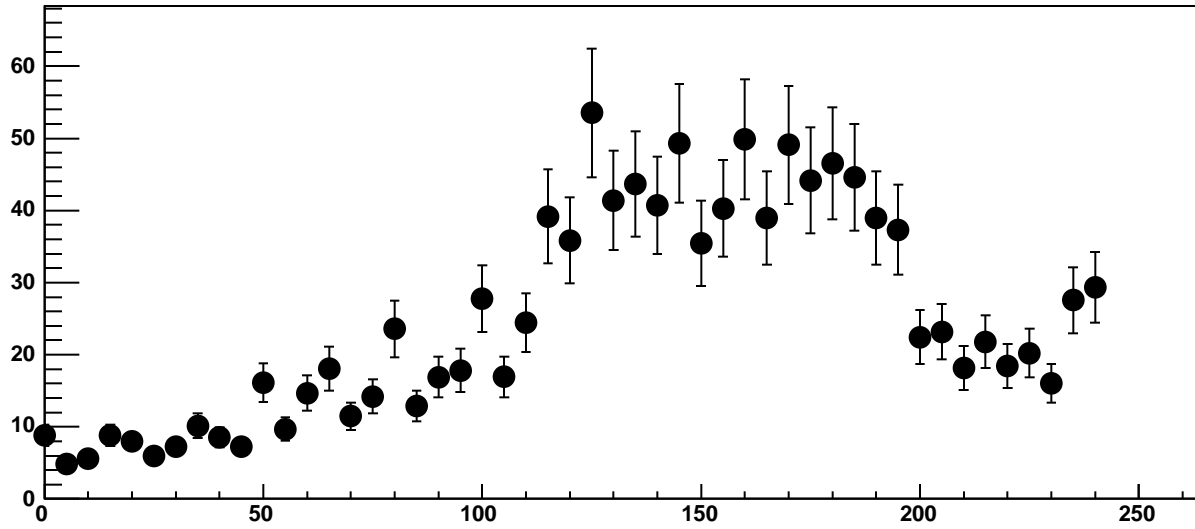


Chip 1, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

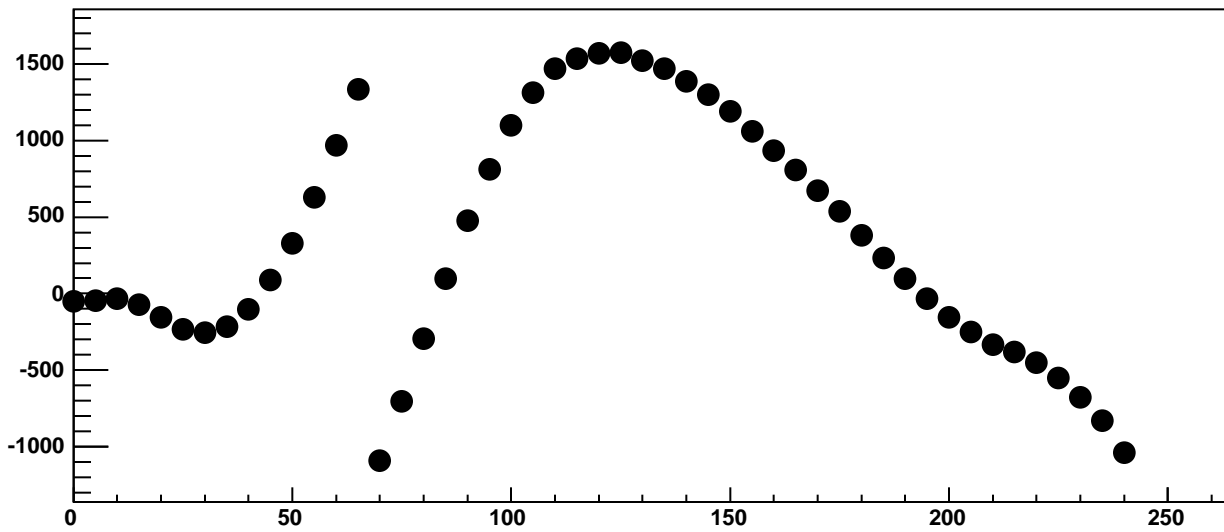


χ^2 / ndf	1.178e+06 / 41
p0	-456.2 ± 0.9717
p1	68.95 ± 0.003122
p2	2817 ± 1.858
p3	-32.48 ± 0.0005047
p4	-0.8536 ± 0.01289

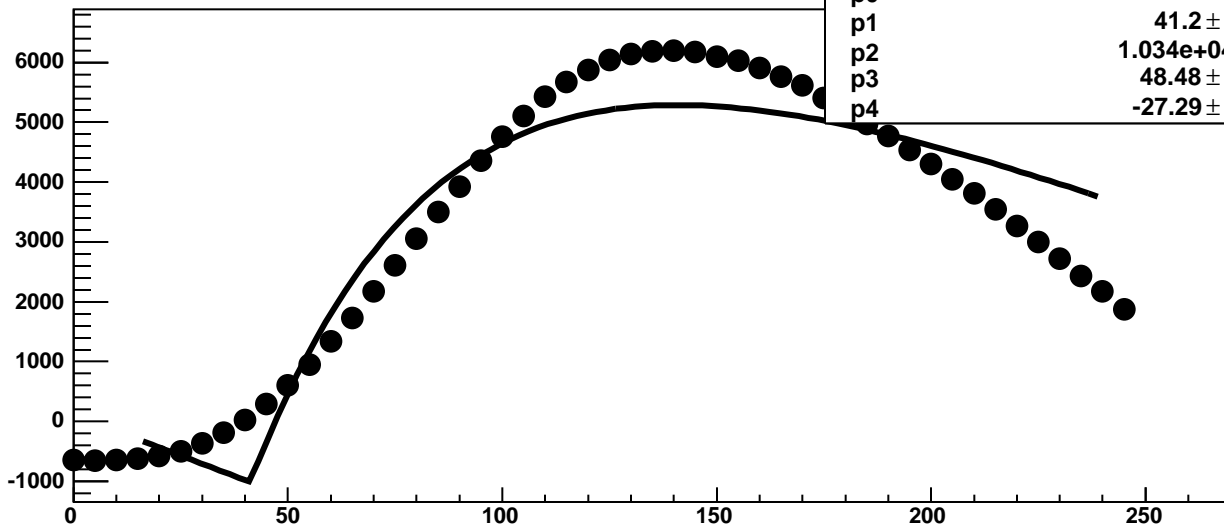
Chip 1, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

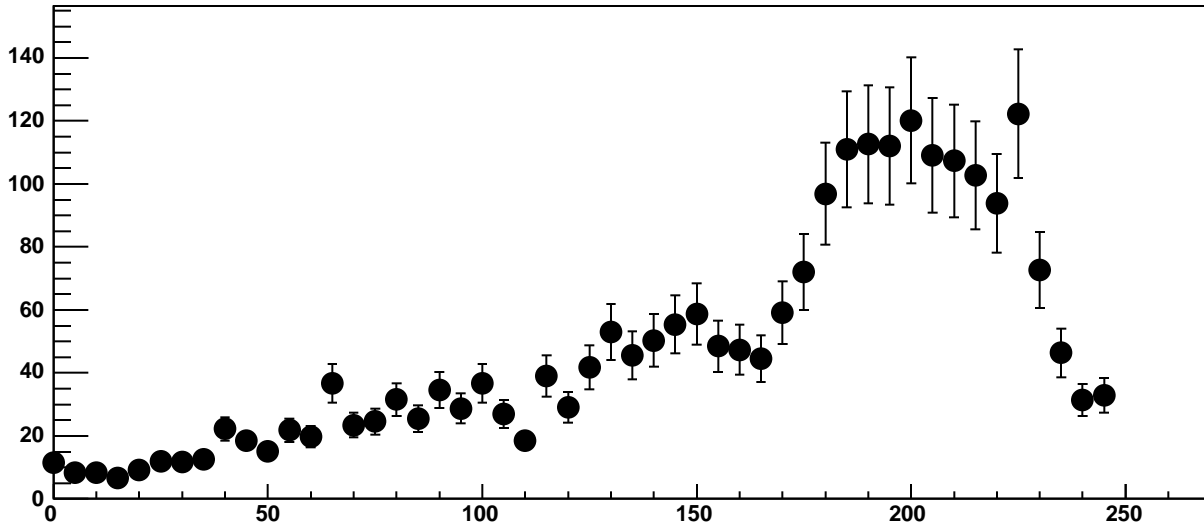


Chip 1, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold

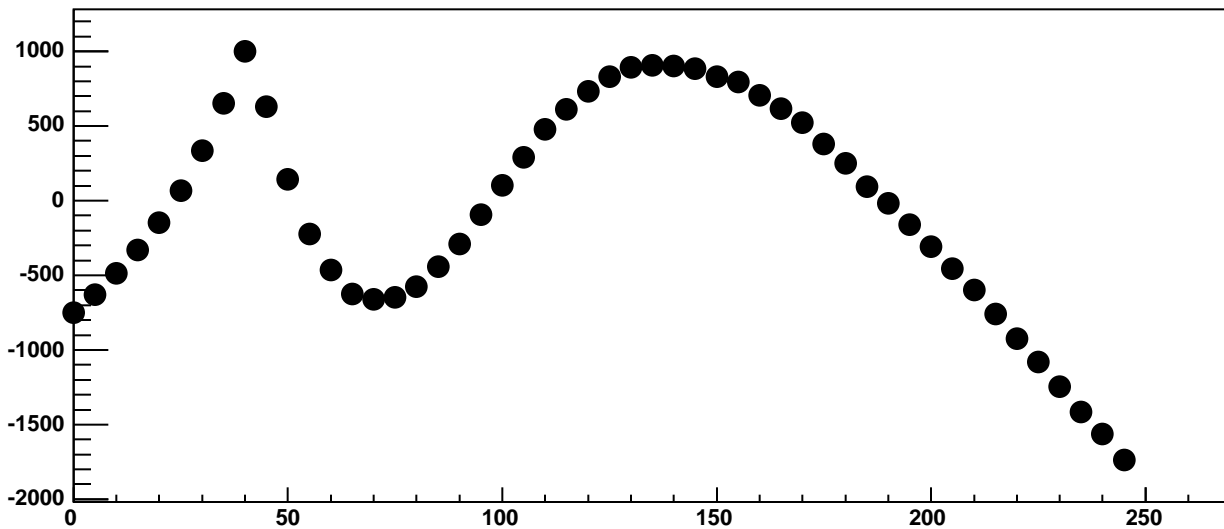


χ^2 / ndf	4e+05 / 41
p0	-1012 ± 1.5
p1	41.2 ± 0.01773
p2	1.034e+04 ± 14.7
p3	48.48 ± 0.07564
p4	-27.29 ± 0.07275

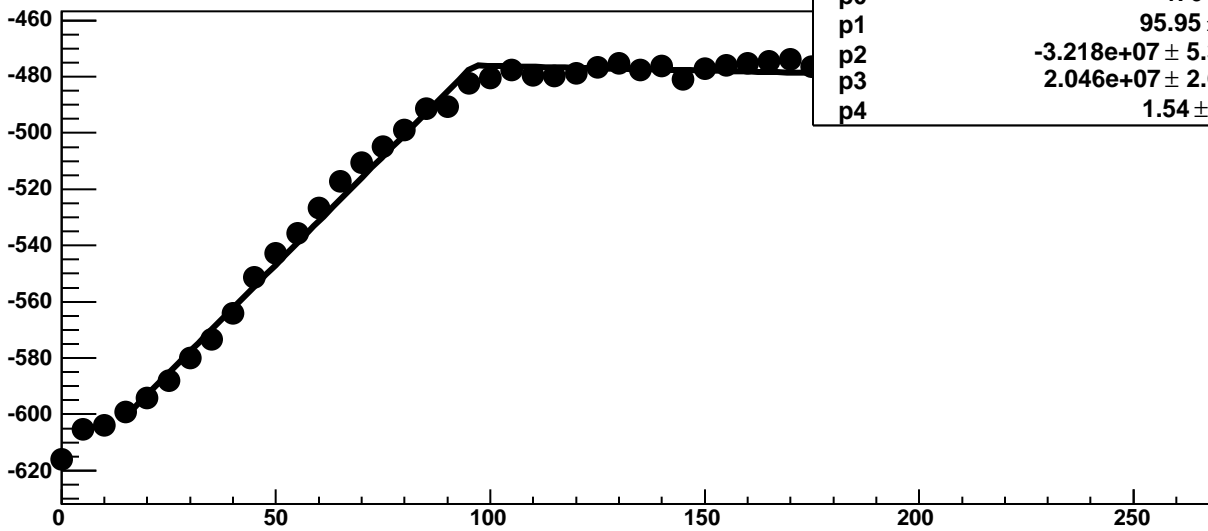
Chip 1, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

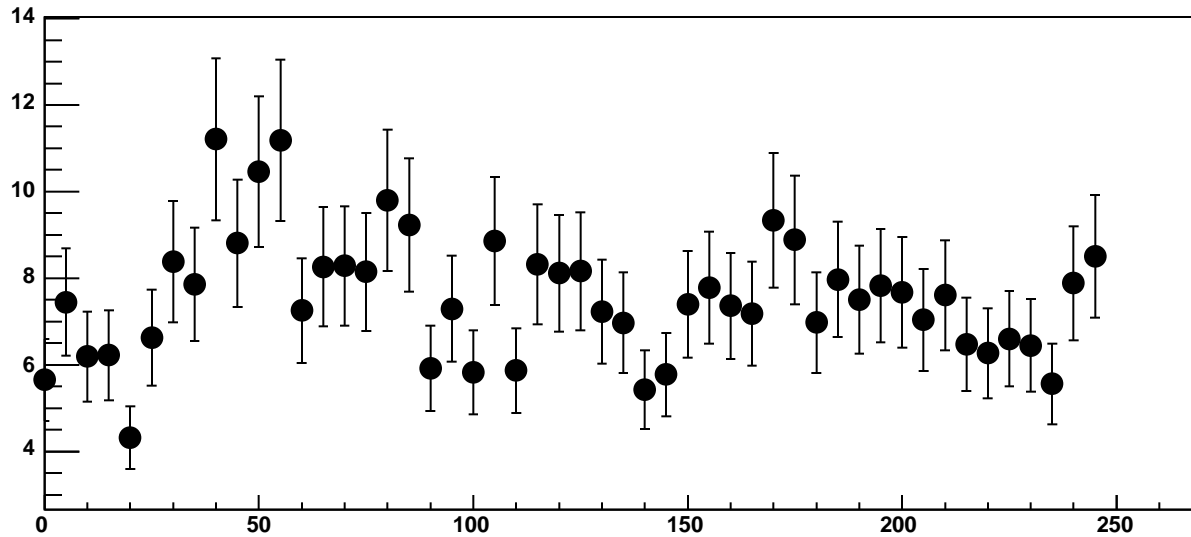


Chip 1, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold

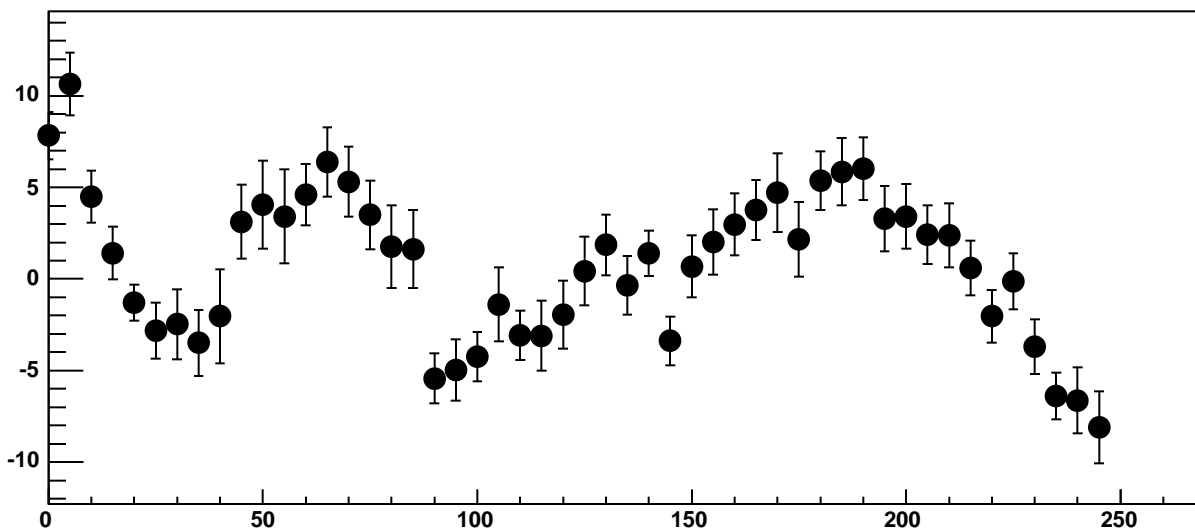


χ^2 / ndf	211.4 / 41
p0	-476 ± 0.5842
p1	95.95 ± 0.6364
p2	$-3.218e+07 \pm 5.308e+05$
p3	$2.046e+07 \pm 2.616e+05$
p4	1.54 ± 0.01504

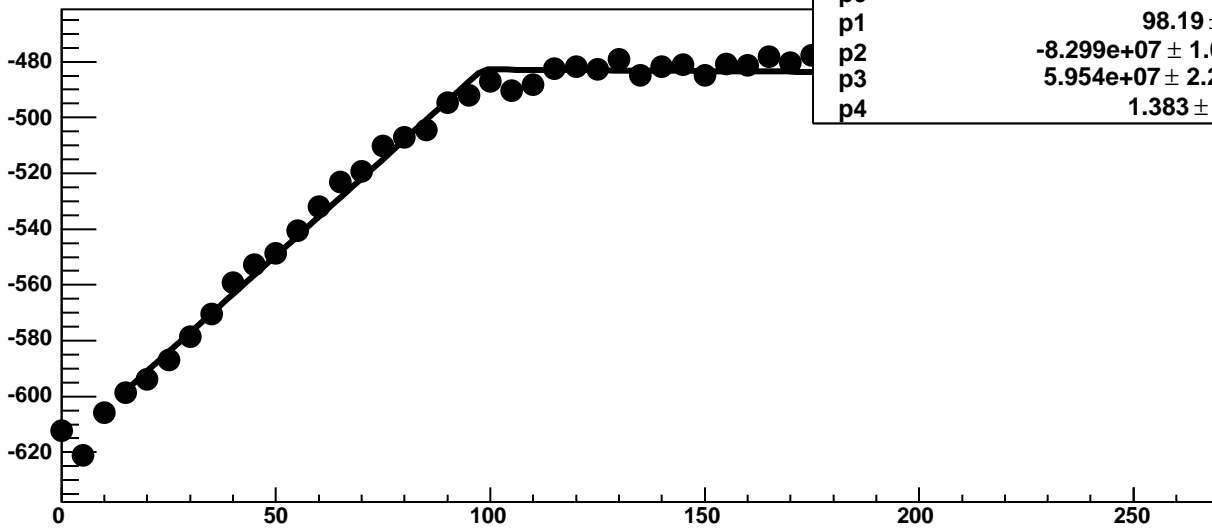
Chip 1, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold

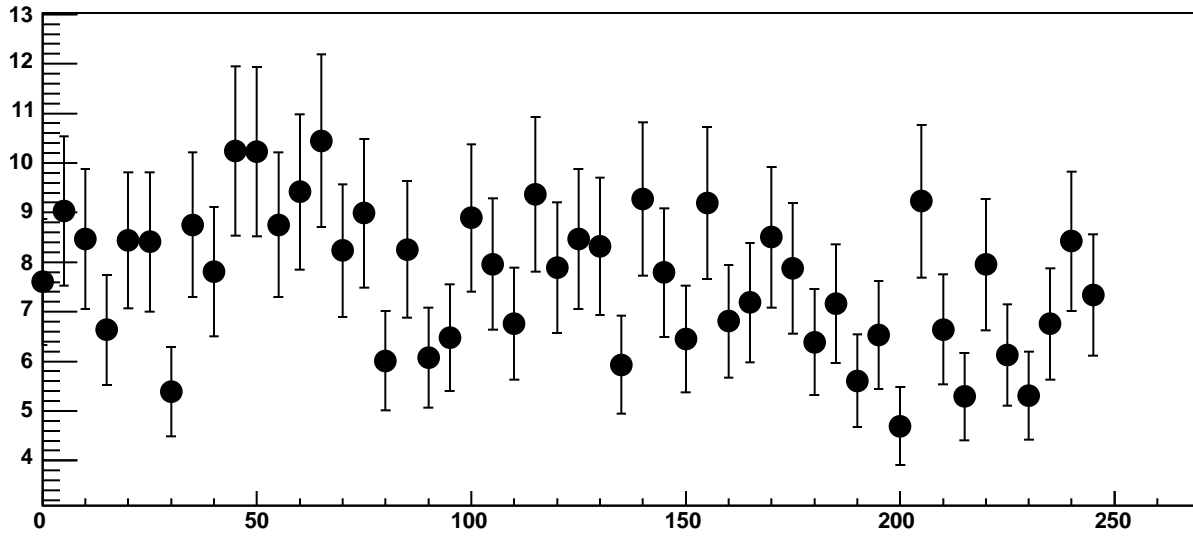


Chip 1, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

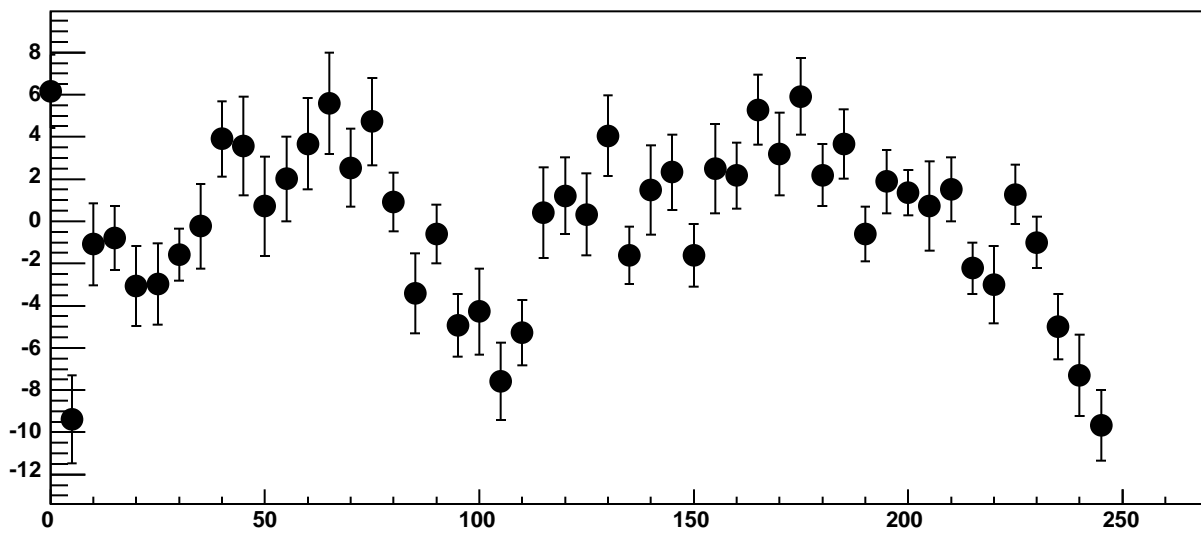


χ^2 / ndf	159.5 / 41
p0	-482.8 ± 0.6448
p1	98.19 ± 0.7353
p2	-8.299e+07 ± 1.086e+06
p3	5.954e+07 ± 2.252e+05
p4	1.383 ± 0.01583

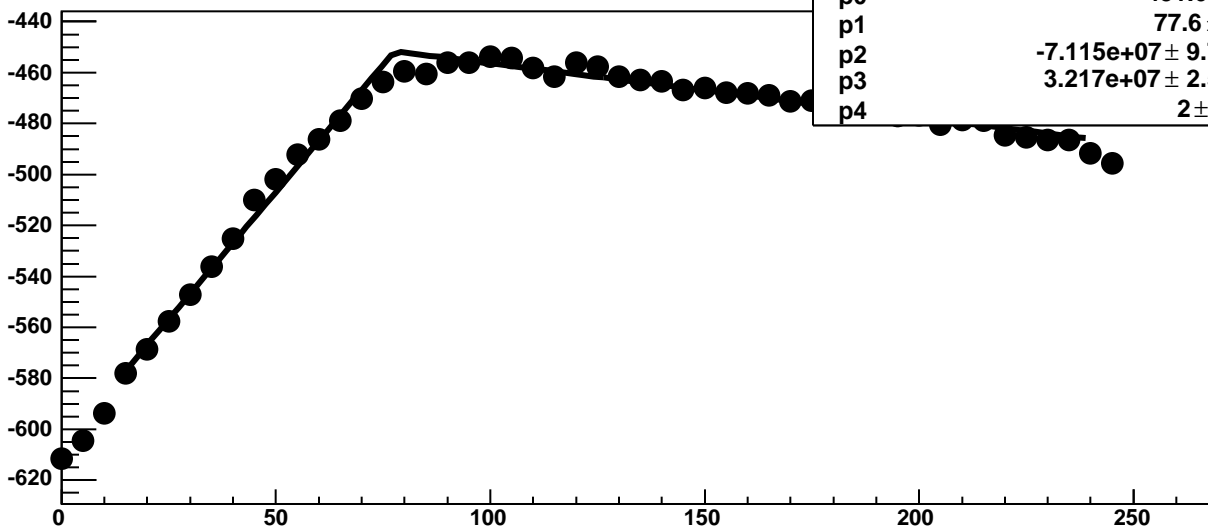
Chip 1, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

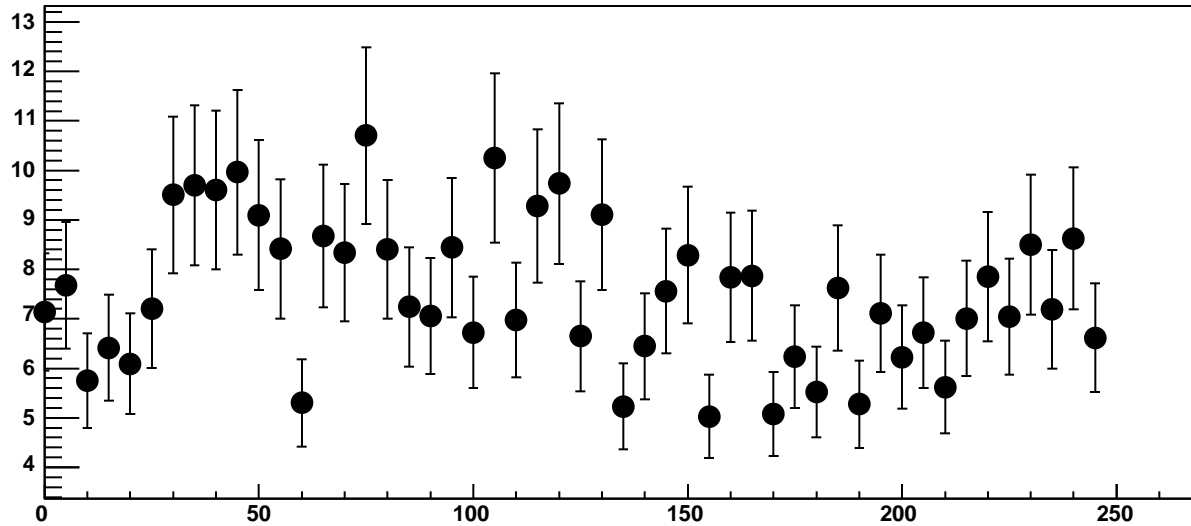


Chip 1, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

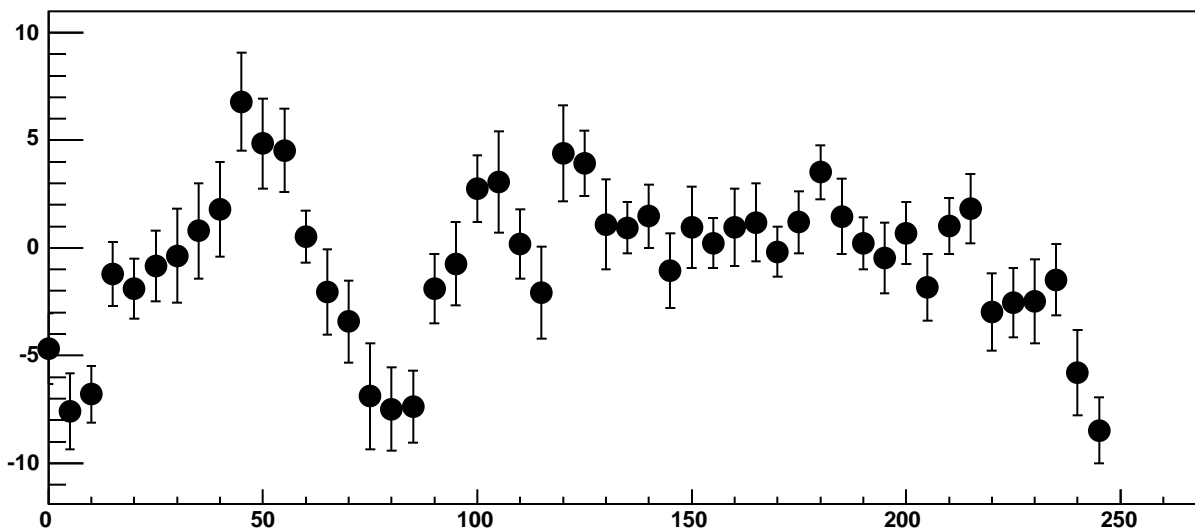


χ^2 / ndf	120.5 / 41
p0	-451.6 ± 0.5541
p1	77.6 ± 0.5395
p2	$-7.115\text{e}+07 \pm 9.763\text{e}+05$
p3	$3.217\text{e}+07 \pm 2.524\text{e}+05$
p4	2 ± 0.02559

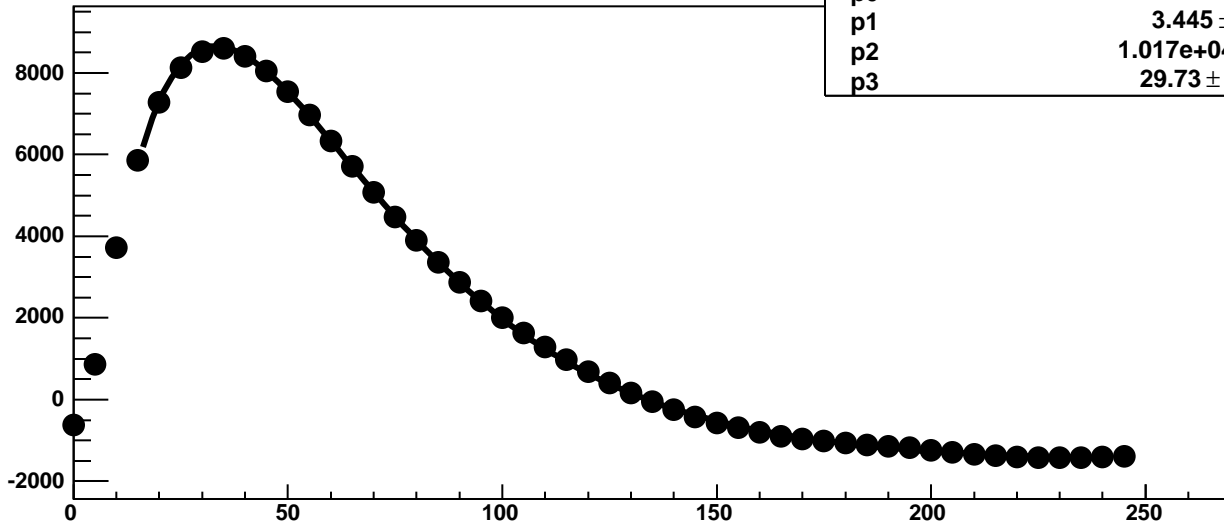
Chip 1, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

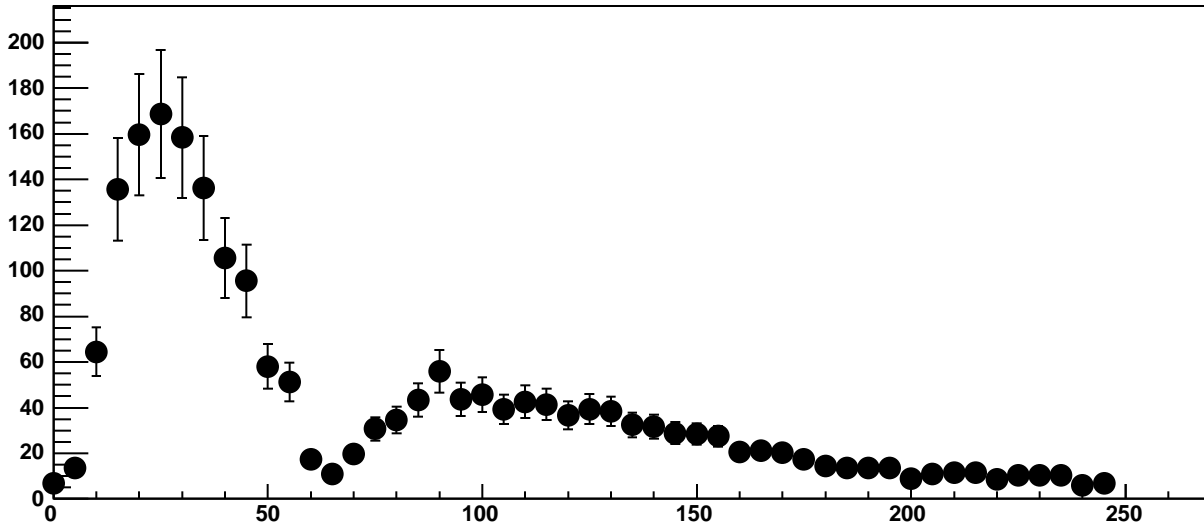


Chip 1, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold

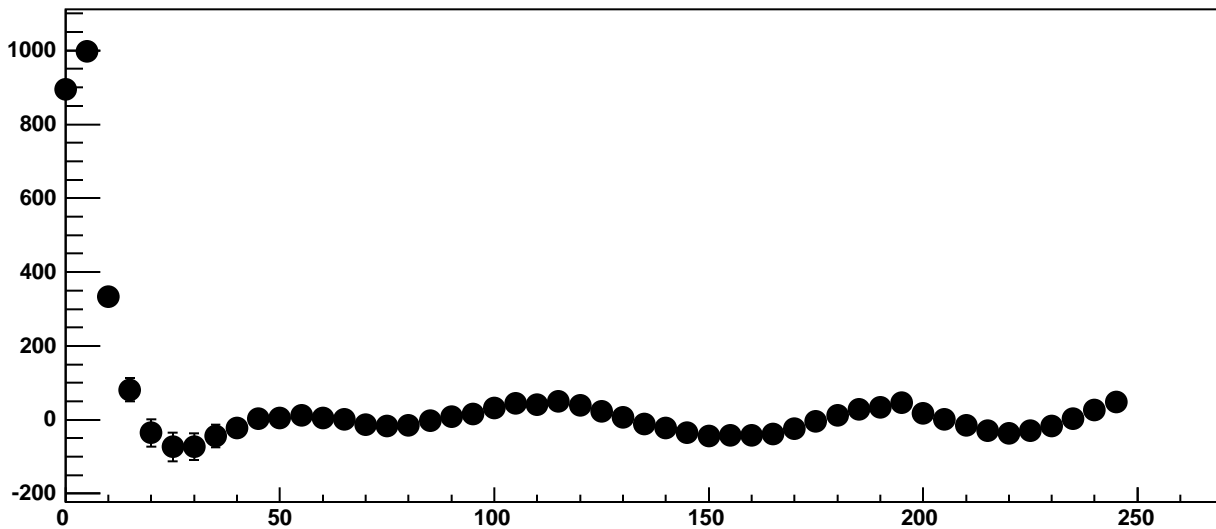


χ^2 / ndf	2008 / 42
p0	-1510 ± 0.9489
p1	3.445 ± 0.0575
p2	1.017e+04 ± 7.24
p3	29.73 ± 0.02223

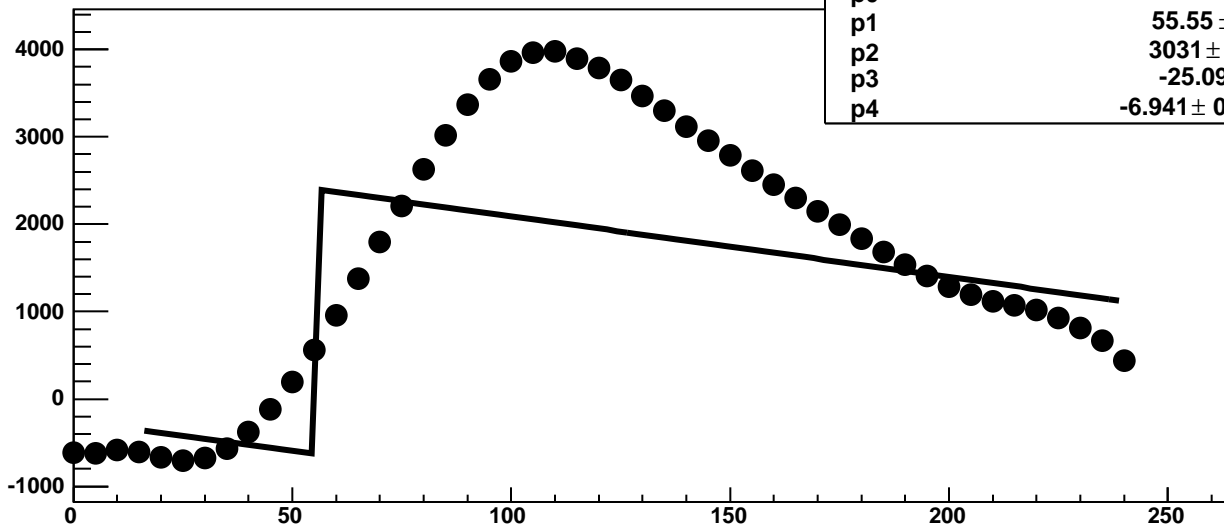
Chip 1, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold



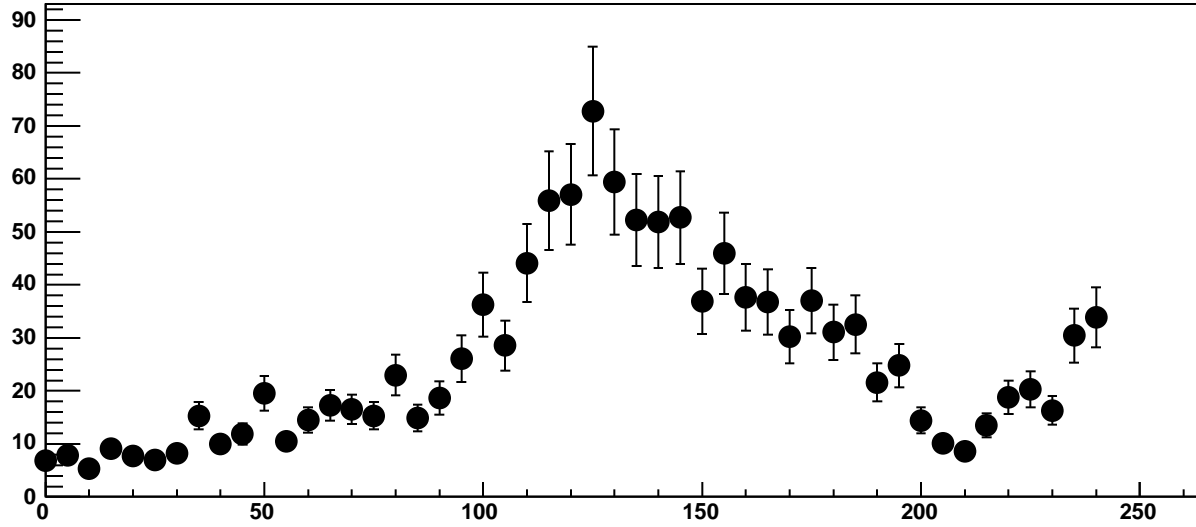
Chip 1, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold



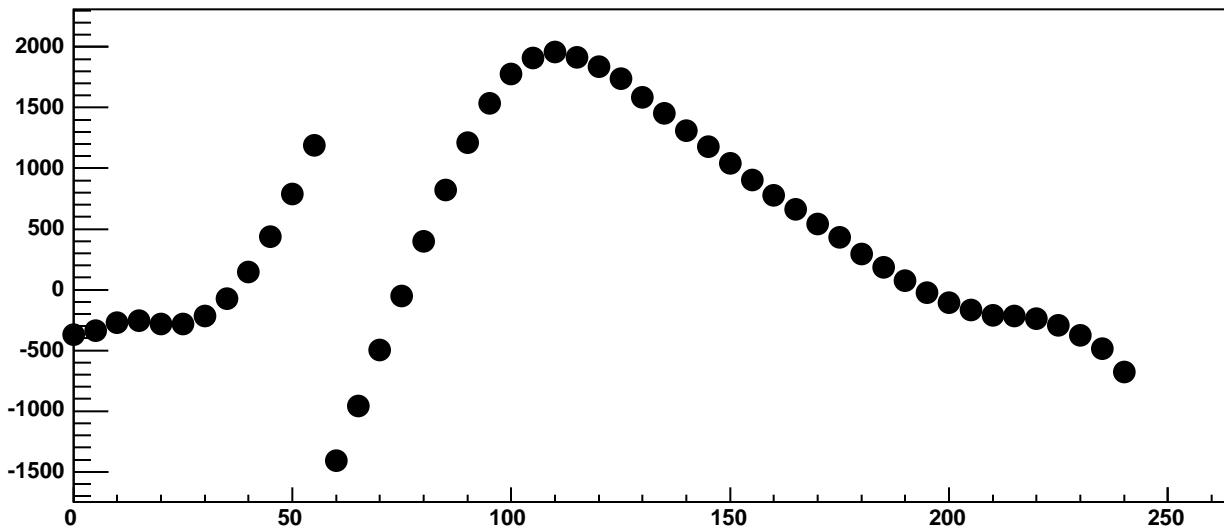
Chip 1, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold



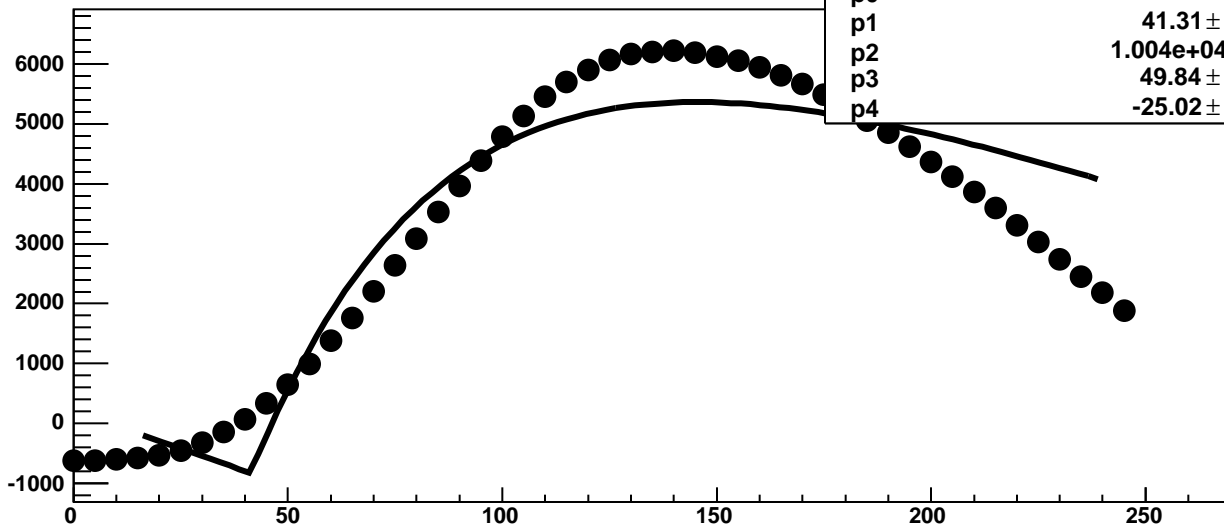
Chip 1, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold

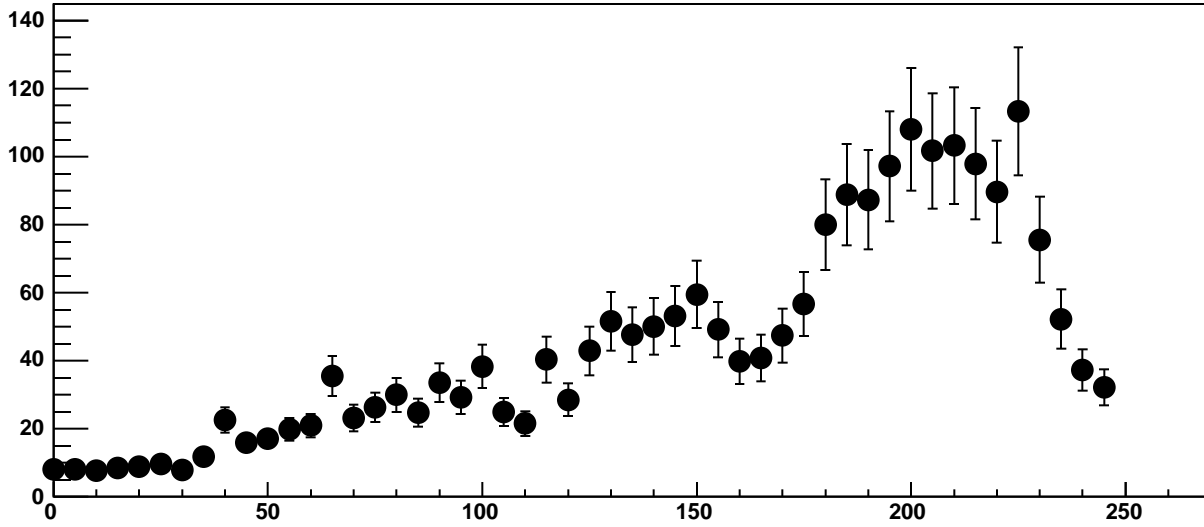


Chip 1, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

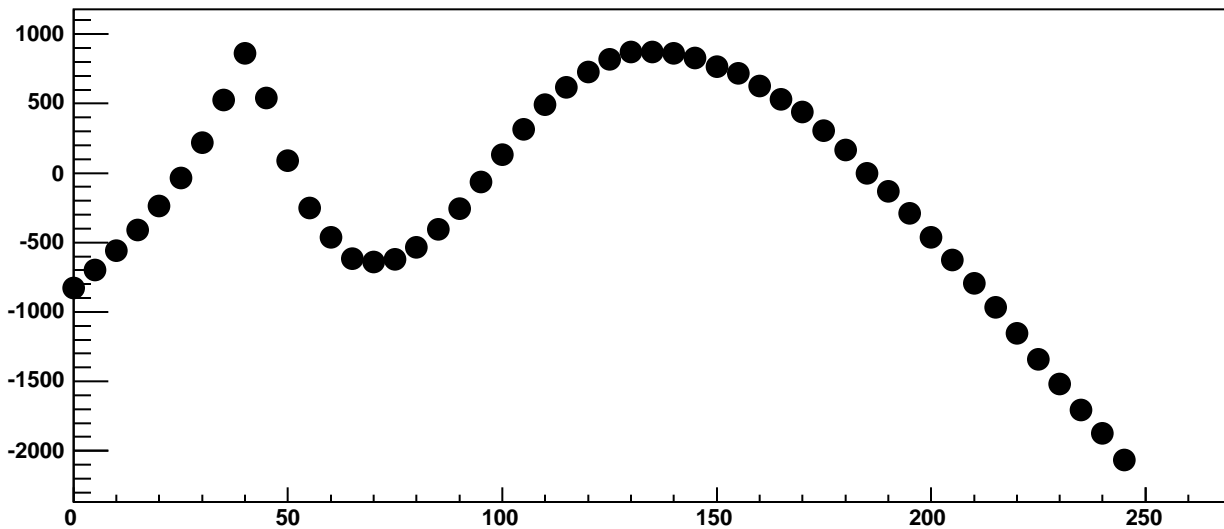


χ^2 / ndf	3.793e+05 / 41
p0	-829.1 ± 1.42
p1	41.31 ± 0.01762
p2	1.004e+04 ± 15.22
p3	49.84 ± 0.07997
p4	-25.02 ± 0.07706

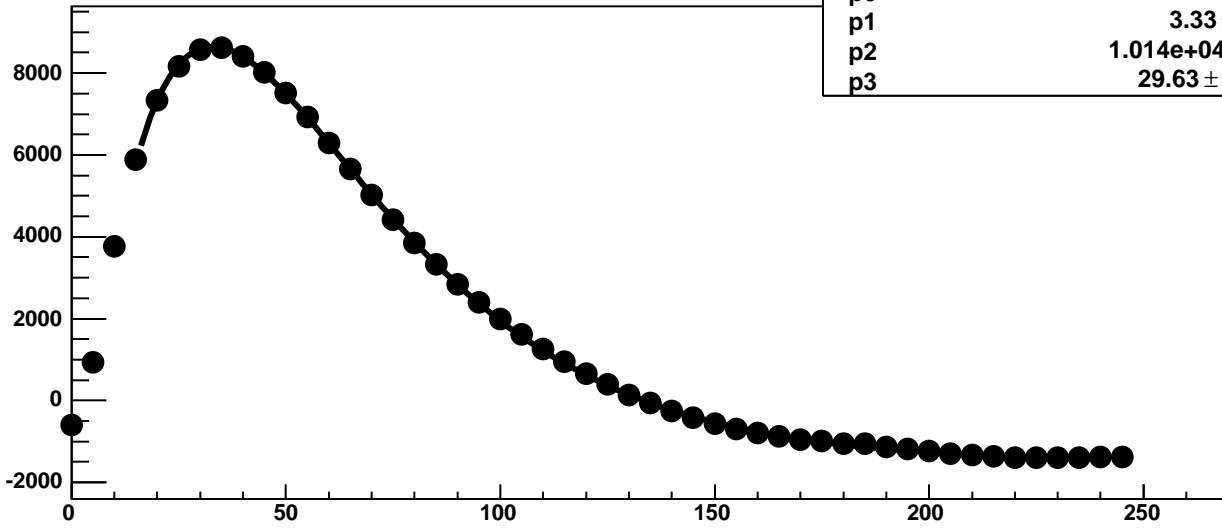
Chip 1, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

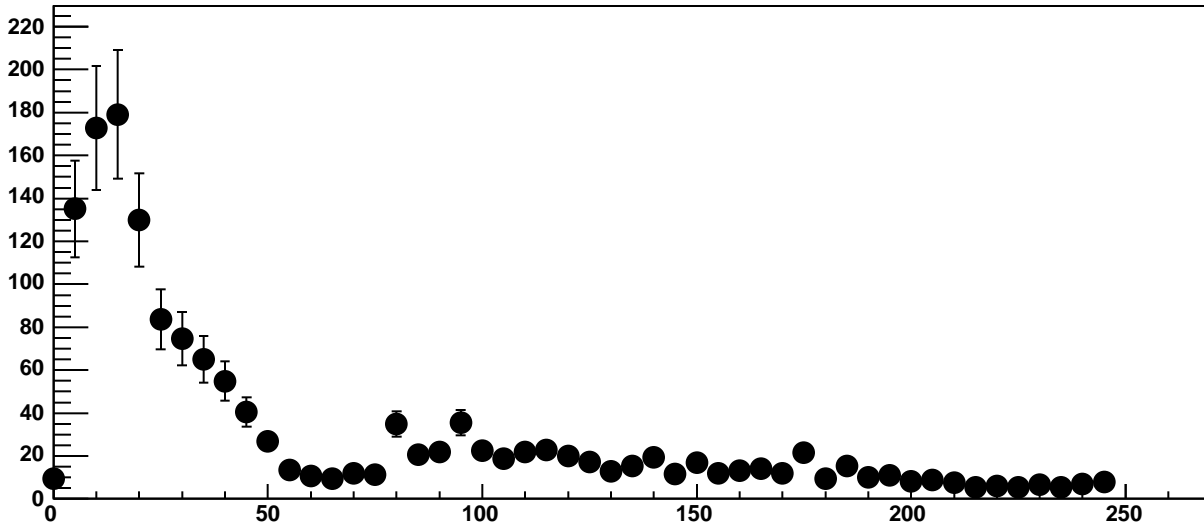


Chip 1, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold

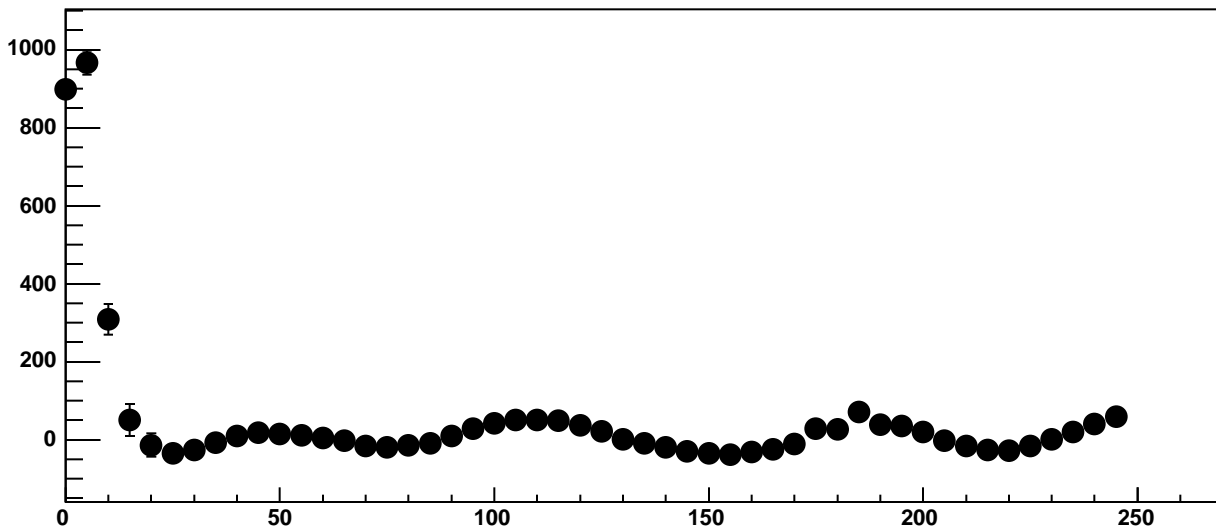


χ^2 / ndf	4385 / 42
p0	-1497 ± 0.6247
p1	3.33 ± 0.0471
p2	$1.014\text{e}+04 \pm 5.194$
p3	29.63 ± 0.01383

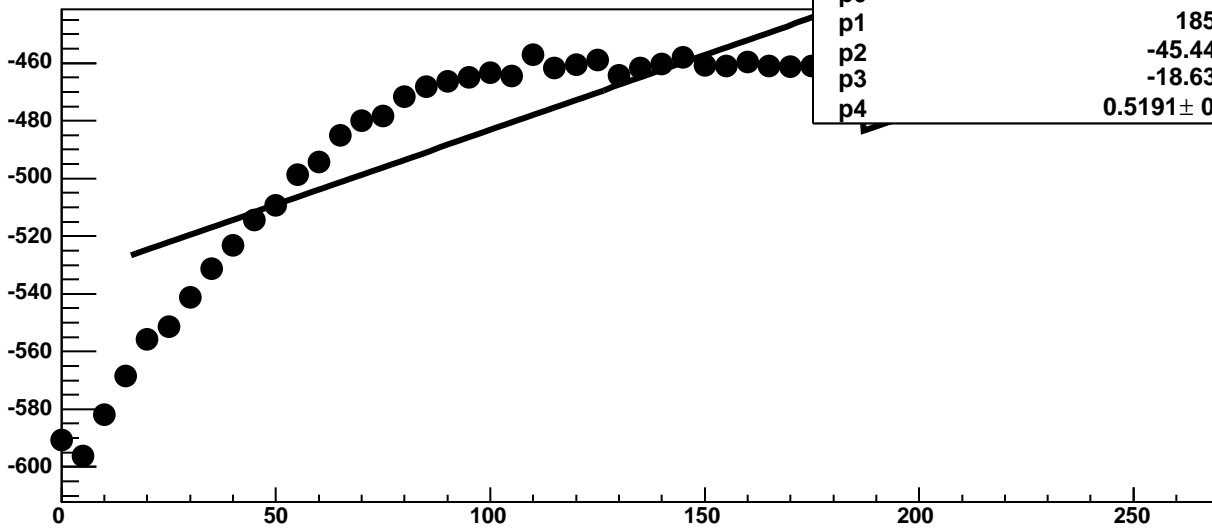
Chip 1, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold

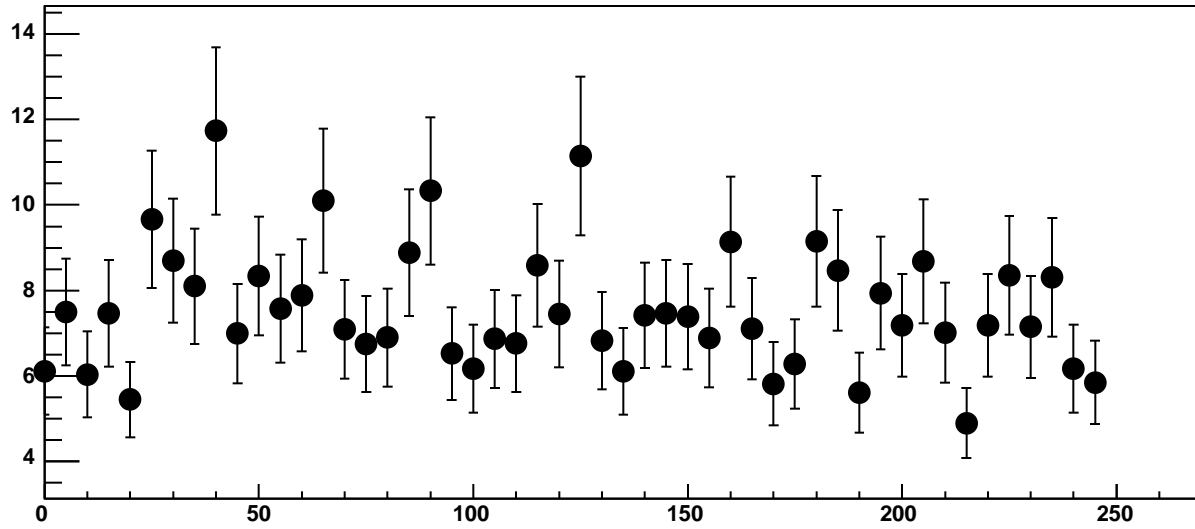


Chip 1, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

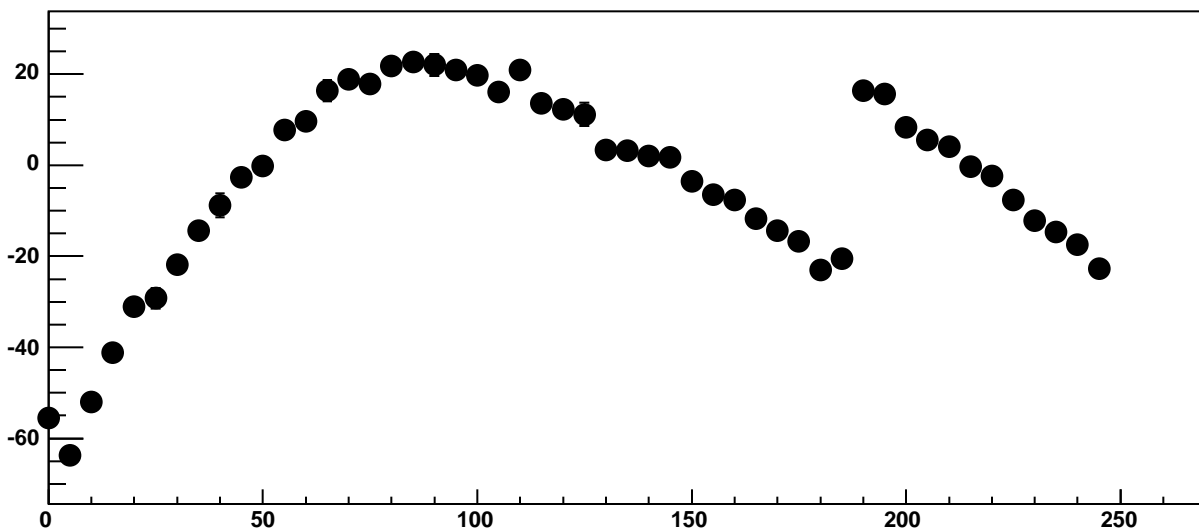


χ^2 / ndf	4299 / 41
p0	-439 ± 2.203
p1	185 ± 4.137
p2	-45.44 ± 3.319
p3	-18.63 ± 24.49
p4	0.5191 ± 0.003213

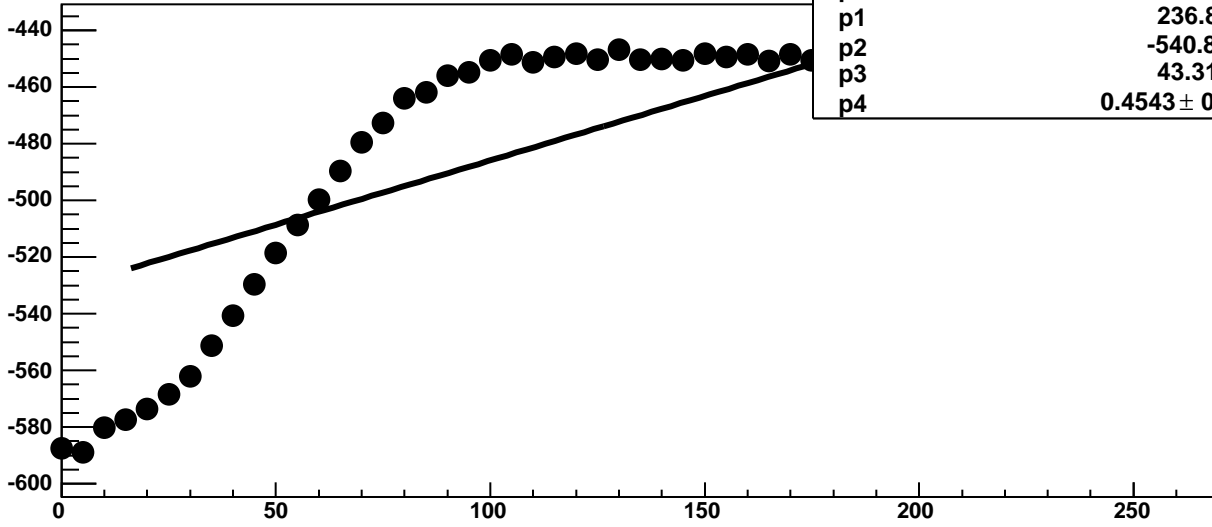
Chip 1, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

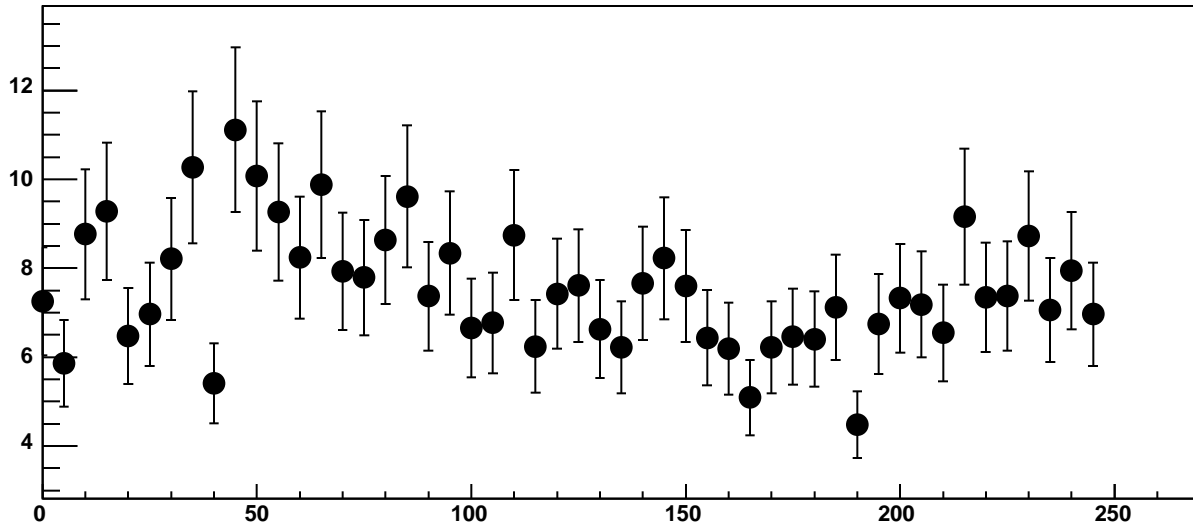


Chip 1, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

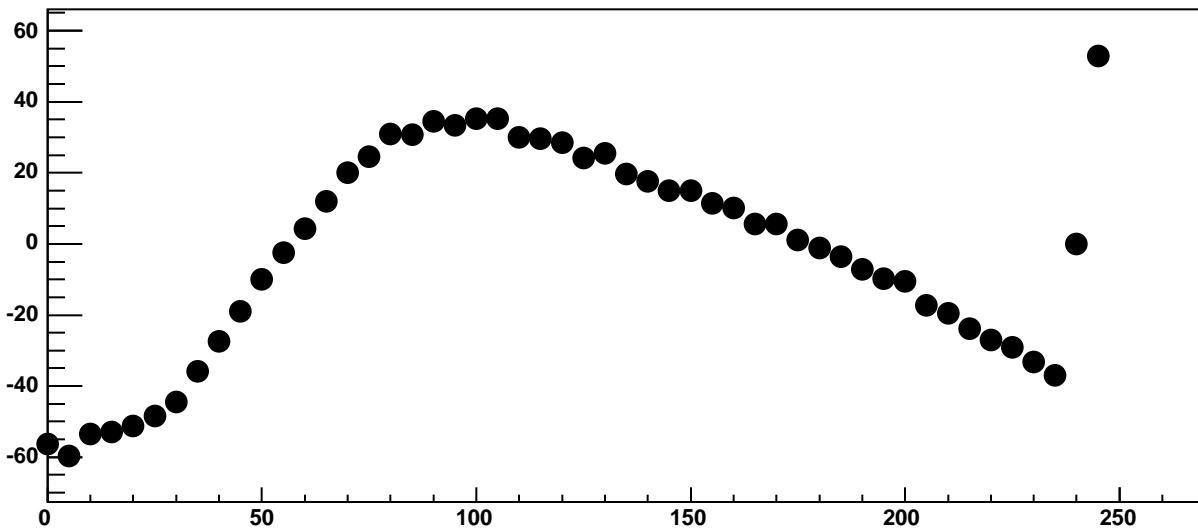


χ^2 / ndf	1.051e+04 / 41
p0	-423.7 ± 0.9493
p1	236.8 ± 1.821
p2	-540.8 ± 290.9
p3	43.31 ± 19.89
p4	0.4543 ± 0.003925

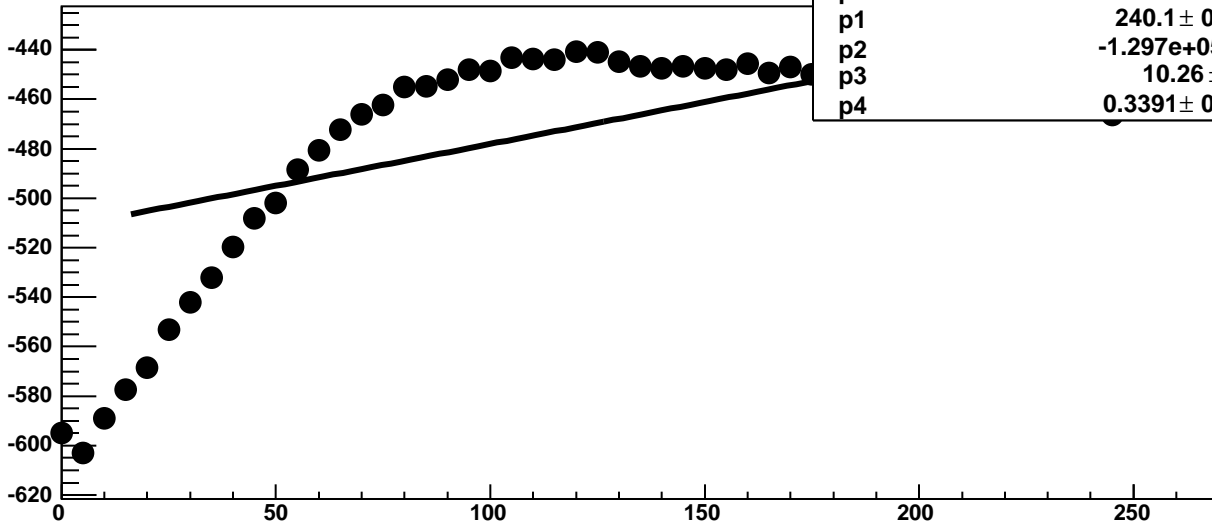
Chip 1, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

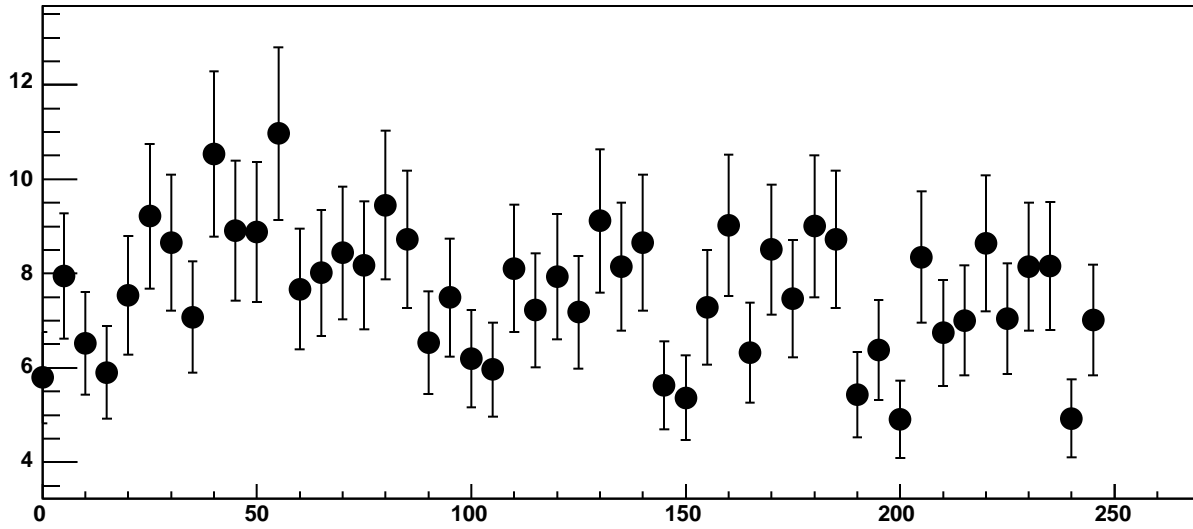


Chip 1, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold

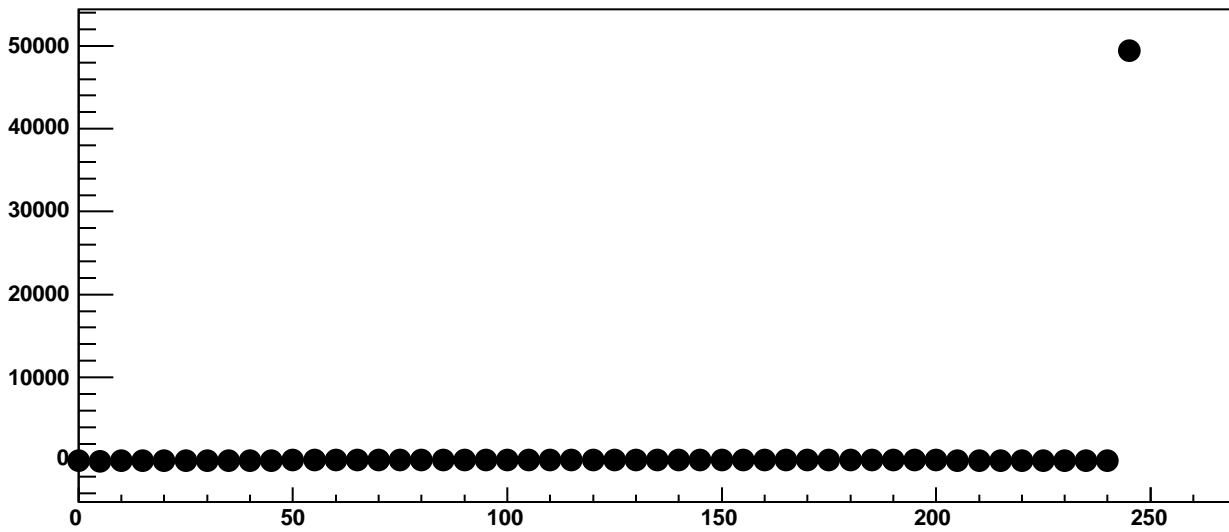


χ^2 / ndf	1.172e+04 / 41
p0	-430.6 ± 0.4573
p1	240.1 ± 0.002604
p2	-1.297e+05 ± 302
p3	10.26 ± 0.9197
p4	0.3391 ± 0.003715

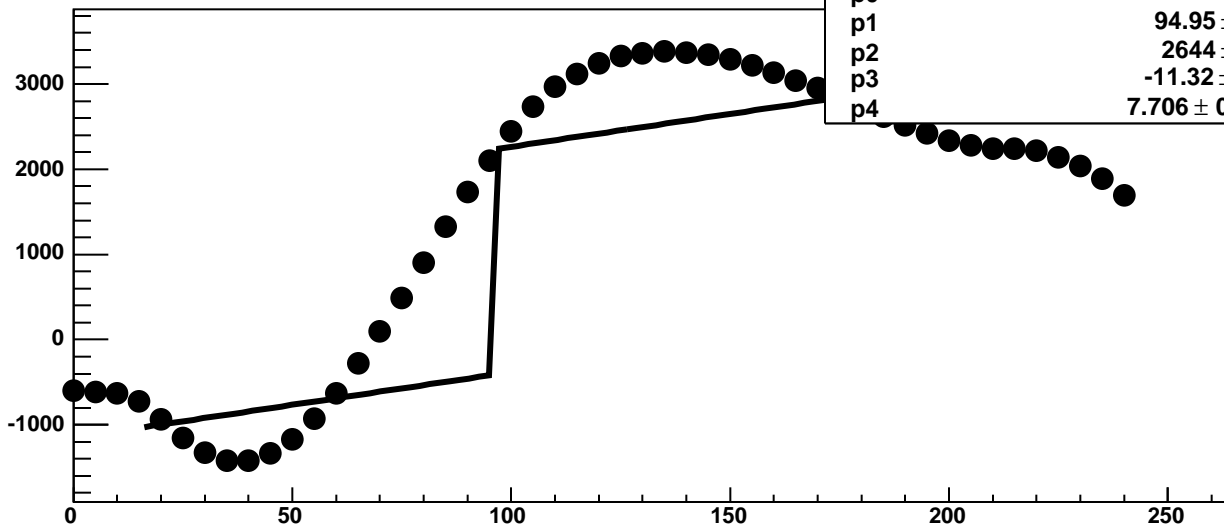
Chip 1, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold

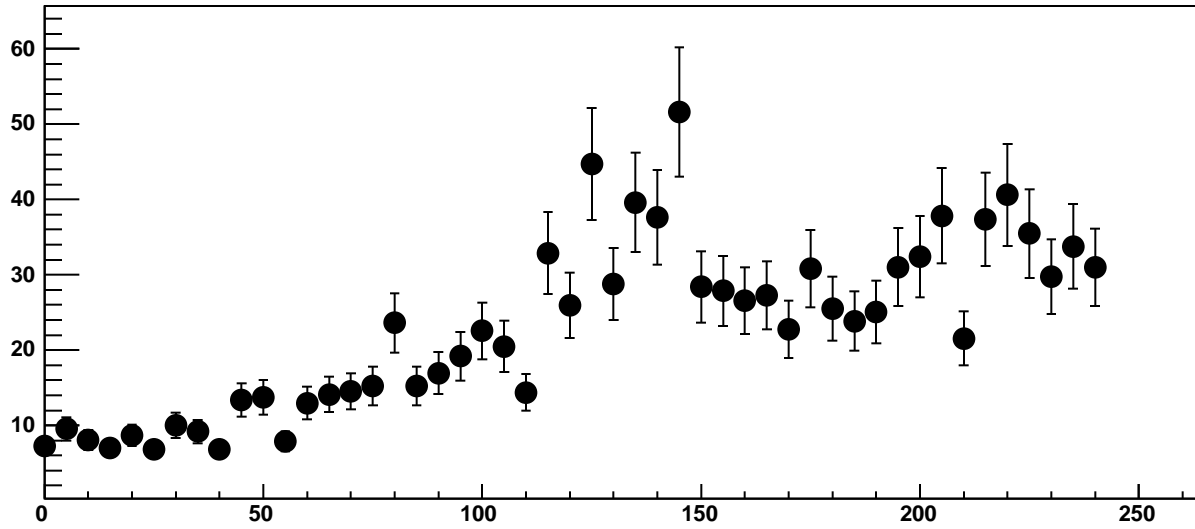


Chip 1, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold

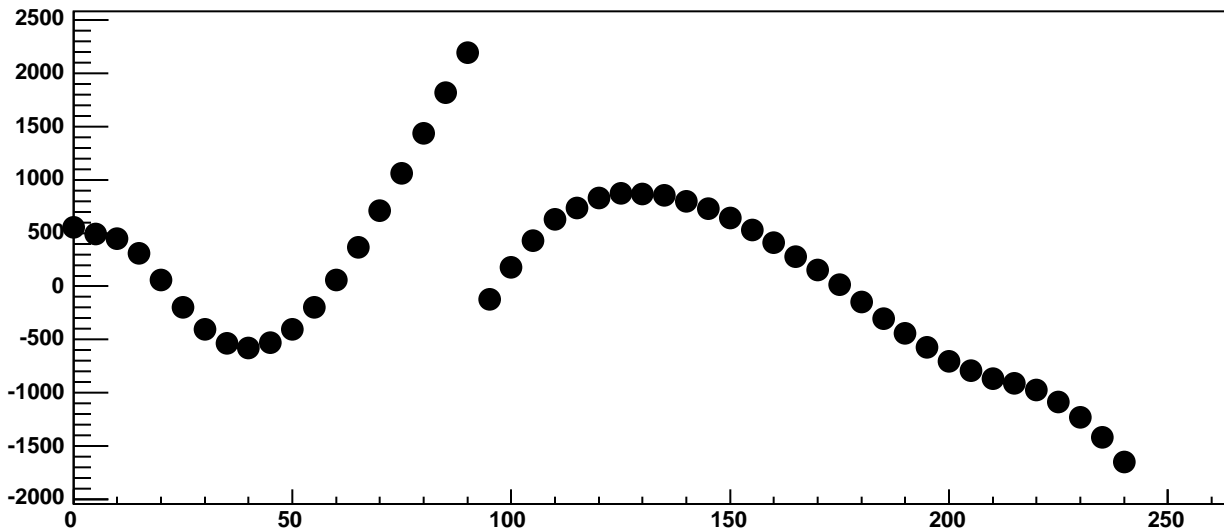


χ^2 / ndf	1.523e+06 / 41
p0	-419.5 ± 0.2935
p1	94.95 ± 0.4498
p2	2644 ± 0.4212
p3	-11.32 ± 0.2425
p4	7.706 ± 0.003702

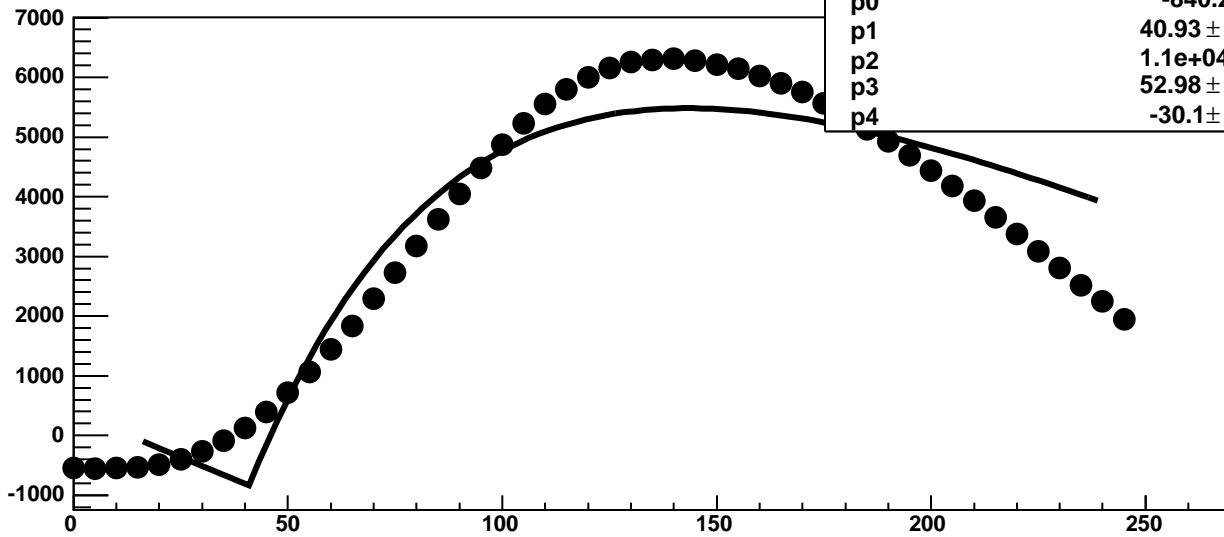
Chip 1, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold

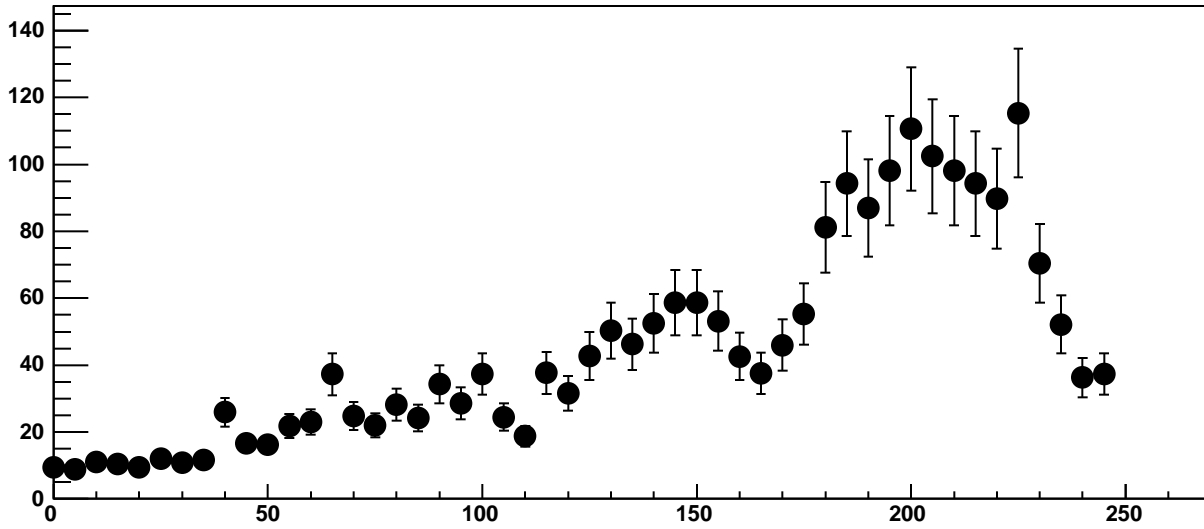


Chip 1, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold

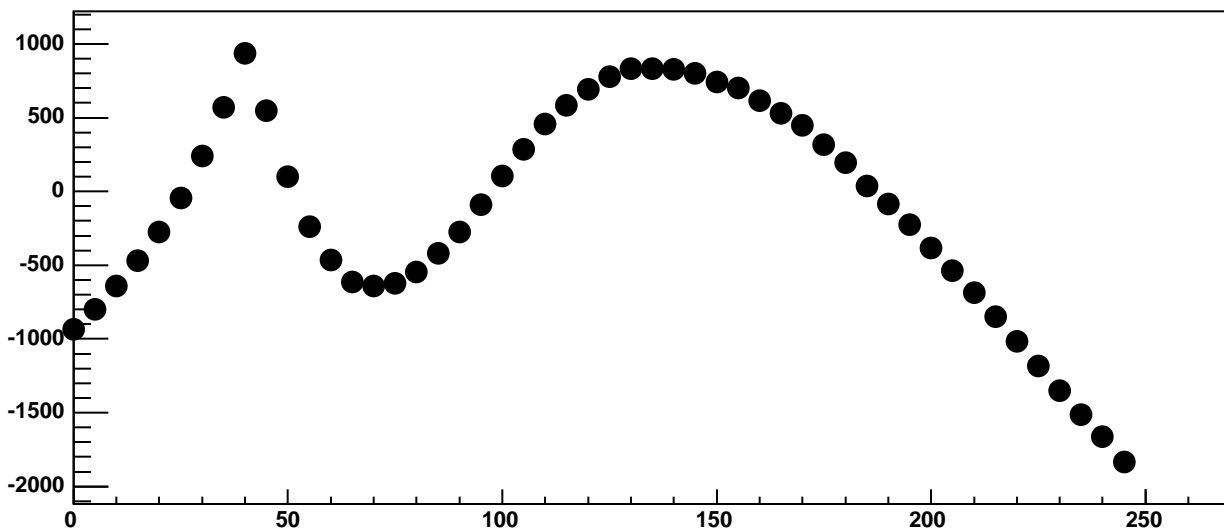


χ^2 / ndf	3.561e+05 / 41
p0	-840.2 ± 1.541
p1	40.93 ± 0.01796
p2	1.1e+04 ± 17.44
p3	52.98 ± 0.08667
p4	-30.1 ± 0.08474

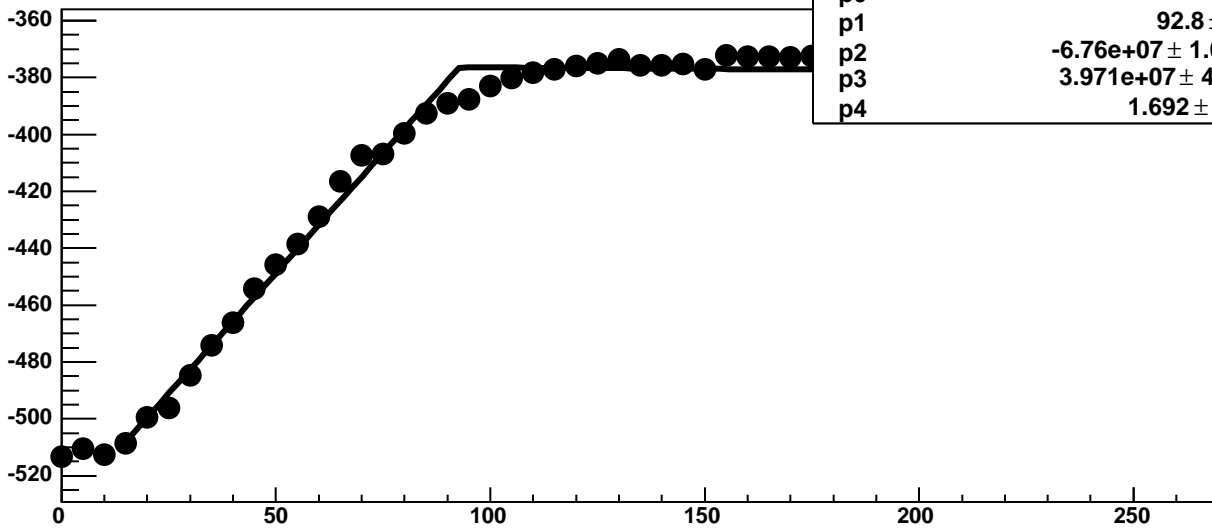
Chip 1, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

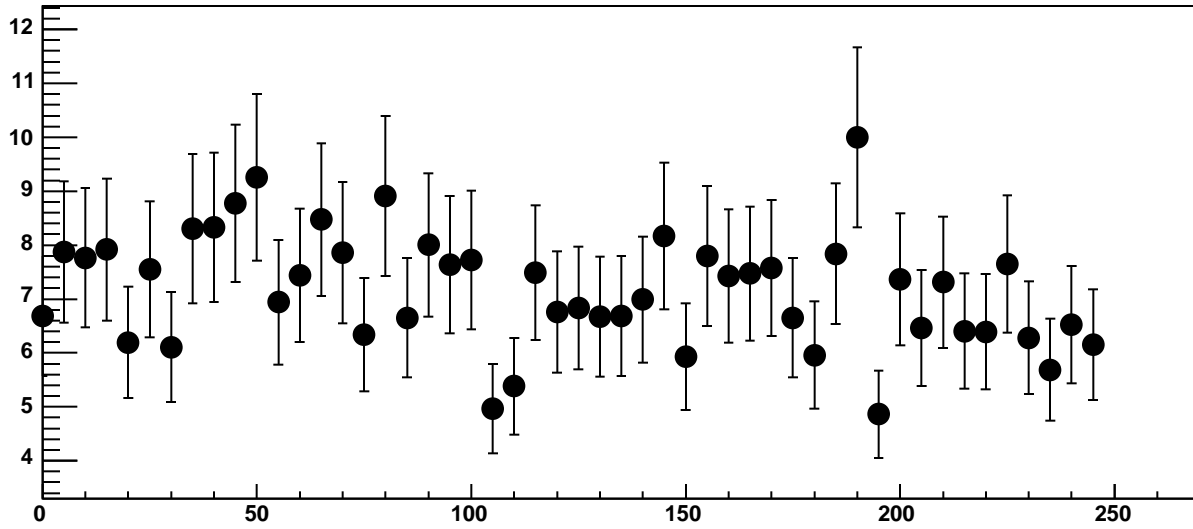


Chip 1, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold

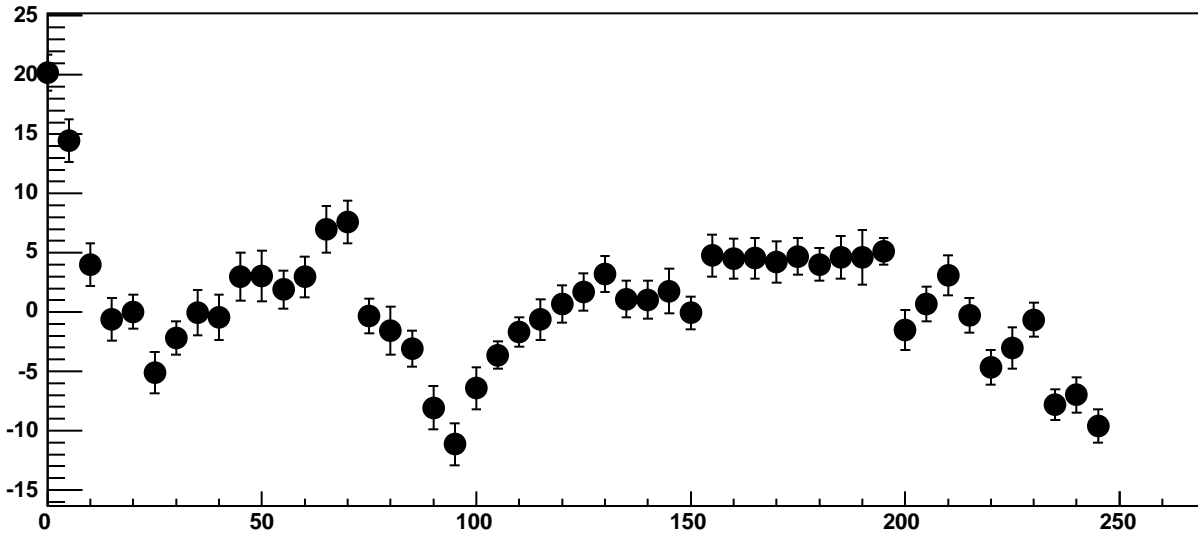


χ^2 / ndf	300 / 41
p0	-376.4 ± 0.544
p1	92.8 ± 0.6039
p2	$-6.76\text{e}+07 \pm 1.024\text{e}+06$
p3	$3.971\text{e}+07 \pm 4.37\text{e}+05$
p4	1.692 ± 0.01836

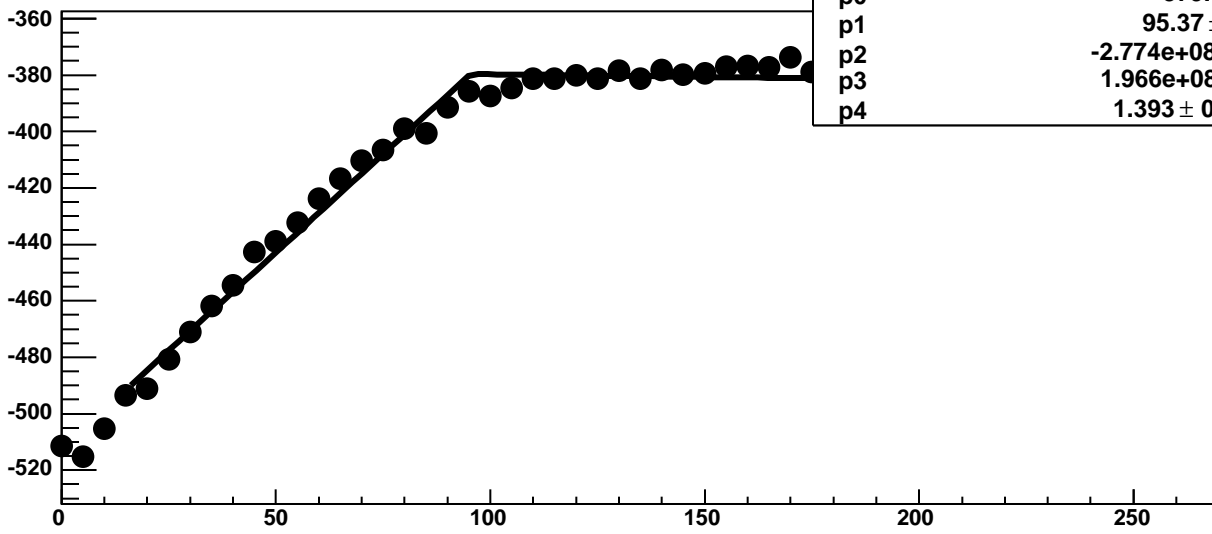
Chip 1, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold

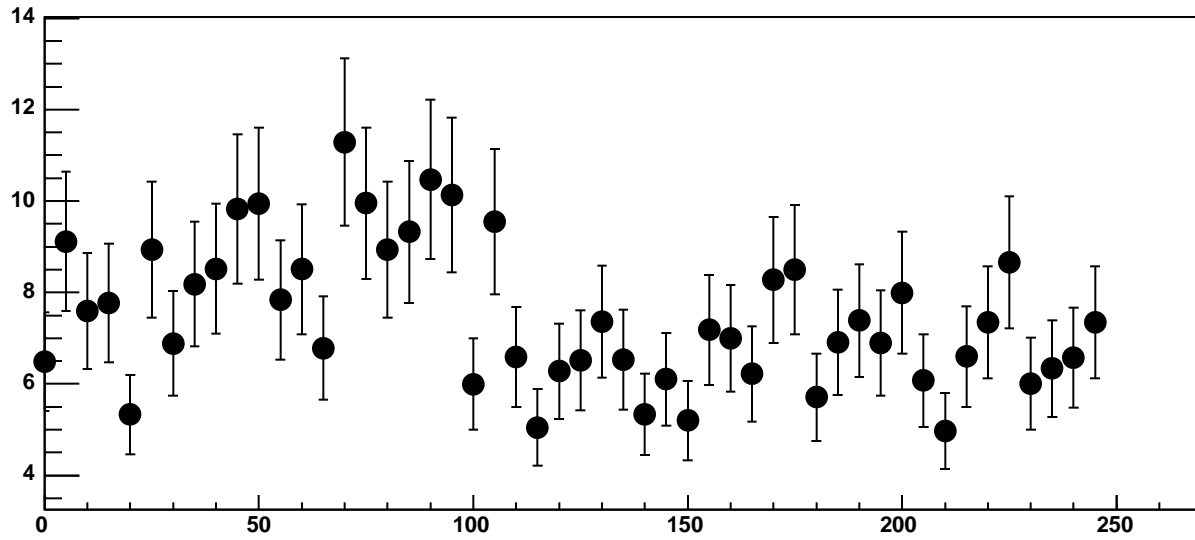


Chip 1, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

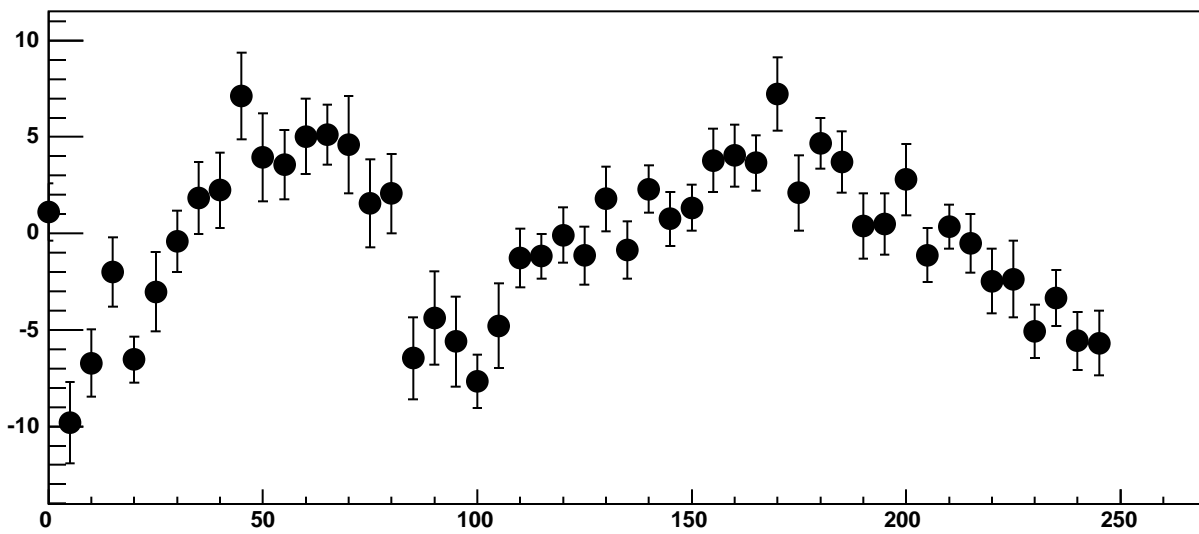


χ^2 / ndf	229.2 / 41
p0	-379.7 ± 0.52
p1	95.37 ± 0.6459
p2	$-2.774\text{e}+08 \pm 1.414$
p3	$1.966\text{e}+08 \pm 1.414$
p4	1.393 ± 0.006206

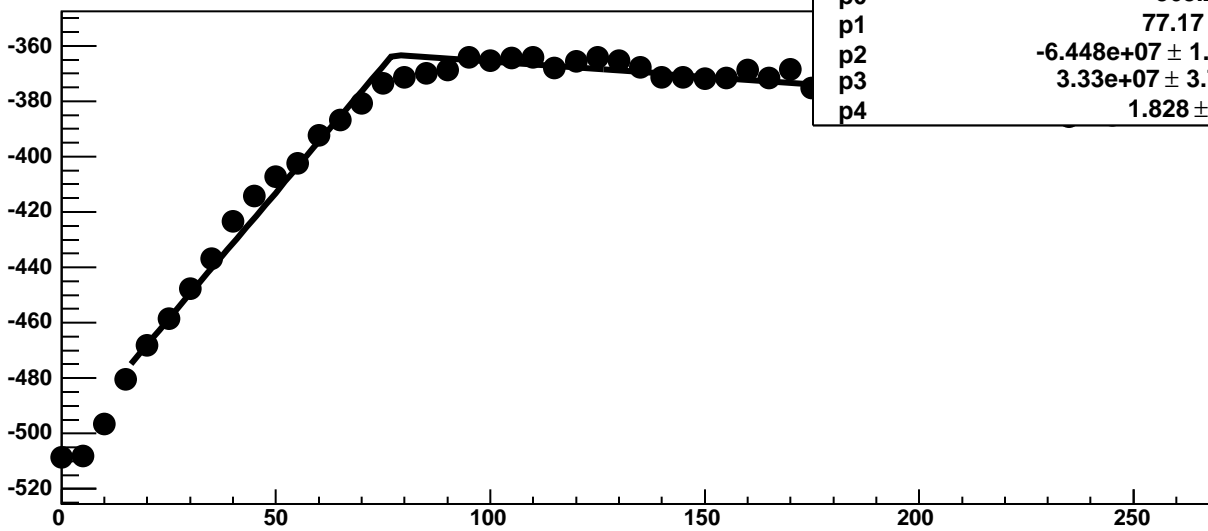
Chip 1, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

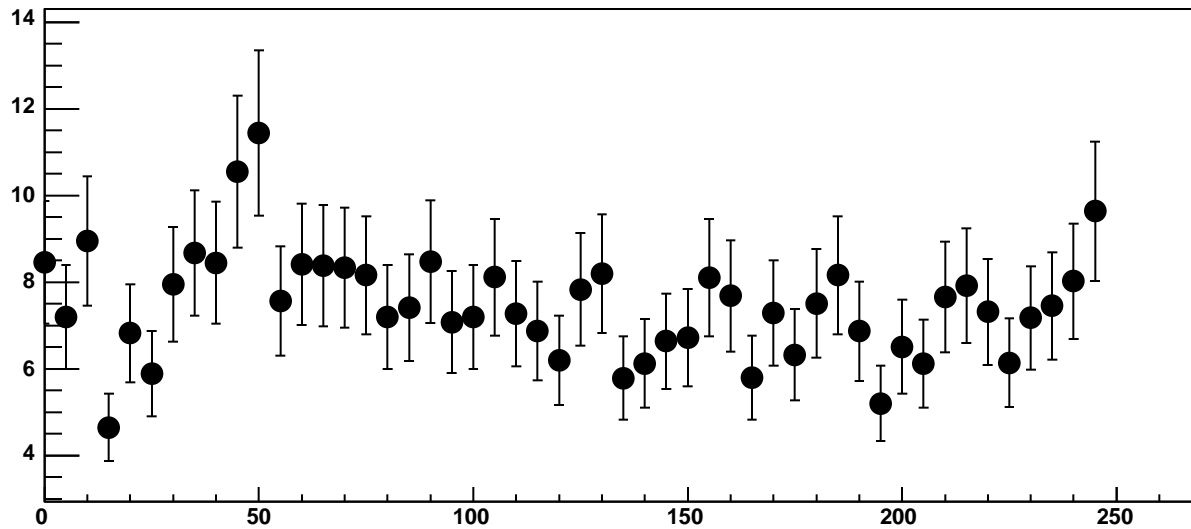


Chip 1, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

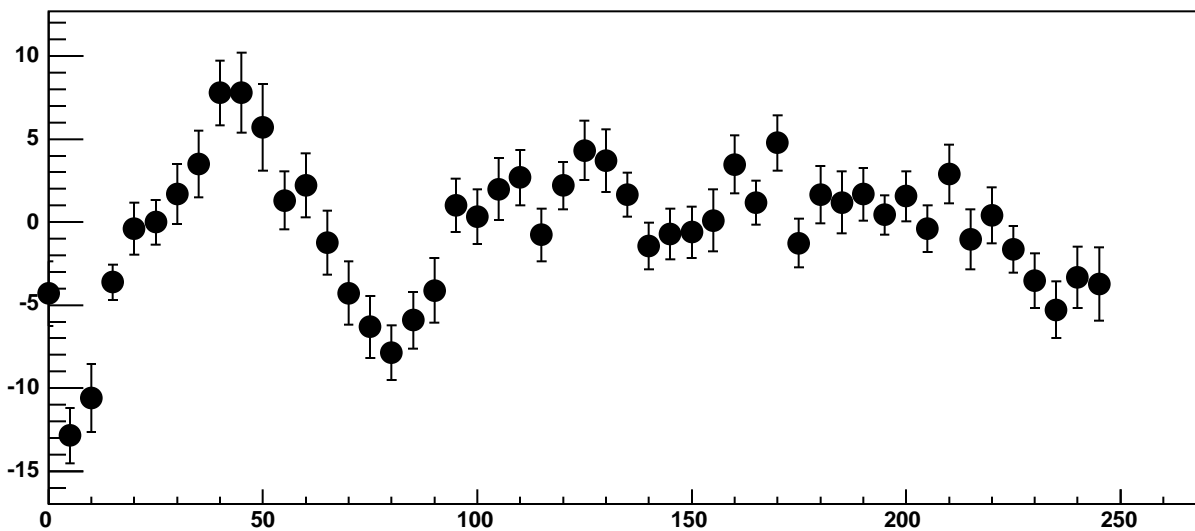


χ^2 / ndf	163.5 / 41
p0	-363.2 ± 0.554
p1	77.17 ± 0.6033
p2	$-6.448\text{e}+07 \pm 1.056\text{e}+06$
p3	$3.33\text{e}+07 \pm 3.767\text{e}+05$
p4	1.828 ± 0.02336

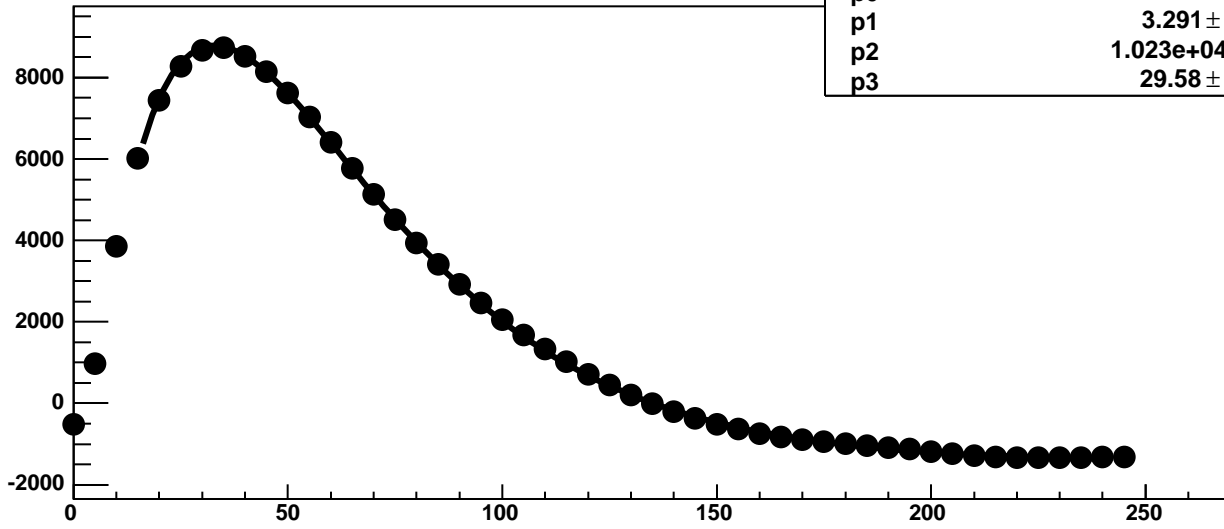
Chip 1, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

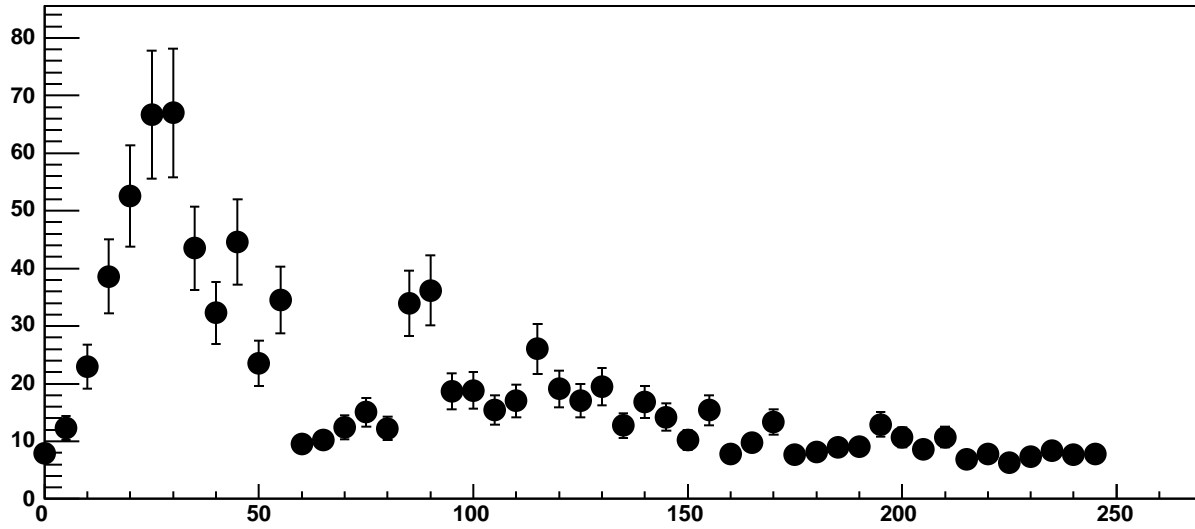


Chip 1, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold

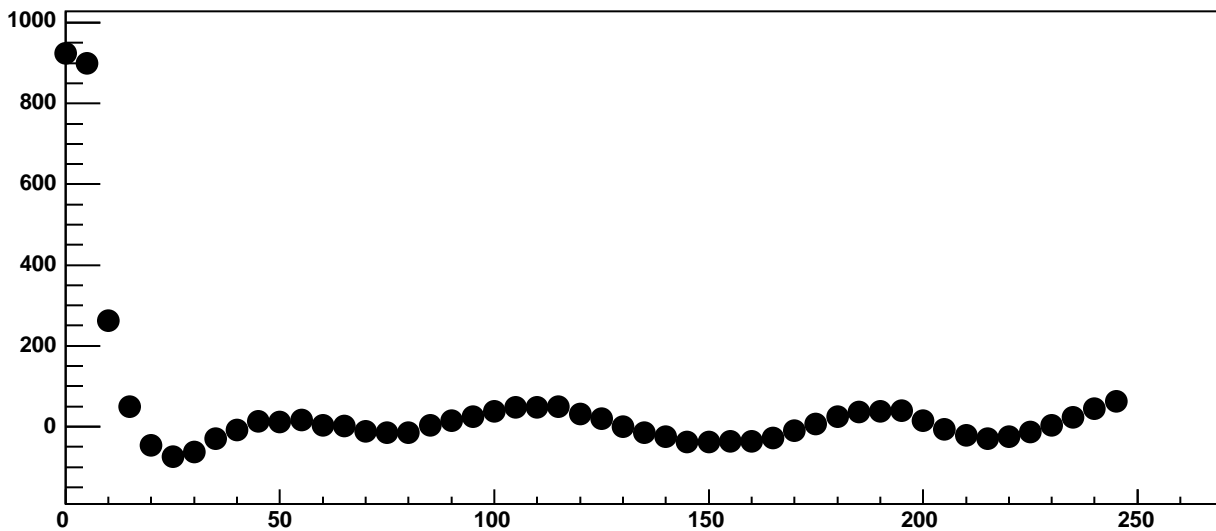


χ^2 / ndf	4431 / 42
p0	-1439 ± 0.6741
p1	3.291 ± 0.02032
p2	1.023e+04 ± 2.742
p3	29.58 ± 0.01066

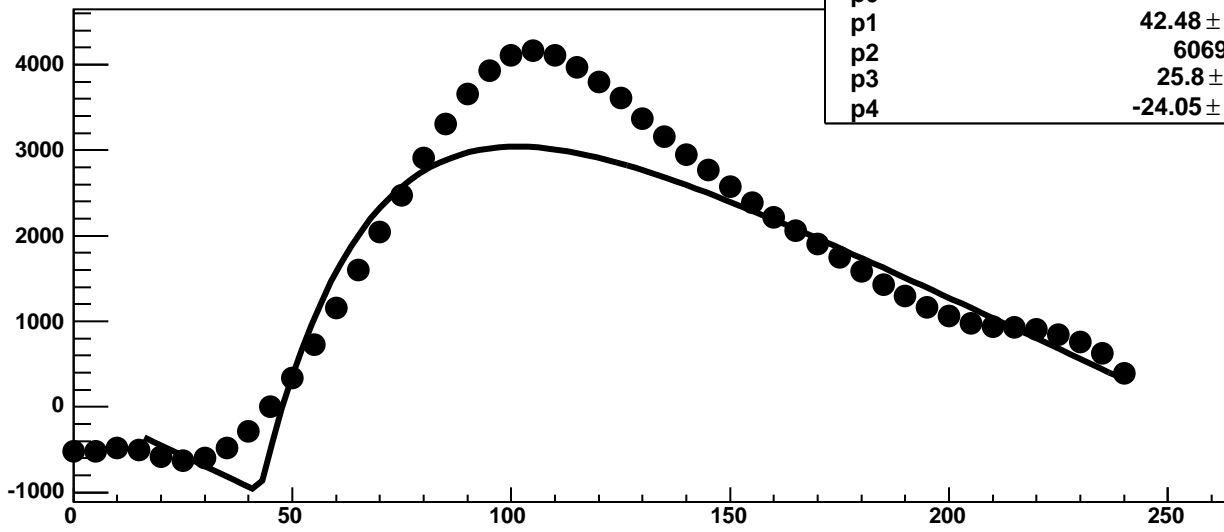
Chip 1, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold

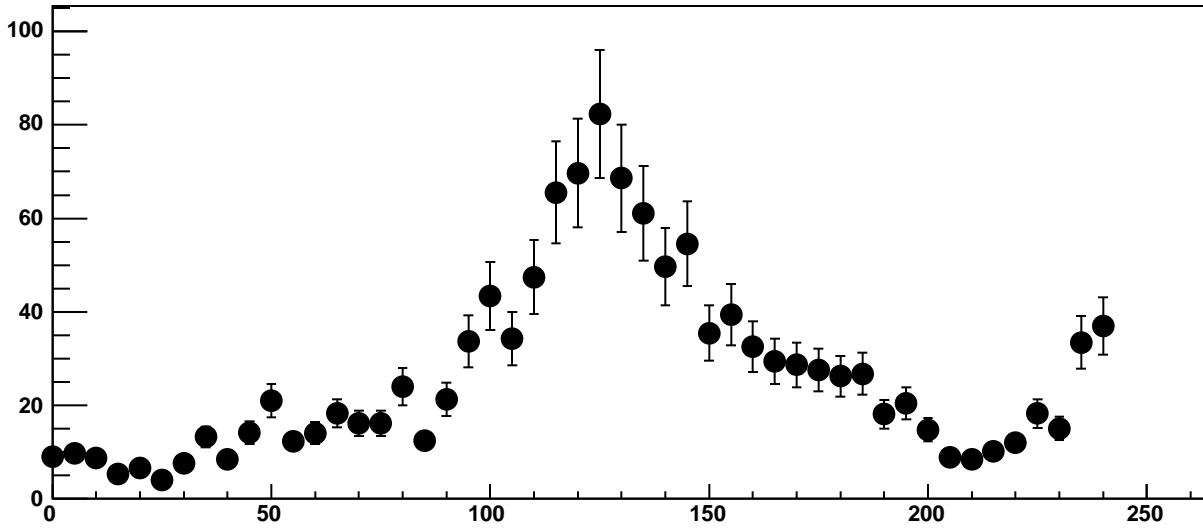


Chip 1, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

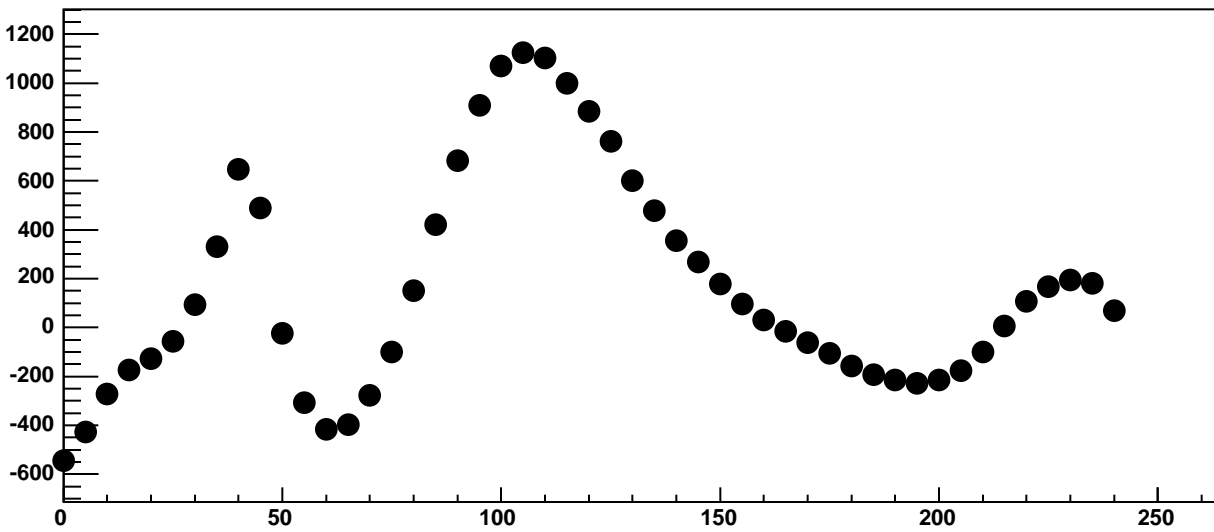


χ^2 / ndf	3.618e+05 / 41
p0	-992.2 ± 0.7735
p1	42.48 ± 0.01422
p2	6069 ± 5.693
p3	25.8 ± 0.03721
p4	-24.05 ± 0.03093

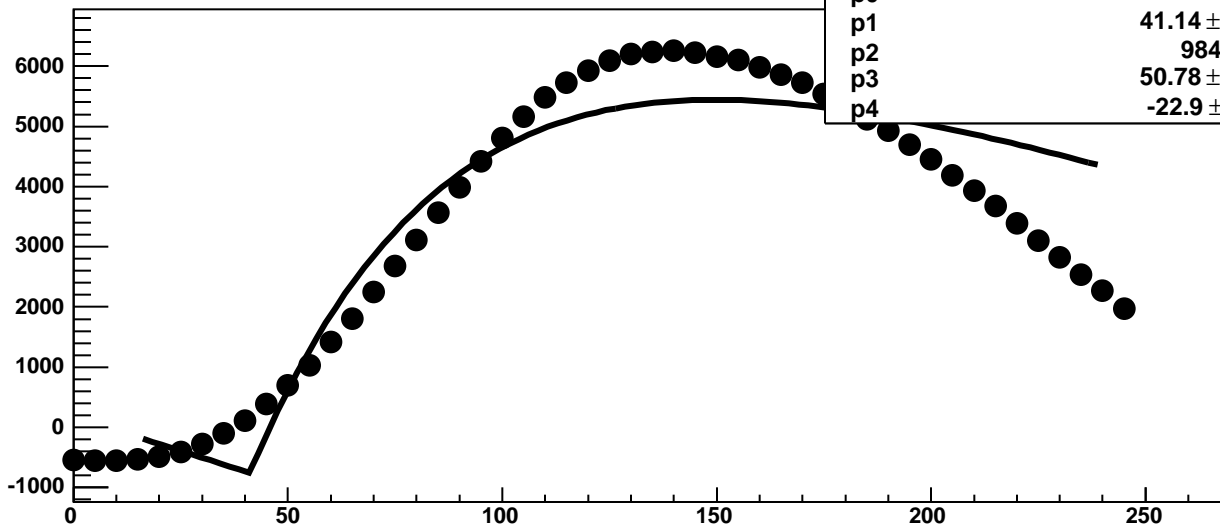
Chip 1, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



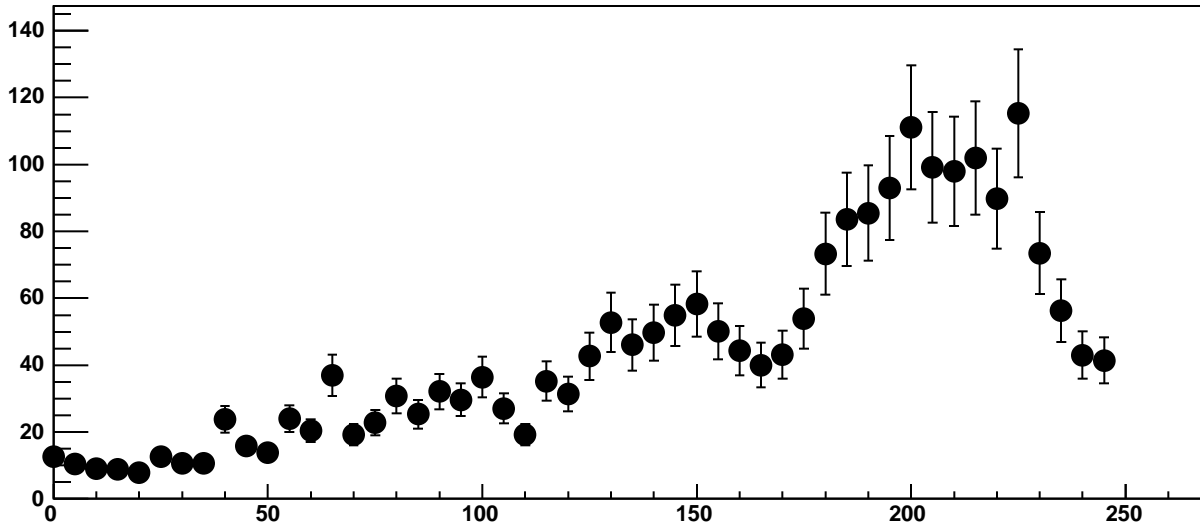
Chip 1, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold



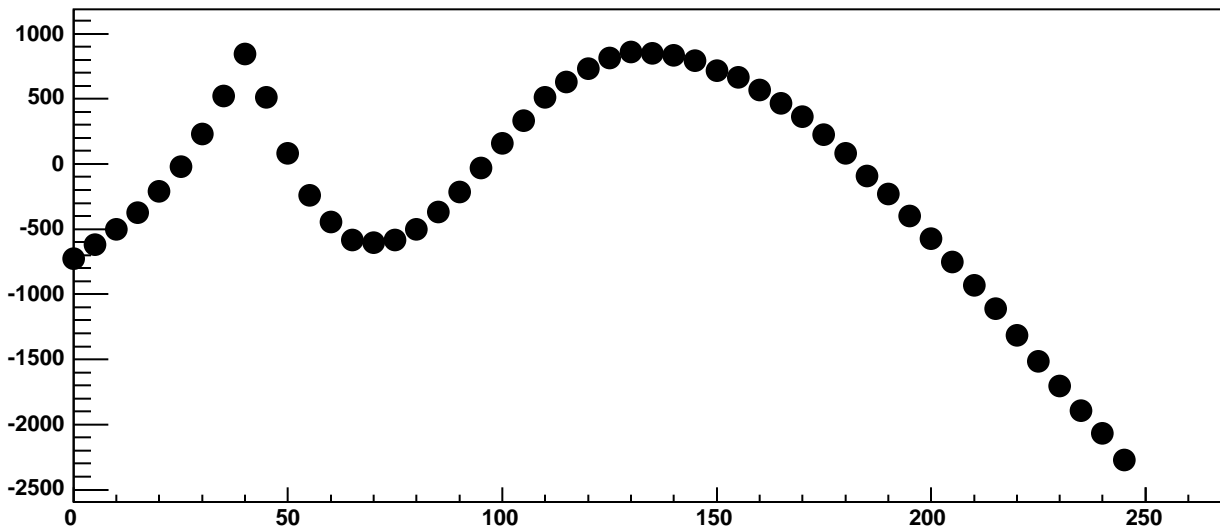
Chip 1, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold



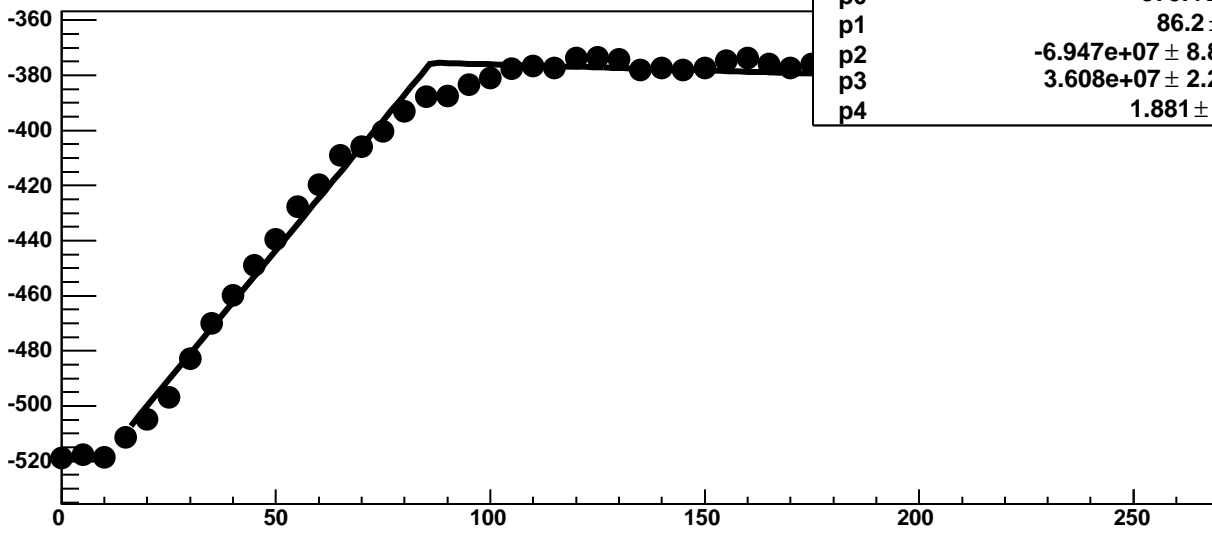
Chip 1, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold

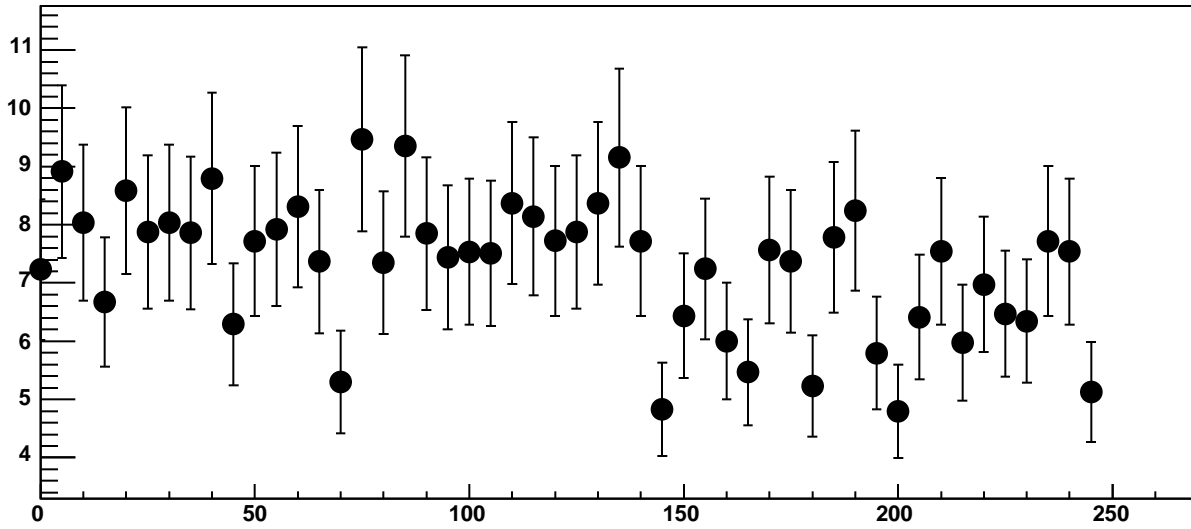


Chip 1, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold

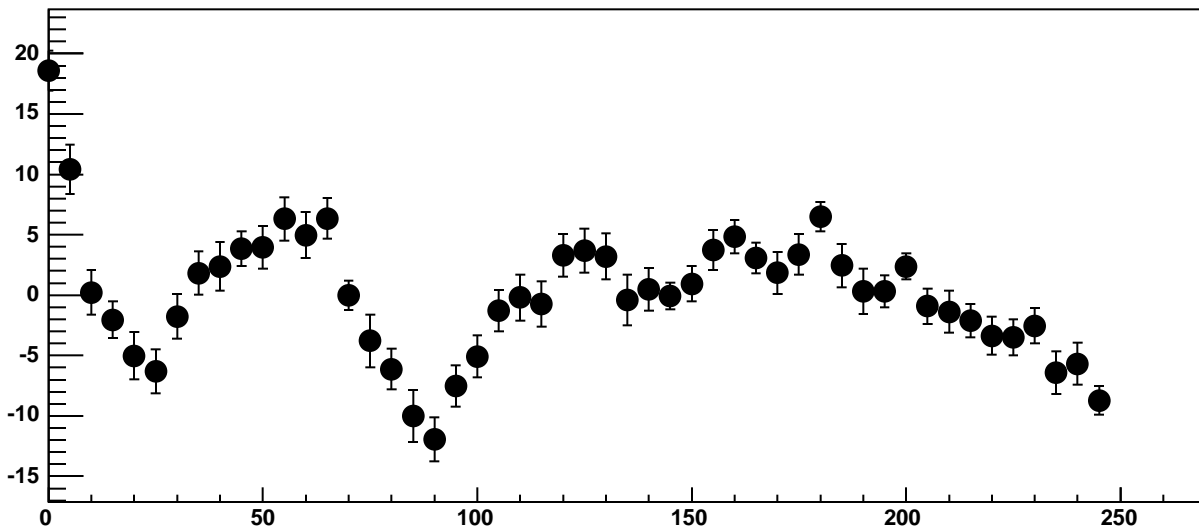


χ^2 / ndf	295.5 / 41
p0	-375.4 ± 0.6065
p1	86.2 ± 0.5587
p2	$-6.947\text{e}+07 \pm 8.824\text{e}+05$
p3	$3.608\text{e}+07 \pm 2.287\text{e}+05$
p4	1.881 ± 0.02095

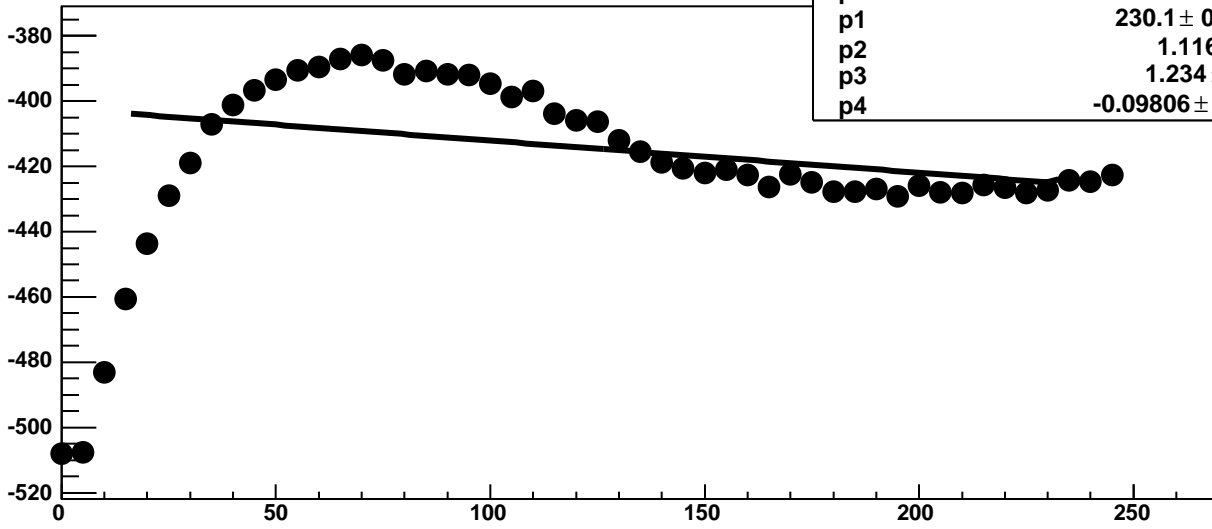
Chip 1, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold

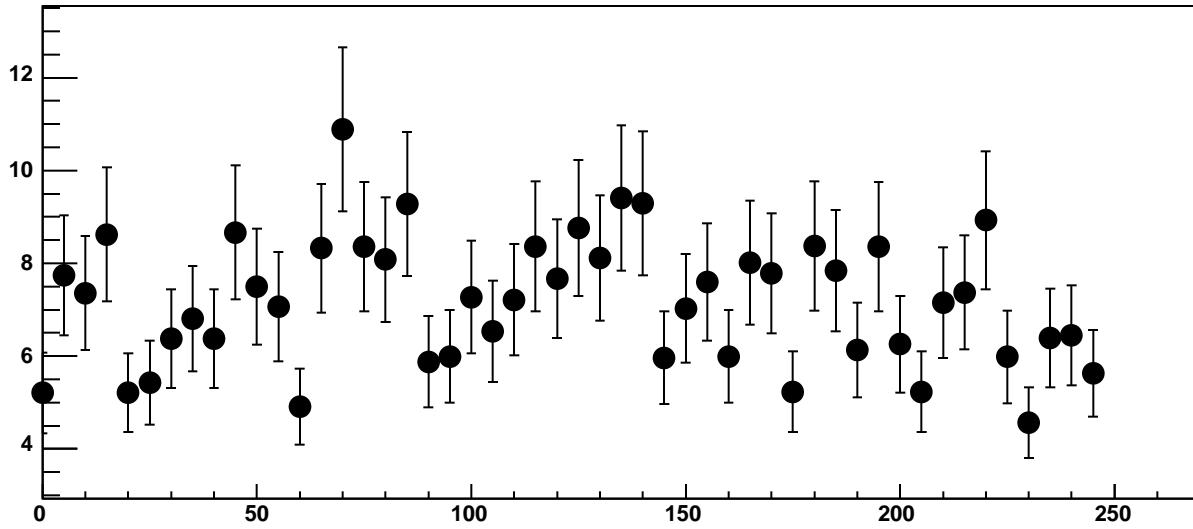


Chip 1, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold

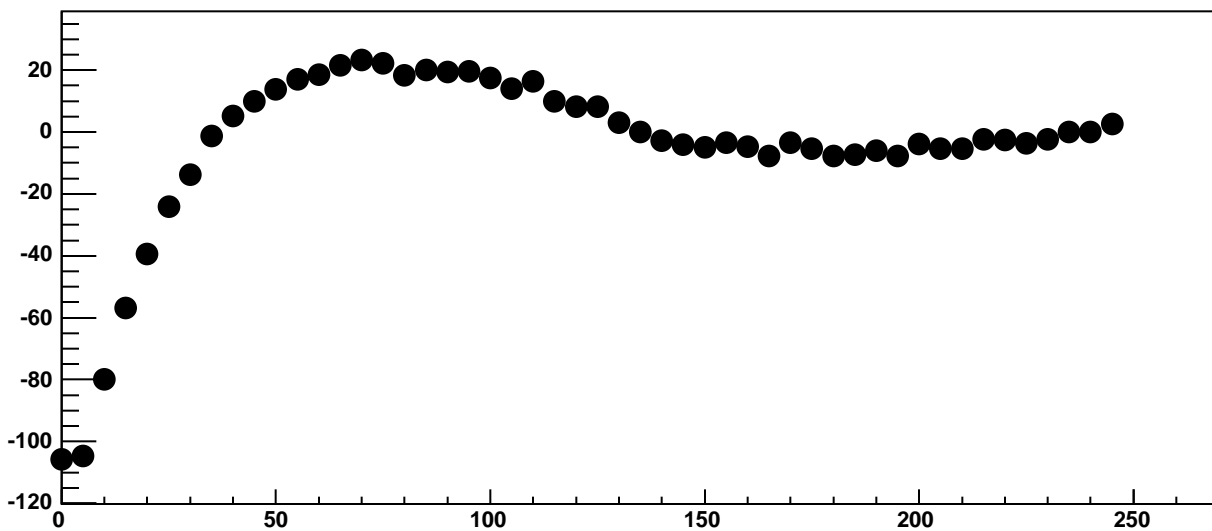


χ^2 / ndf	4421 / 41
p0	-424.8 ± 0.4644
p1	230.1 ± 0.001647
p2	1.116 ± 1.348
p3	1.234 ± 0.1941
p4	-0.09806 ± 0.00347

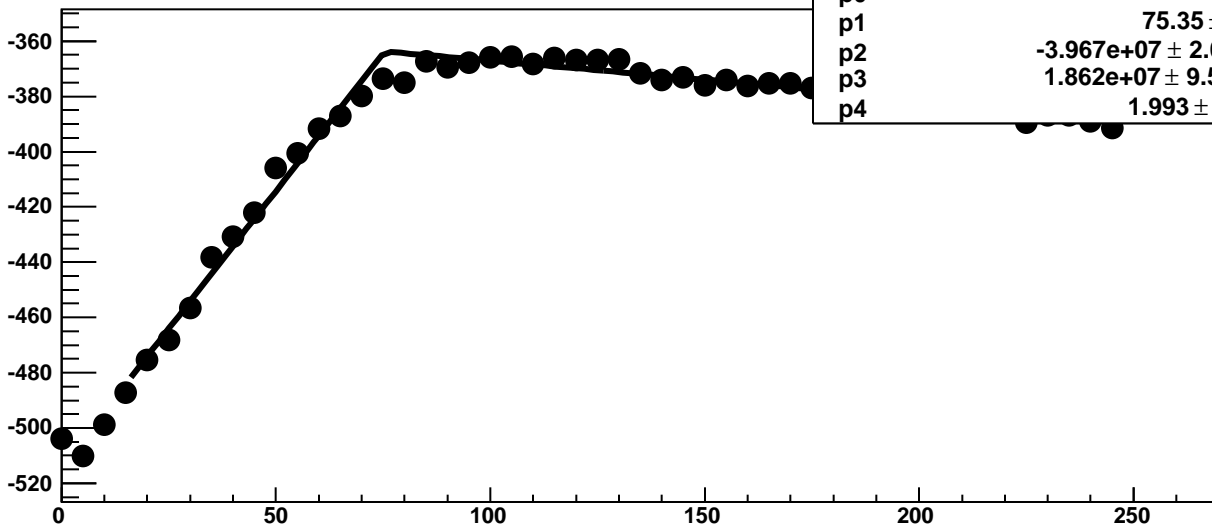
Chip 1, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold

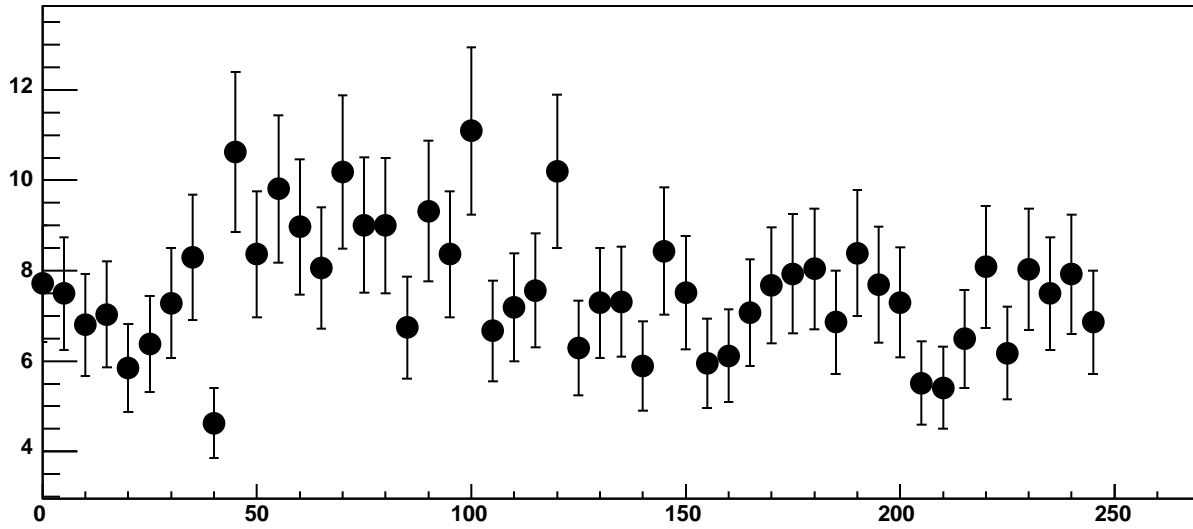


Chip 1, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

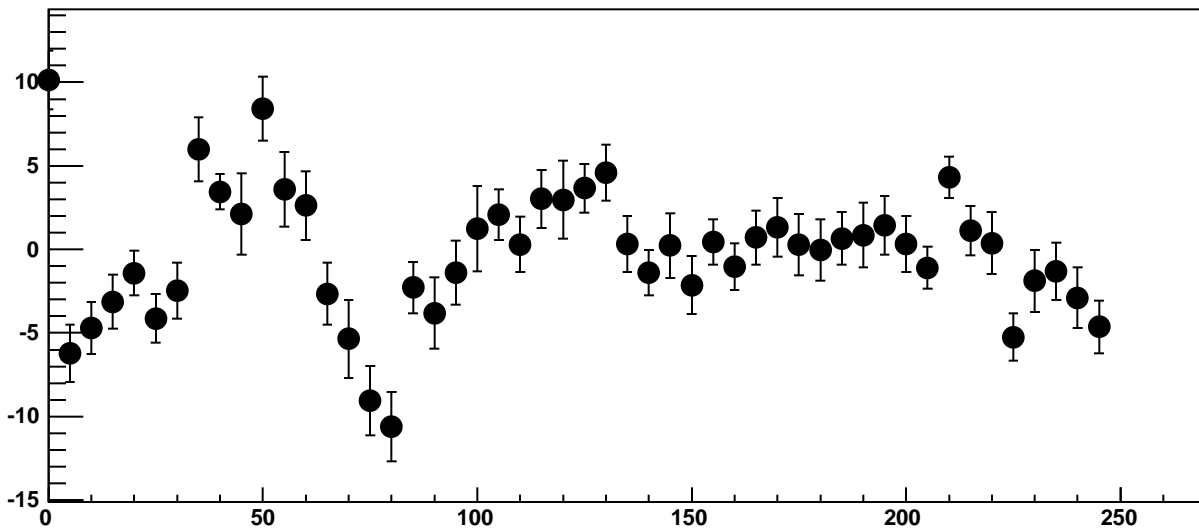


χ^2 / ndf	175.8 / 41
p0	-363.7 ± 0.591
p1	75.35 ± 0.5777
p2	$-3.967\text{e}+07 \pm 2.076\text{e}+06$
p3	$1.862\text{e}+07 \pm 9.594\text{e}+05$
p4	1.993 ± 0.02664

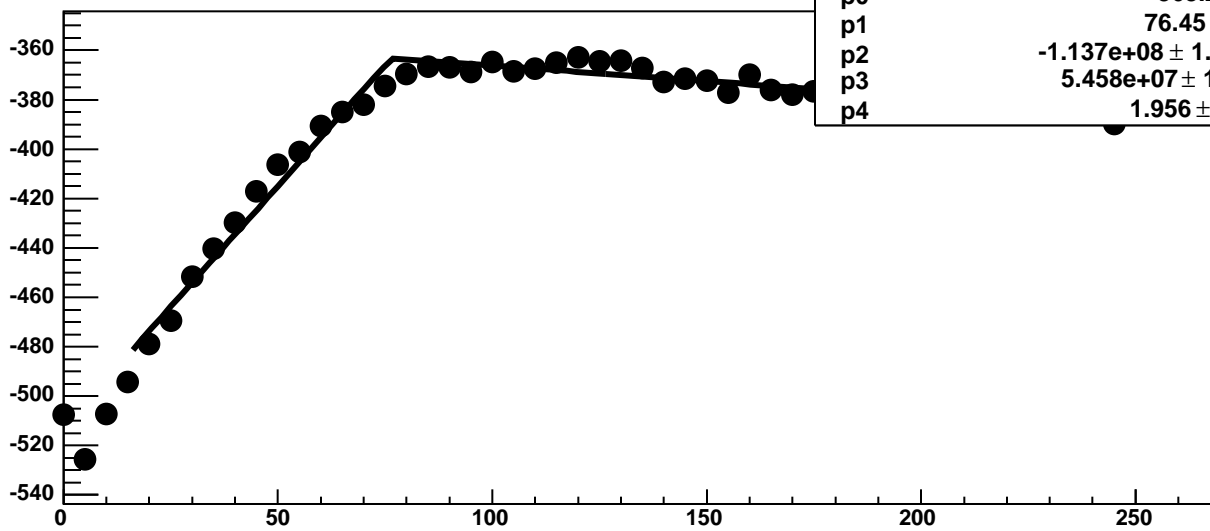
Chip 1, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

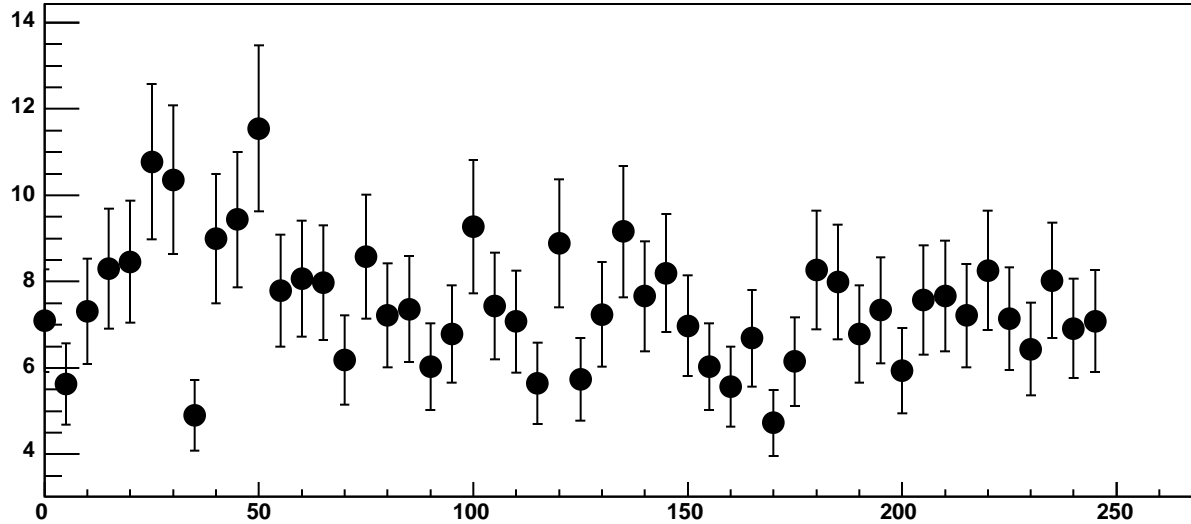


Chip 1, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

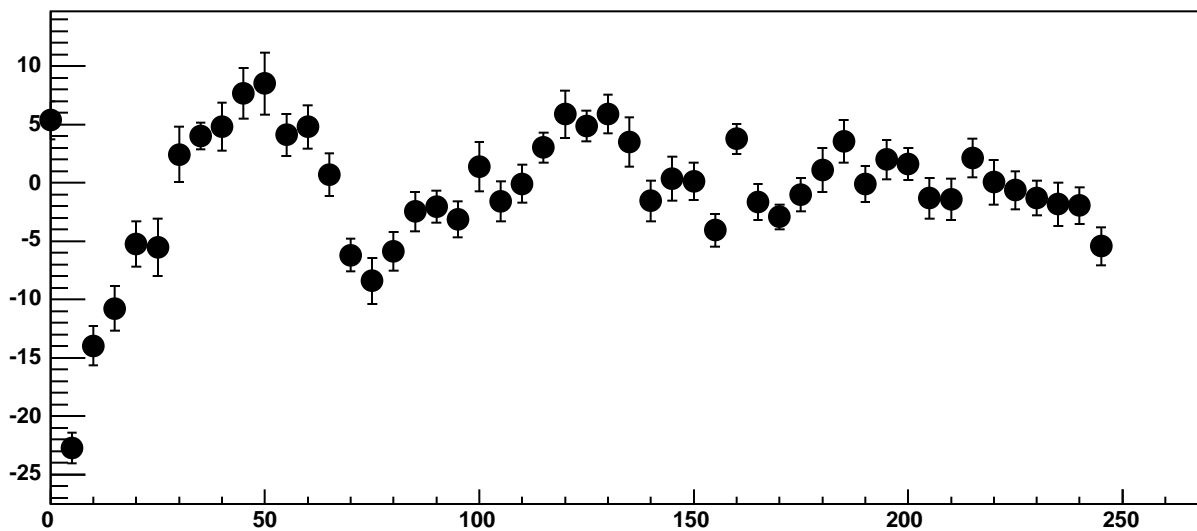


χ^2 / ndf	241.2 / 41
p0	-363.2 ± 0.538
p1	76.45 ± 0.4189
p2	$-1.137\text{e}+08 \pm 1.546\text{e}+06$
p3	$5.458\text{e}+07 \pm 1.03\text{e}+06$
p4	1.956 ± 0.01845

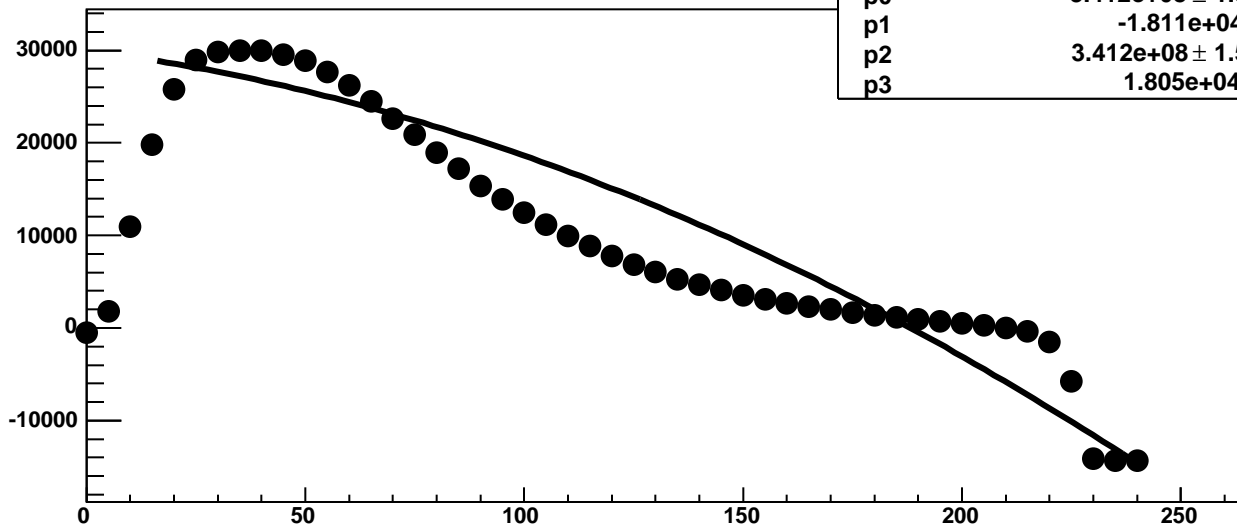
Chip 1, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



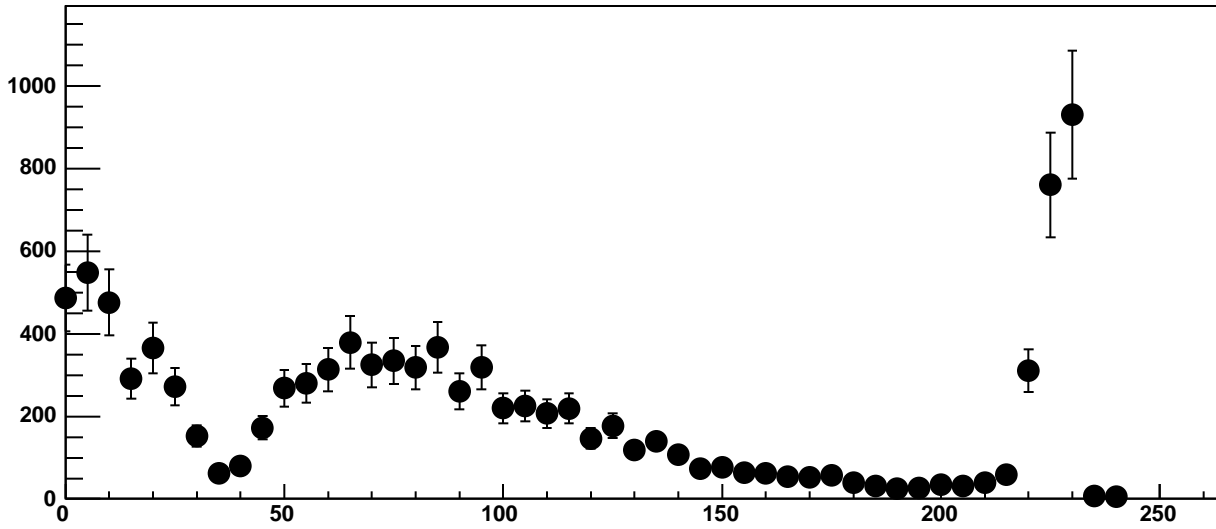
Chip 1, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold



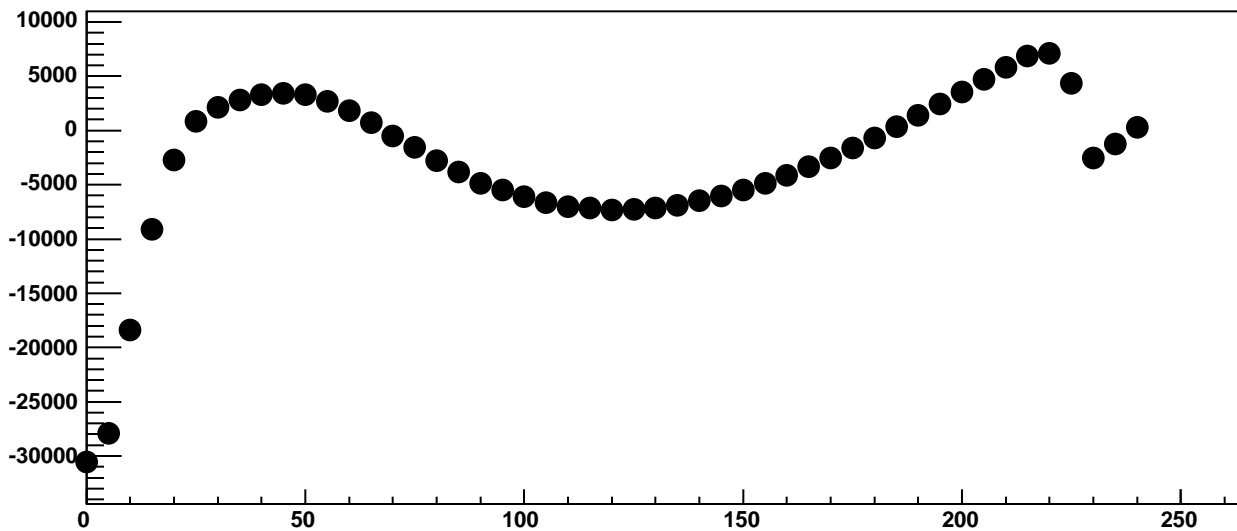
Chip 1, Channel 6, Enable 5!, DAC=4000, ADC Mean vs Hold



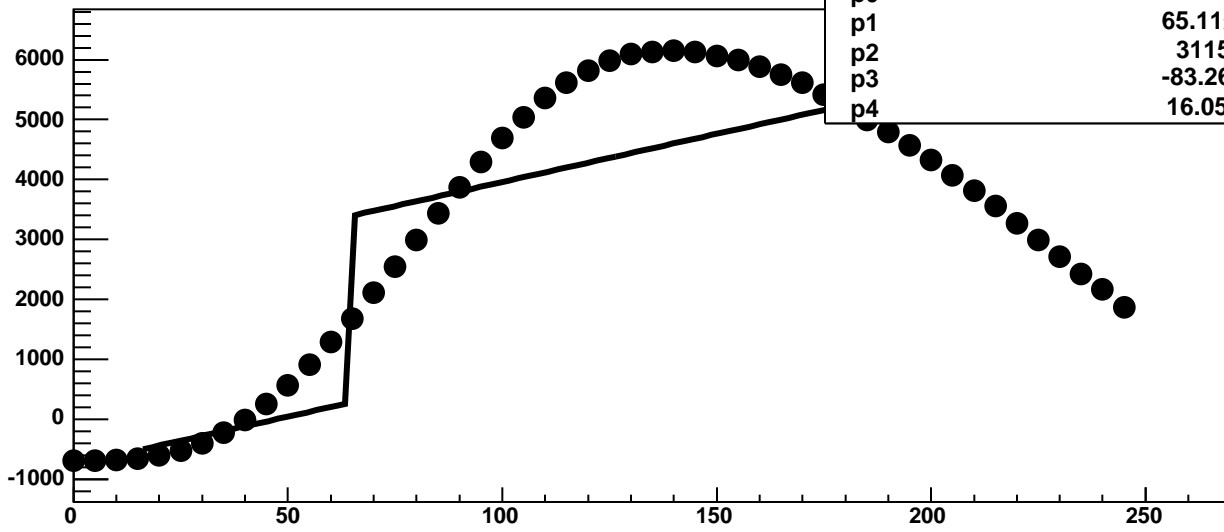
Chip 1, Channel 6, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 6, Enable 5!, DAC=4000, ADC Residuals vs Hold

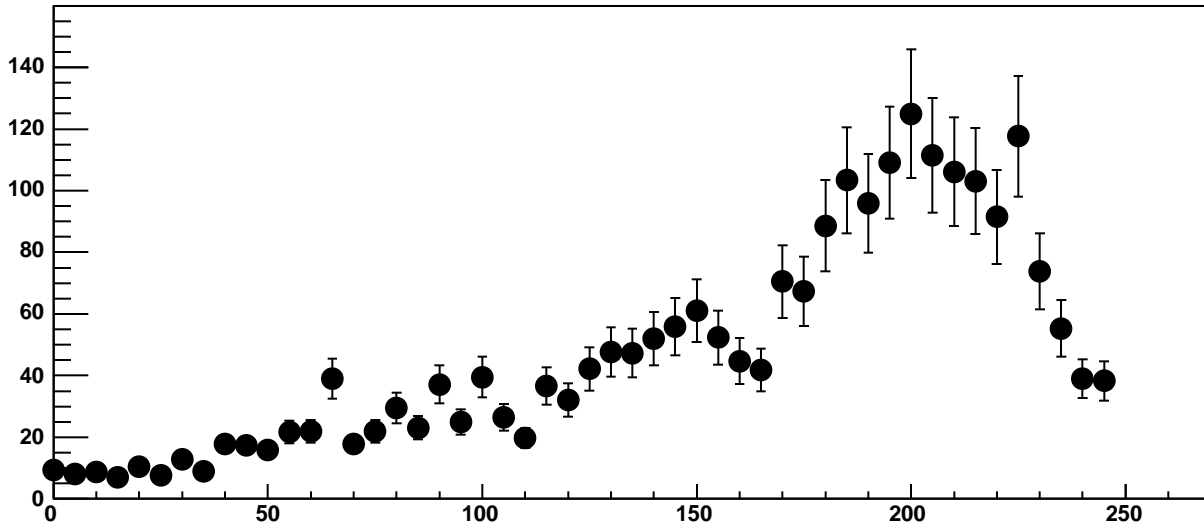


Chip 1, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

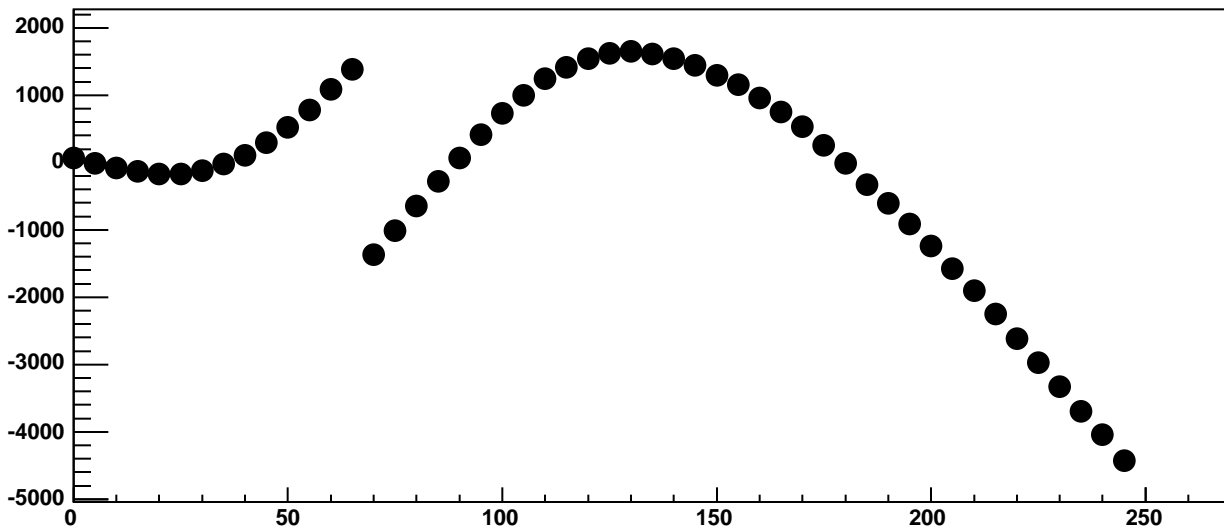


χ^2 / ndf	1.023e+06 / 41
p0	284.3 ± 2.099
p1	65.11 ± 0.4201
p2	3115 ± 9.019
p3	-83.26 ± 3.671
p4	16.05 ± 0.147

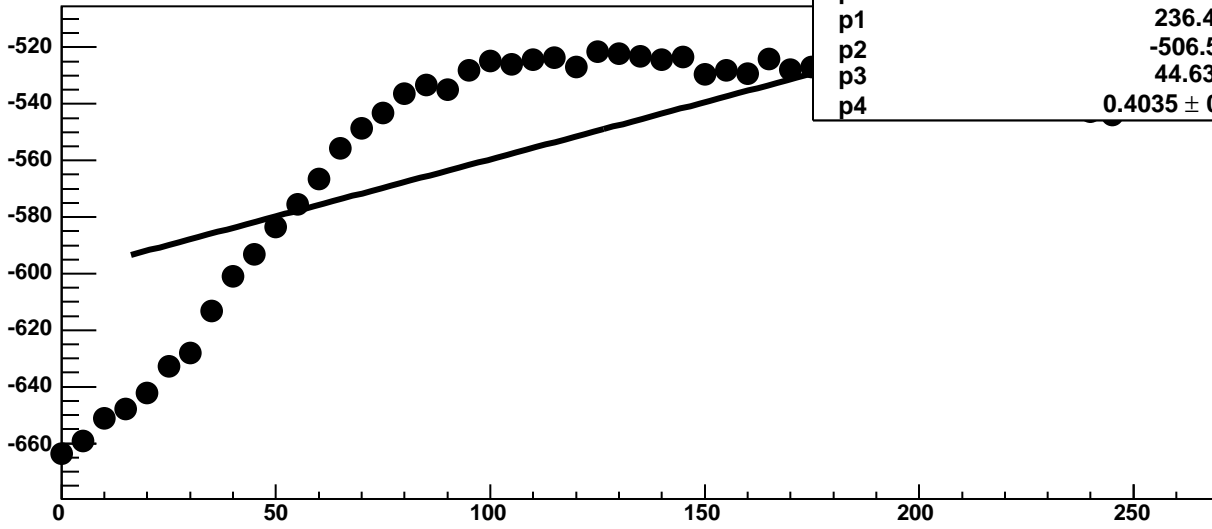
Chip 1, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

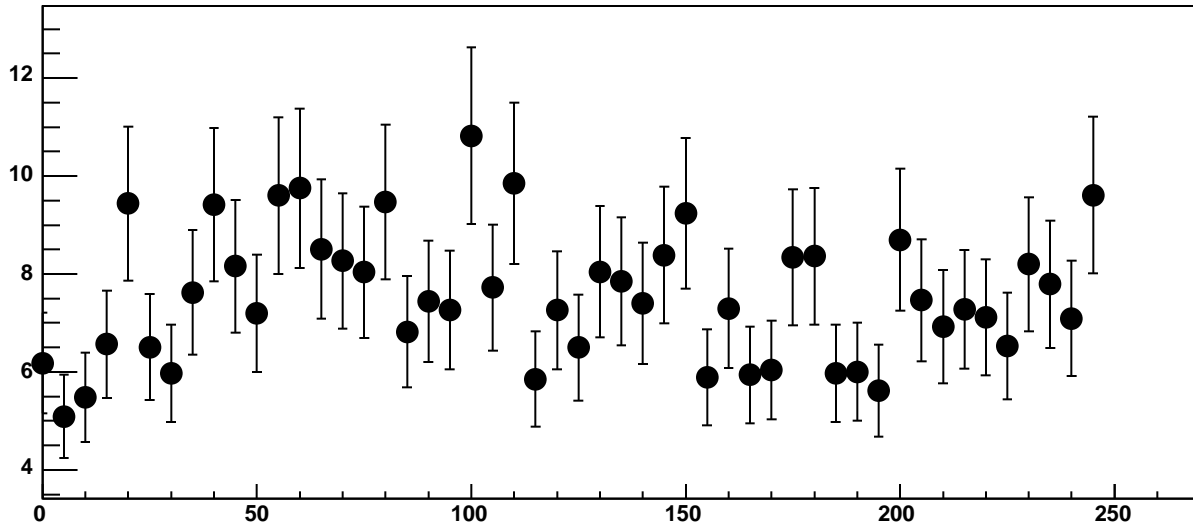


Chip 1, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold

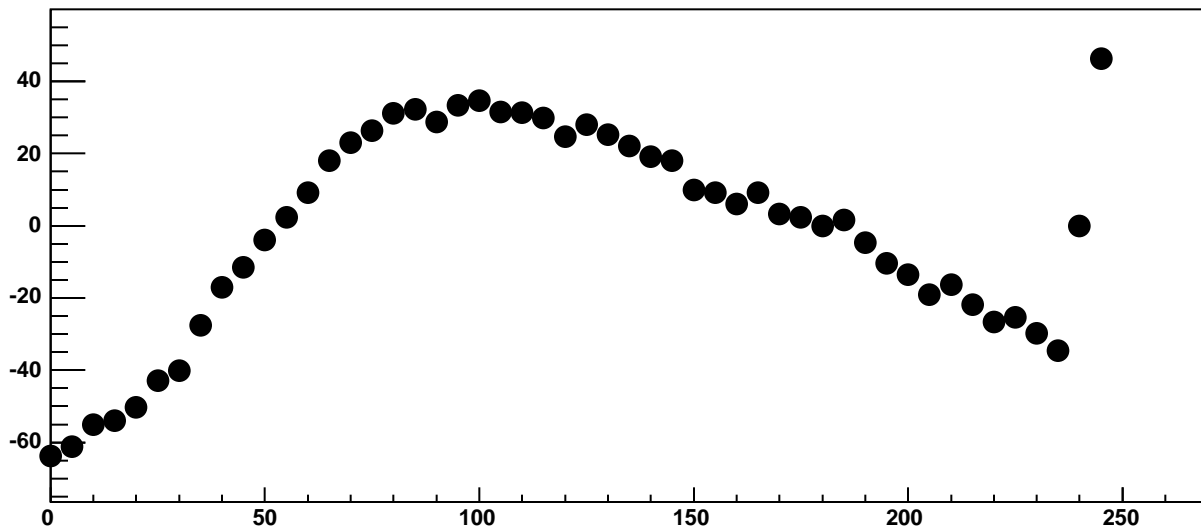


χ^2 / ndf	9791 / 41
p0	-504.6 ± 0.9268
p1	236.4 ± 1.976
p2	-506.5 ± 257.1
p3	44.63 ± 19.46
p4	0.4035 ± 0.003861

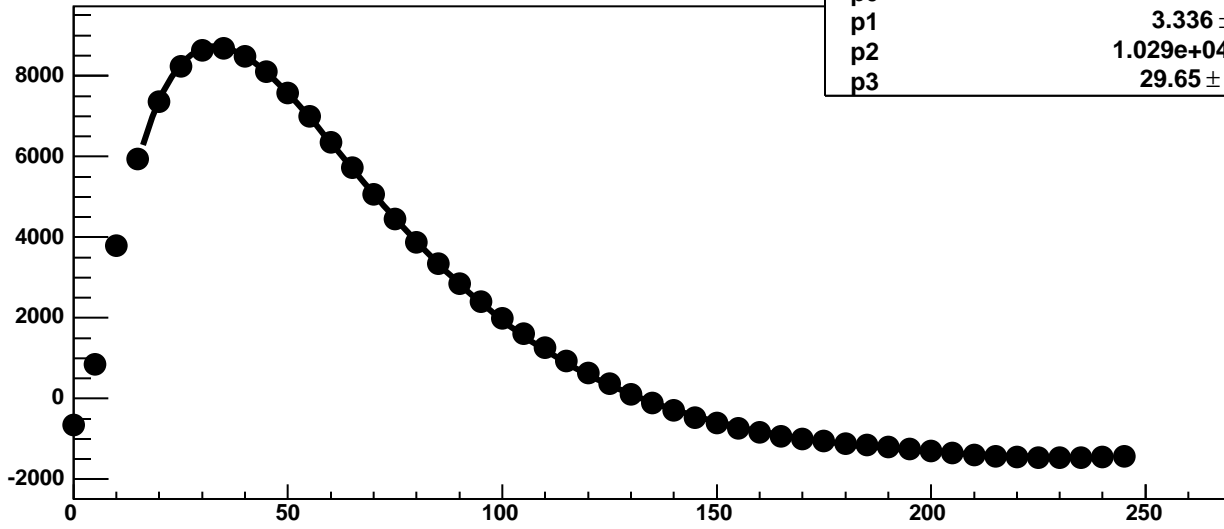
Chip 1, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

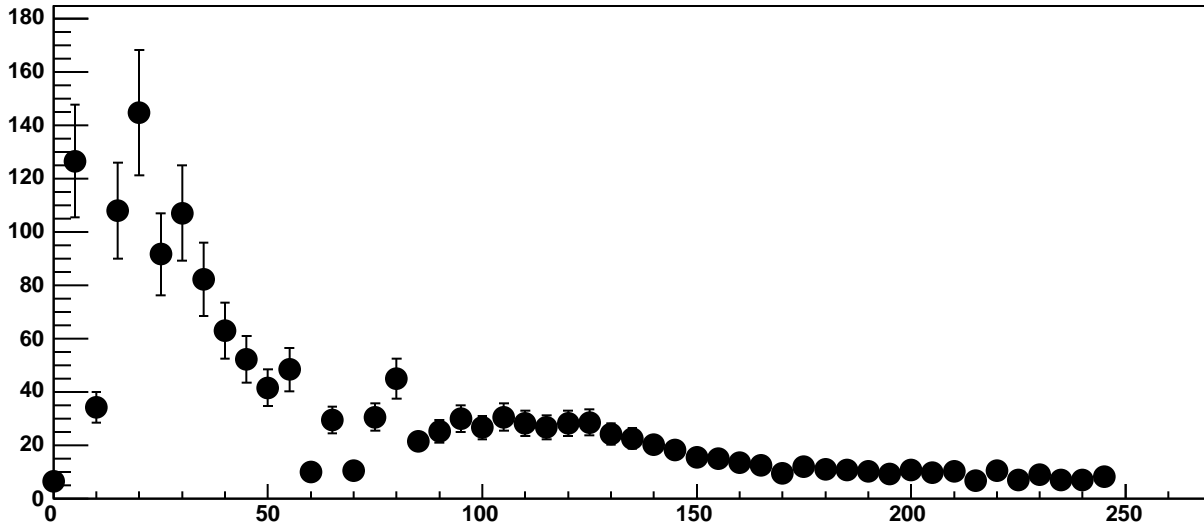


Chip 1, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold

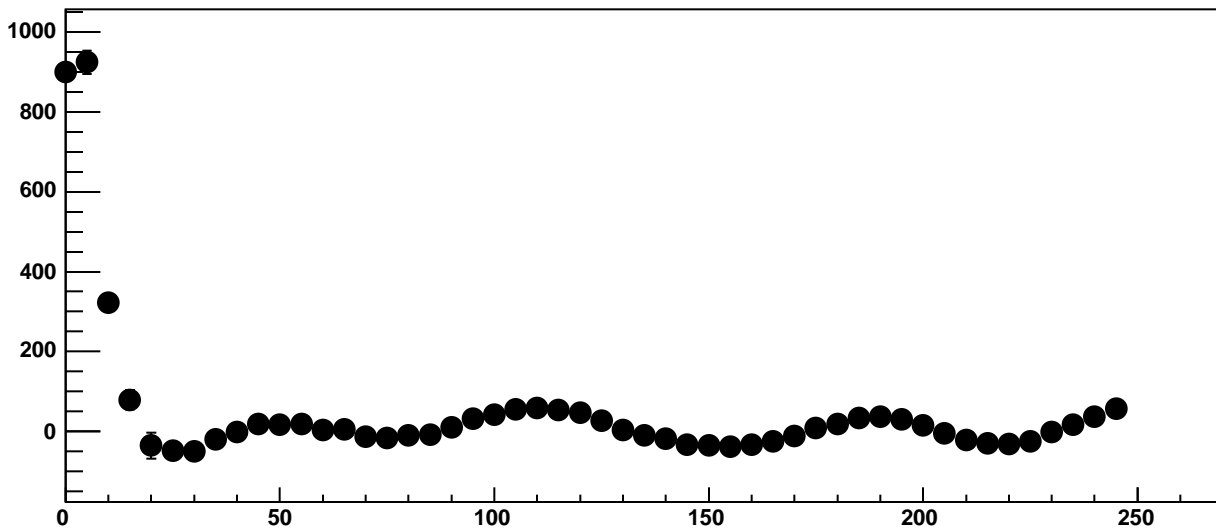


χ^2 / ndf	3069 / 42
p0	-1561 ± 0.7797
p1	3.336 ± 0.0436
p2	1.029e+04 ± 5.261
p3	29.65 ± 0.01572

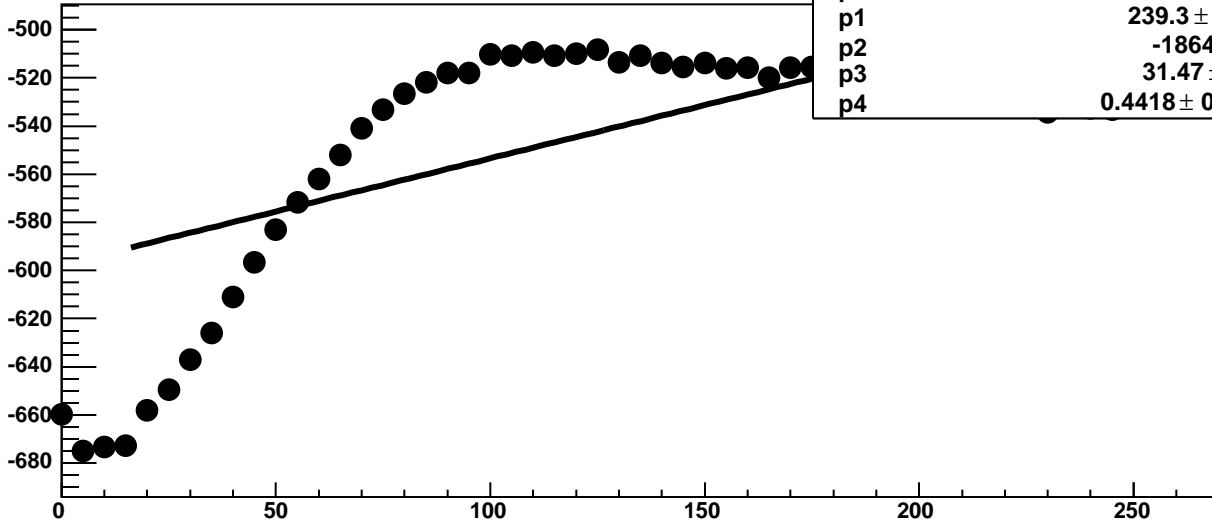
Chip 1, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold

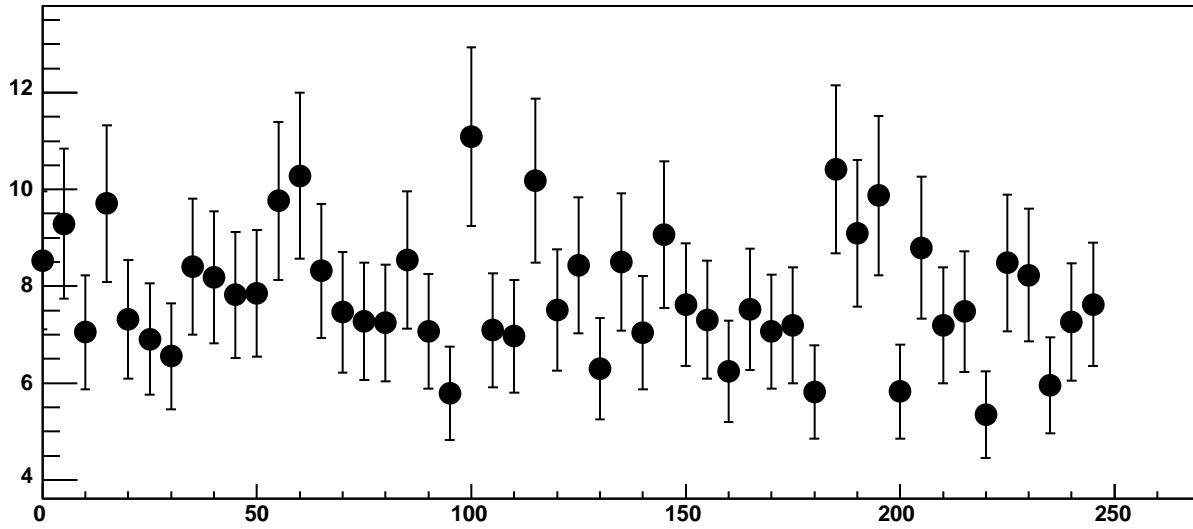


Chip 1, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

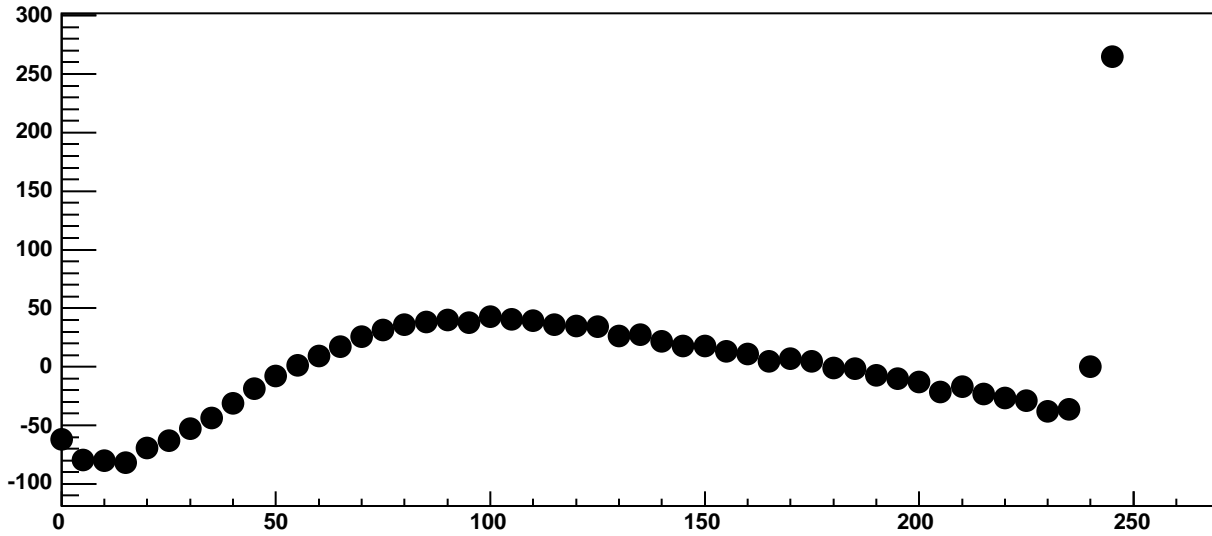


χ^2 / ndf	1.558e+04 / 41
p0	-491.9 ± 0.4945
p1	239.3 ± 0.02568
p2	-1864 ± 18.75
p3	31.47 ± 0.1088
p4	0.4418 ± 0.003913

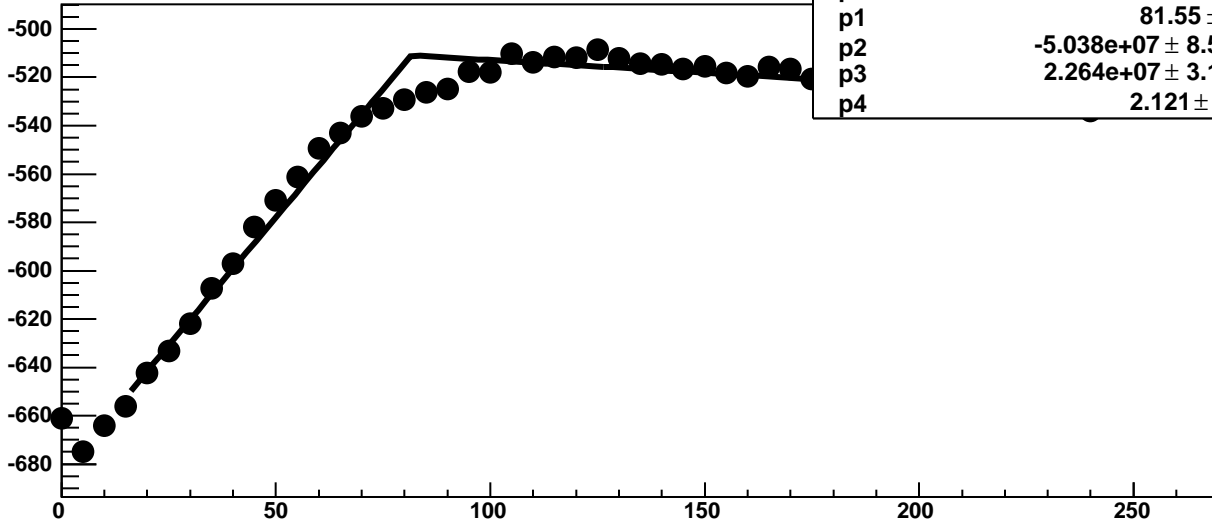
Chip 1, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

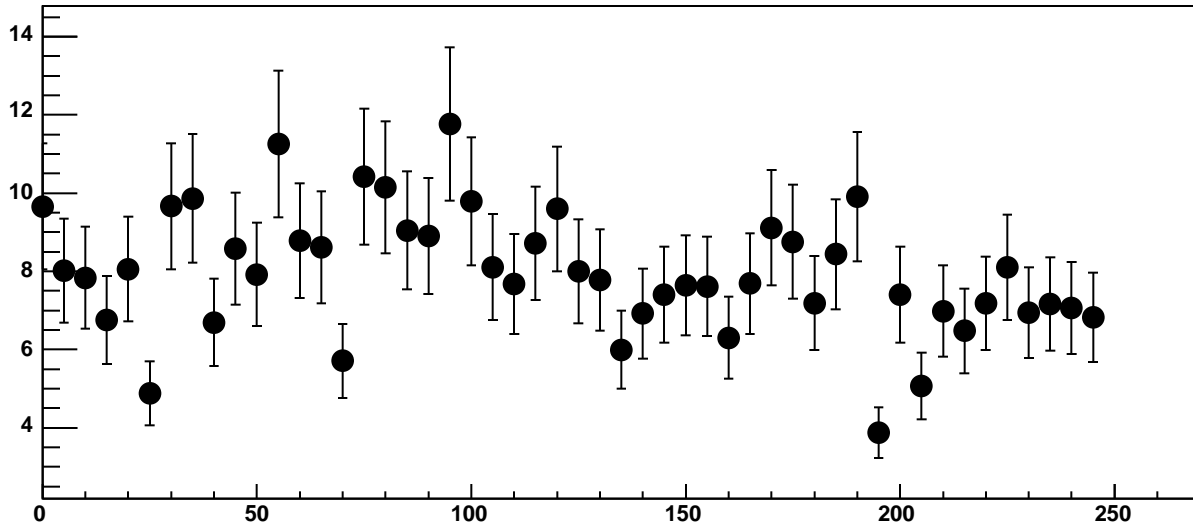


Chip 1, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

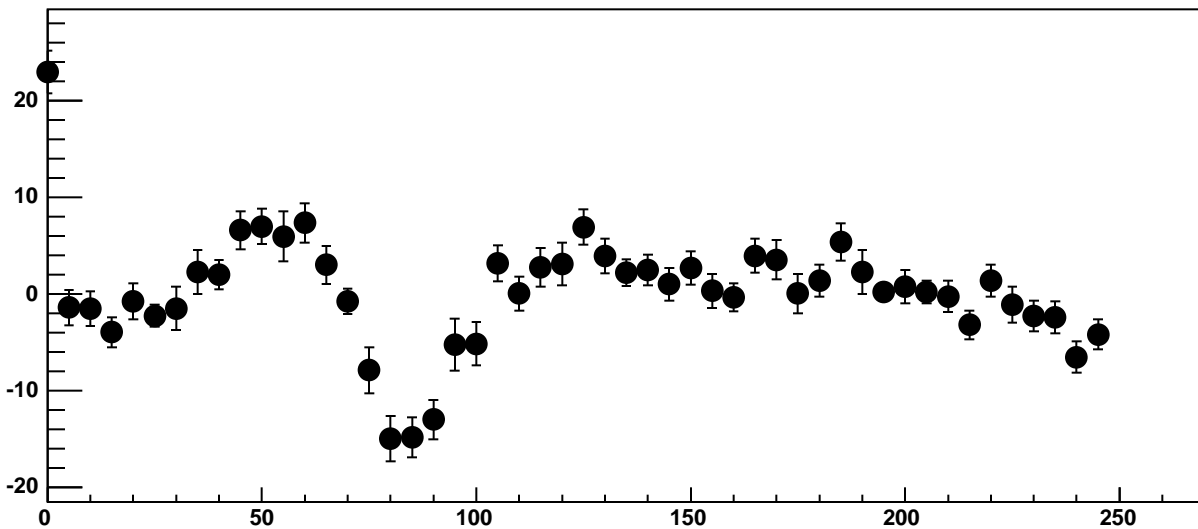


χ^2 / ndf	291.6 / 41
p0	-511 ± 0.6491
p1	81.55 ± 0.5396
p2	$-5.038e+07 \pm 8.528e+05$
p3	$2.264e+07 \pm 3.199e+05$
p4	2.121 ± 0.02307

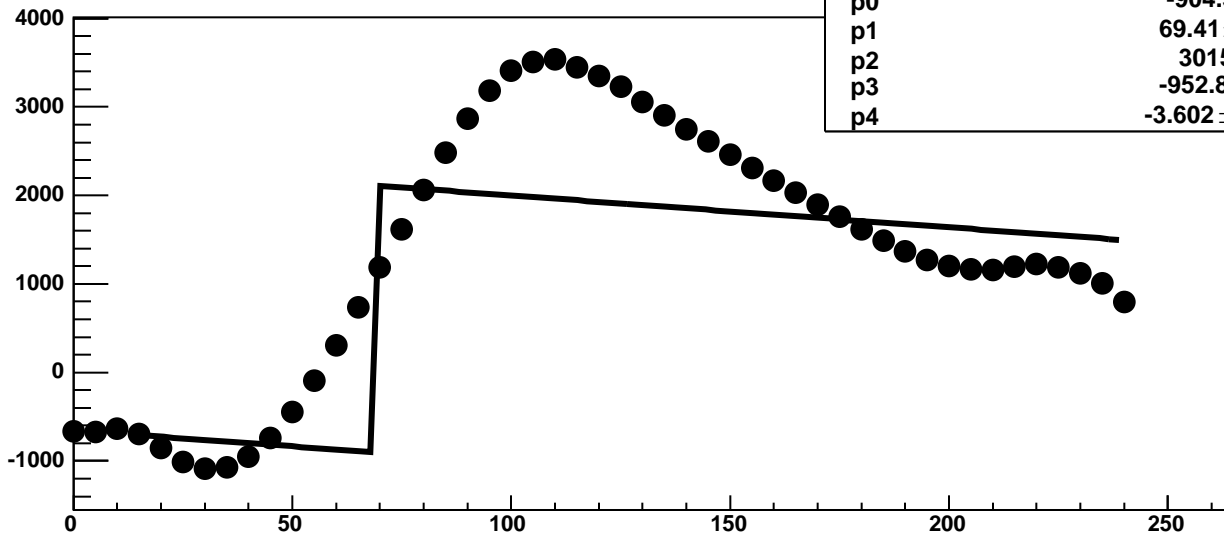
Chip 1, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

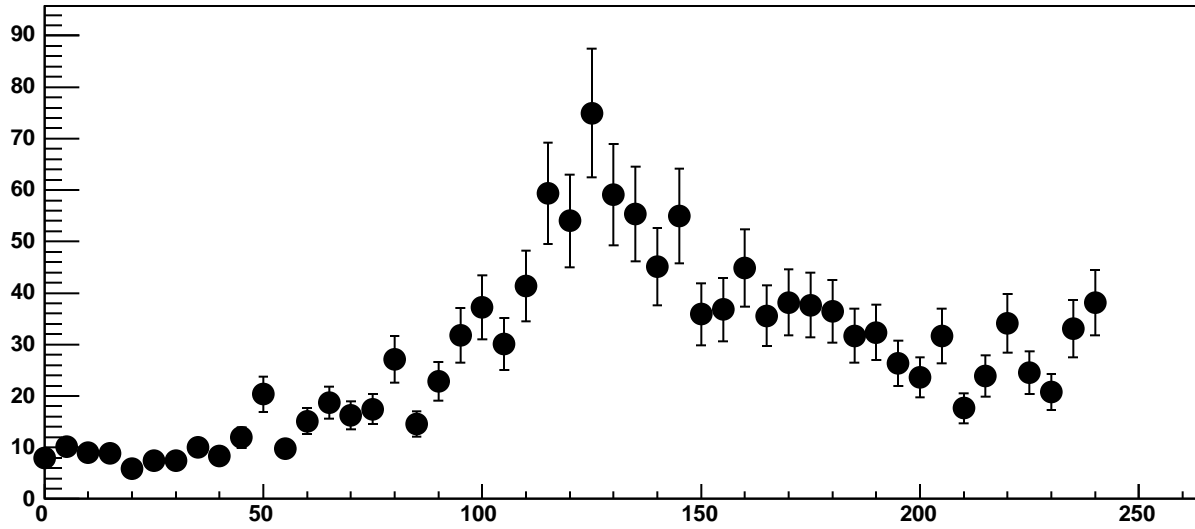


Chip 1, Channel 7, Enable 5, DAC=4000, ADC Mean vs Hold

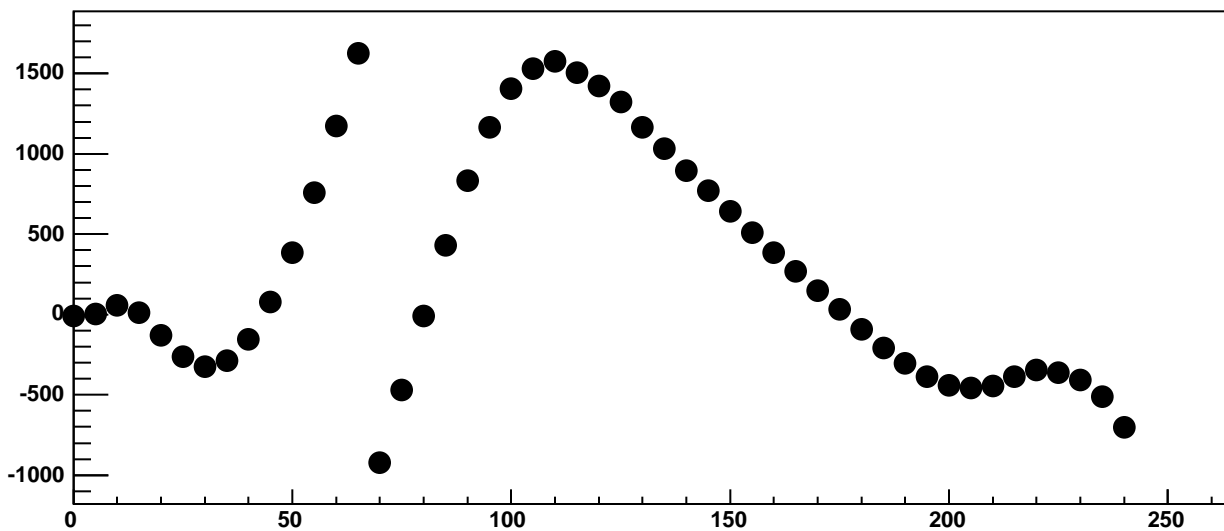


χ^2 / ndf	8.47e+05 / 41
p0	-904.5 ± 4.74
p1	69.41 ± 0.1901
p2	3015 ± 8.311
p3	-952.8 ± 14.92
p4	-3.602 ± 0.0985

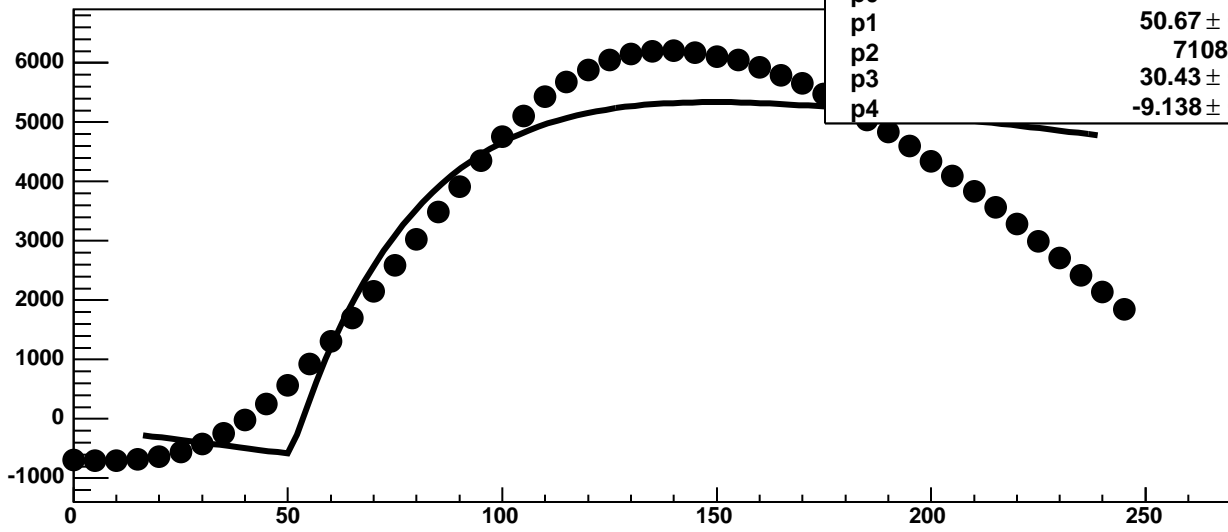
Chip 1, Channel 7, Enable 5, DAC=4000, ADC Noise vs Hold



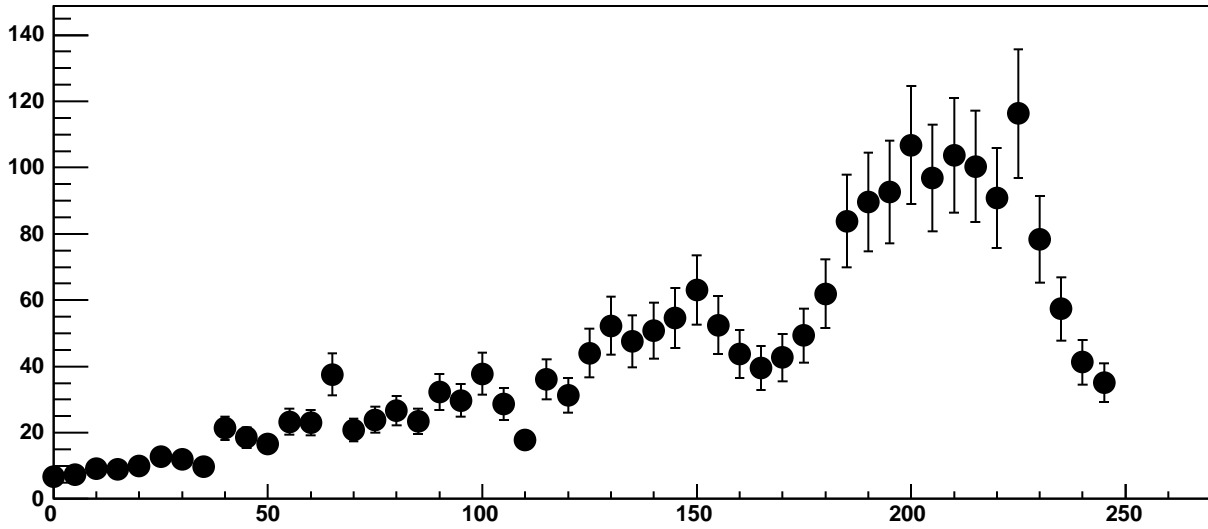
Chip 1, Channel 7, Enable 5, DAC=4000, ADC Residuals vs Hold



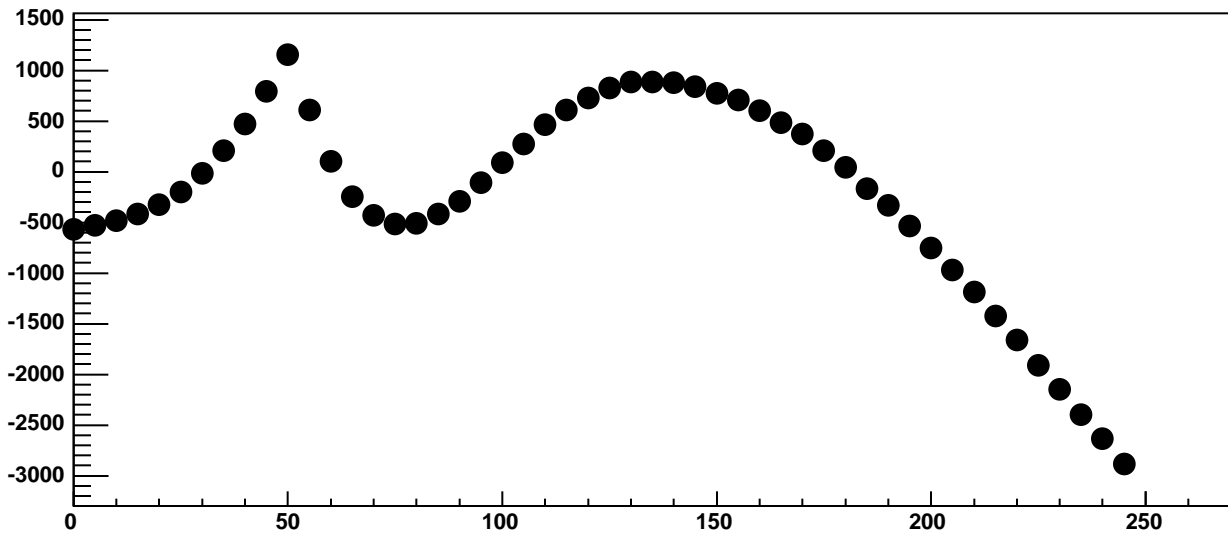
Chip 1, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold



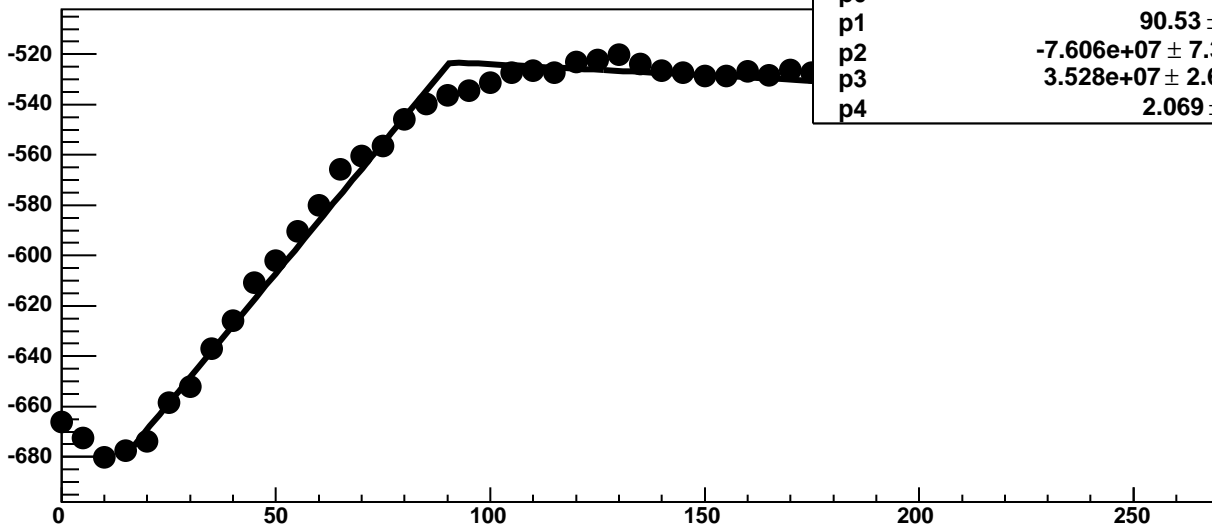
Chip 1, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

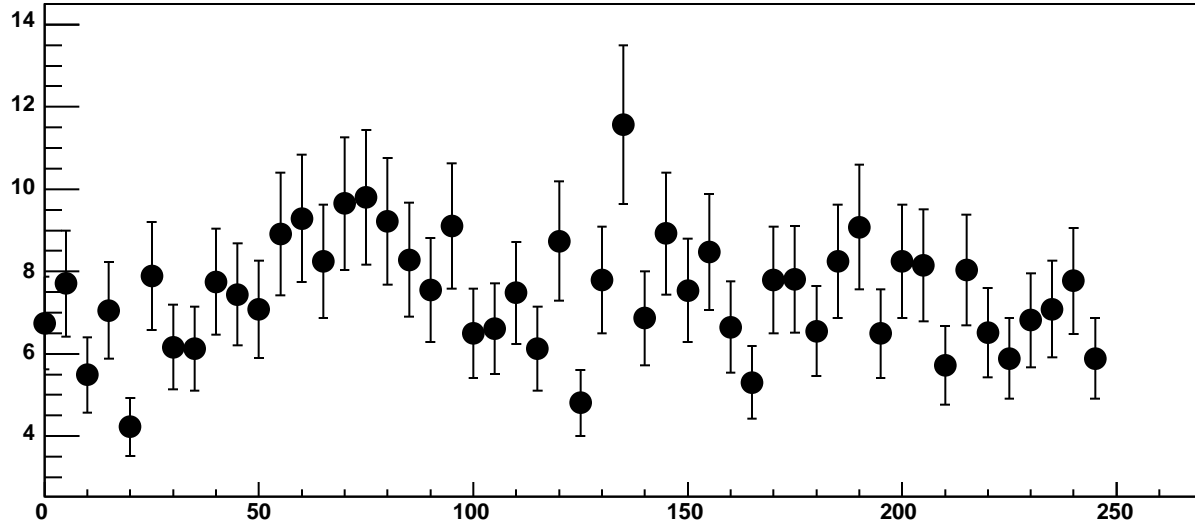


Chip 1, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

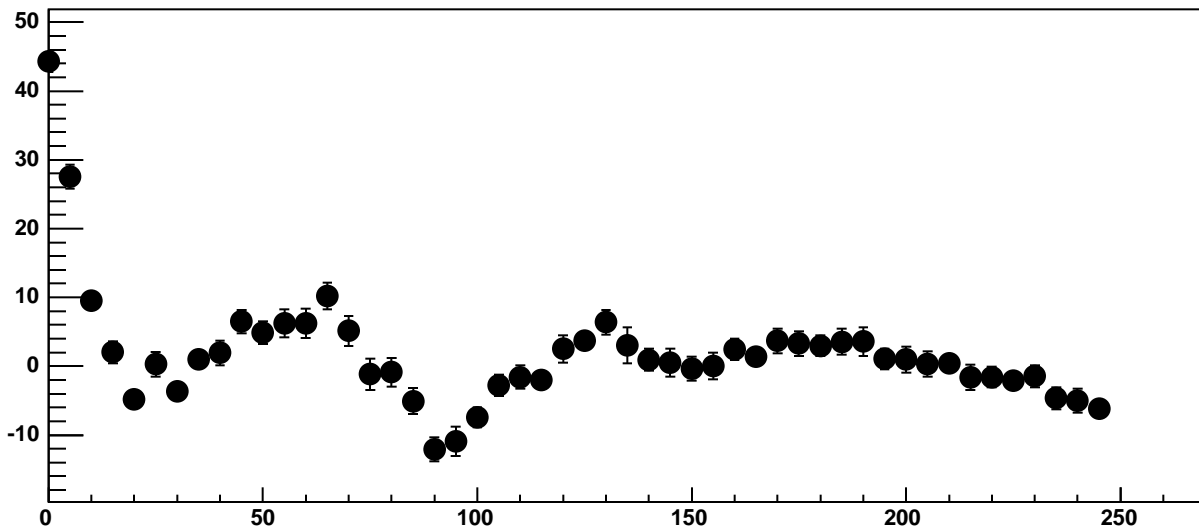


χ^2 / ndf	296.6 / 41
p0	-523.2 ± 0.6021
p1	90.53 ± 0.5065
p2	$-7.606\text{e}+07 \pm 7.311\text{e}+05$
p3	$3.528\text{e}+07 \pm 2.667\text{e}+05$
p4	2.069 ± 0.0177

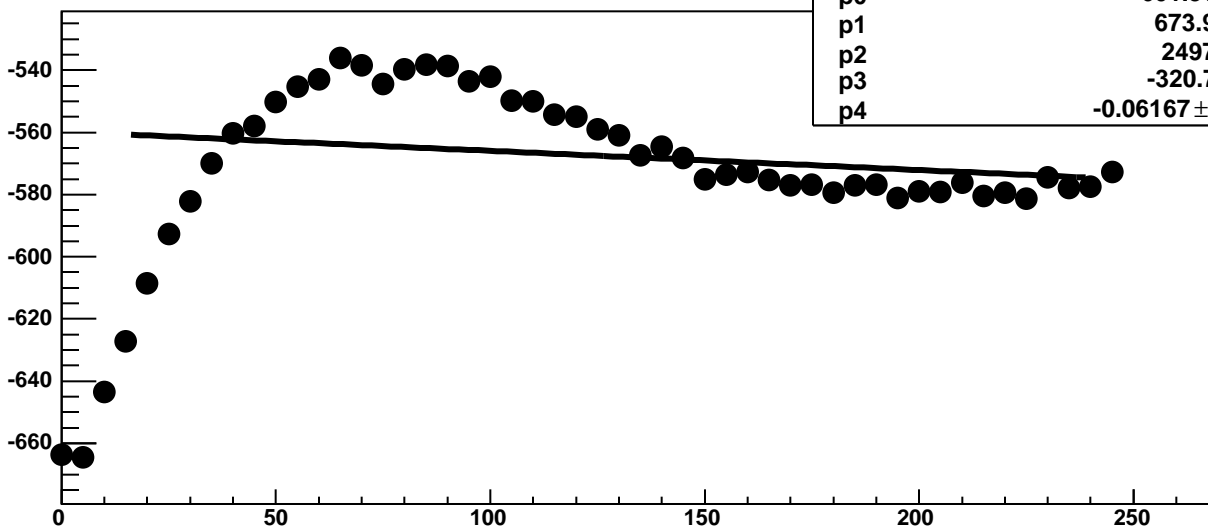
Chip 1, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

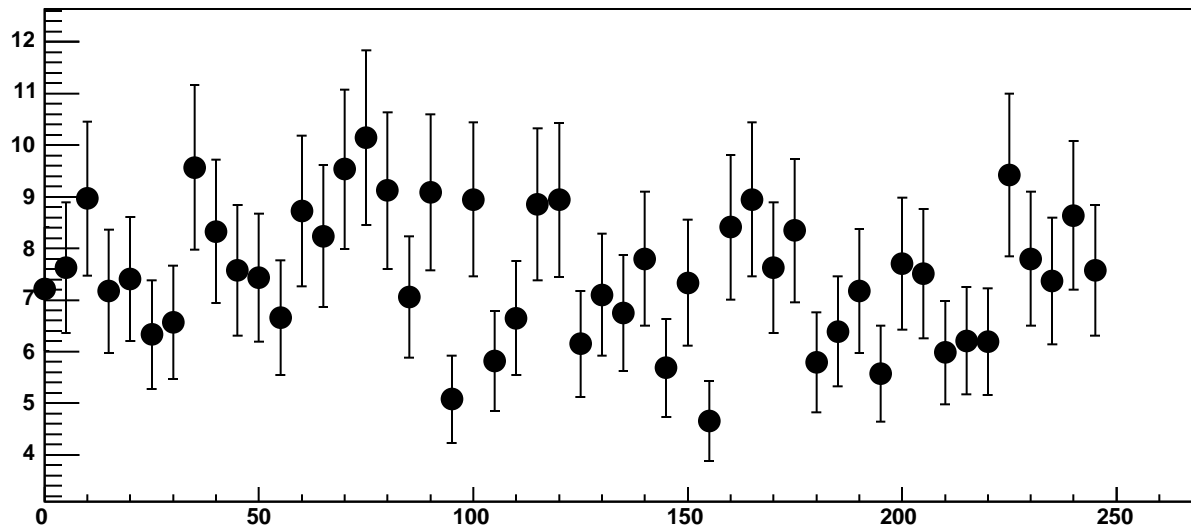


Chip 1, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold

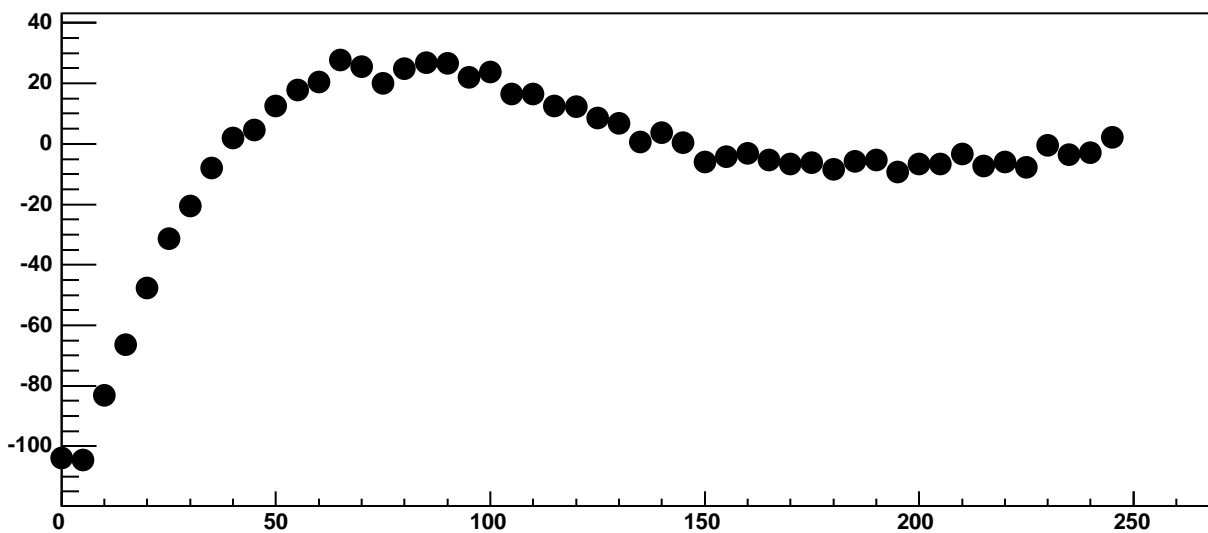


χ^2 / ndf	5611 / 41
p0	-601.3 ± 0.2404
p1	673.9 ± 3.898
p2	2497 ± 84.85
p3	-320.7 ± 1.414
p4	-0.06167 ± 0.00044

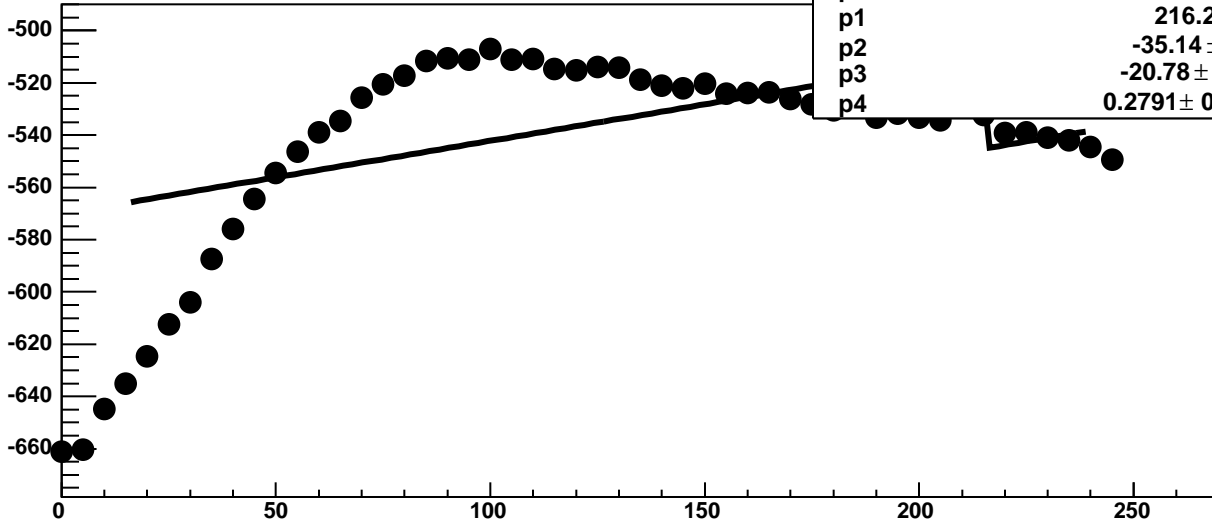
Chip 1, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold

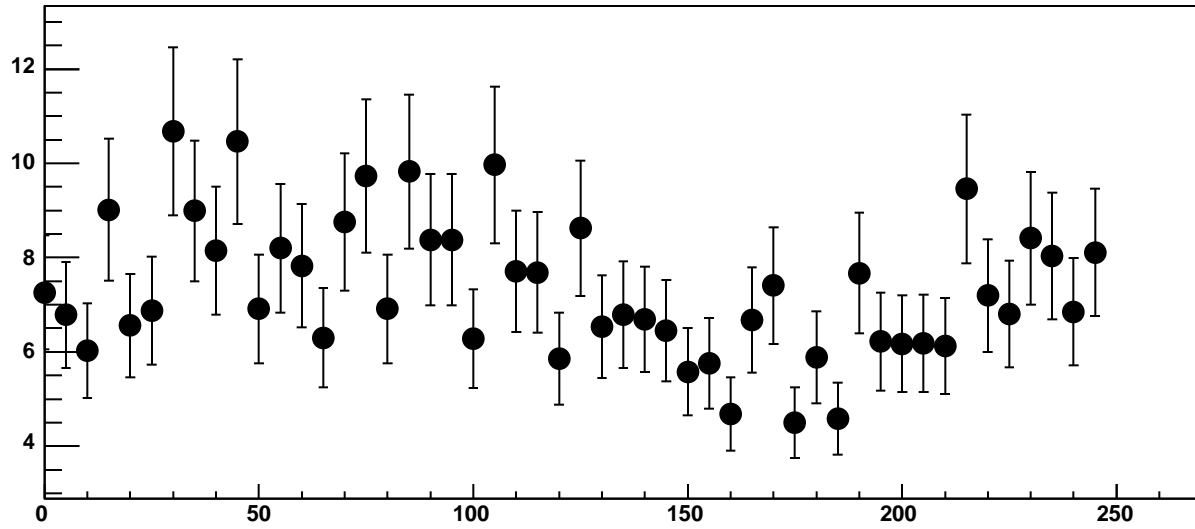


Chip 1, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

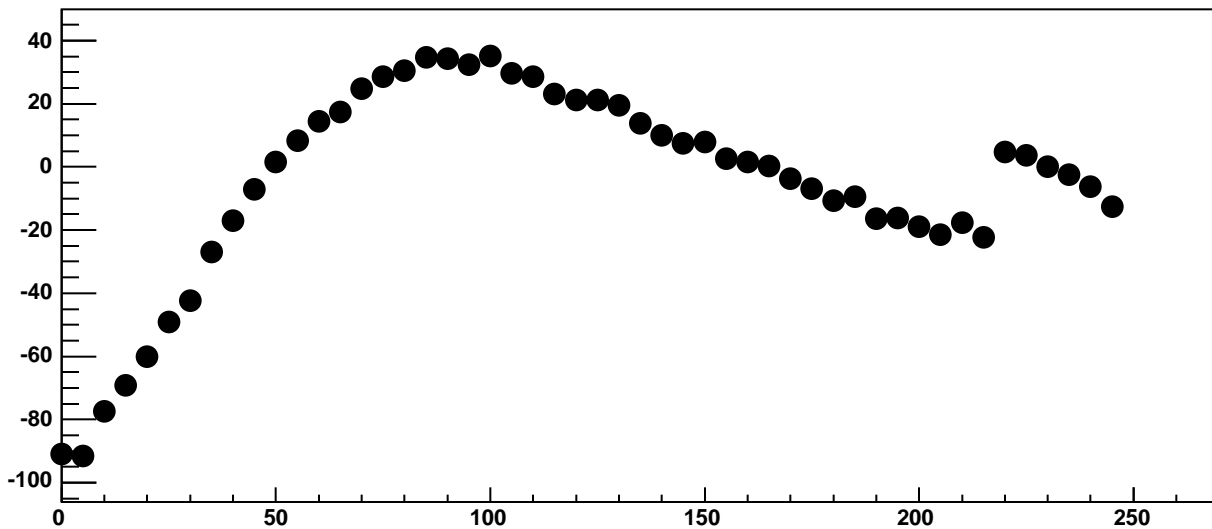


χ^2 / ndf	9036 / 41
p0	-509.8 ± 0.4168
p1	216.2 ± 1.495
p2	-35.14 ± 0.8446
p3	-20.78 ± 0.02474
p4	0.2791 ± 0.004309

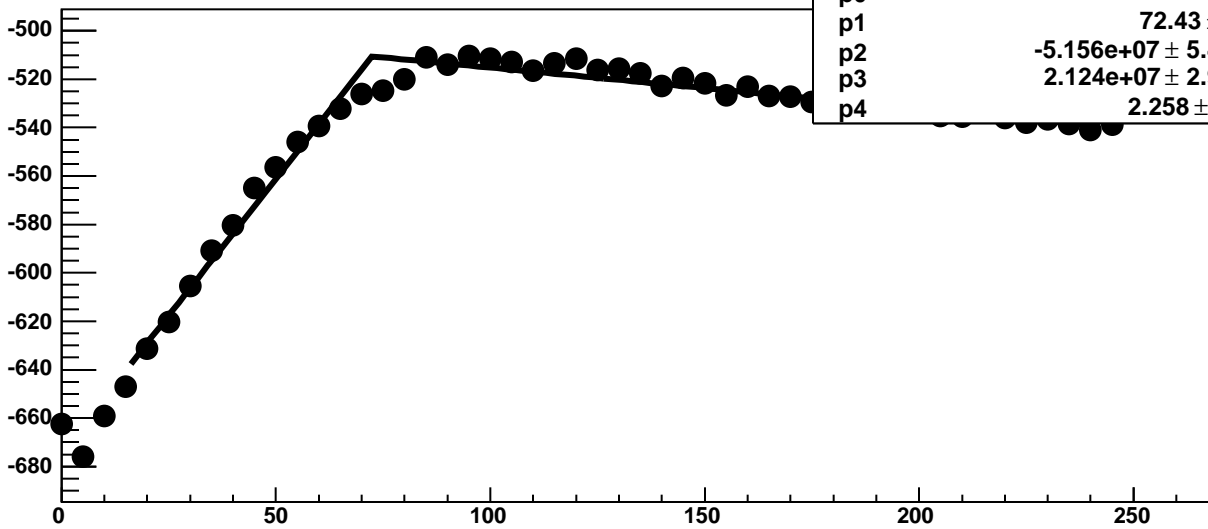
Chip 1, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

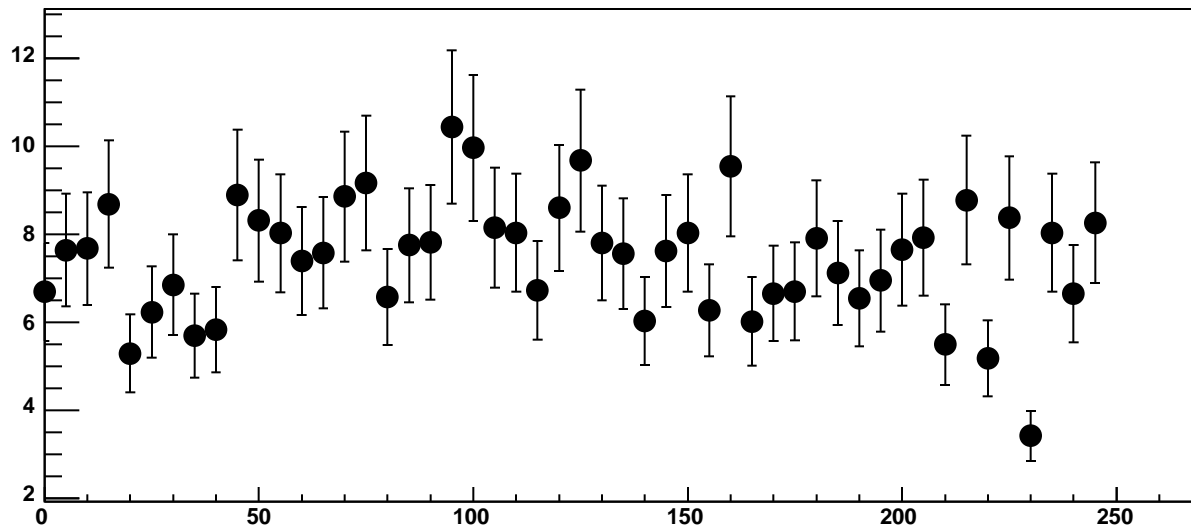


Chip 1, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

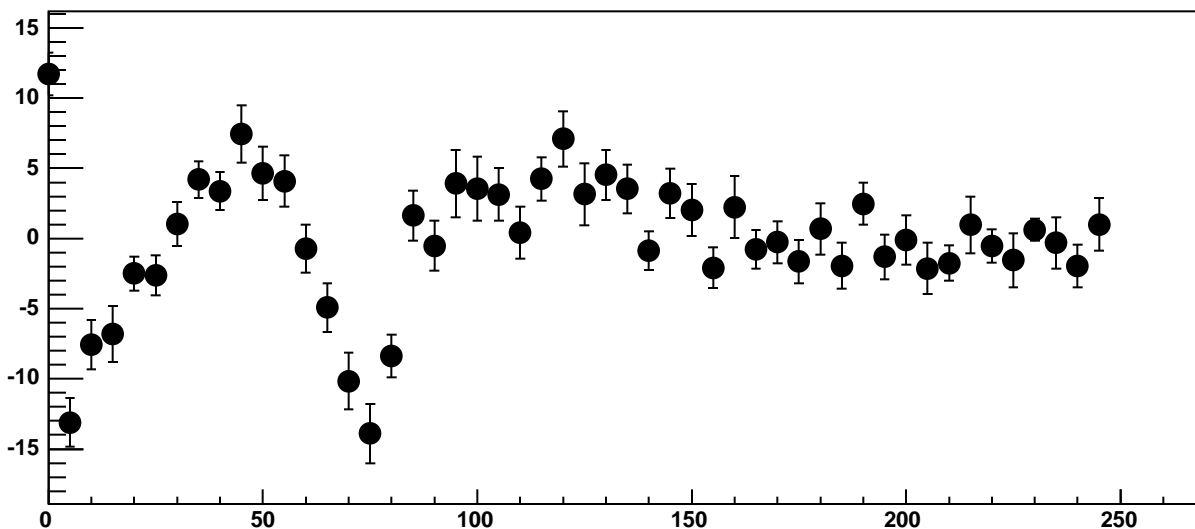


χ^2 / ndf	231.2 / 41
p0	-510.5 ± 0.555
p1	72.43 ± 0.3776
p2	$-5.156\text{e}+07 \pm 5.421\text{e}+05$
p3	$2.124\text{e}+07 \pm 2.975\text{e}+05$
p4	2.258 ± 0.01796

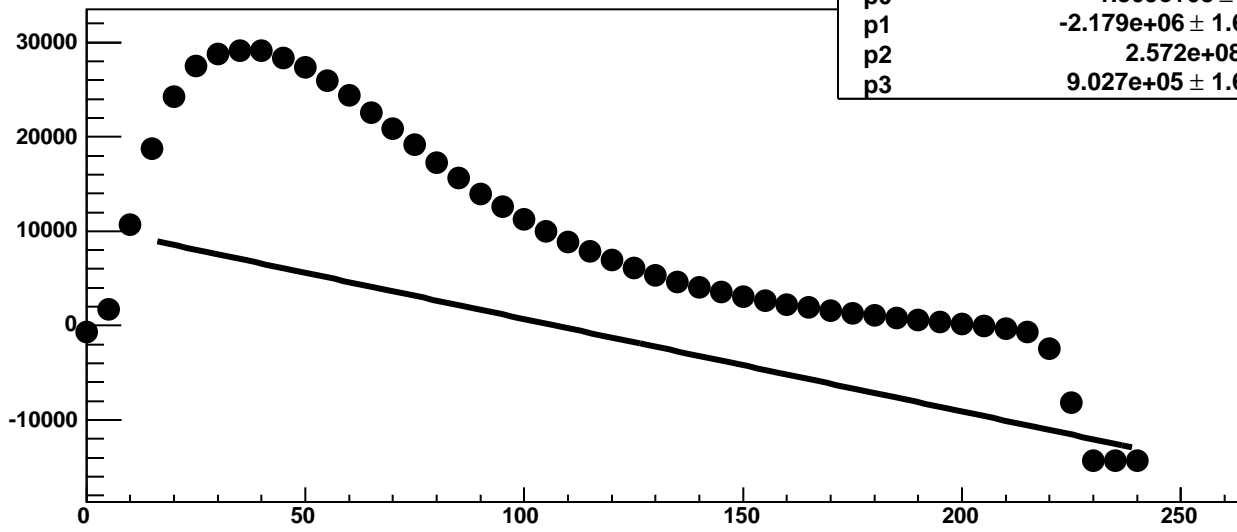
Chip 1, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

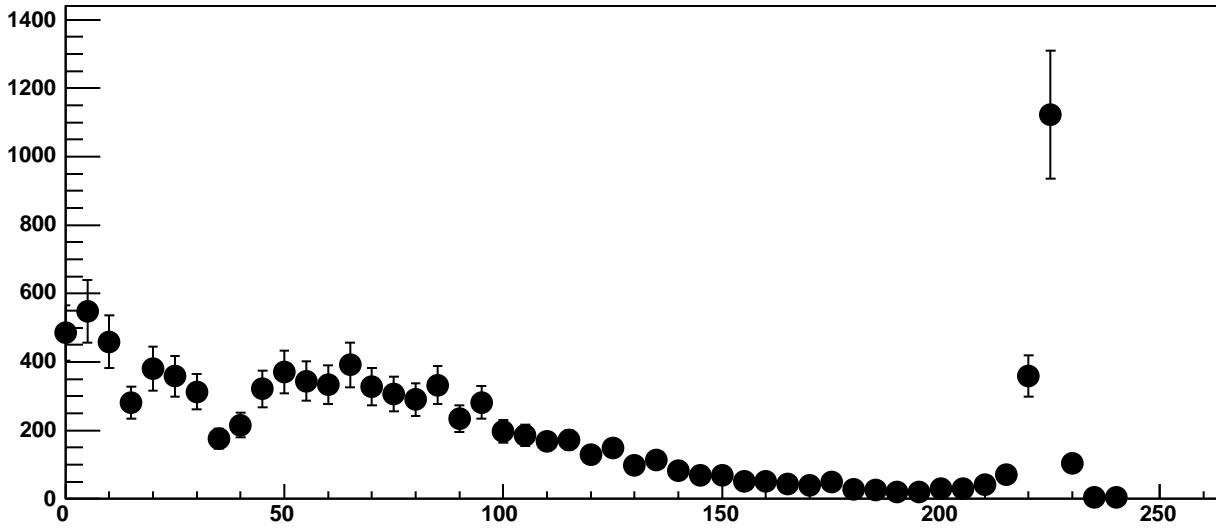


Chip 1, Channel 8, Enable 5!, DAC=4000, ADC Mean vs Hold

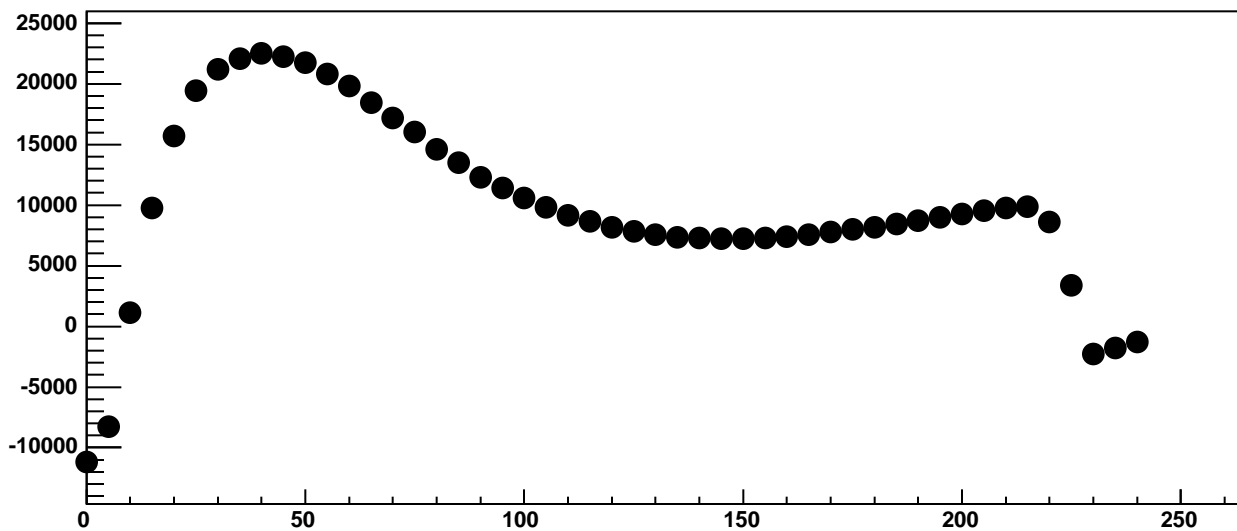


χ^2 / ndf	2.463e+07 / 42
p0	-1.509e+08 ± 1.6e+05
p1	-2.179e+06 ± 1.666e+05
p2	2.572e+08 ± 36.53
p3	9.027e+05 ± 1.666e+05

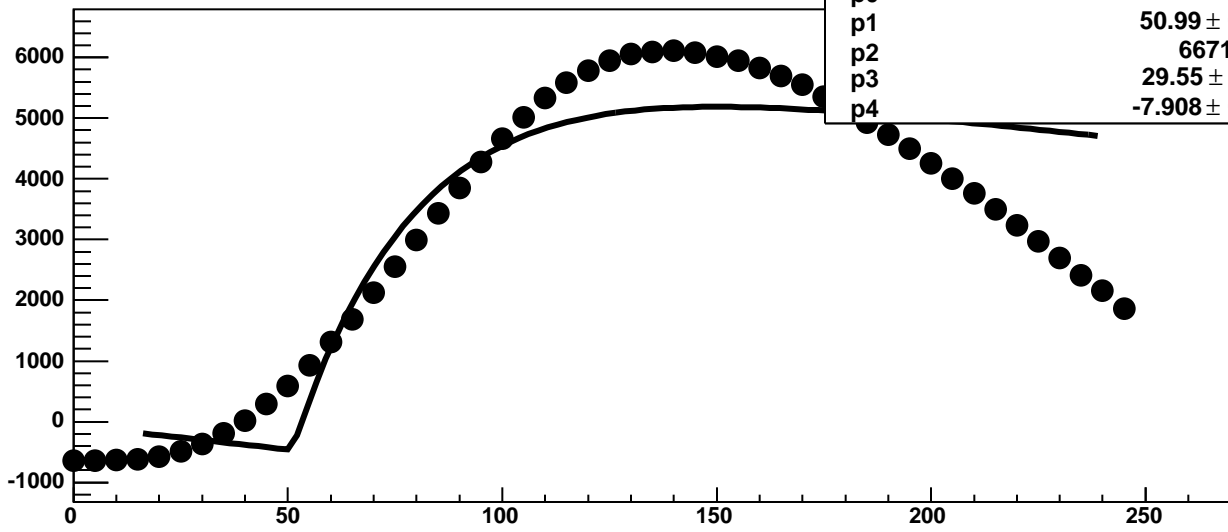
Chip 1, Channel 8, Enable 5!, DAC=4000, ADC Noise vs Hold



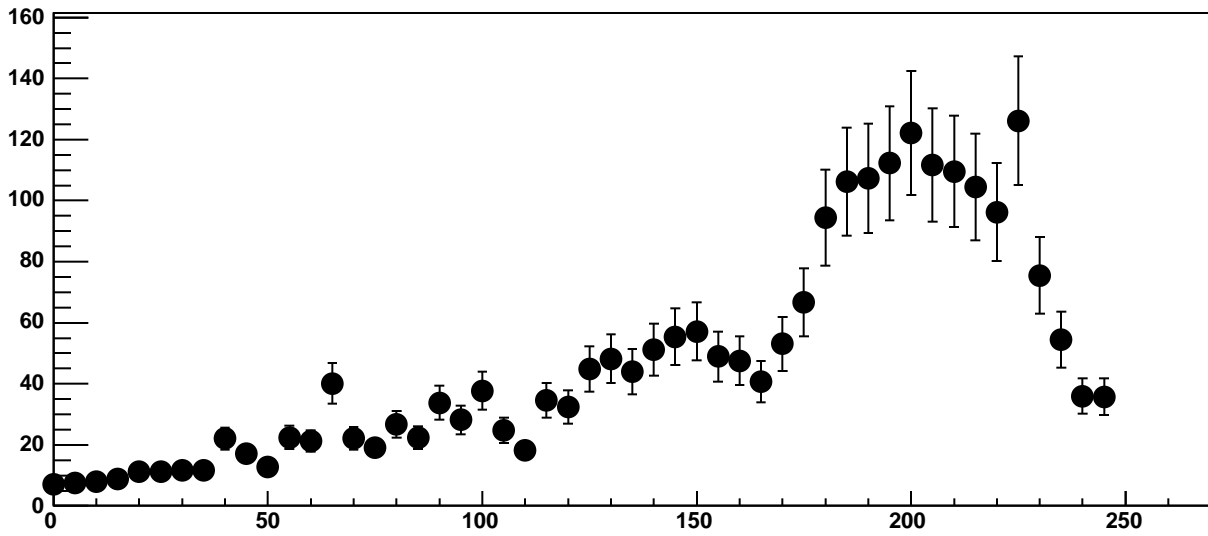
Chip 1, Channel 8, Enable 5!, DAC=4000, ADC Residuals vs Hold



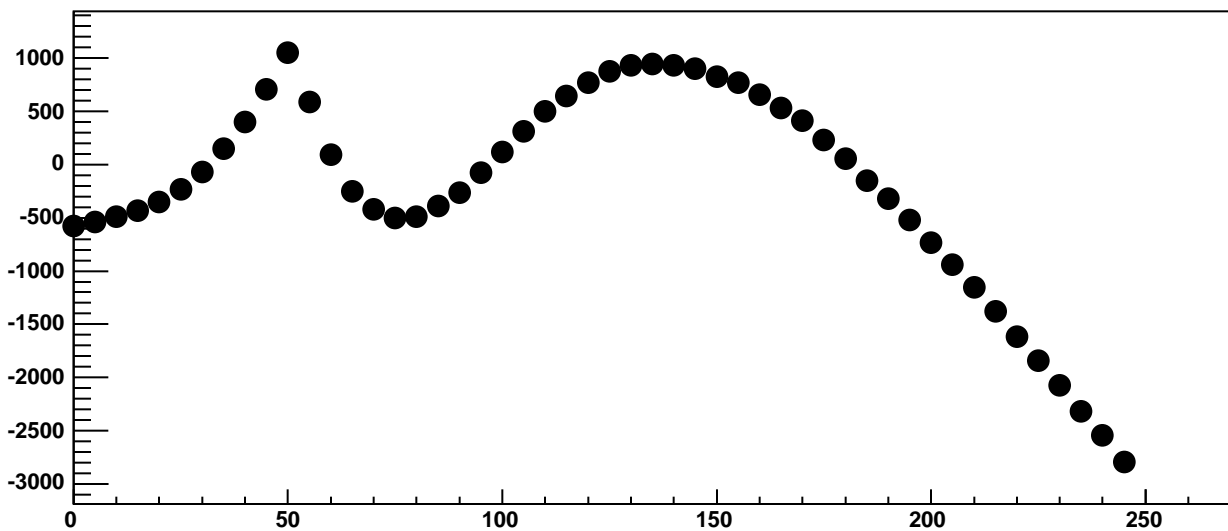
Chip 1, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold



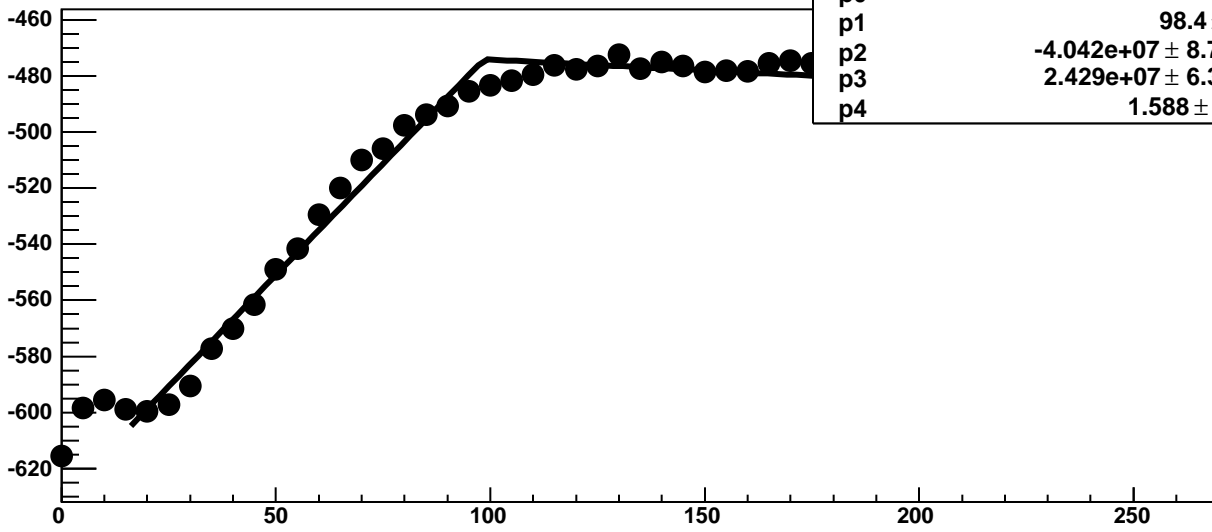
Chip 1, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold



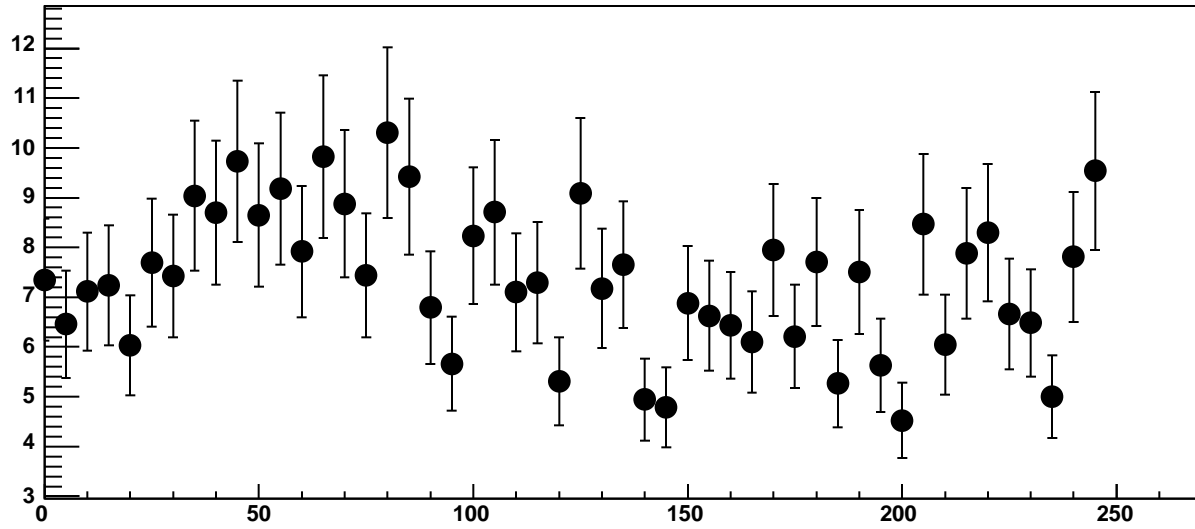
Chip 1, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold



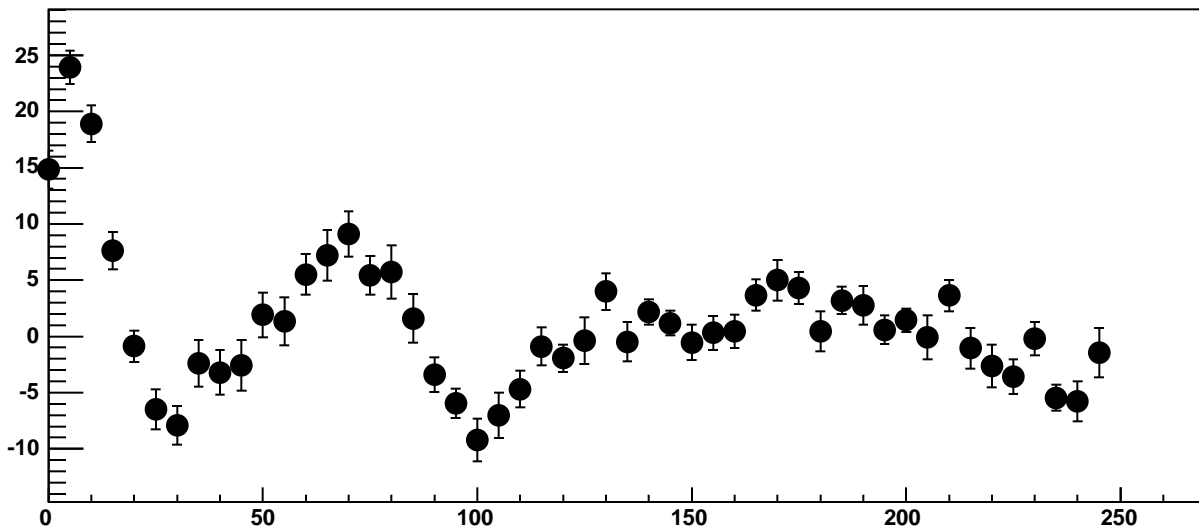
Chip 1, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold



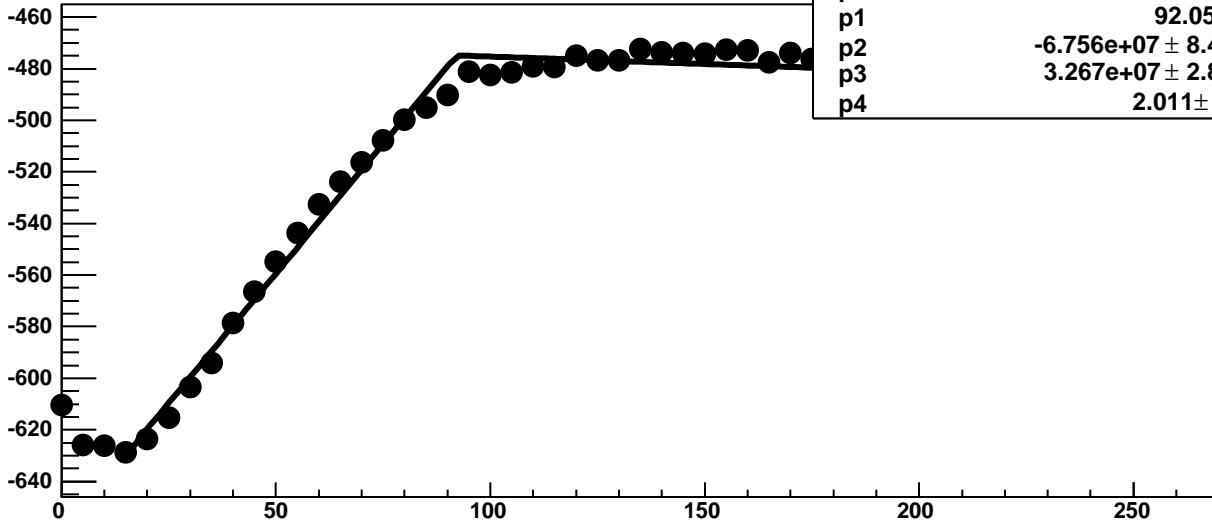
Chip 1, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

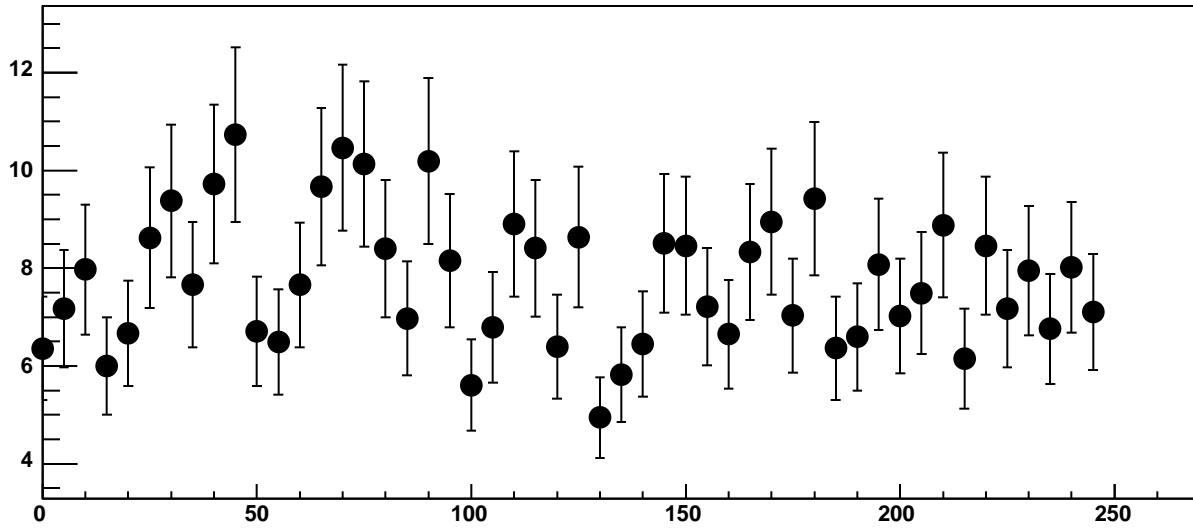


Chip 1, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

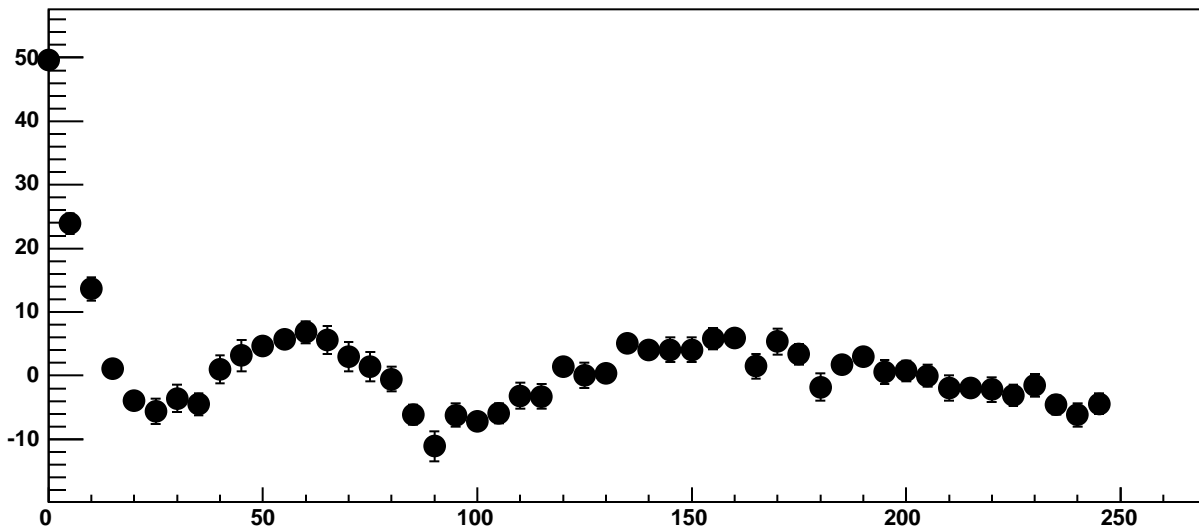


χ^2 / ndf	279.1 / 41
p0	-474.9 ± 0.5678
p1	92.05 ± 0.533
p2	$-6.756e+07 \pm 8.427e+05$
p3	$3.267e+07 \pm 2.826e+05$
p4	2.011 ± 0.01877

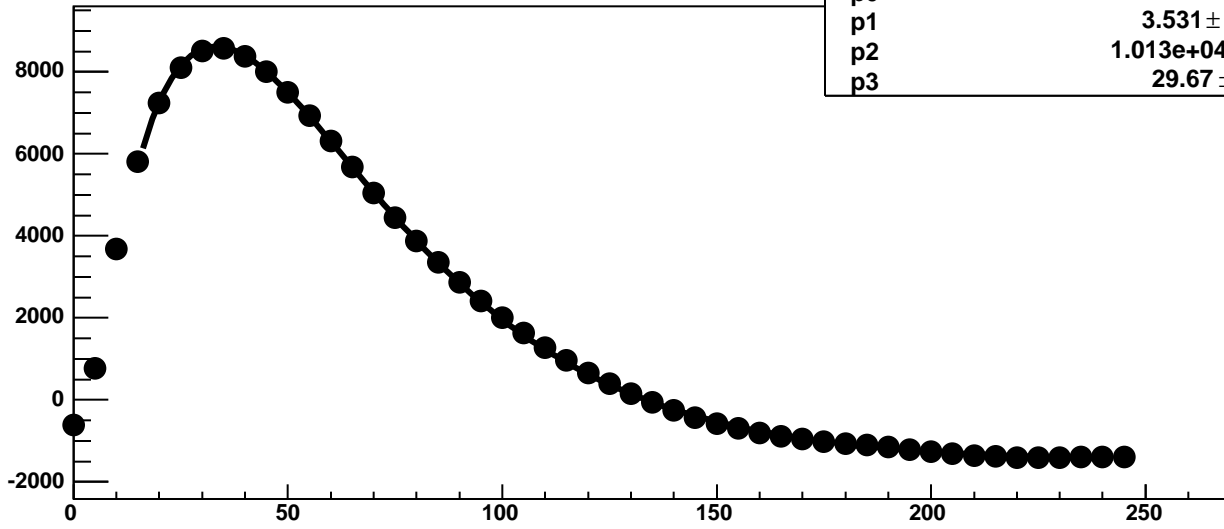
Chip 1, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold

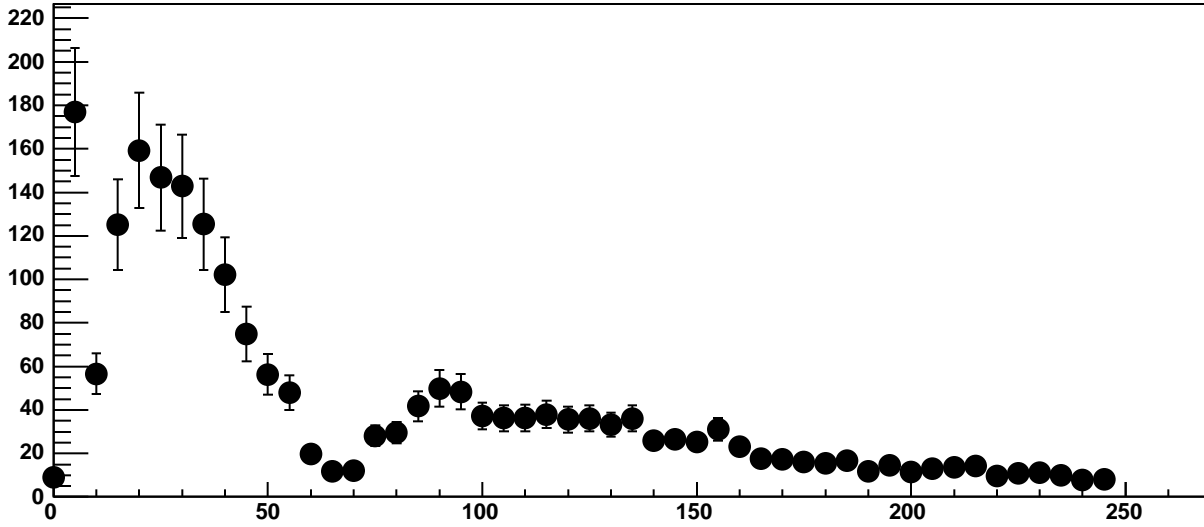


Chip 1, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold

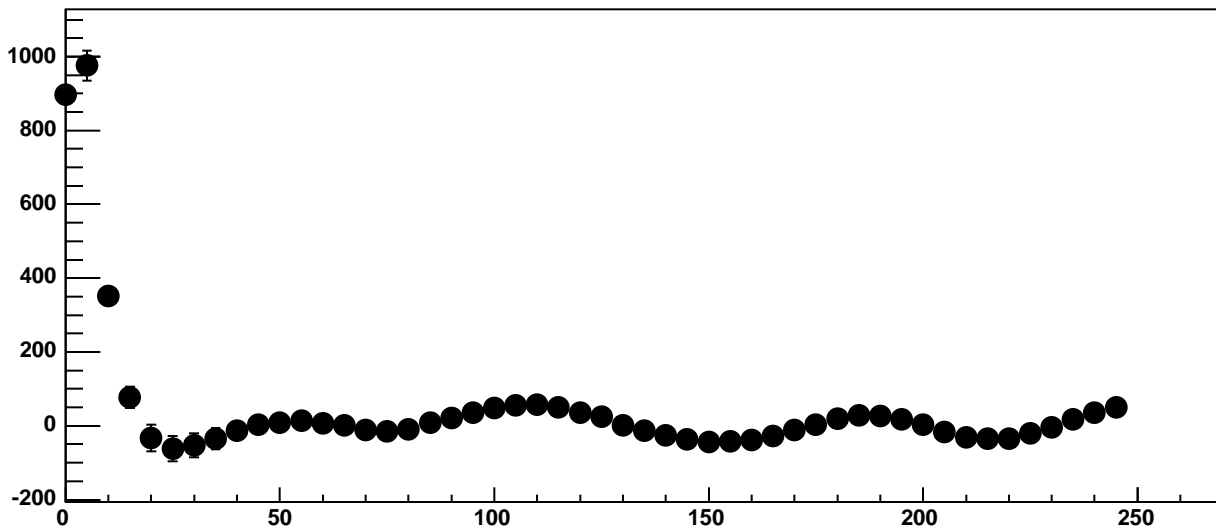


χ^2 / ndf	1784 / 42
p0	-1506 ± 1.017
p1	3.531 ± 0.05403
p2	$1.013\text{e}+04 \pm 6.793$
p3	29.67 ± 0.0215

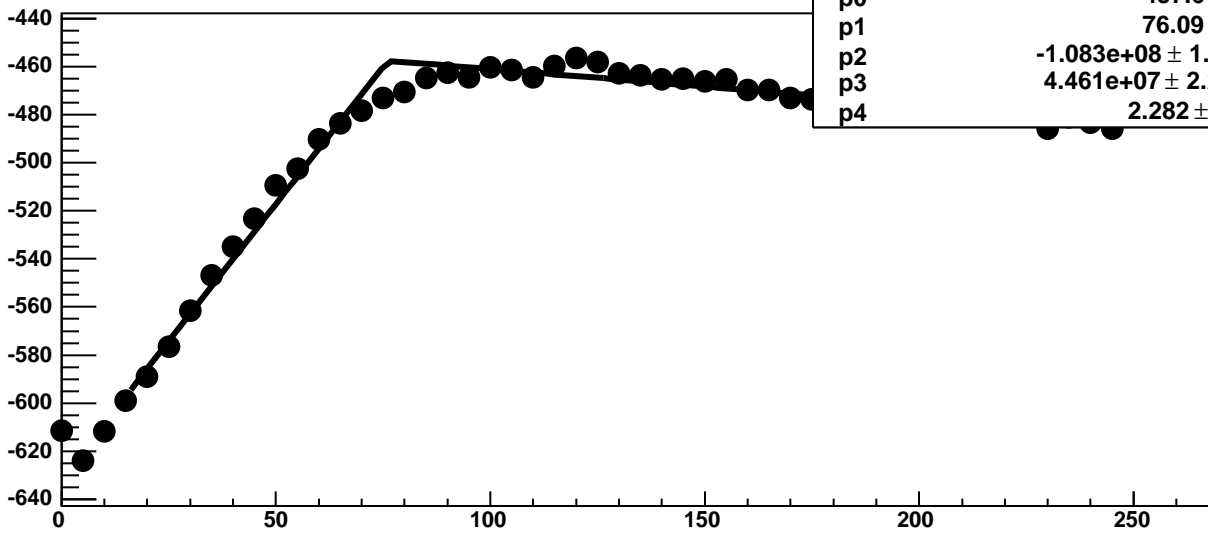
Chip 1, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold

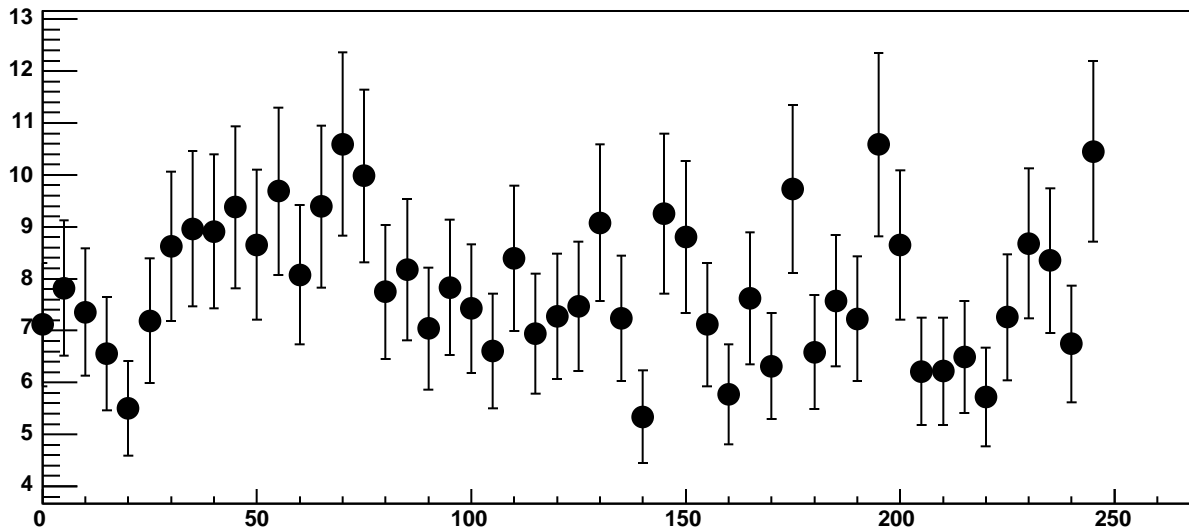


Chip 1, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

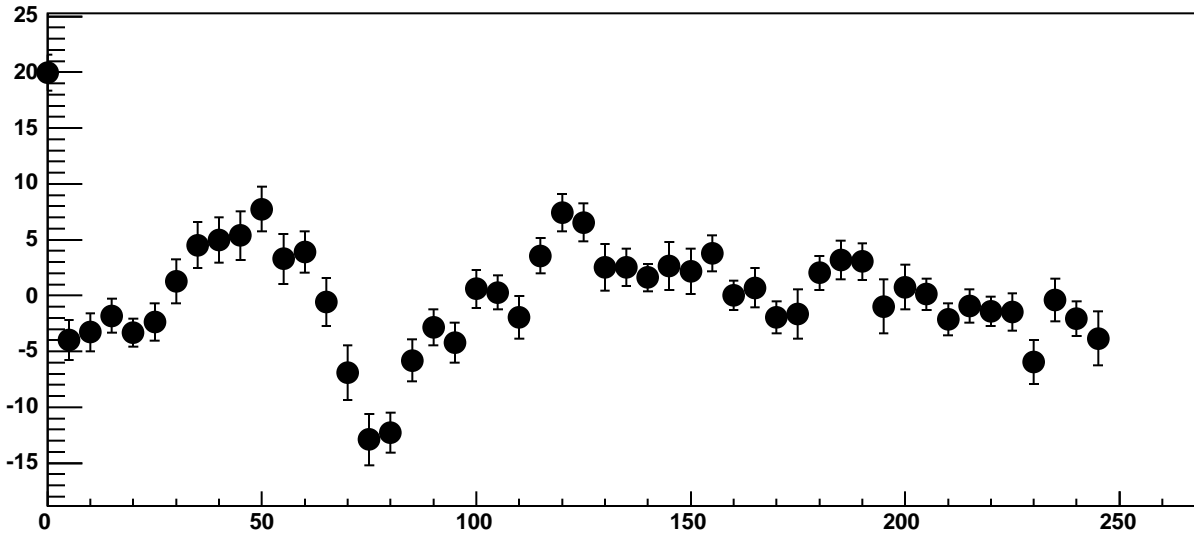


χ^2 / ndf	236 / 41
p0	-457.6 ± 0.5679
p1	76.09 ± 0.5282
p2	$-1.083\text{e}+08 \pm 1.347\text{e}+06$
p3	$4.461\text{e}+07 \pm 2.257\text{e}+05$
p4	2.282 ± 0.02722

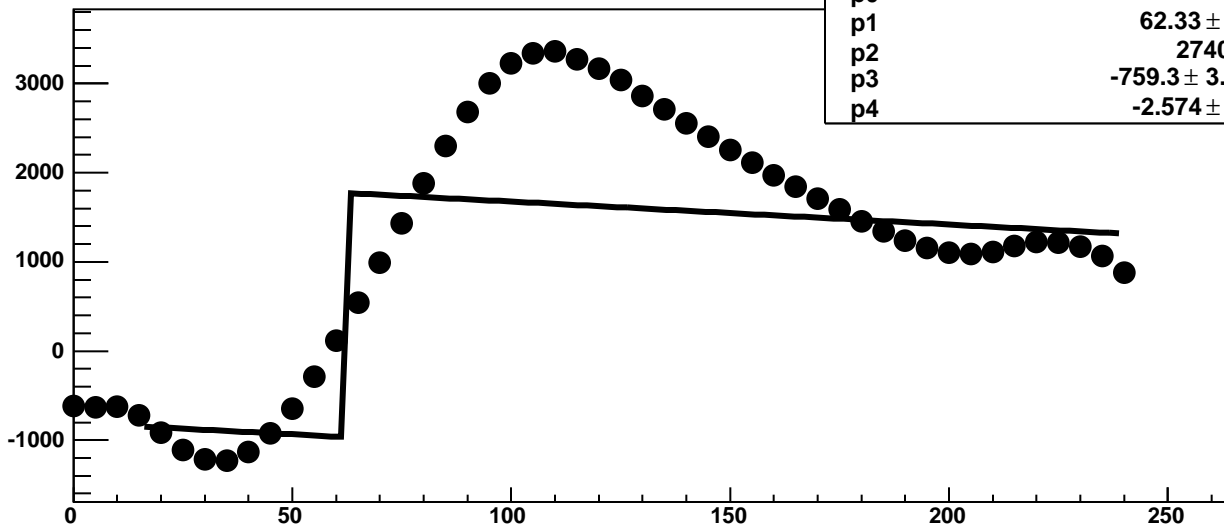
Chip 1, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



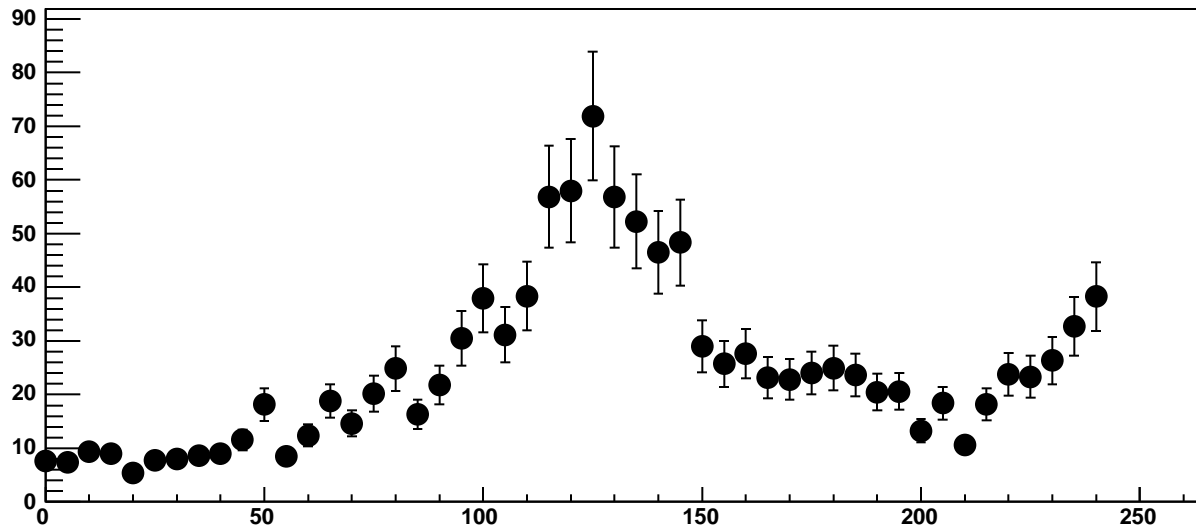
Chip 1, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold



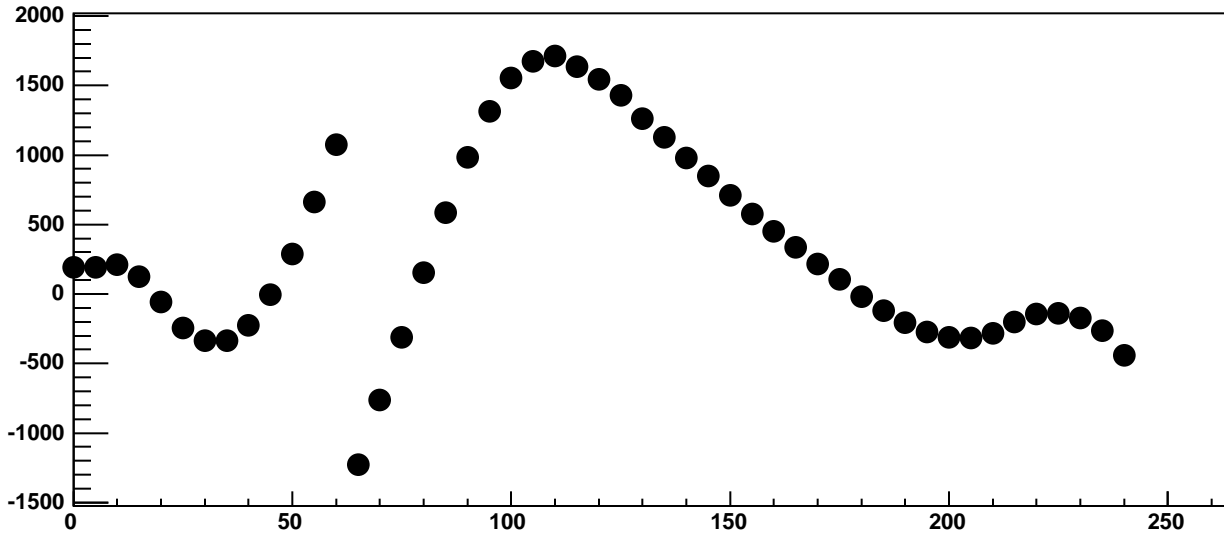
Chip 1, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold



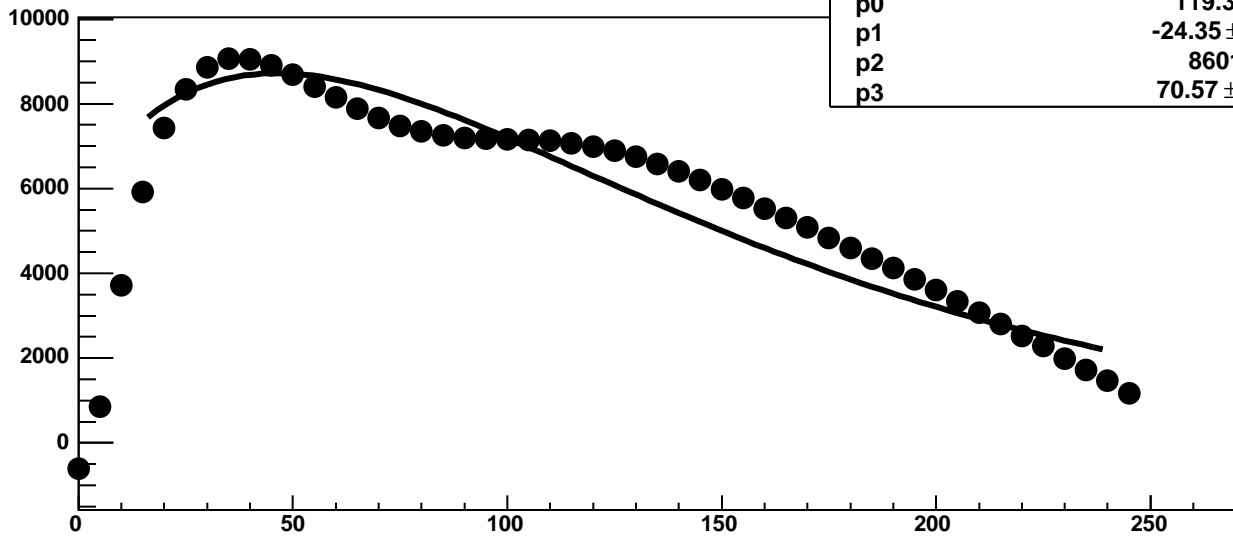
Chip 1, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold

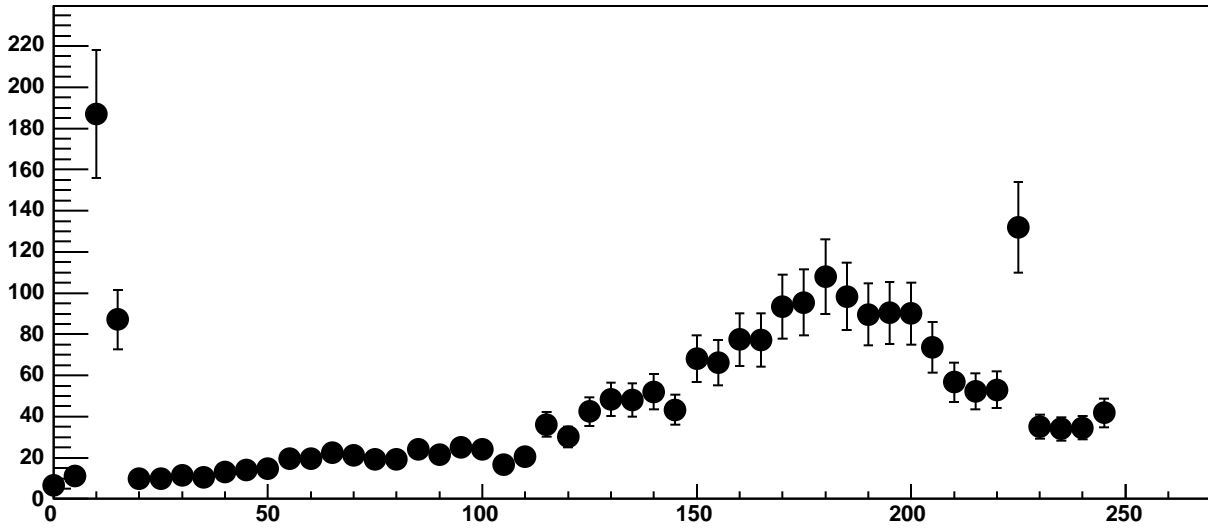


Chip 1, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold

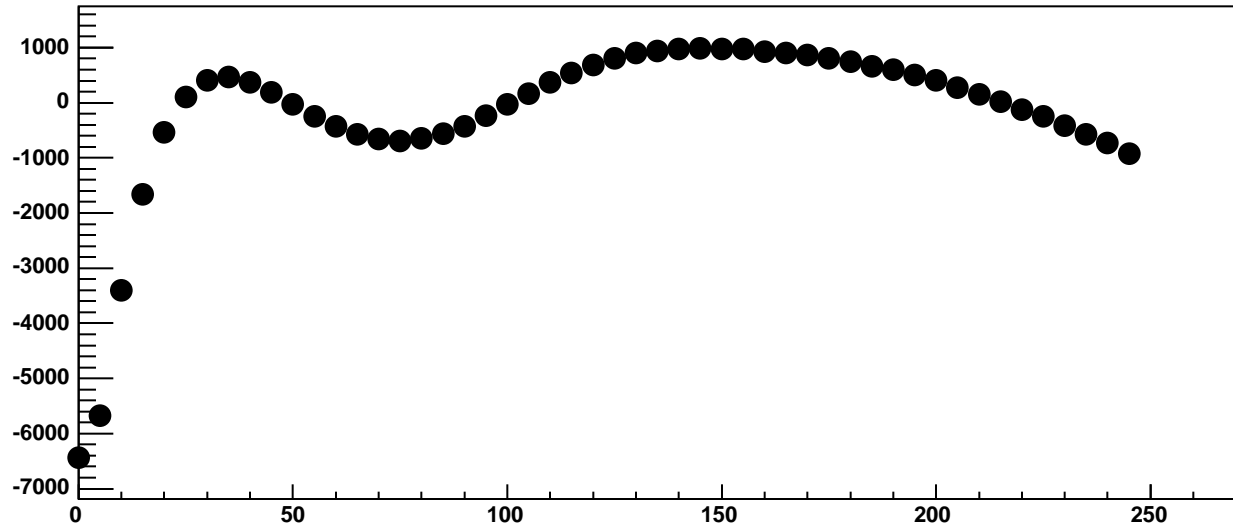


χ^2 / ndf	3.504e+05 / 42
p0	119.3 ± 14.66
p1	-24.35 ± 0.1353
p2	8601 ± 13.9
p3	70.57 ± 0.1335

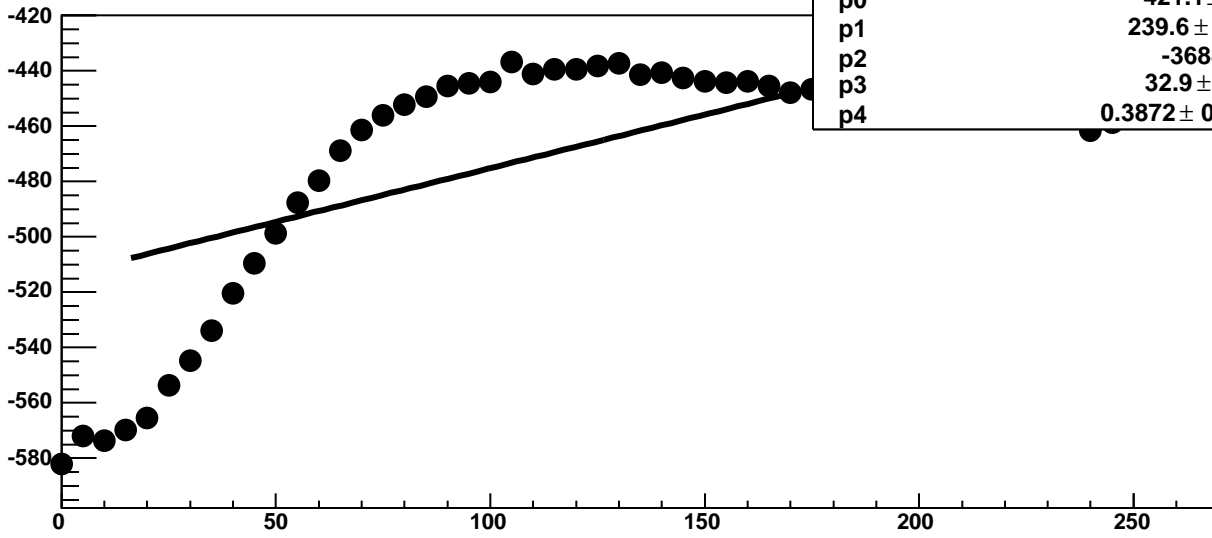
Chip 1, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold

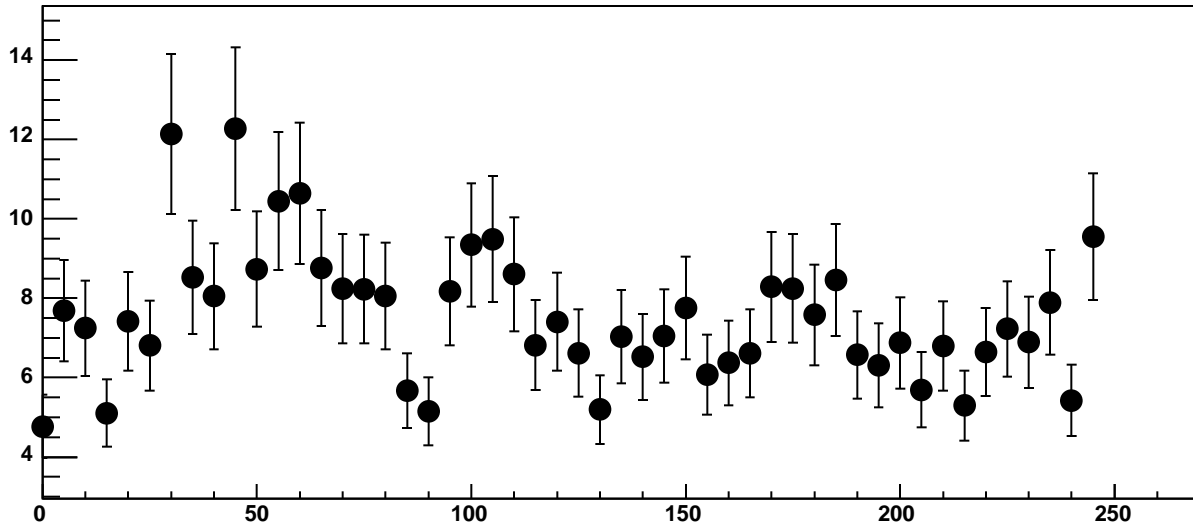


Chip 1, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

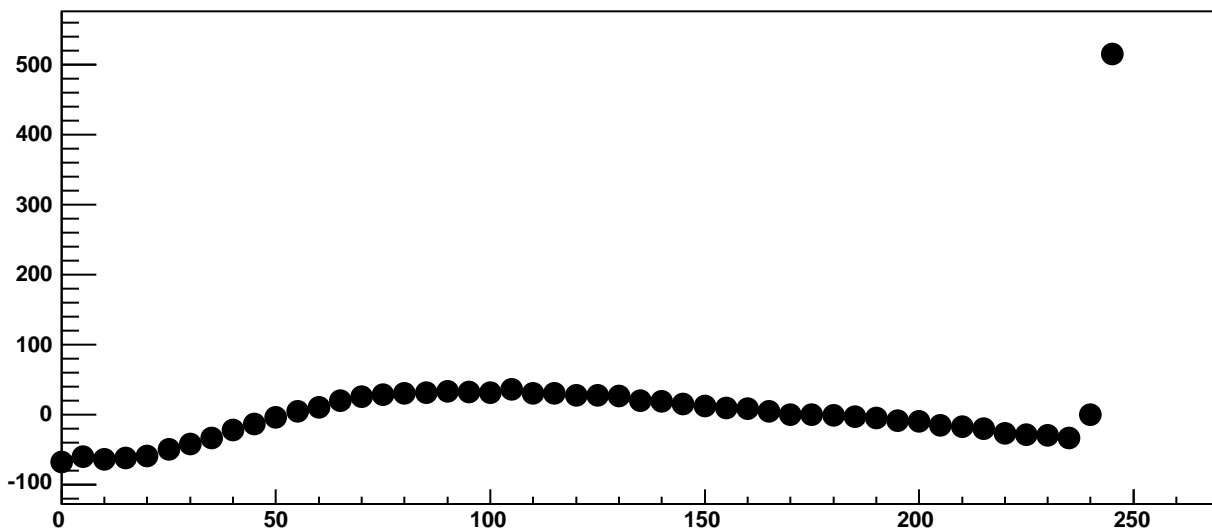


χ^2 / ndf	1.284e+04 / 41
p0	-421.1 ± 0.4714
p1	239.6 ± 0.01259
p2	-3688 ± 7.95
p3	32.9 ± 0.08861
p4	0.3872 ± 0.003775

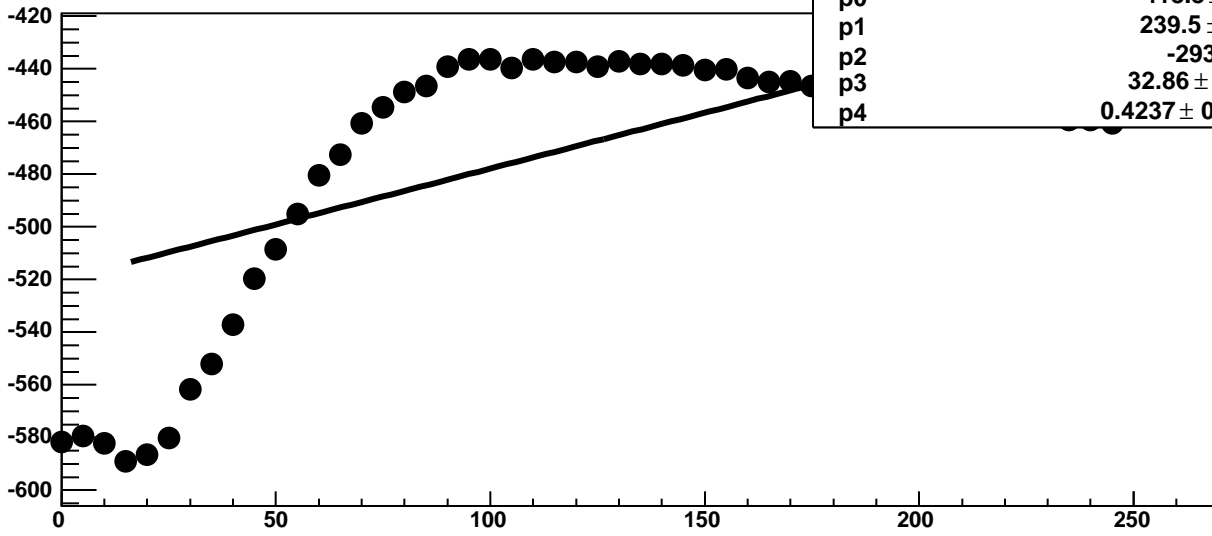
Chip 1, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

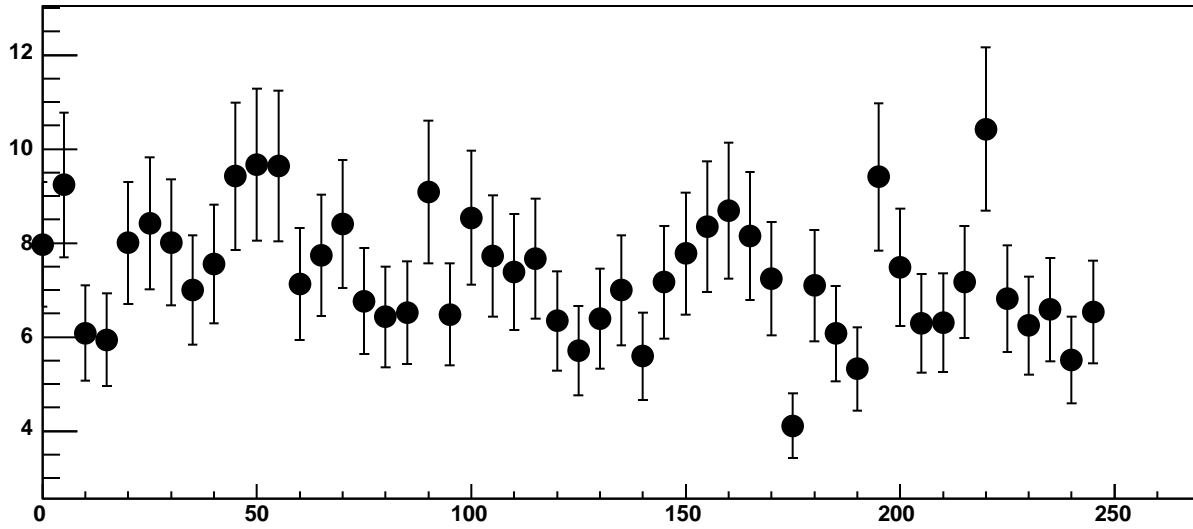


Chip 1, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

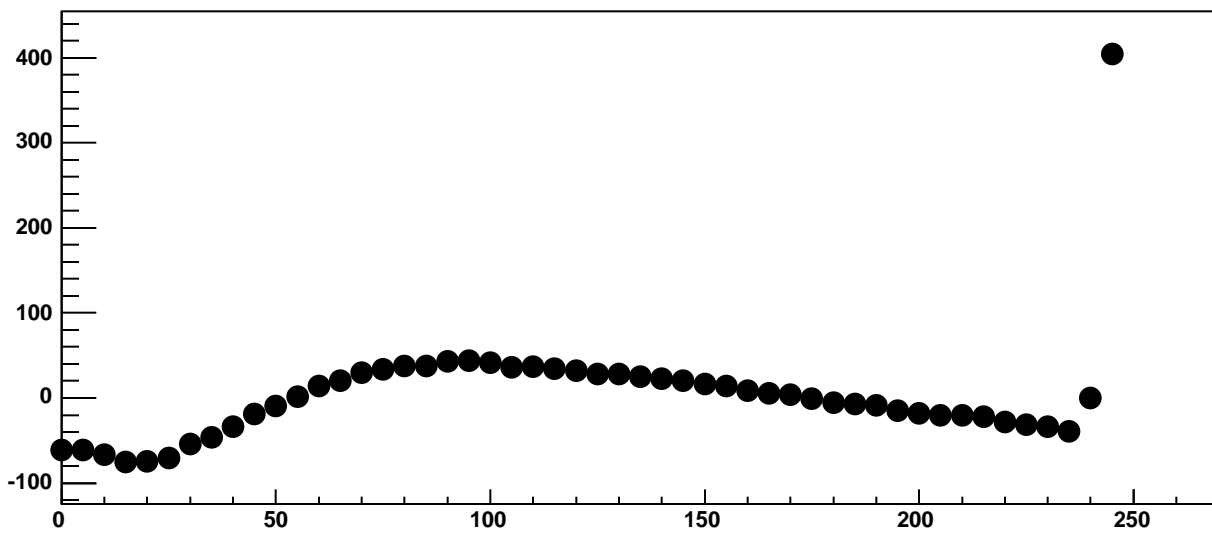


χ^2 / ndf	1.827e+04 / 41
p0	-418.8 ± 0.4689
p1	239.5 ± 0.0156
p2	-2931 ± 8.31
p3	32.86 ± 0.08972
p4	0.4237 ± 0.003755

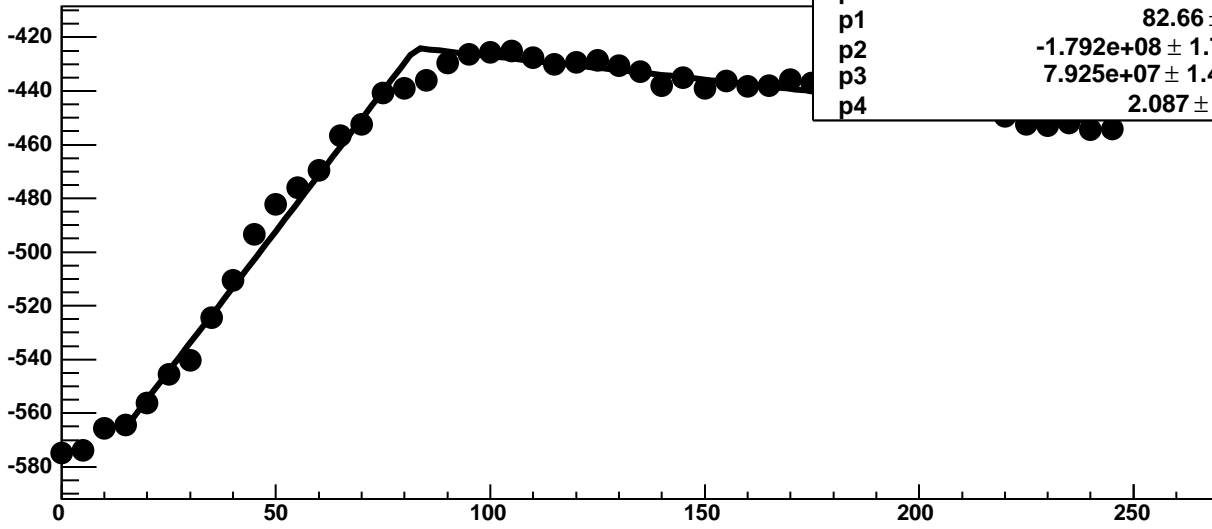
Chip 1, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

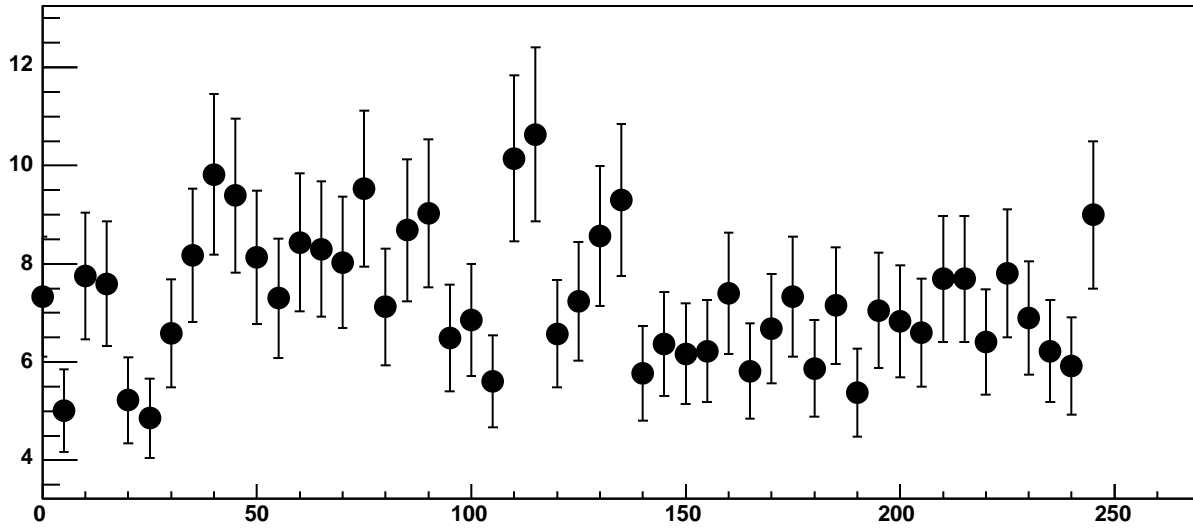


Chip 1, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold

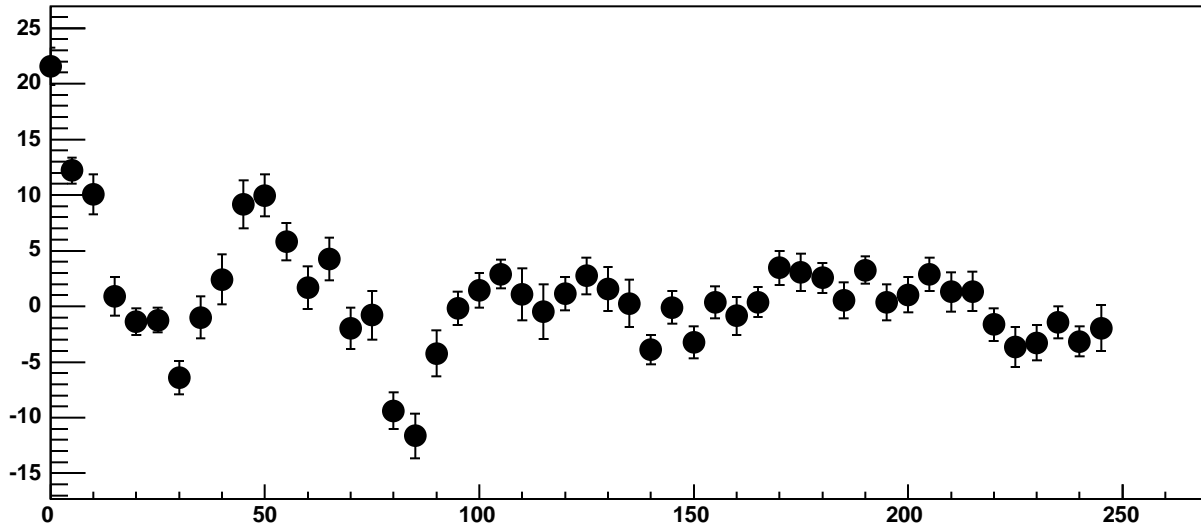


χ^2 / ndf	224 / 41
p0	-424 ± 0.5486
p1	82.66 ± 0.5009
p2	$-1.792\text{e}+08 \pm 1.768\text{e}+06$
p3	$7.925\text{e}+07 \pm 1.418\text{e}+05$
p4	2.087 ± 0.02134

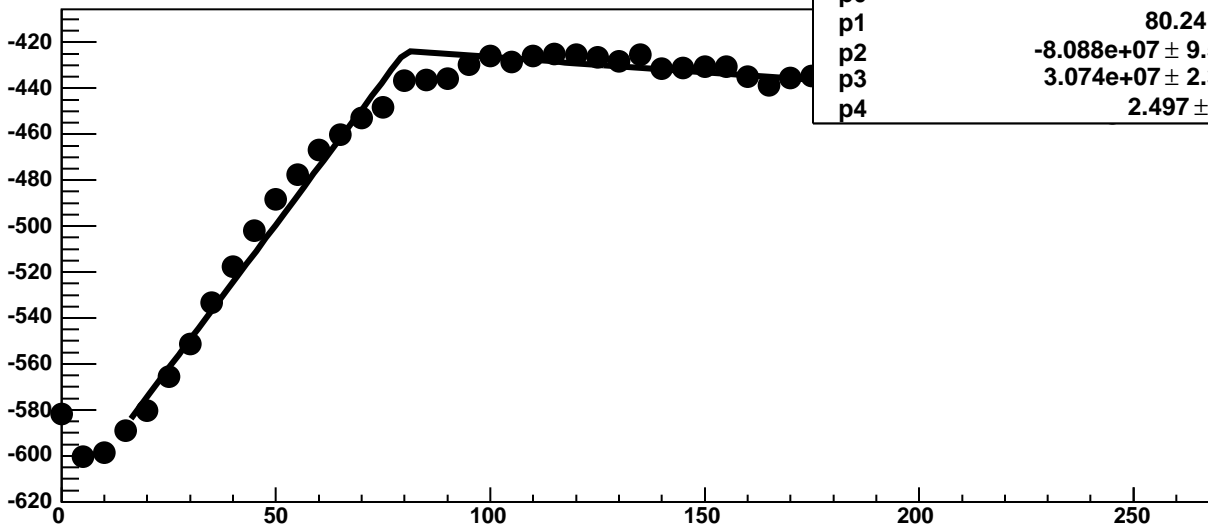
Chip 1, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold

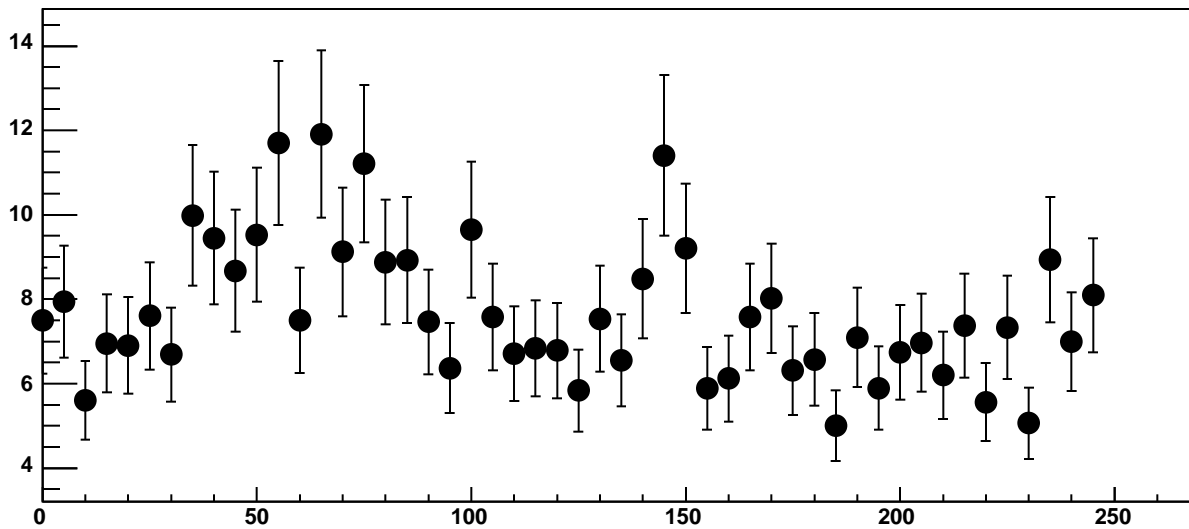


Chip 1, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

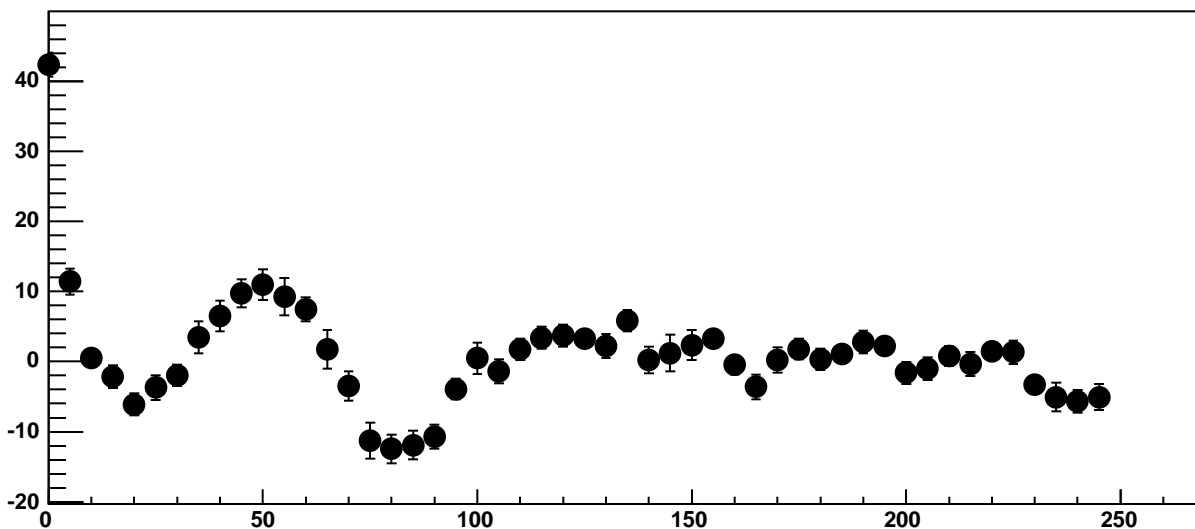


χ^2 / ndf	338.9 / 41
p0	-423.8 ± 0.5867
p1	80.24 ± 0.4751
p2	$-8.088\text{e}+07 \pm 9.571\text{e}+05$
p3	$3.074\text{e}+07 \pm 2.346\text{e}+05$
p4	2.497 ± 0.02566

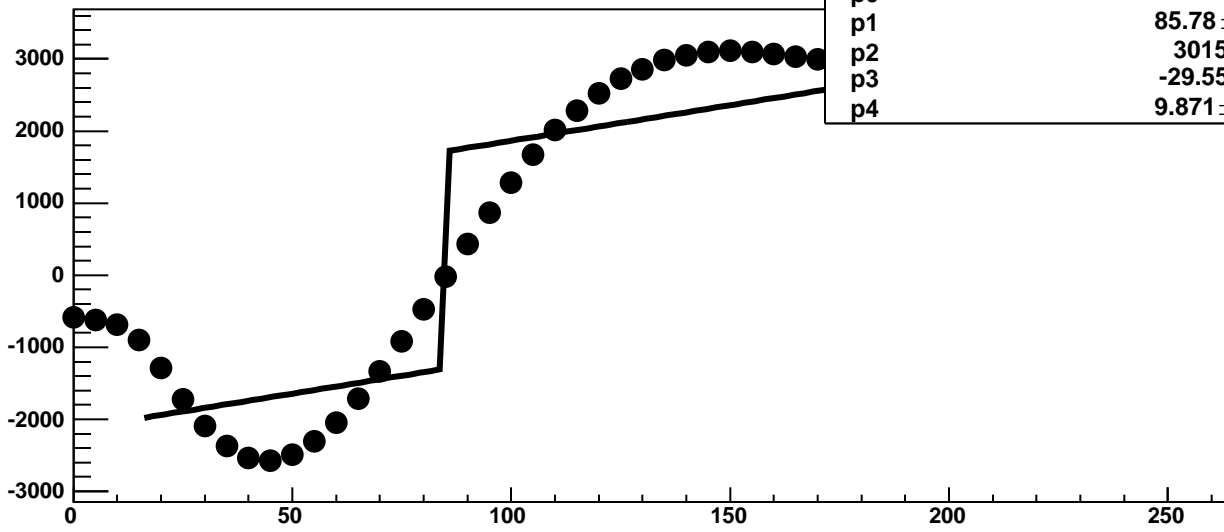
Chip 1, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

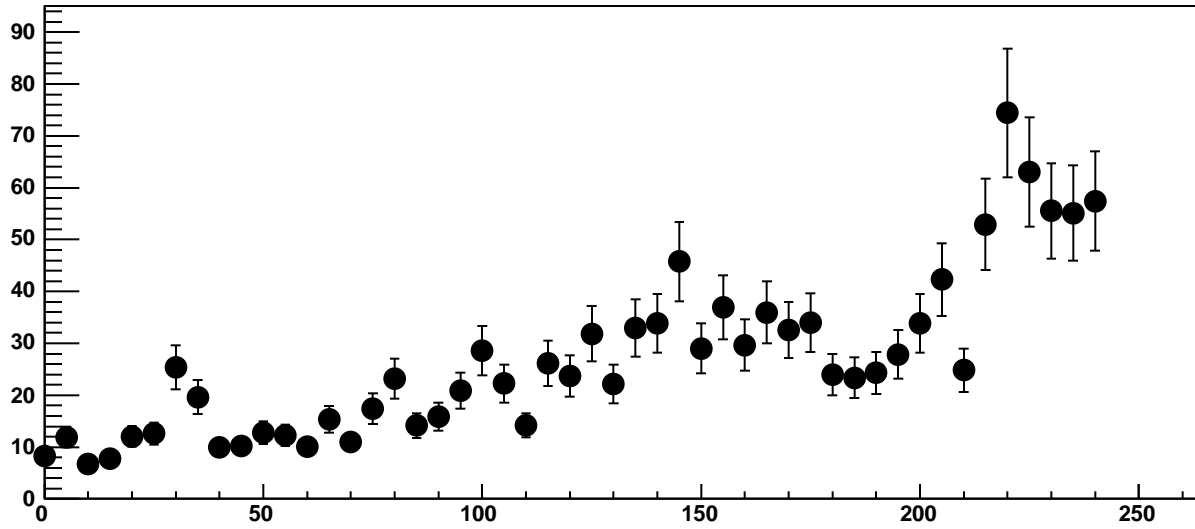


Chip 1, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold

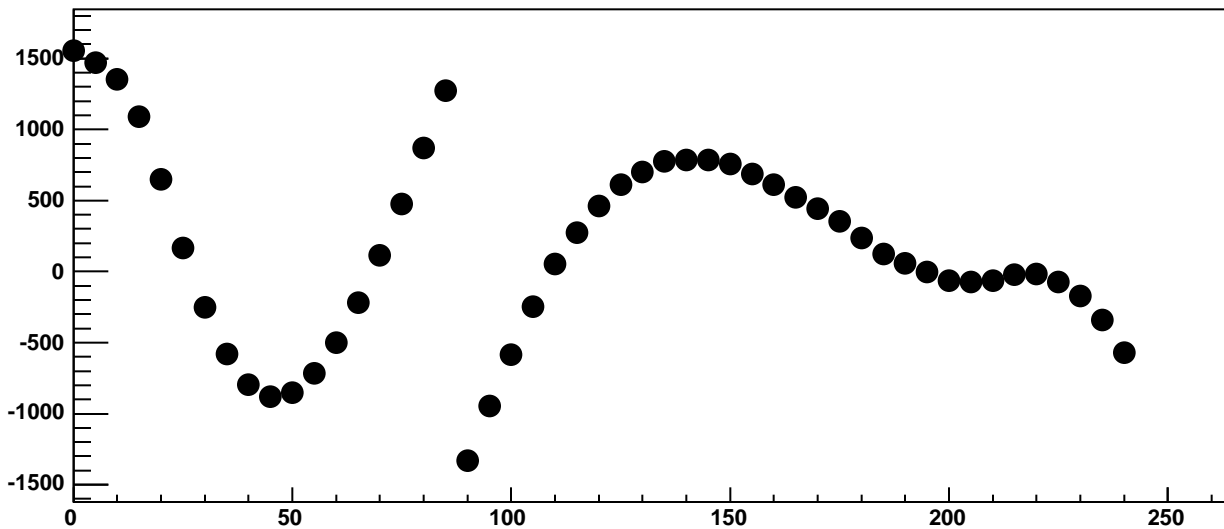


χ^2 / ndf	1.397e+06 / 41
p0	-1289 ± 0.5406
p1	85.78 ± 0.6743
p2	3015 ± 1.592
p3	-29.55 ± 1.238
p4	9.871 ± 0.0454

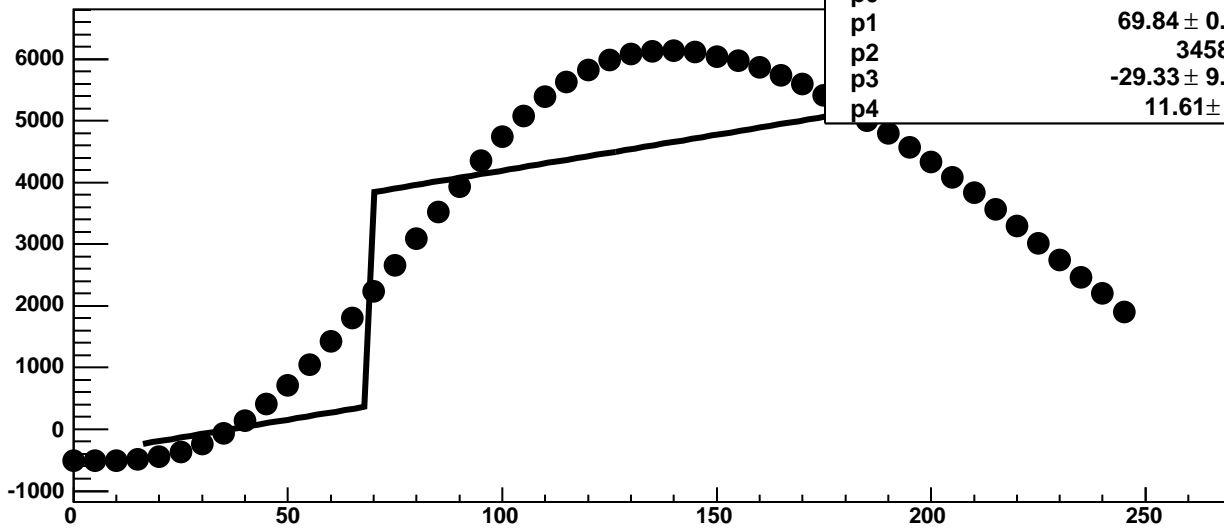
Chip 1, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

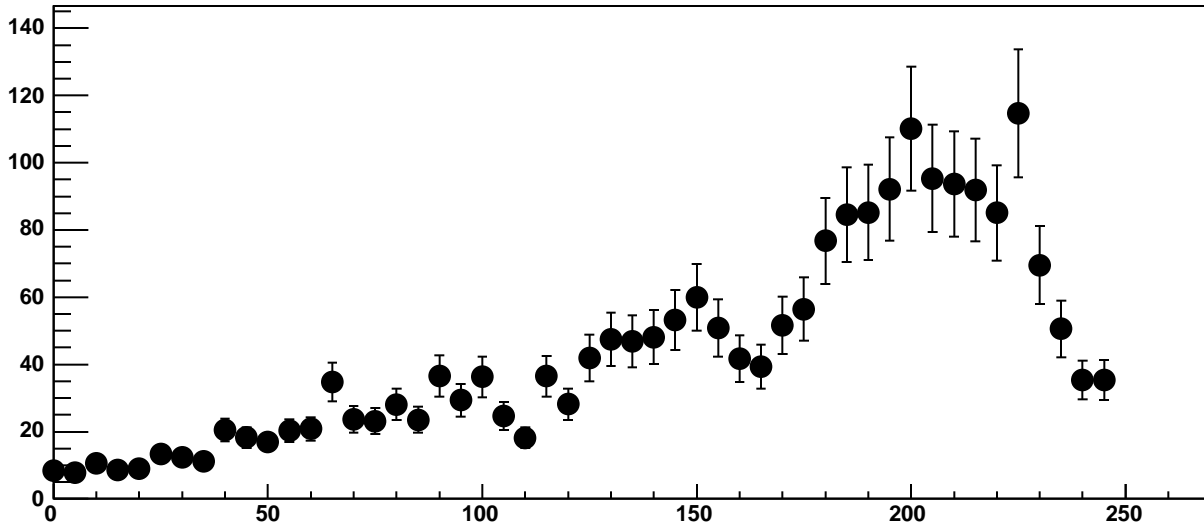


Chip 1, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold

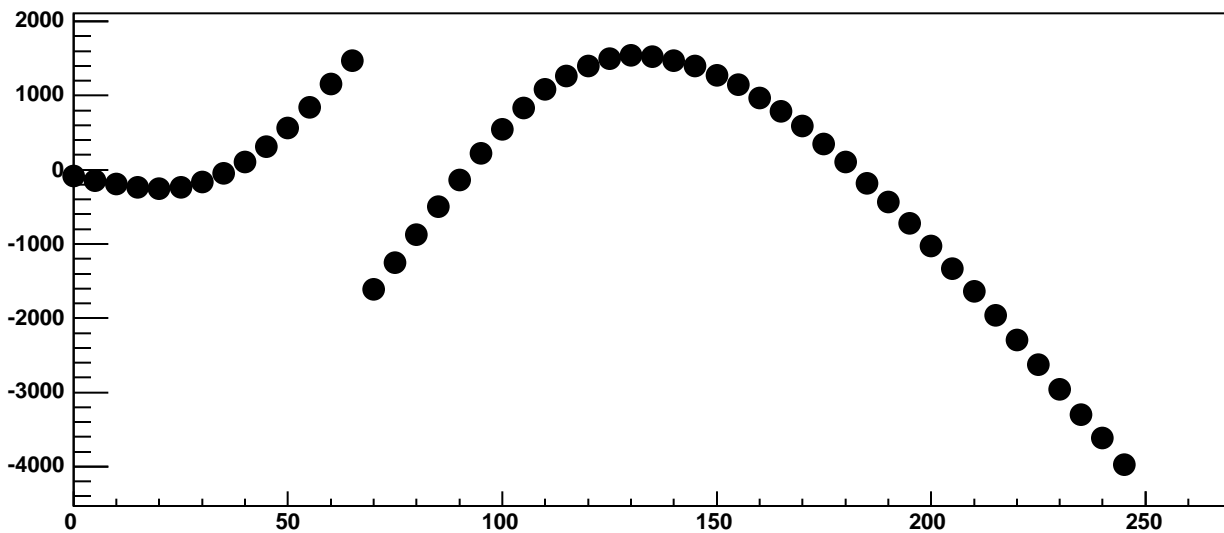


χ^2 / ndf	1.029e+06 / 41
p0	386.8 ± 1.765
p1	69.84 ± 0.0006311
p2	3458 ± 3.761
p3	-29.33 ± 9.769e-05
p4	11.61 ± 0.03844

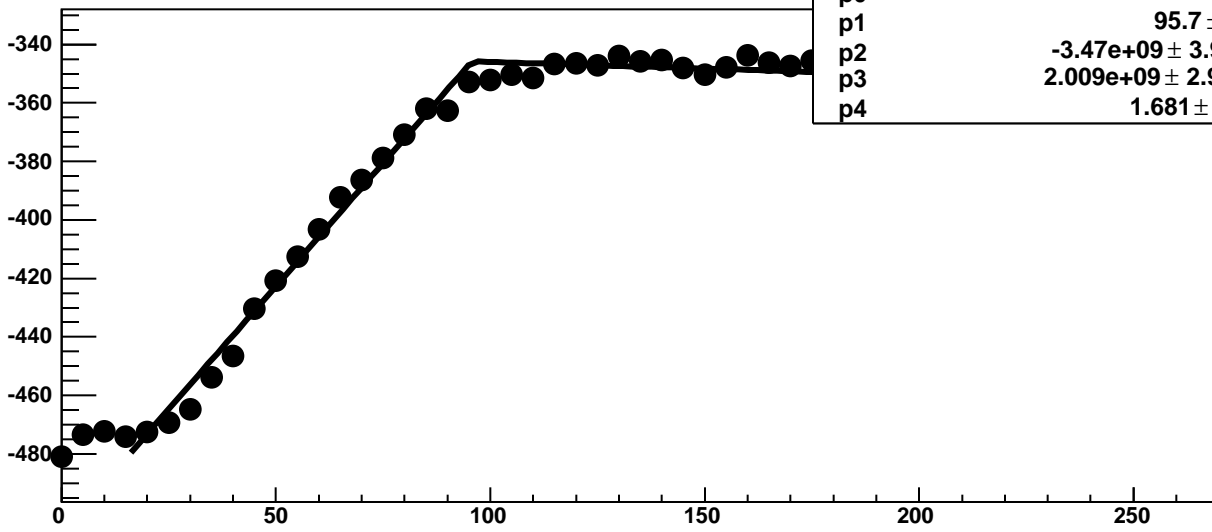
Chip 1, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold

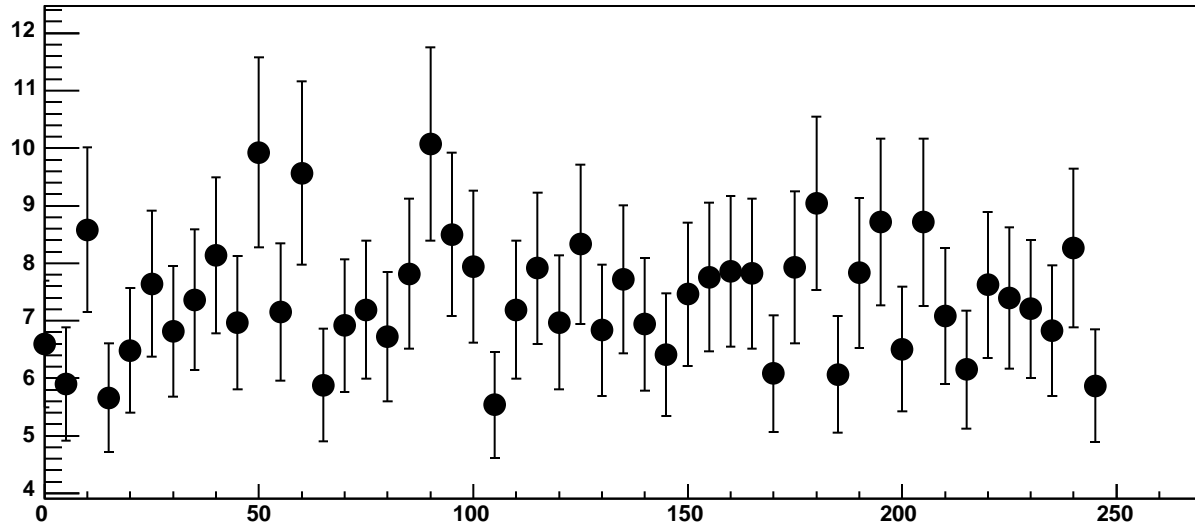


Chip 1, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

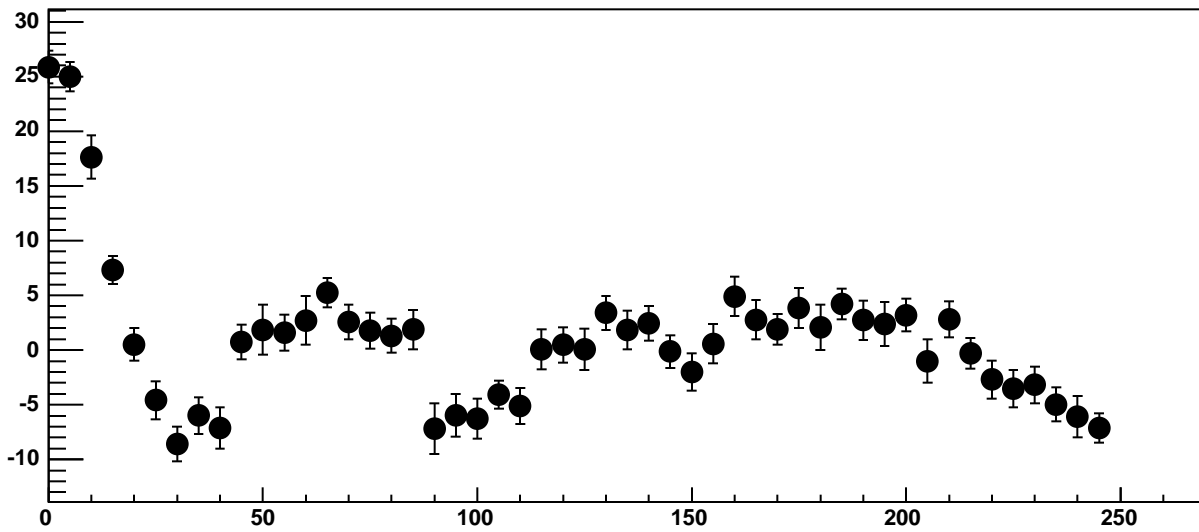


χ^2 / ndf	249.1 / 41
p0	-345.8 ± 0.6067
p1	95.7 ± 0.7035
p2	$-3.47\text{e}+09 \pm 3.995\text{e}+07$
p3	$2.009\text{e}+09 \pm 2.942\text{e}+05$
p4	1.681 ± 0.01974

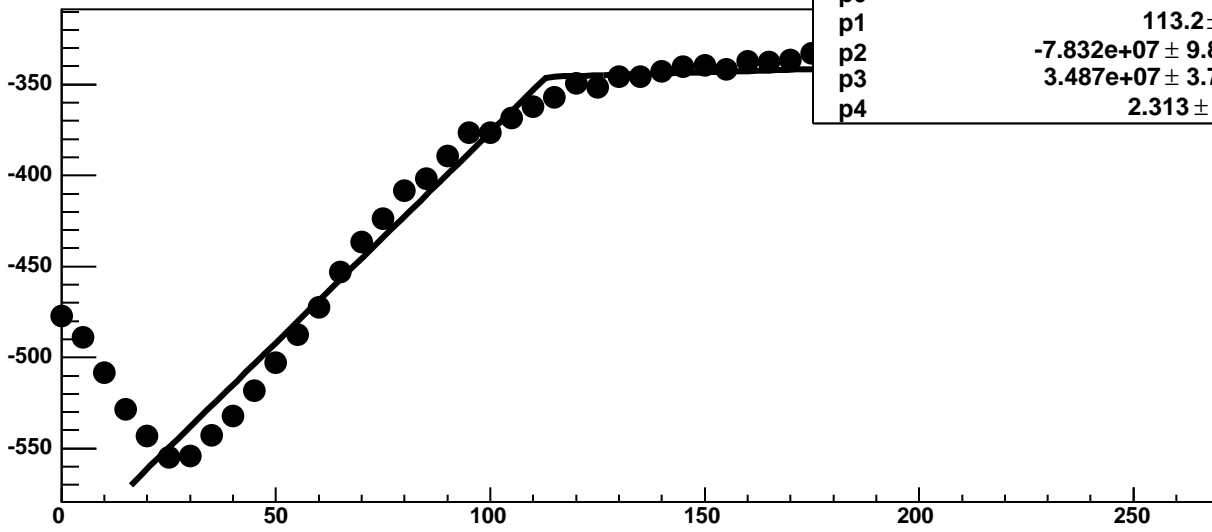
Chip 1, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold

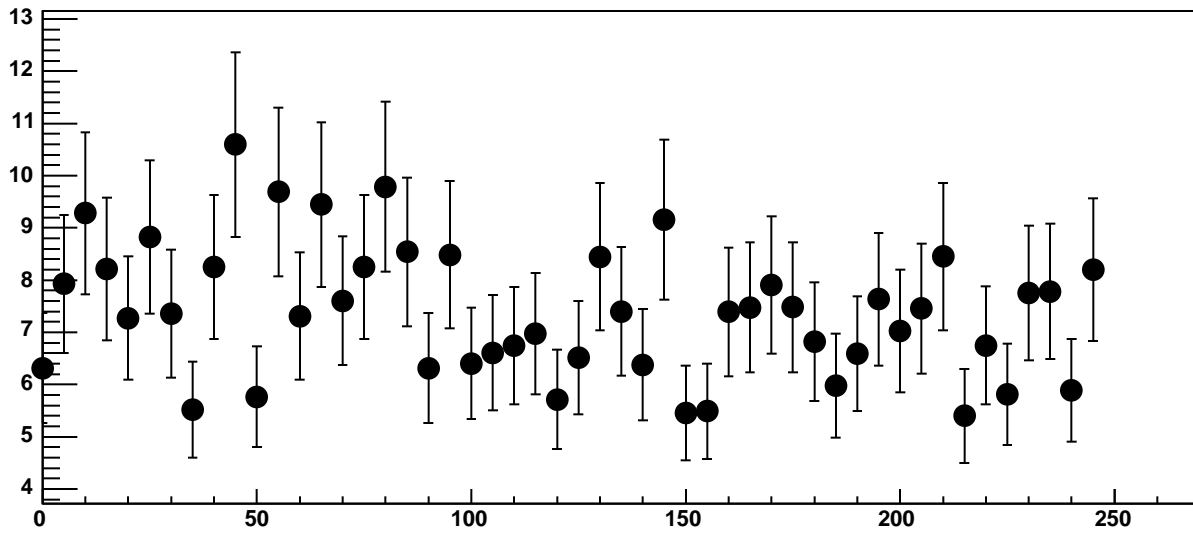


Chip 1, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

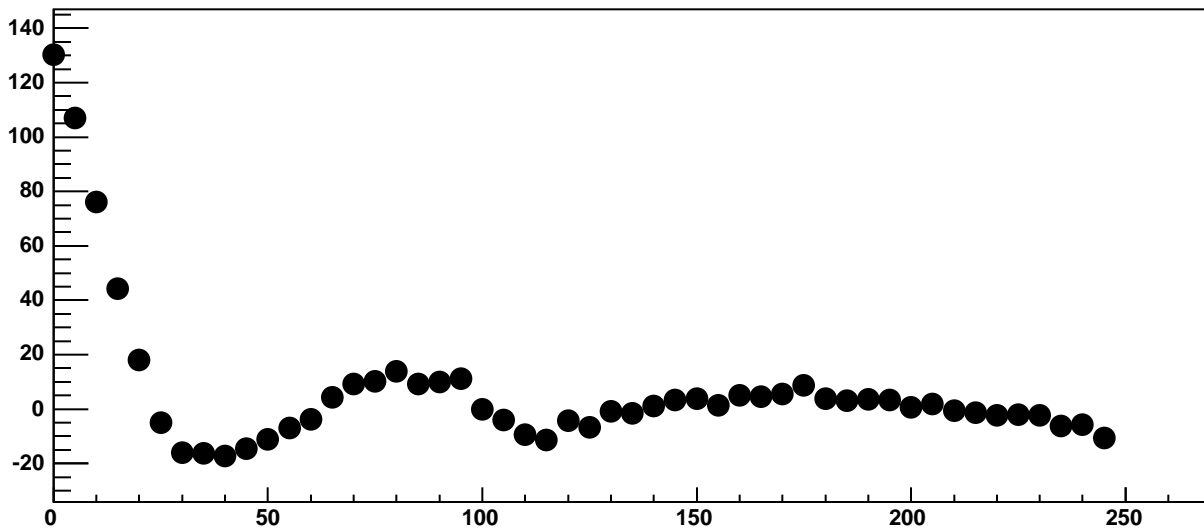


χ^2 / ndf	1588 / 41
p0	-345.7 ± 0.6161
p1	113.2 ± 0.4246
p2	$-7.832\text{e}+07 \pm 9.841\text{e}+05$
p3	$3.487\text{e}+07 \pm 3.706\text{e}+05$
p4	2.313 ± 0.01284

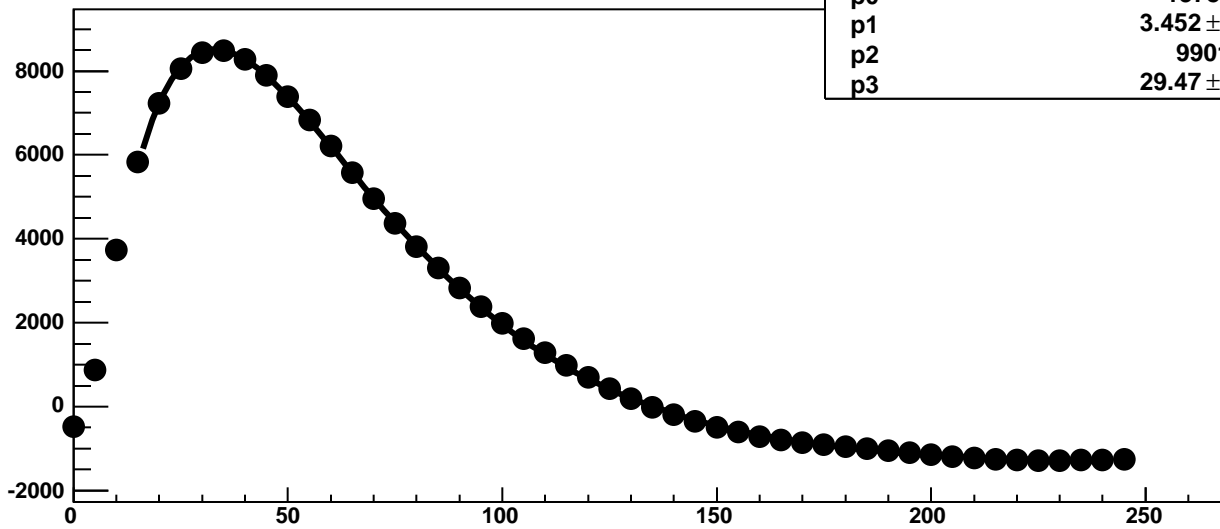
Chip 1, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



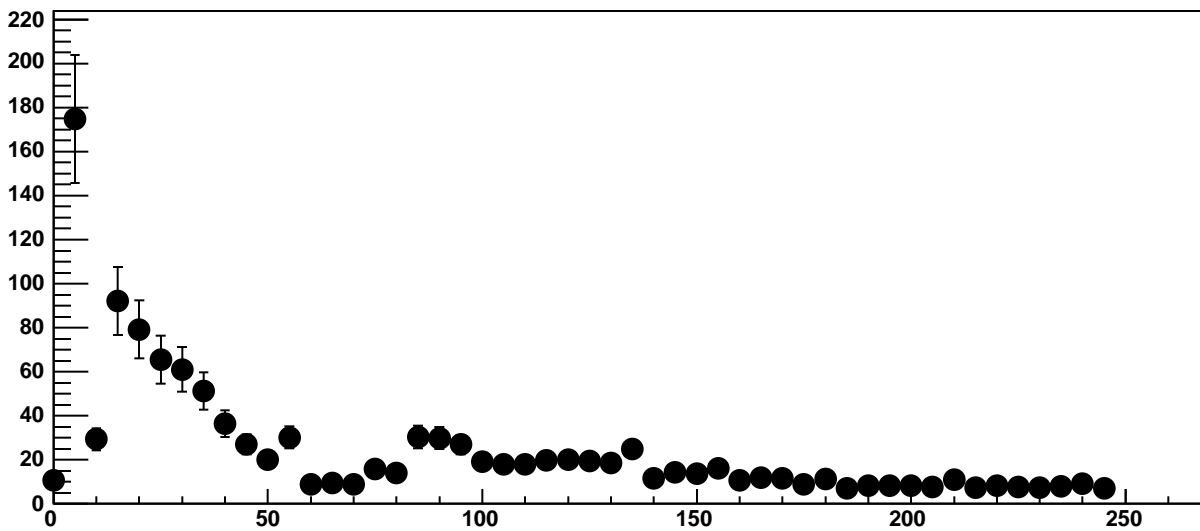
Chip 1, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold



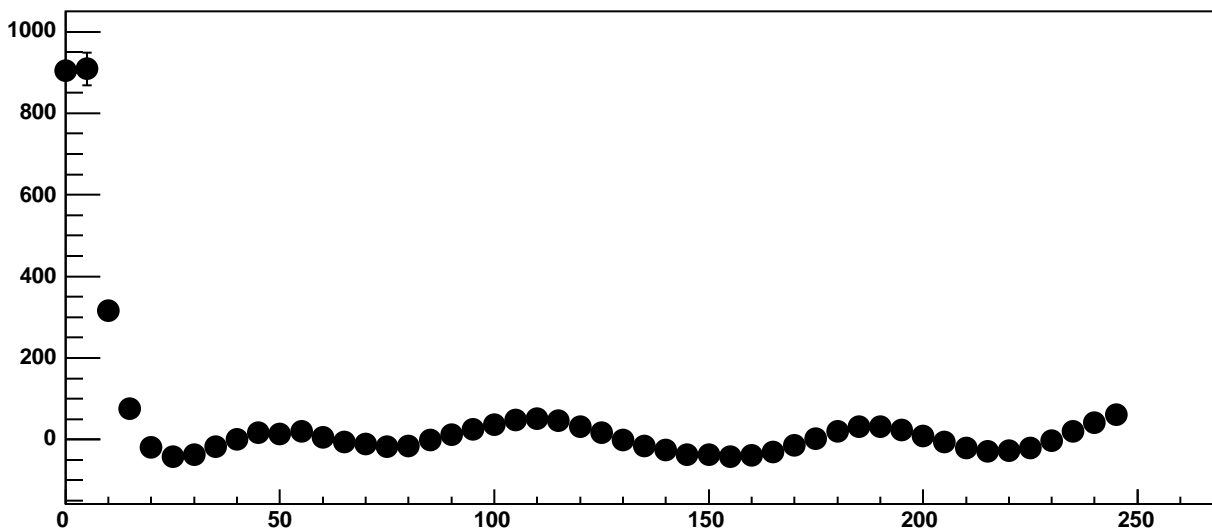
Chip 1, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold



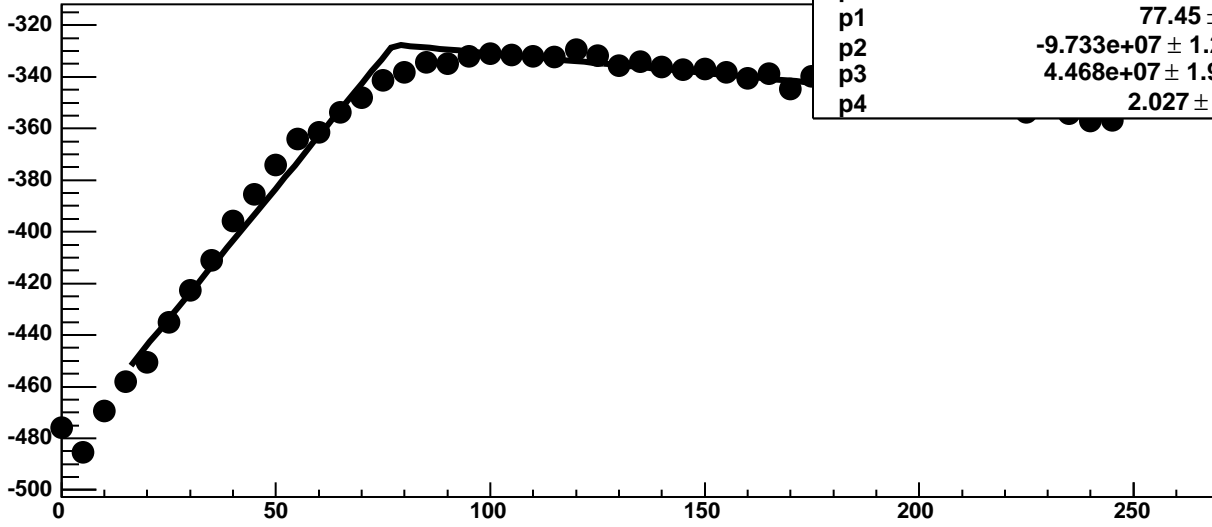
Chip 1, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold

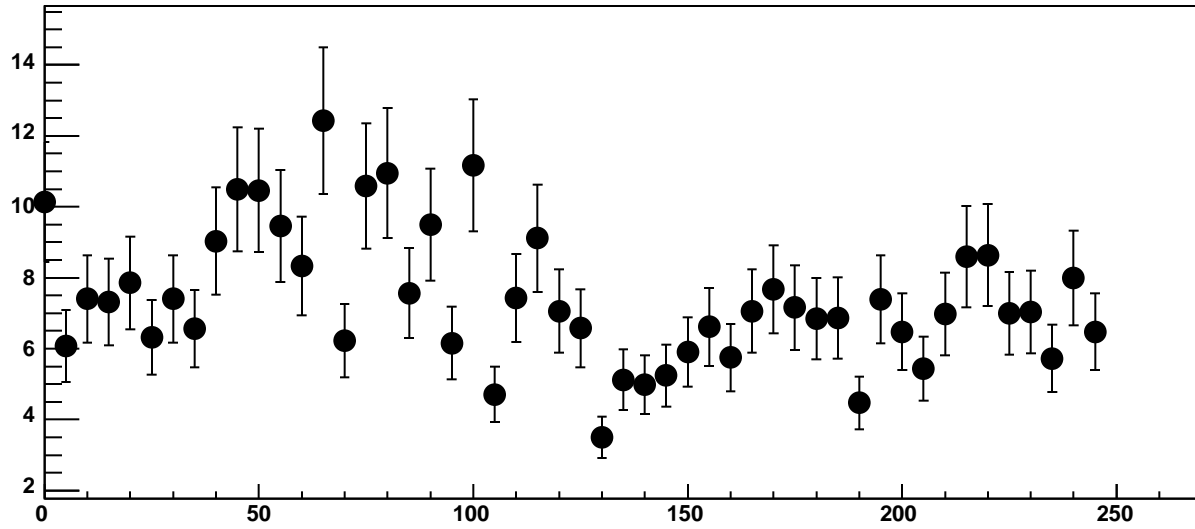


Chip 1, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

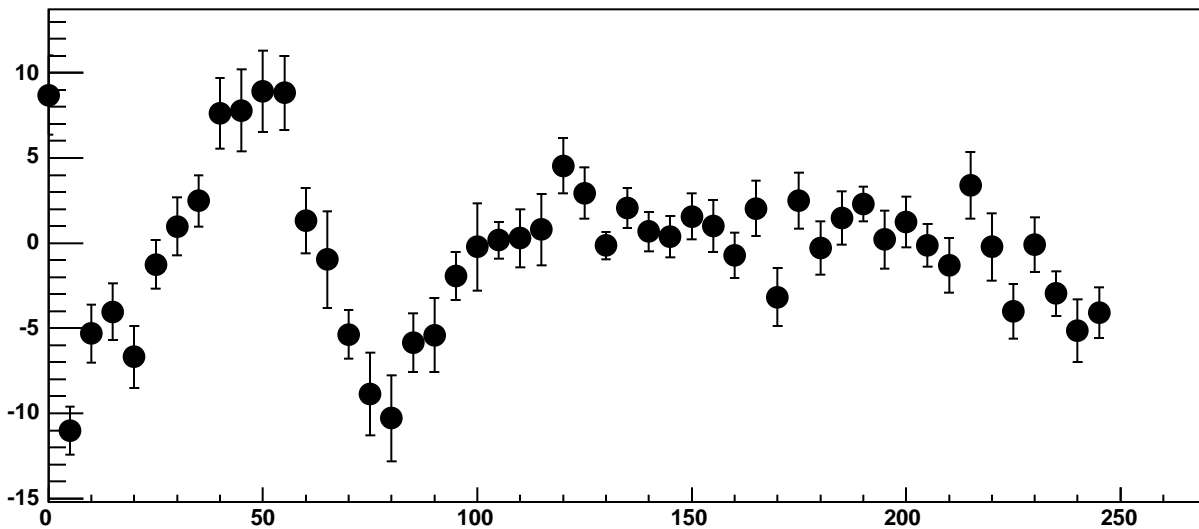


χ^2 / ndf	196.2 / 41
p0	-327.5 ± 0.5088
p1	77.45 ± 0.5565
p2	$-9.733\text{e}+07 \pm 1.268\text{e}+06$
p3	$4.468\text{e}+07 \pm 1.939\text{e}+05$
p4	2.027 ± 0.02684

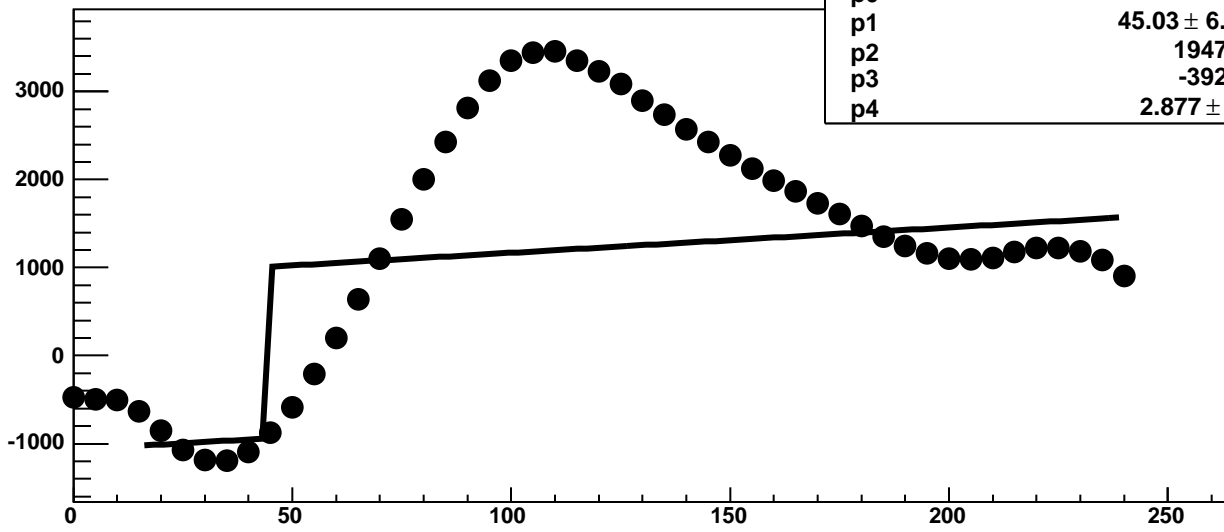
Chip 1, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



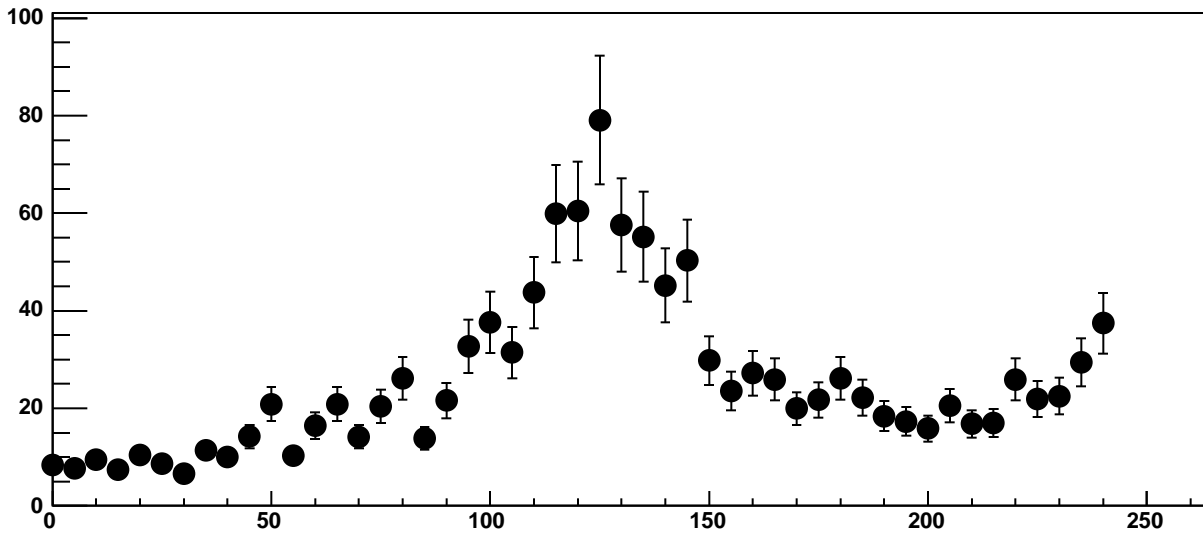
Chip 1, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold



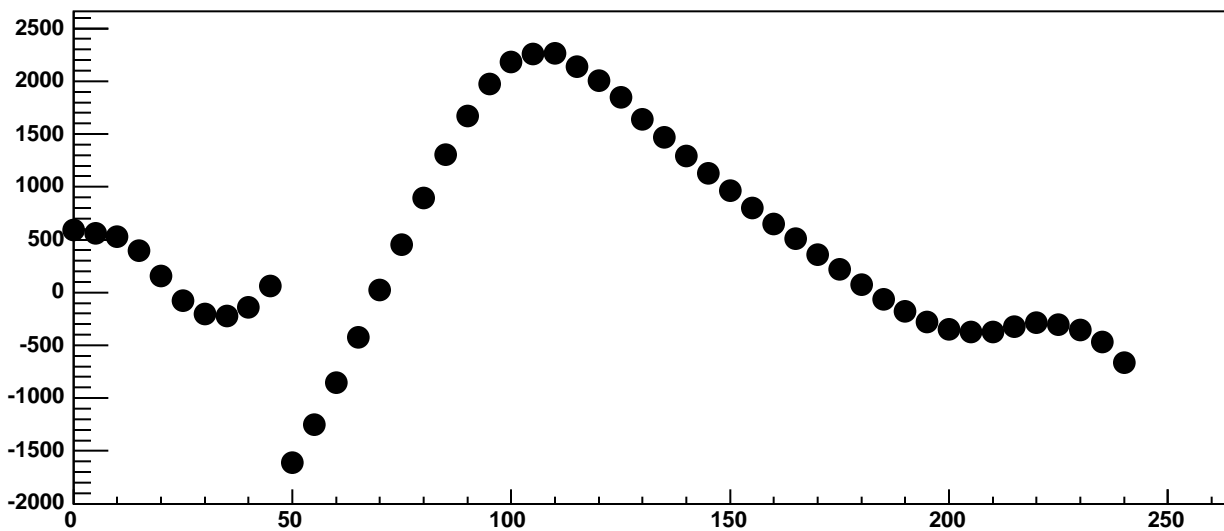
Chip 1, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold



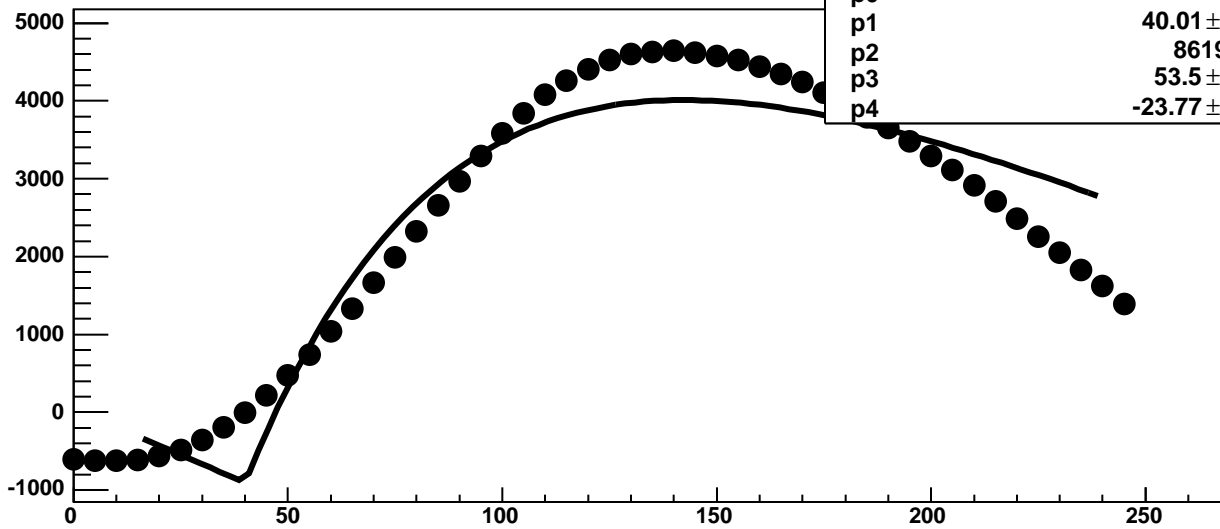
Chip 1, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

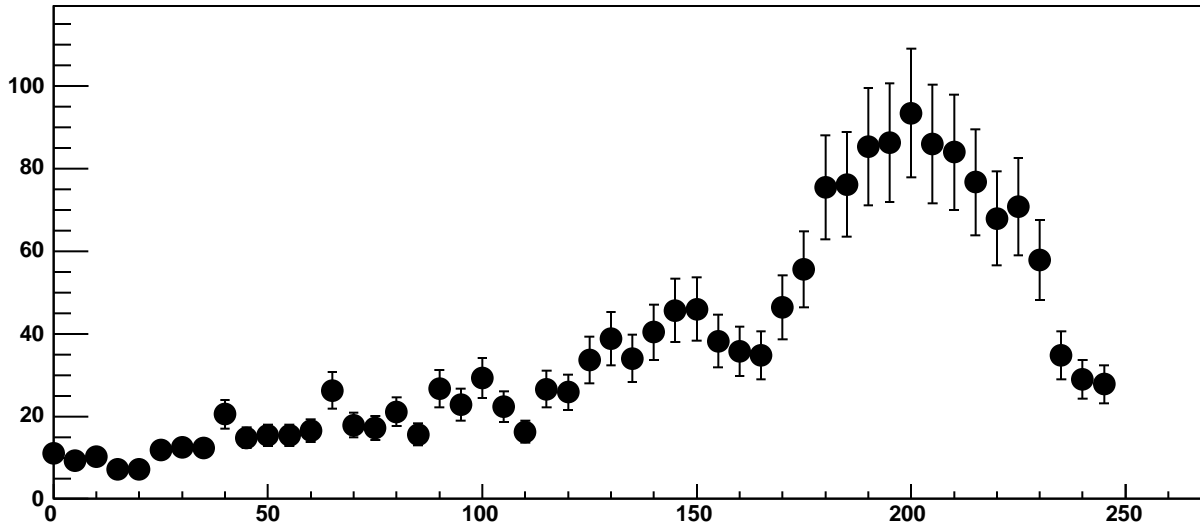


Chip 1, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

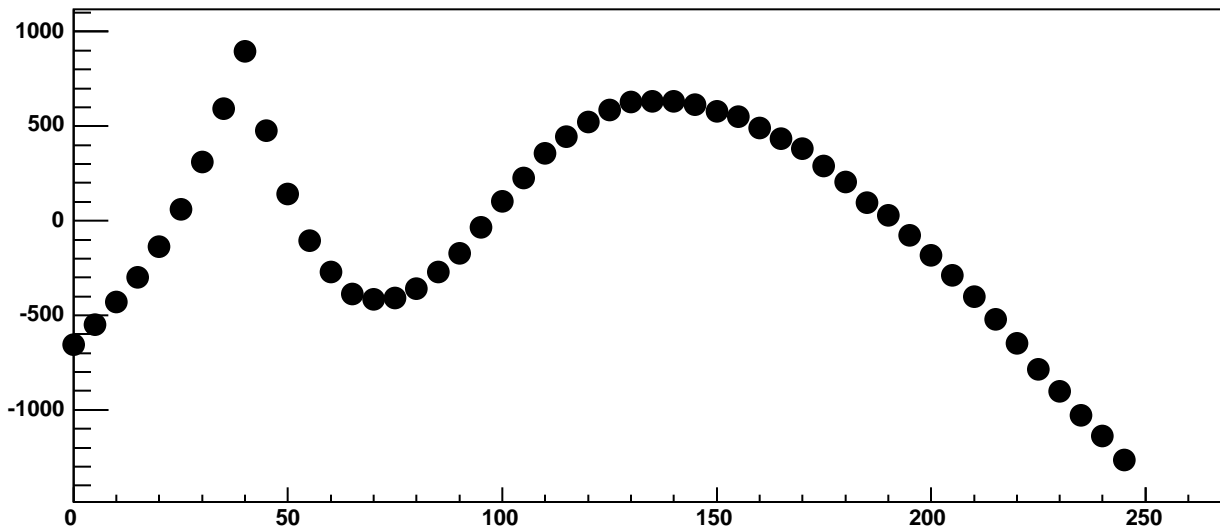


χ^2 / ndf	3.192e+05 / 41
p0	-902.9 ± 1.344
p1	40.01 ± 0.02089
p2	8619 ± 14.62
p3	53.5 ± 0.09374
p4	-23.77 ± 0.06925

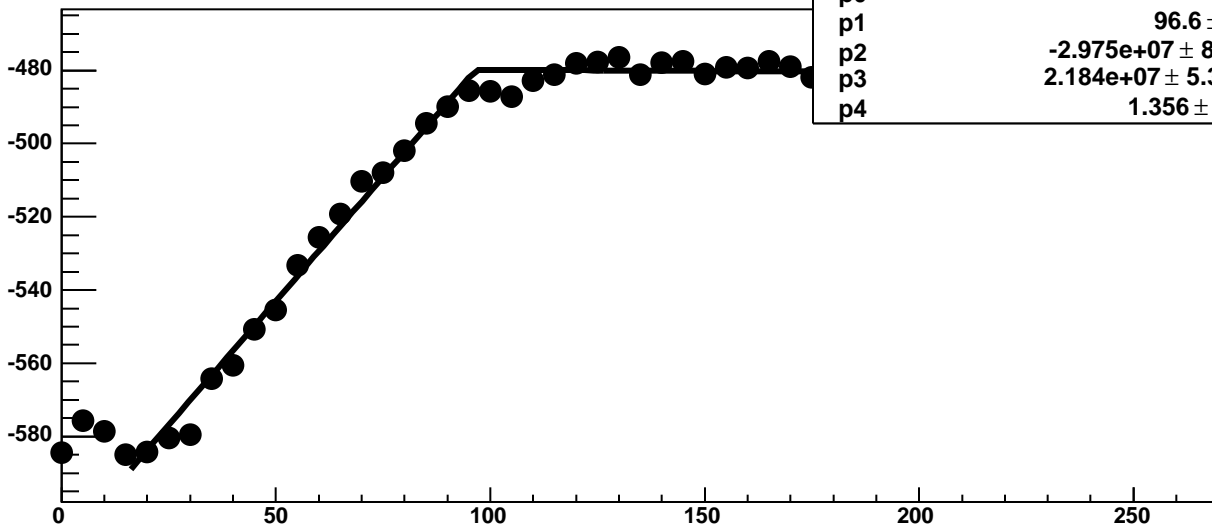
Chip 1, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

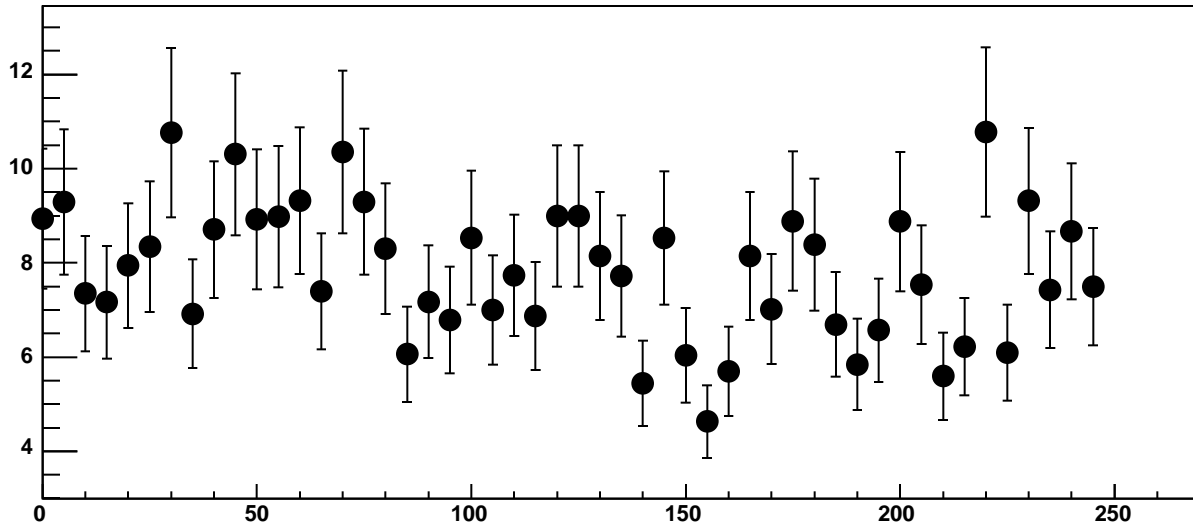


Chip 1, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold

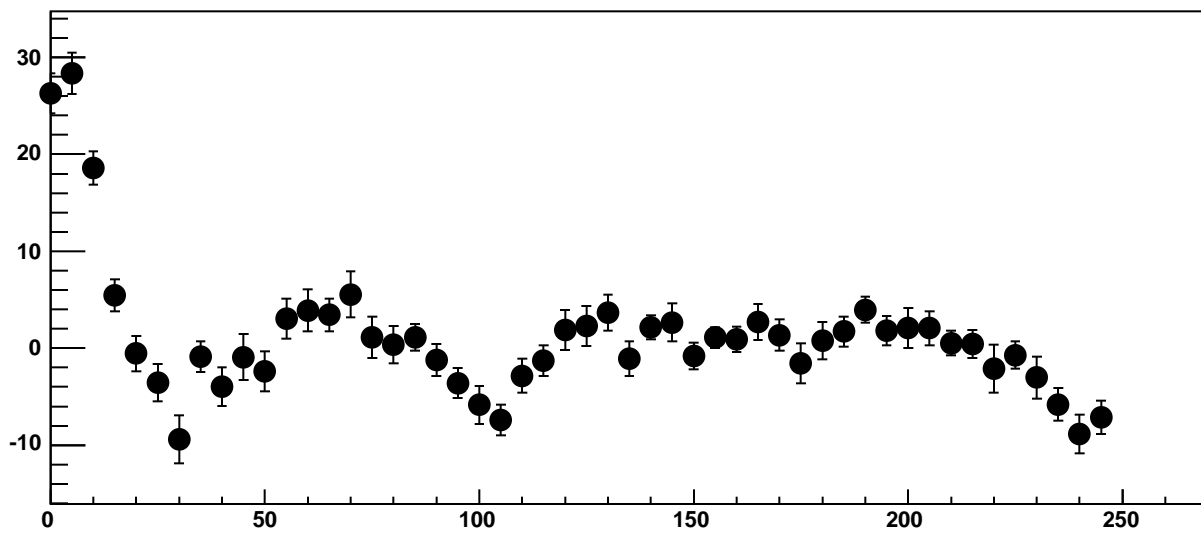


χ^2 / ndf	155 / 41
p0	-479.8 ± 0.6347
p1	96.6 ± 0.7556
p2	$-2.975\text{e}+07 \pm 8.38\text{e}+05$
p3	$2.184\text{e}+07 \pm 5.342\text{e}+05$
p4	1.356 ± 0.01705

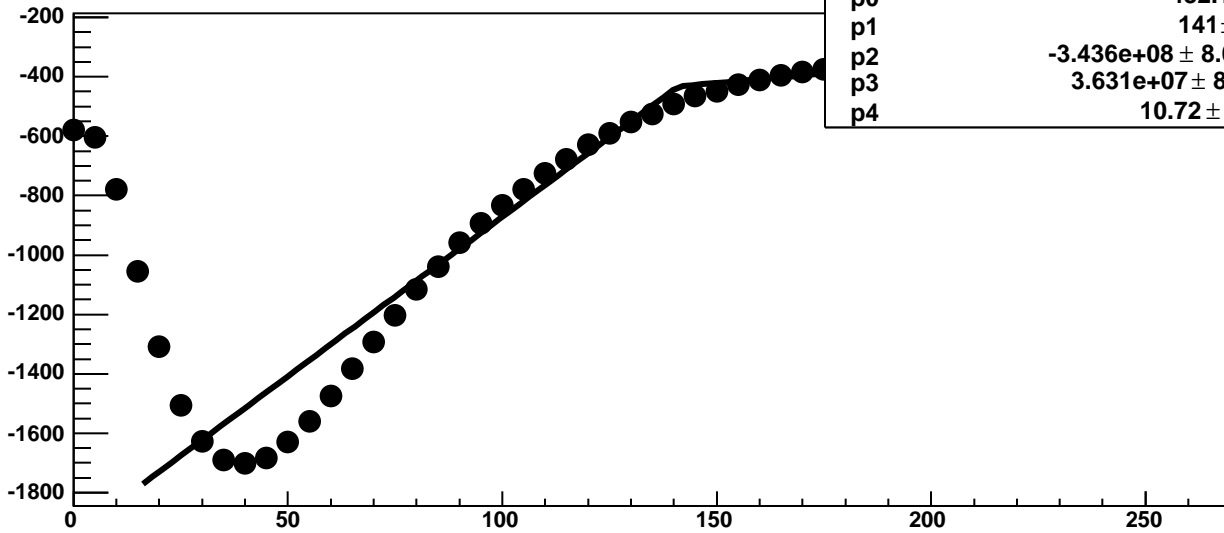
Chip 1, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold

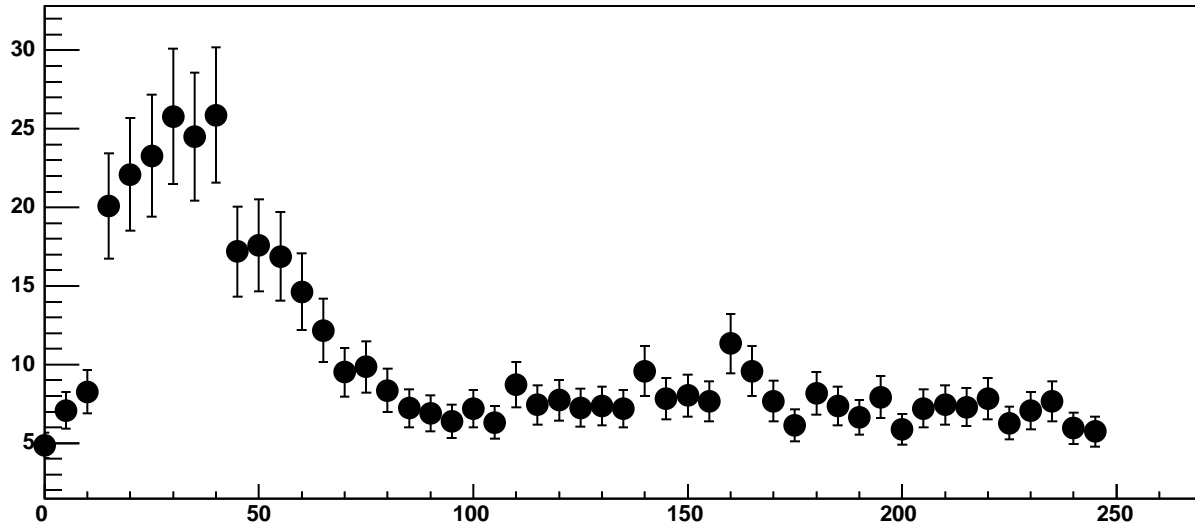


Chip 1, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

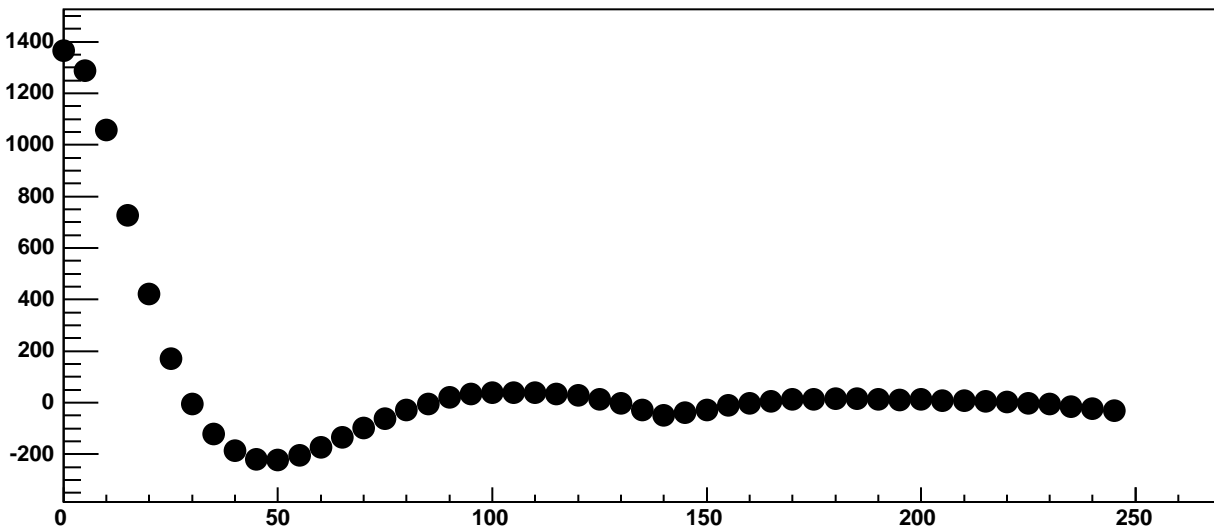


χ^2 / ndf	5.723e+04 / 41
p0	-432.1 ± 0.938
p1	141 ± 0.1198
p2	-3.436e+08 ± 8.018e+06
p3	3.631e+07 ± 8.44e+05
p4	10.72 ± 0.01566

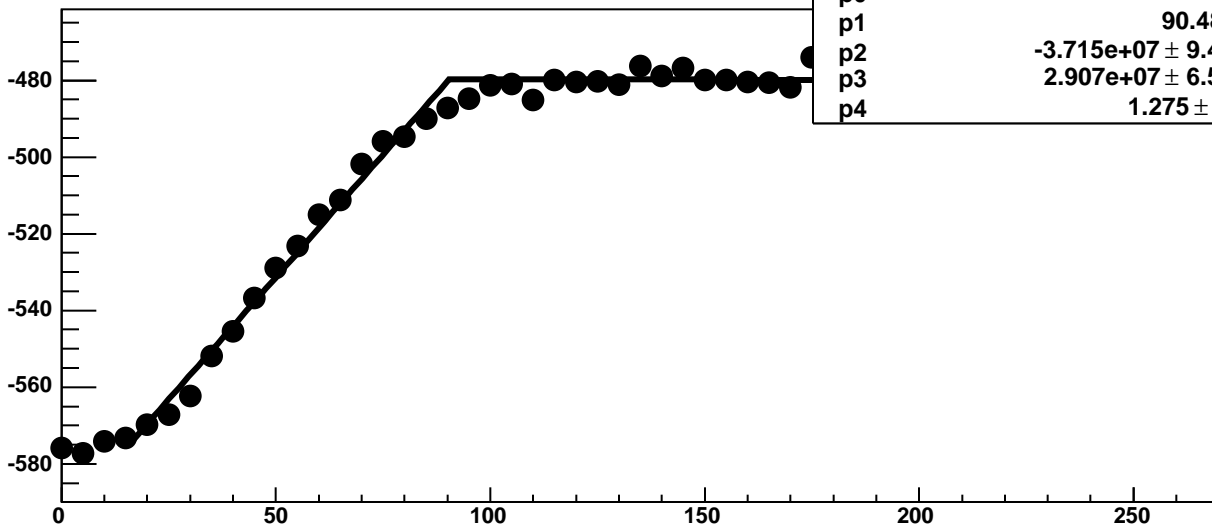
Chip 1, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold



Chip 1, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold



χ^2 / ndf

134 / 41

p0 -479.7 ± 0.6801

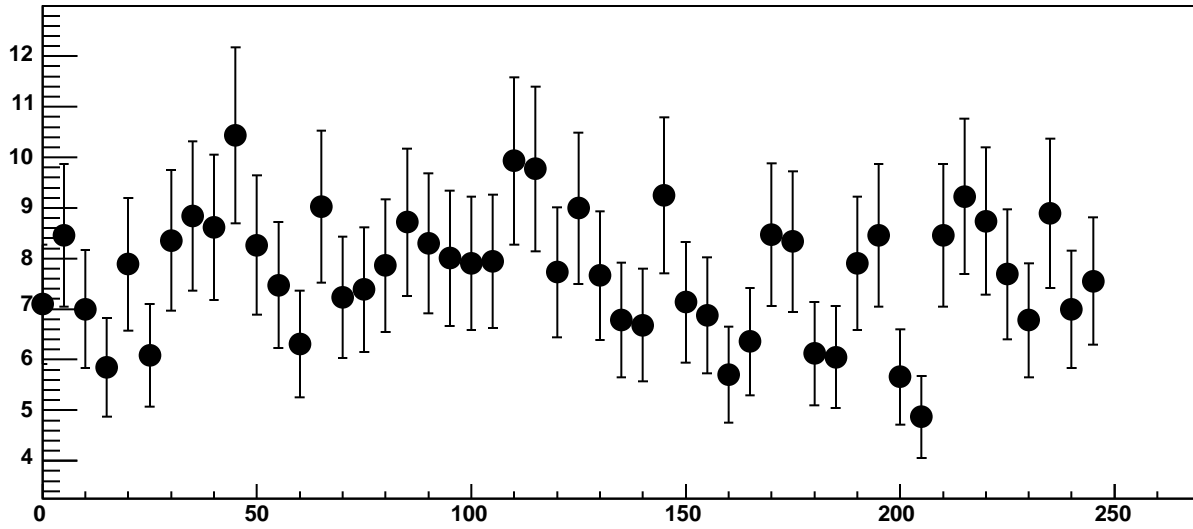
p1 90.48 ± 0.85

p2 $-3.715\text{e}+07 \pm 9.459\text{e}+05$

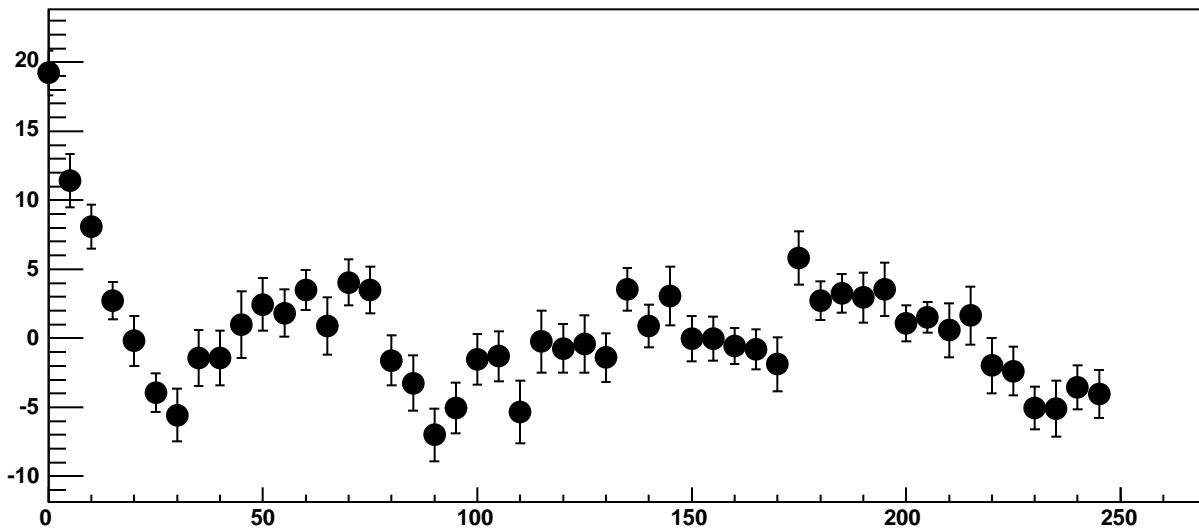
p3 $2.907\text{e}+07 \pm 6.537\text{e}+05$

p4 1.275 ± 0.01816

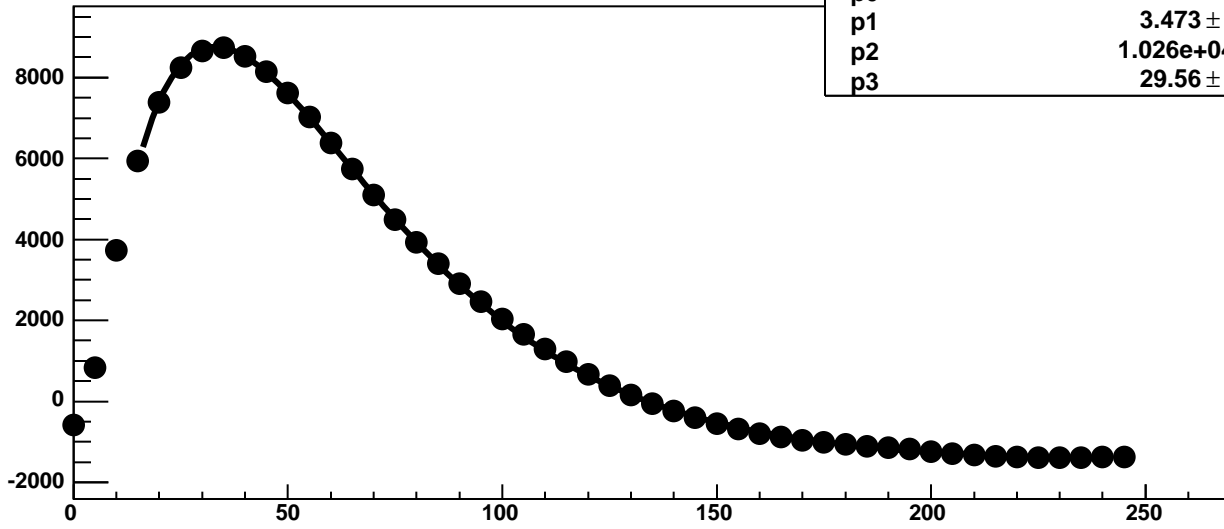
Chip 1, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

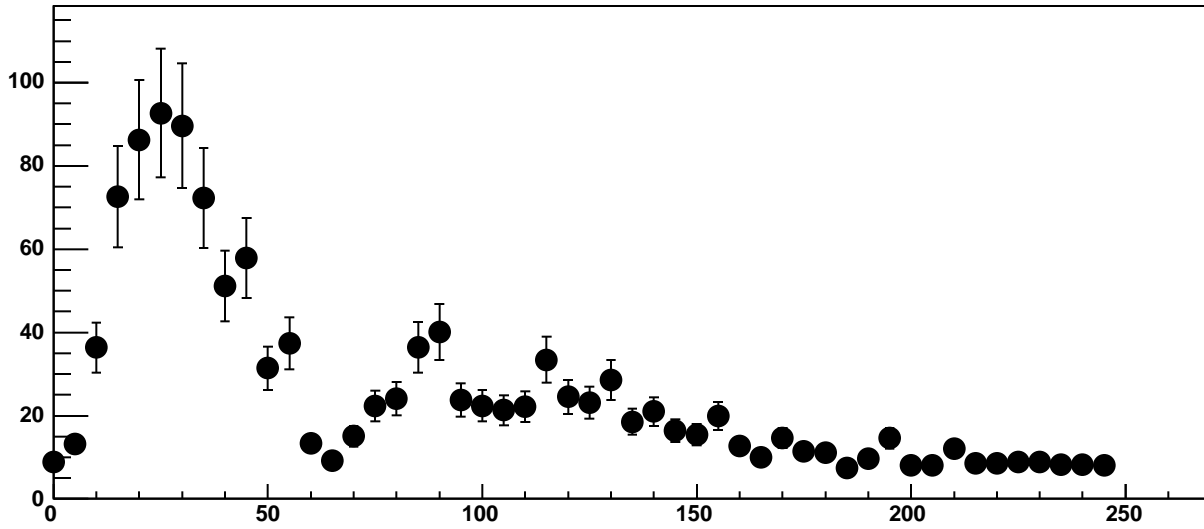


Chip 1, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold

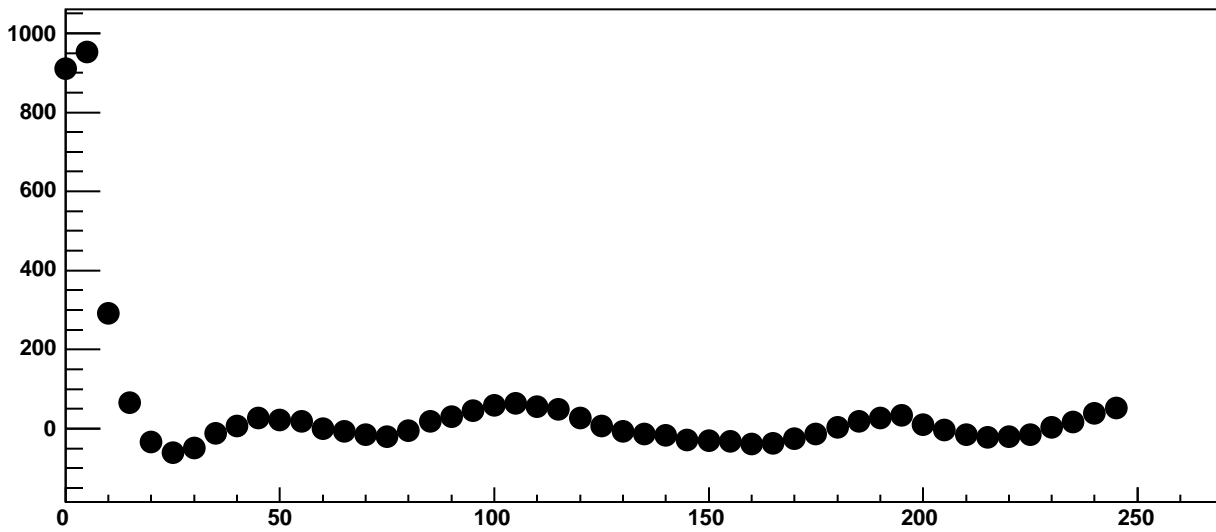


χ^2 / ndf	2597 / 42
p0	-1493 ± 0.7907
p1	3.473 ± 0.03259
p2	1.026e+04 ± 4.14
p3	29.56 ± 0.01434

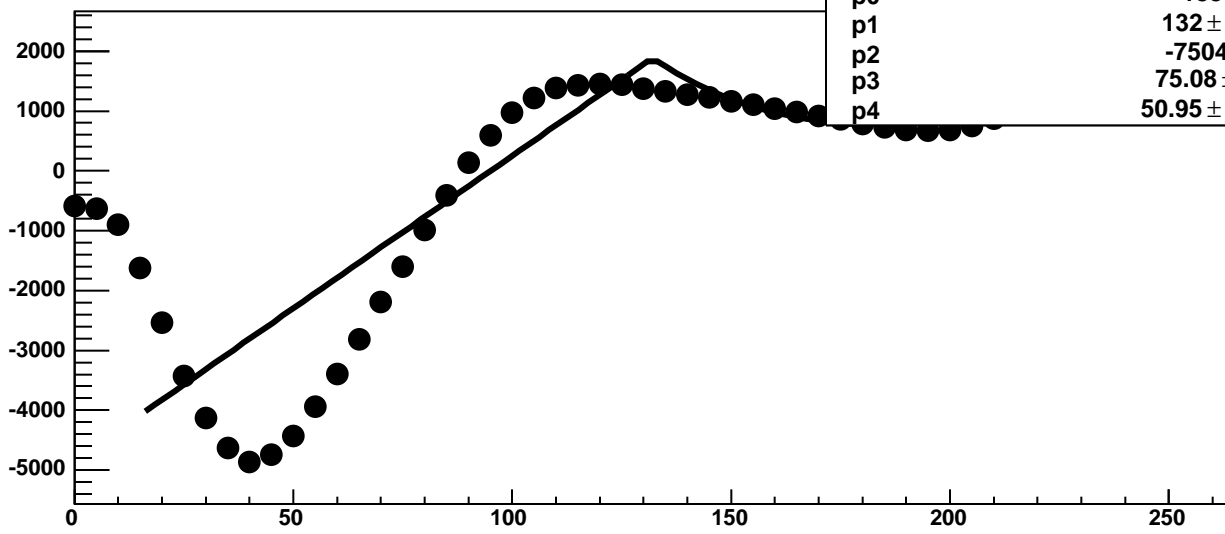
Chip 1, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold

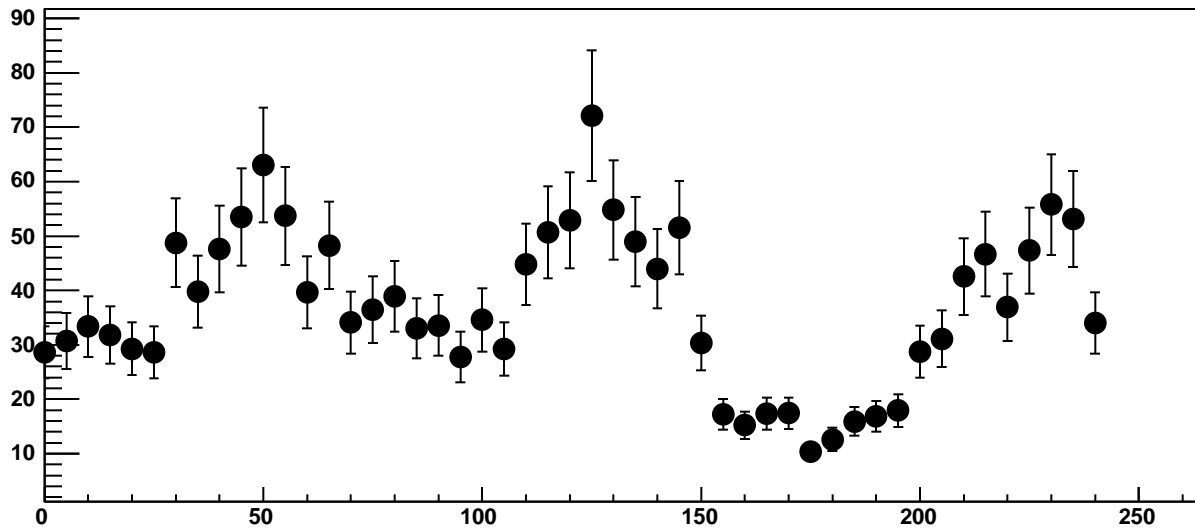


Chip 1, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

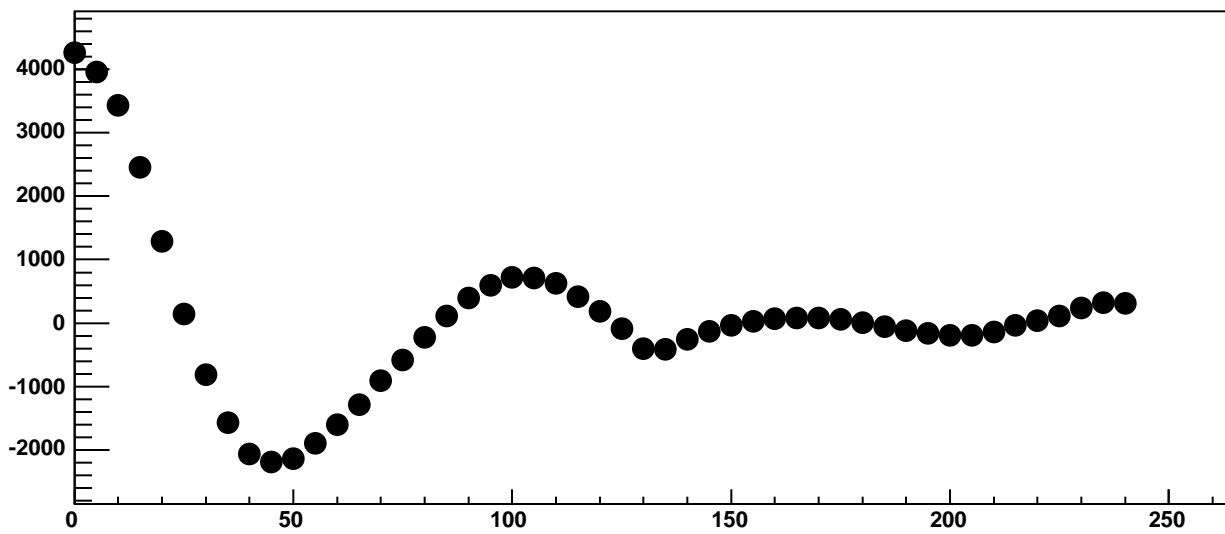


χ^2 / ndf	4.101e+05 / 41
p0	1883 ± 3.72
p1	132 ± 0.07417
p2	-7504 ± 25.25
p3	75.08 ± 0.3918
p4	50.95 ± 0.05314

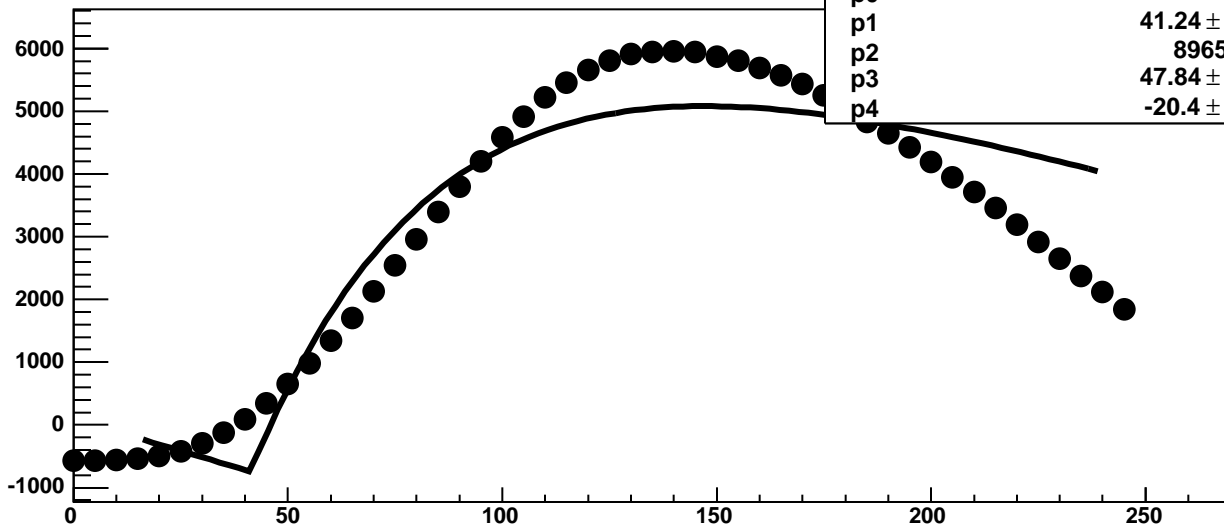
Chip 1, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold

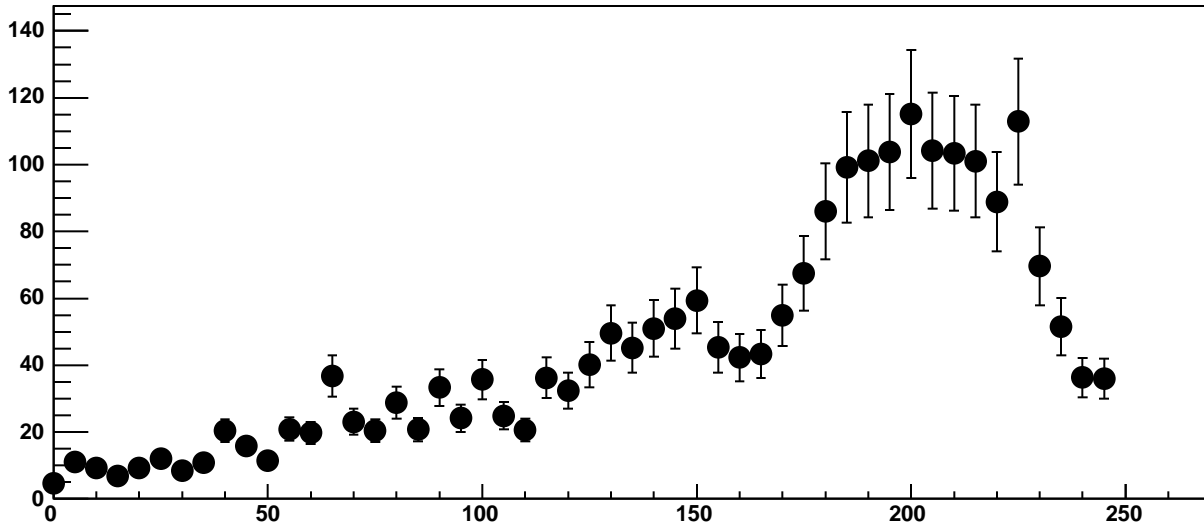


Chip 1, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold

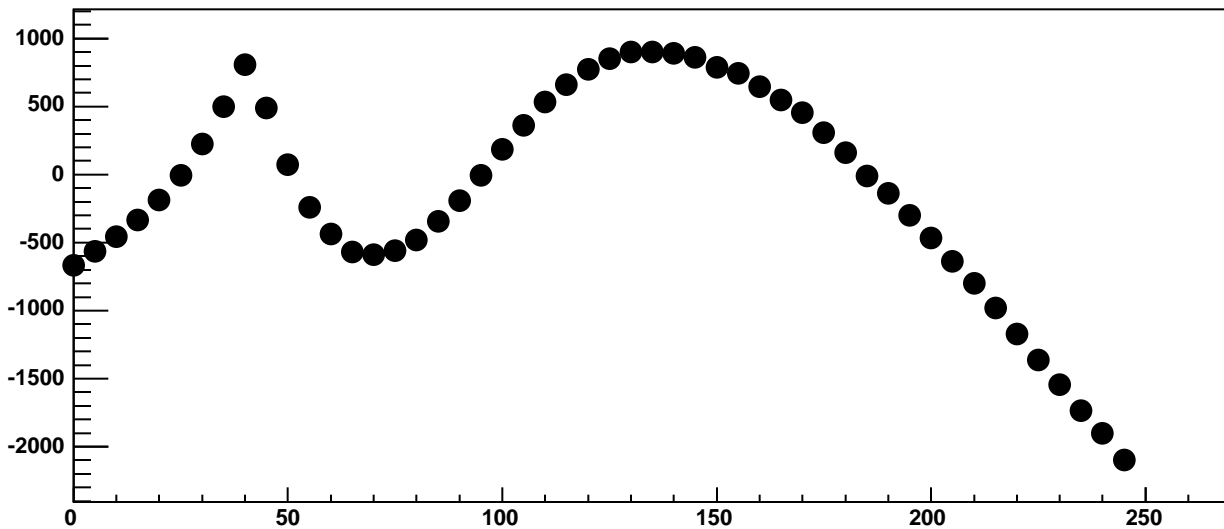


χ^2 / ndf	3.91e+05 / 41
p0	-743.9 ± 1.369
p1	41.24 ± 0.01703
p2	8965 ± 13.44
p3	47.84 ± 0.07537
p4	-20.4 ± 0.06967

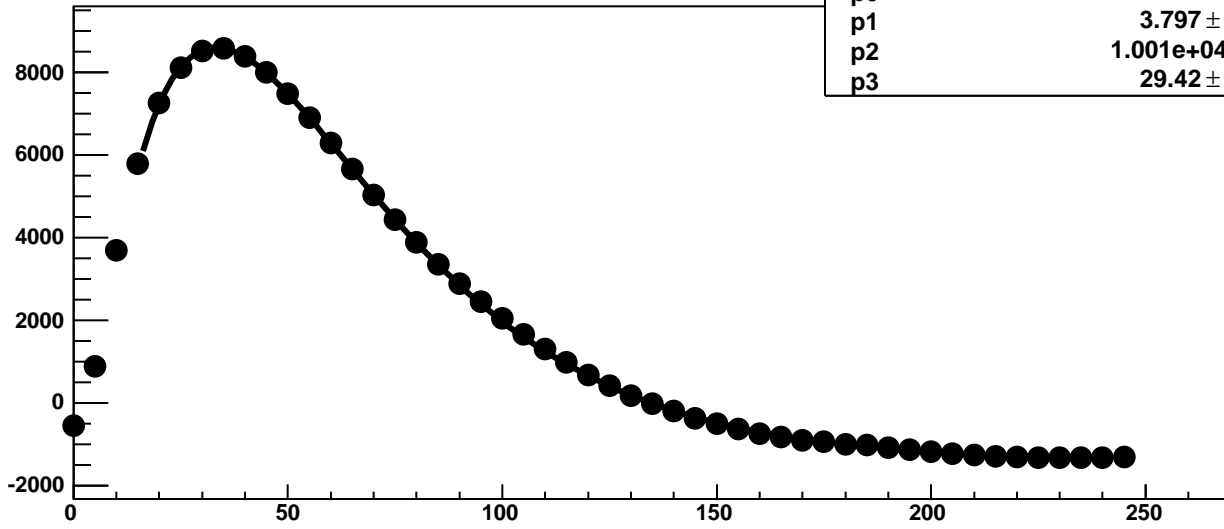
Chip 1, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



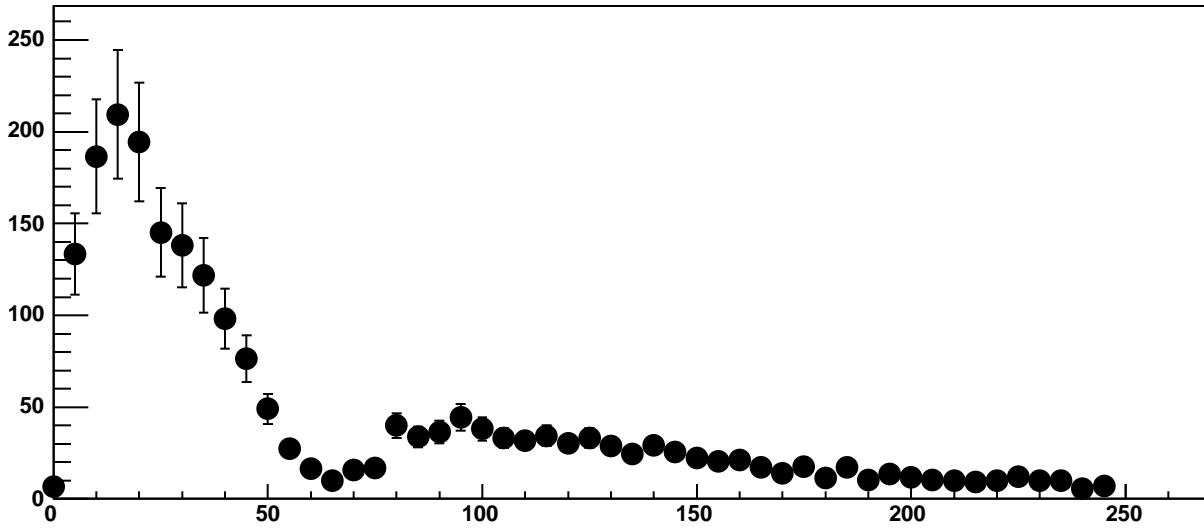
Chip 1, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold



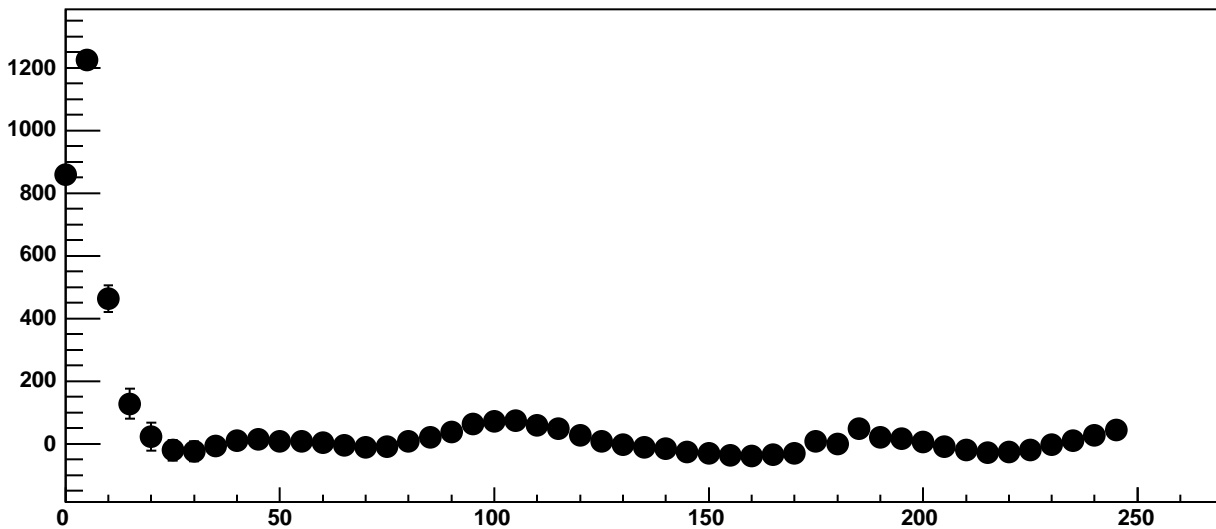
Chip 1, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold



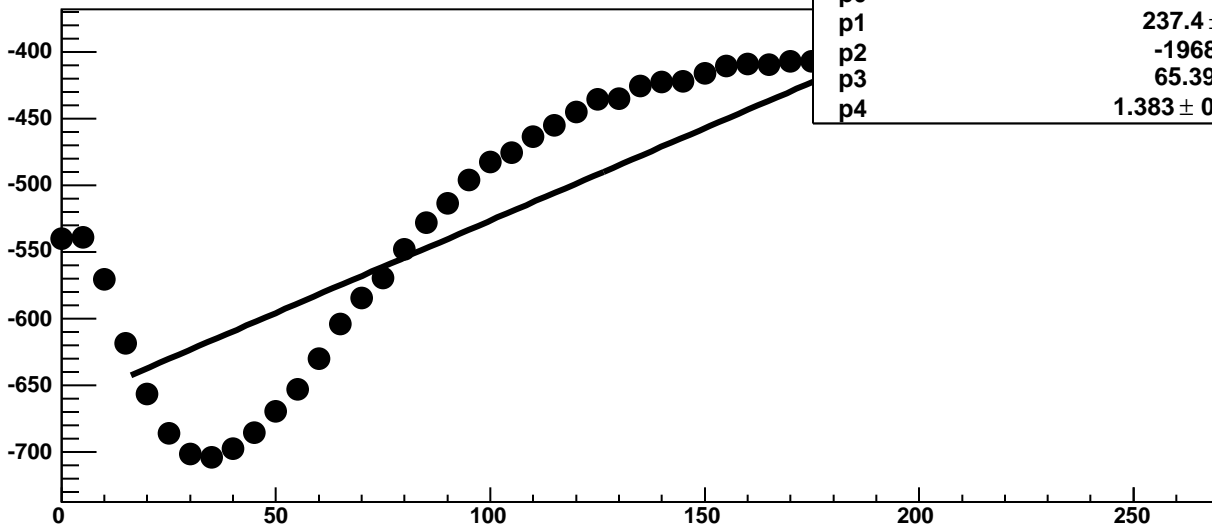
Chip 1, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold

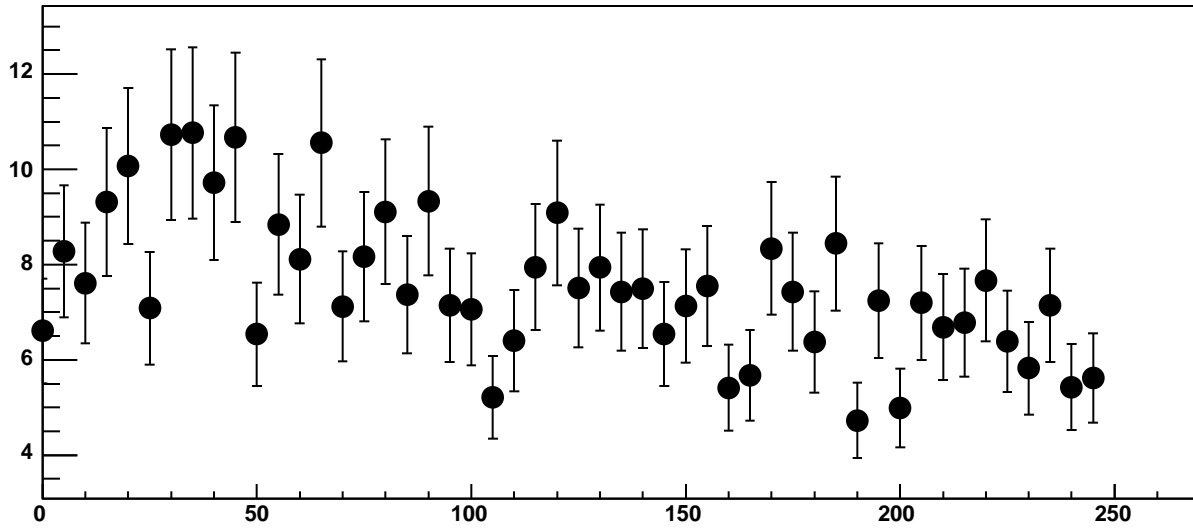


Chip 1, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

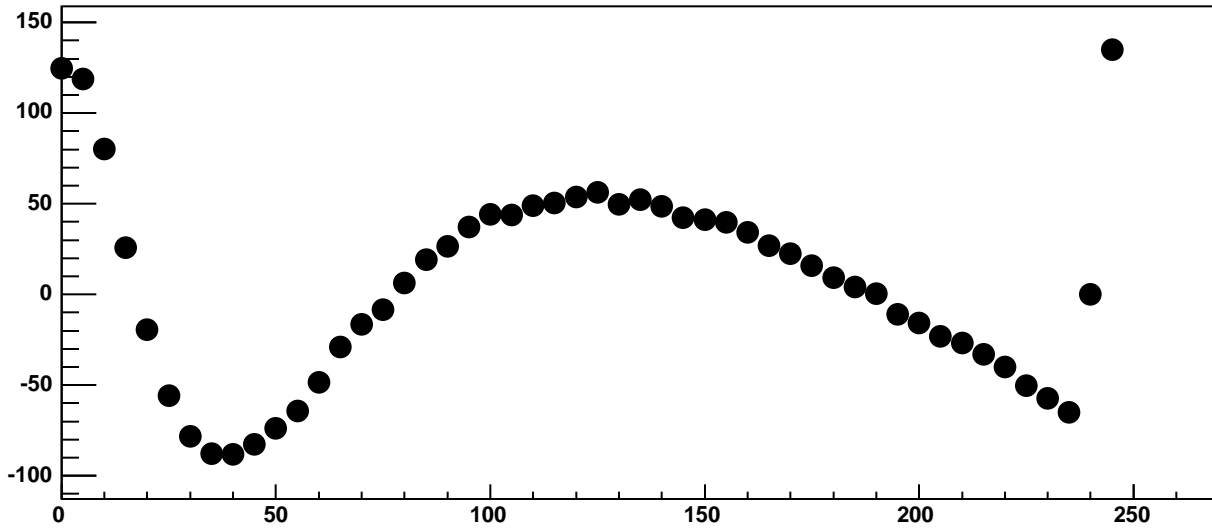


χ^2 / ndf	2.941e+04 / 41
p0	-336.3 \pm 1.003
p1	237.4 \pm 0.6477
p2	-1968 \pm 480.3
p3	65.39 \pm 13.33
p4	1.383 \pm 0.003975

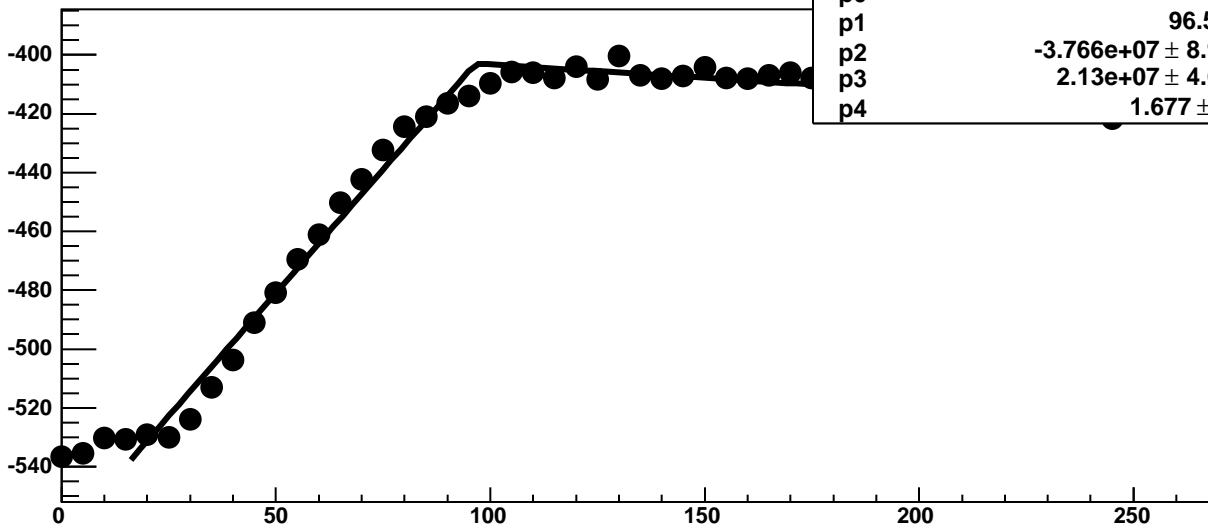
Chip 1, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

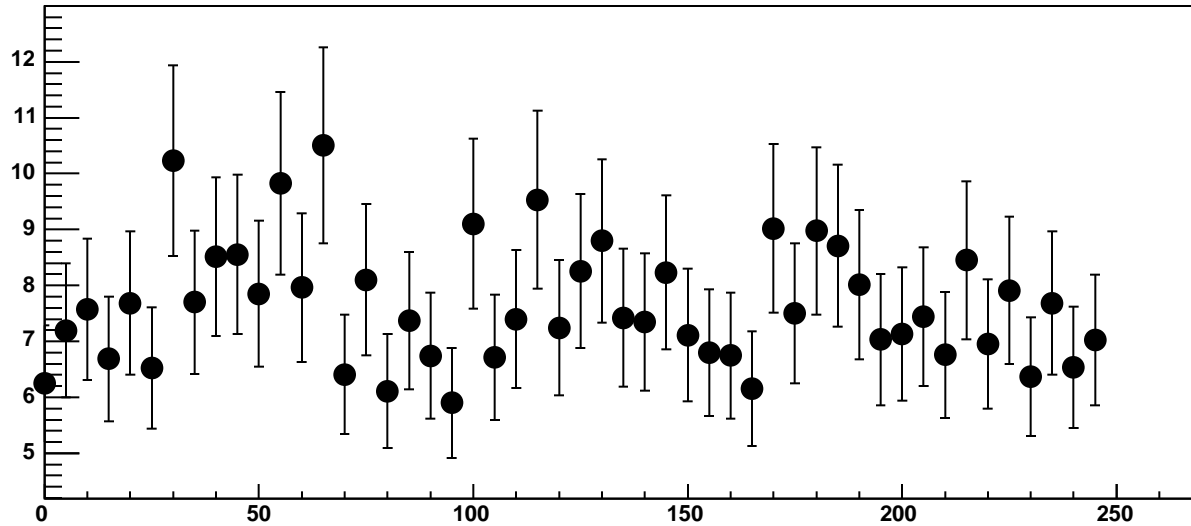


Chip 1, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

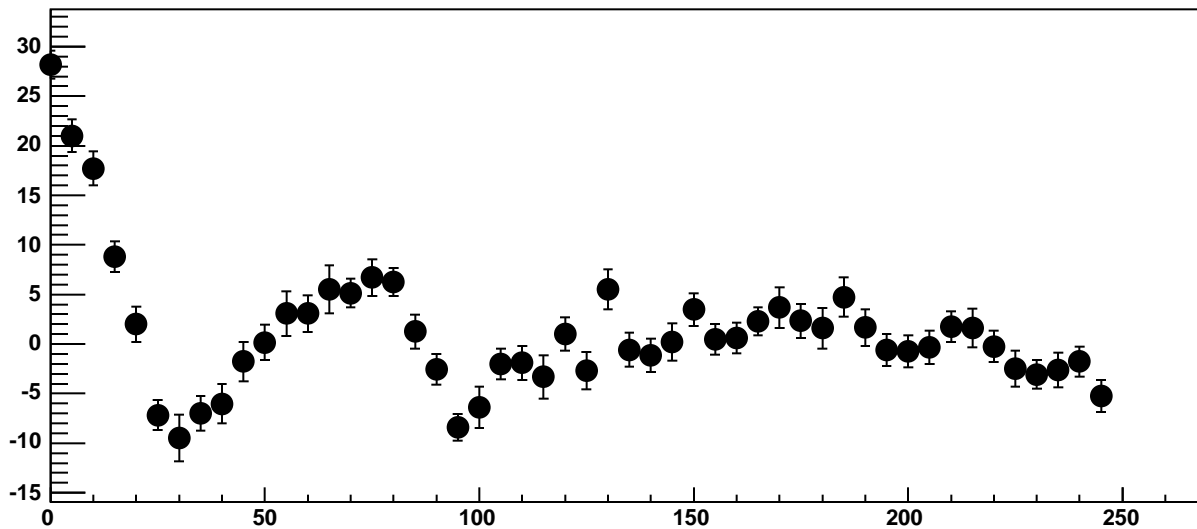


χ^2 / ndf	253.4 / 41
p0	-402.9 ± 0.6282
p1	96.5 ± 0.565
p2	$-3.766e+07 \pm 8.986e+05$
p3	$2.13e+07 \pm 4.664e+05$
p4	1.677 ± 0.01571

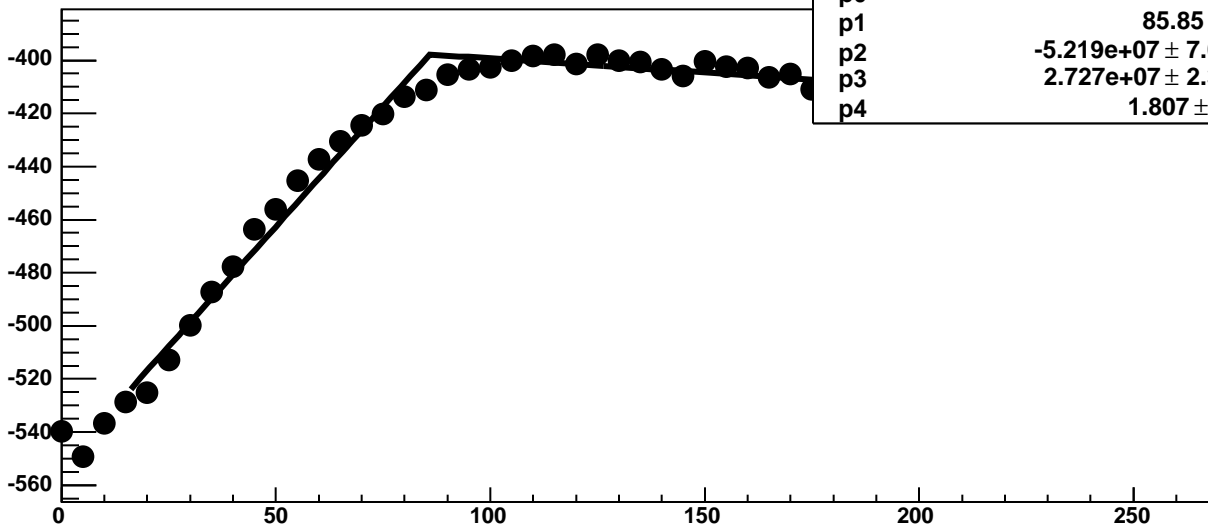
Chip 1, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

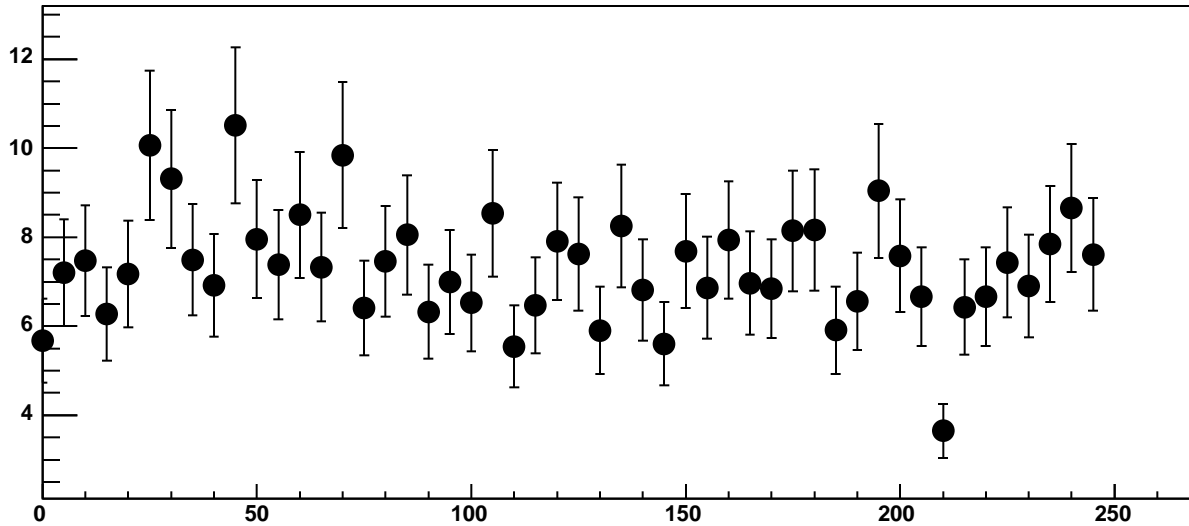


Chip 1, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold

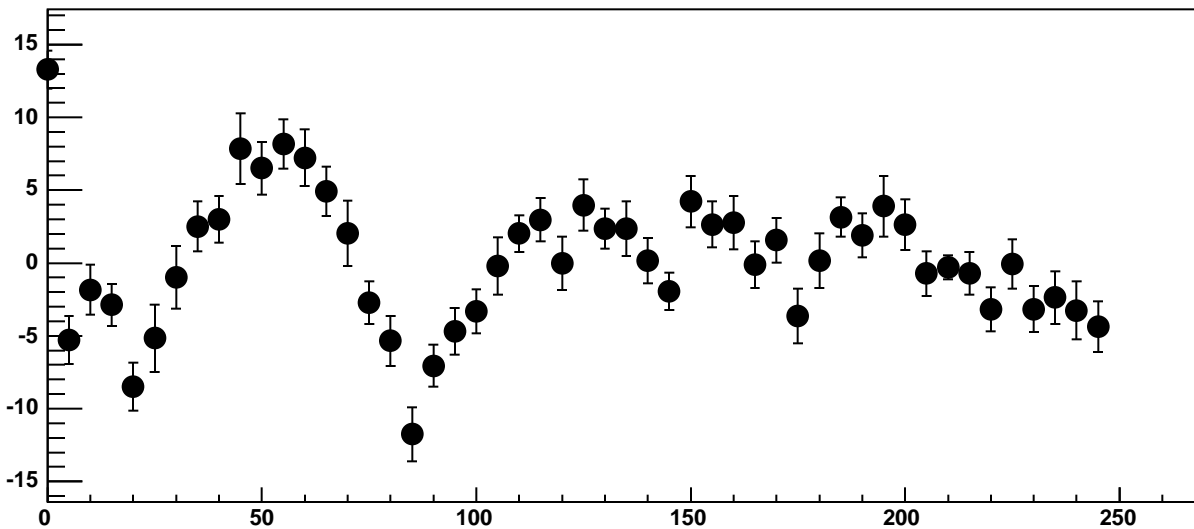


χ^2 / ndf	262.2 / 41
p0	-397.8 ± 0.5388
p1	85.85 ± 0.5423
p2	$-5.219\text{e}+07 \pm 7.096\text{e}+05$
p3	$2.727\text{e}+07 \pm 2.374\text{e}+05$
p4	1.807 ± 0.02042

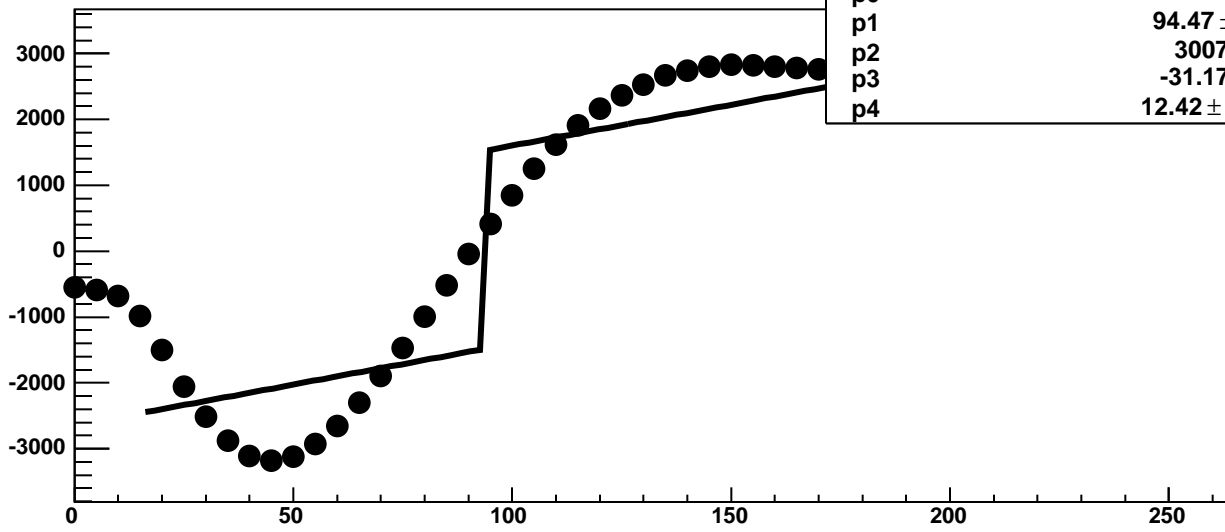
Chip 1, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold

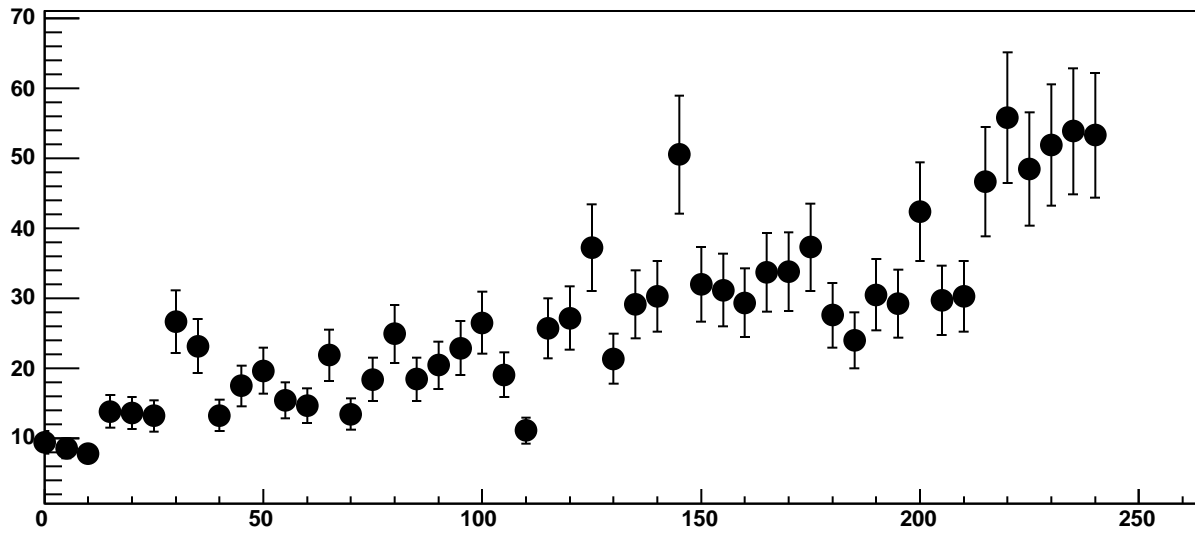


Chip 1, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

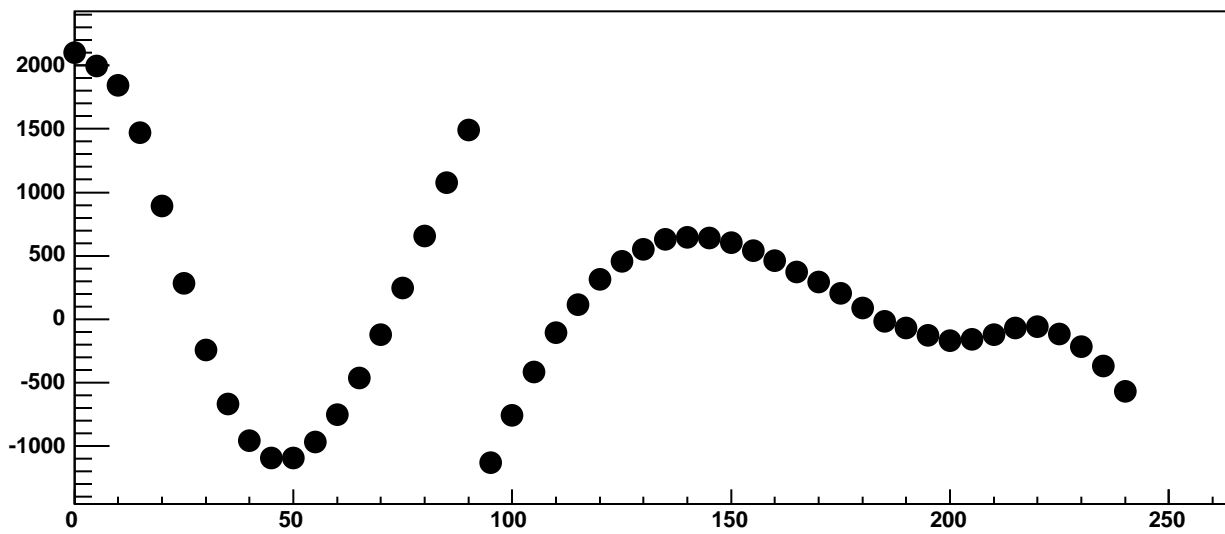


χ^2 / ndf	1.004e+06 / 41
p0	-1473 ± 1.217
p1	94.47 ± 0.6044
p2	3007 ± 4.796
p3	-31.17 ± 1.879
p4	12.42 ± 0.07394

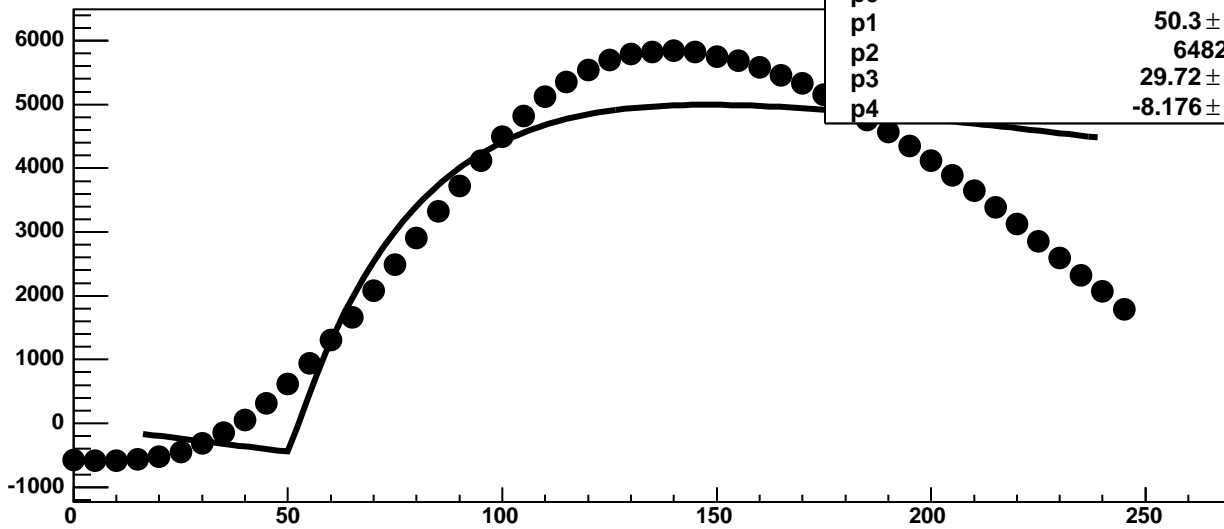
Chip 1, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



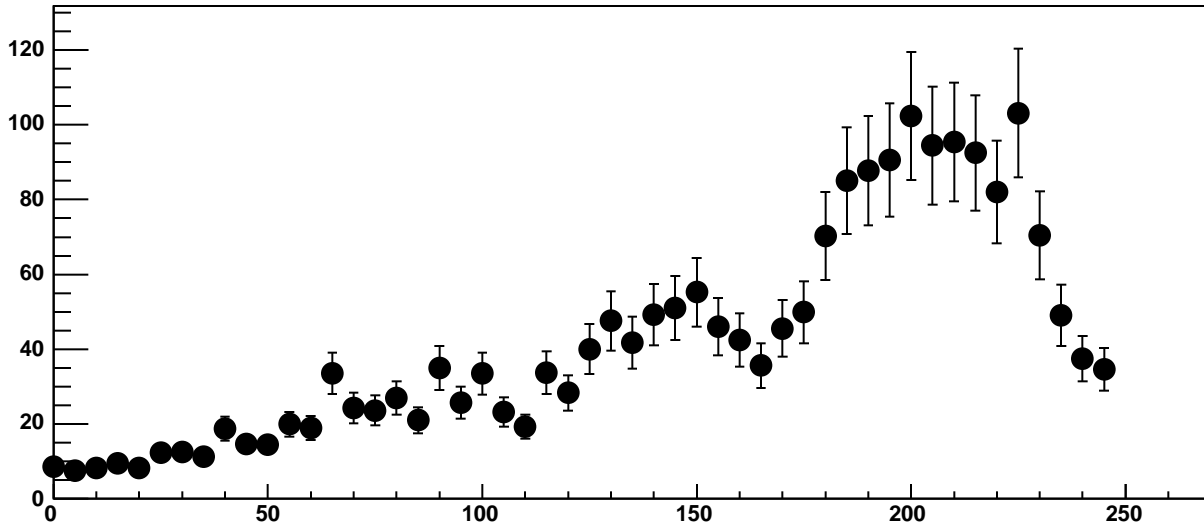
Chip 1, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold



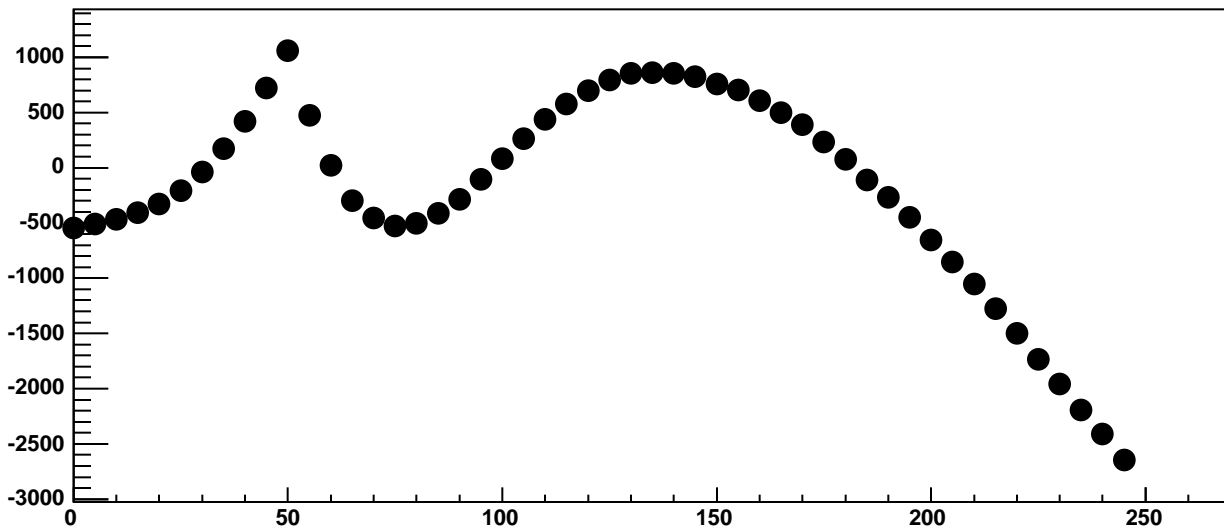
Chip 1, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold



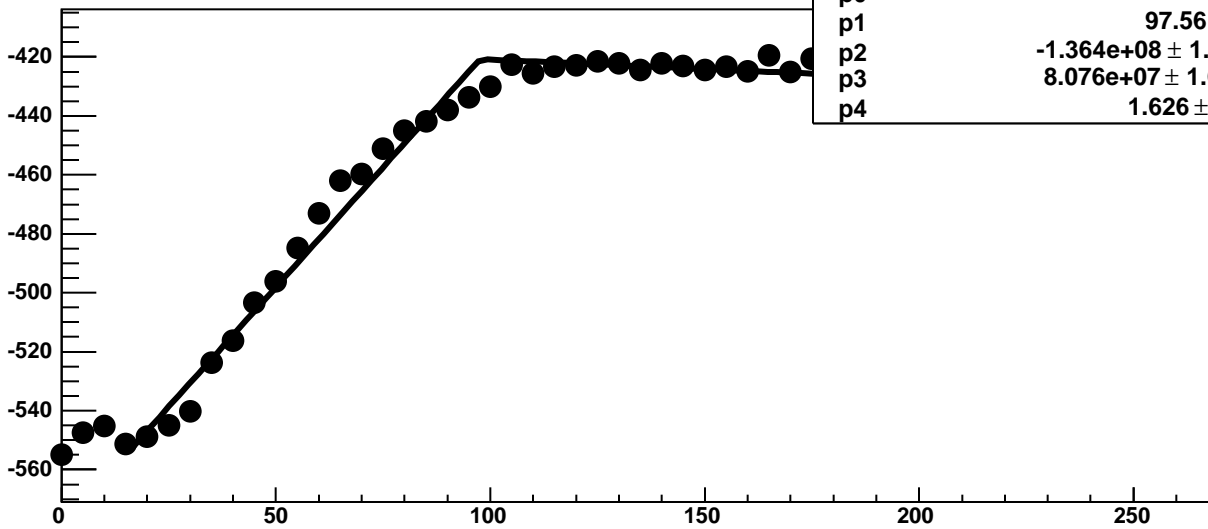
Chip 1, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

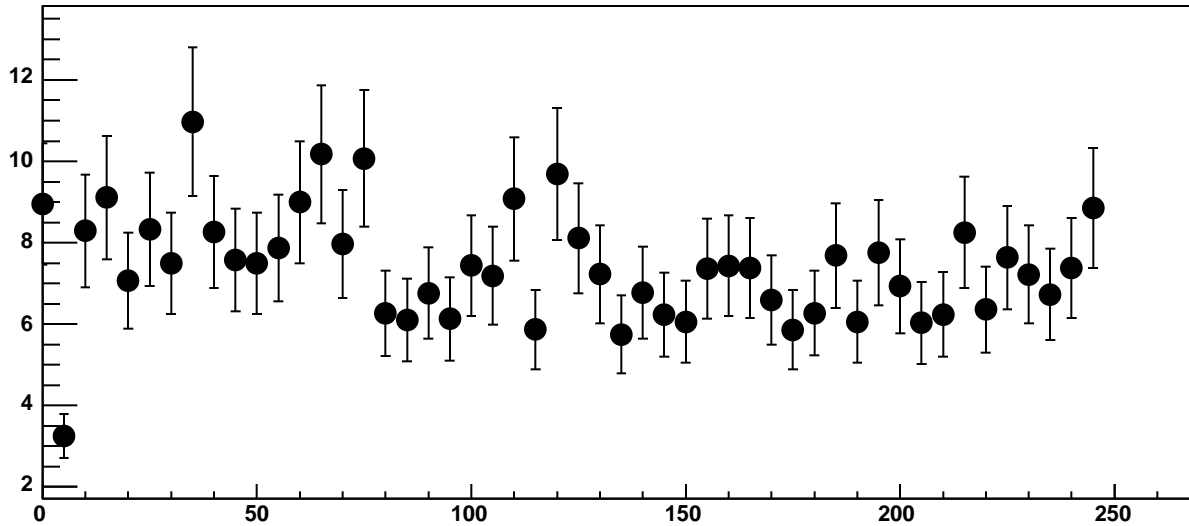


Chip 1, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold

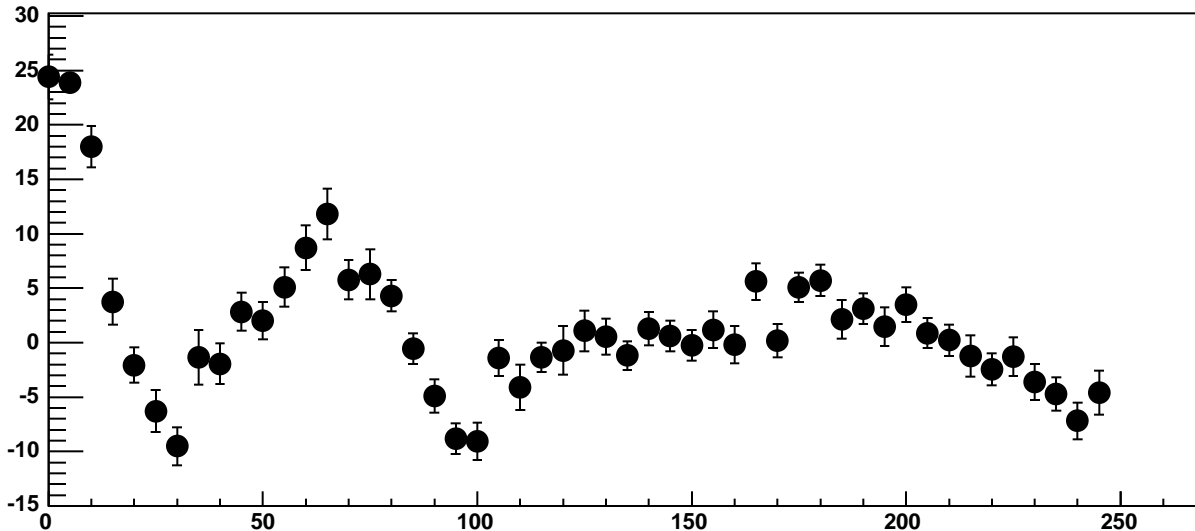


χ^2 / ndf	304.7 / 41
p0	-420.8 ± 0.5869
p1	97.56 ± 0.5811
p2	$-1.364\text{e}+08 \pm 1.484\text{e}+06$
p3	$8.076\text{e}+07 \pm 1.637\text{e}+05$
p4	1.626 ± 0.01662

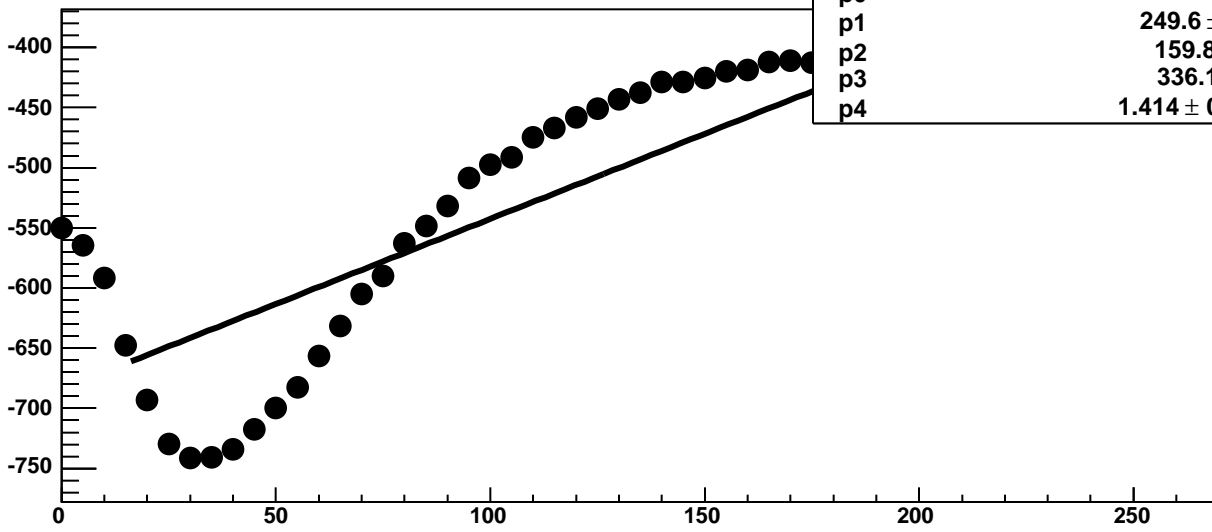
Chip 1, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold

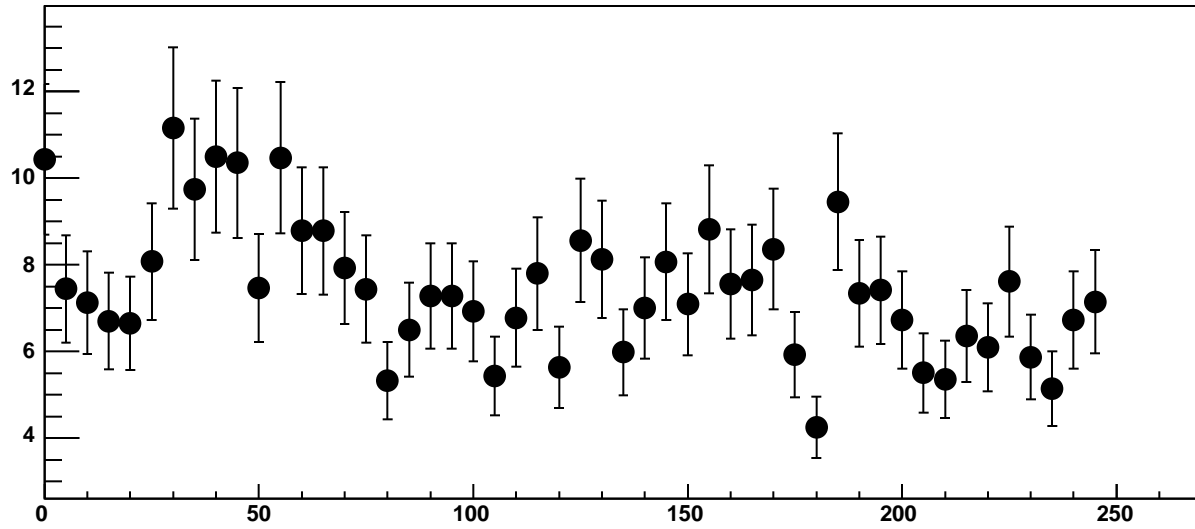


Chip 1, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

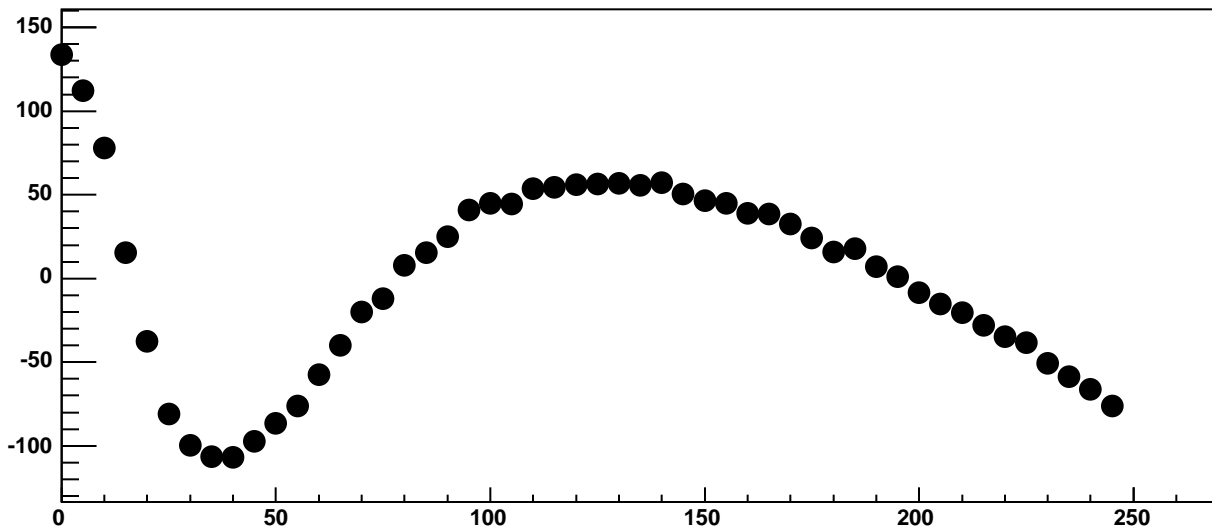


χ^2 / ndf	3.831e+04 / 41
p0	-331 ± 0.2342
p1	249.6 ± 0.1656
p2	159.8 ± 84.85
p3	336.1 ± 1.414
p4	1.414 ± 0.001831

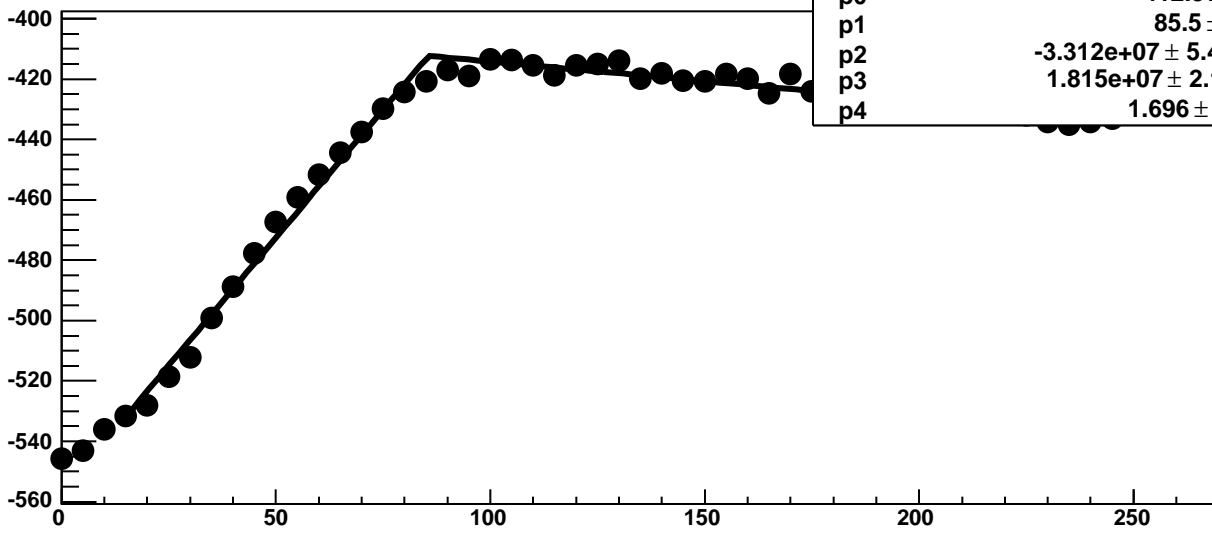
Chip 1, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

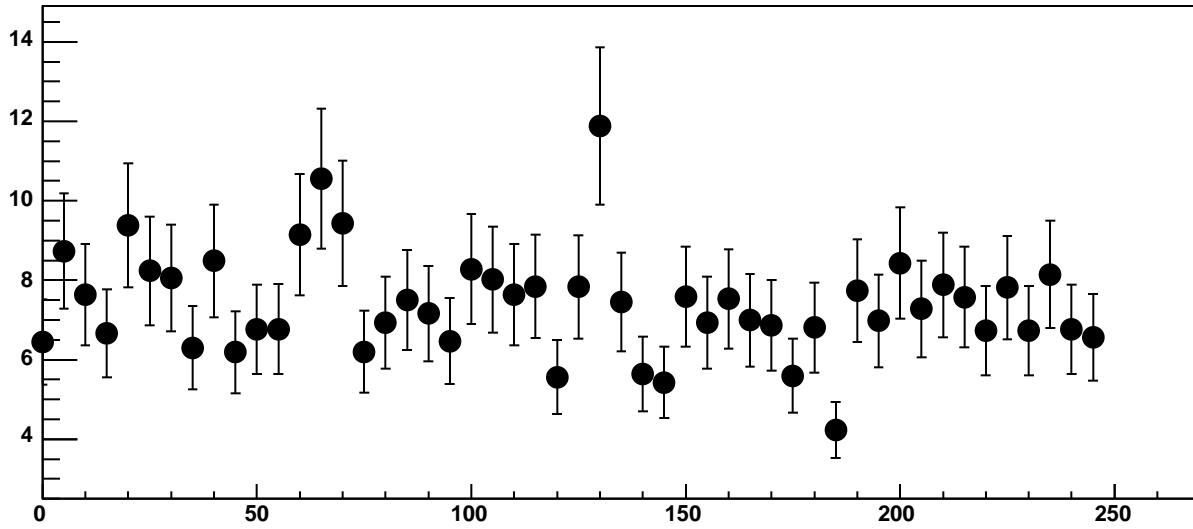


Chip 1, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

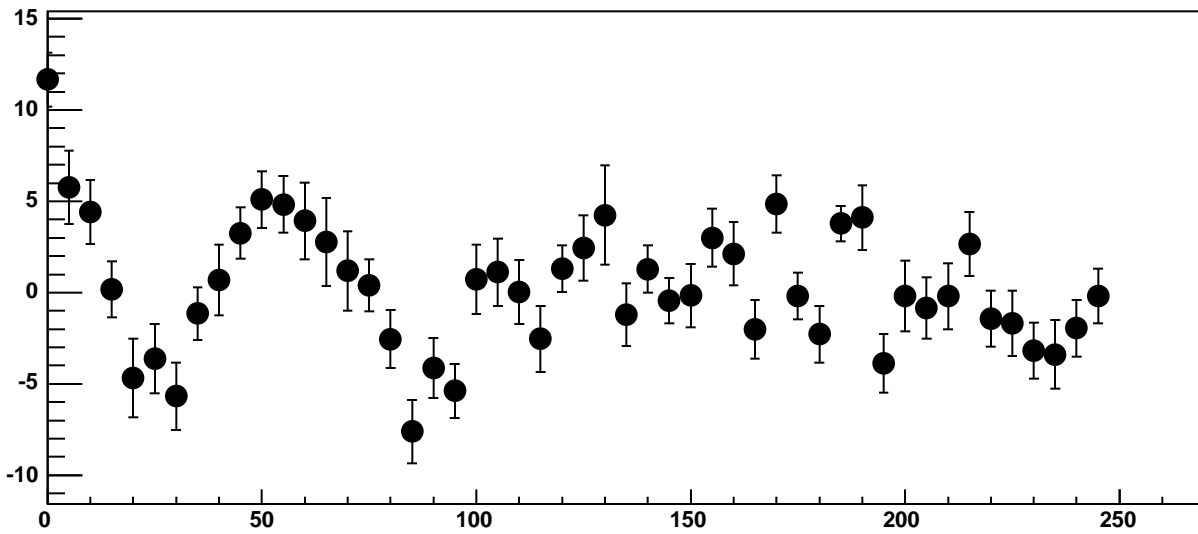


χ^2 / ndf	158.7 / 41
p0	-412.3 ± 0.5632
p1	85.5 ± 0.5686
p2	$-3.312\text{e}+07 \pm 5.448\text{e}+05$
p3	$1.815\text{e}+07 \pm 2.118\text{e}+05$
p4	1.696 ± 0.02042

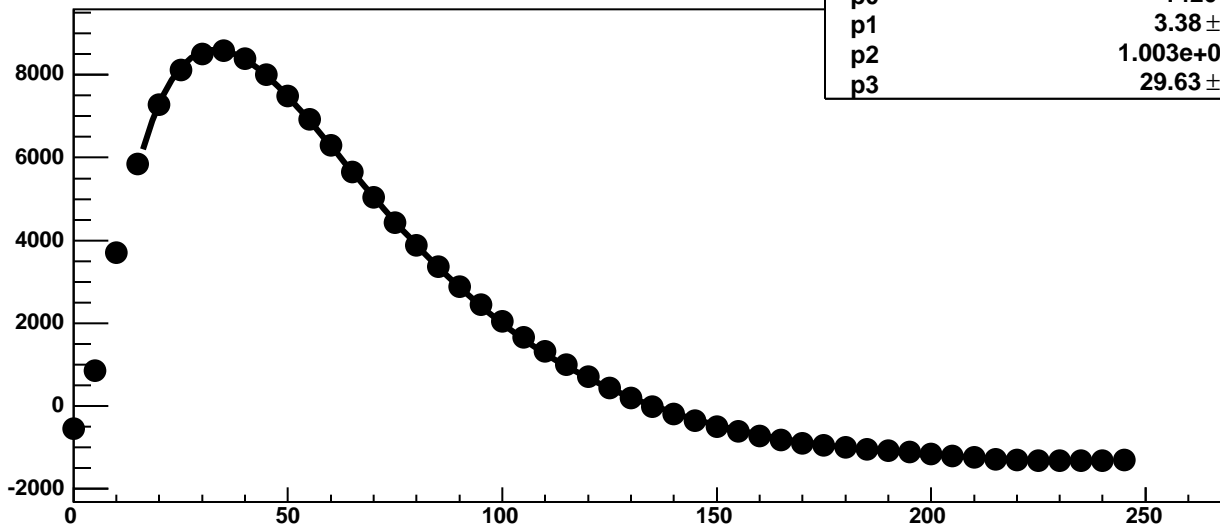
Chip 1, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

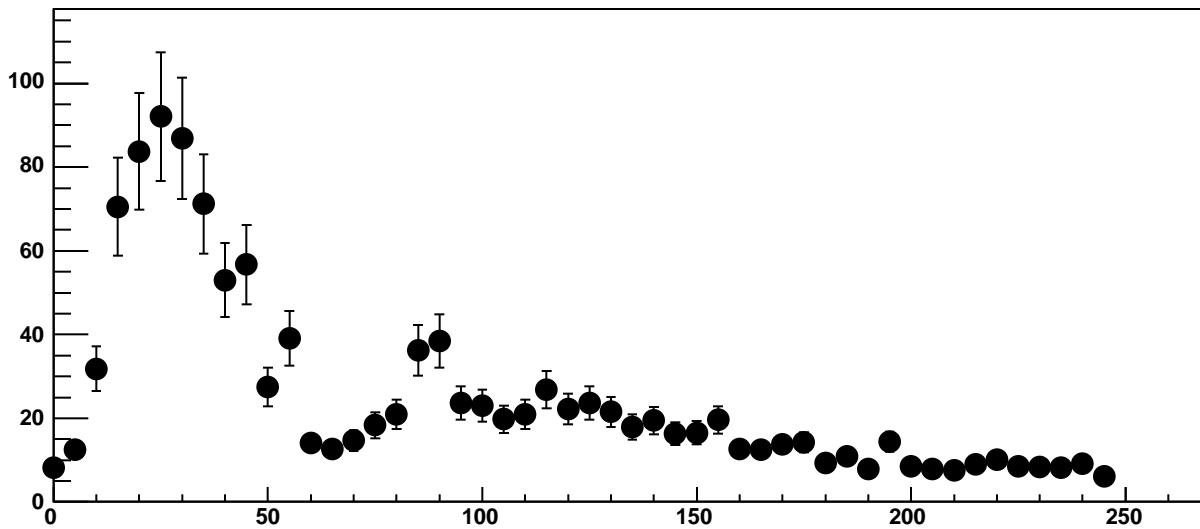


Chip 1, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold

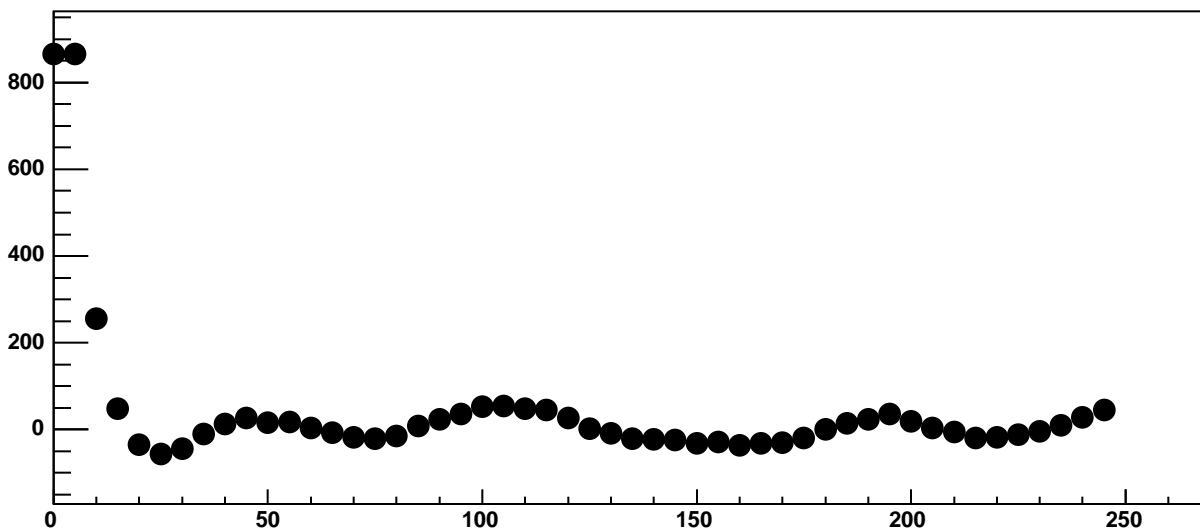


χ^2 / ndf	2017 / 42
p0	-1420 ± 0.7773
p1	3.38 ± 0.03302
p2	1.003e+04 ± 4.09
p3	29.63 ± 0.01442

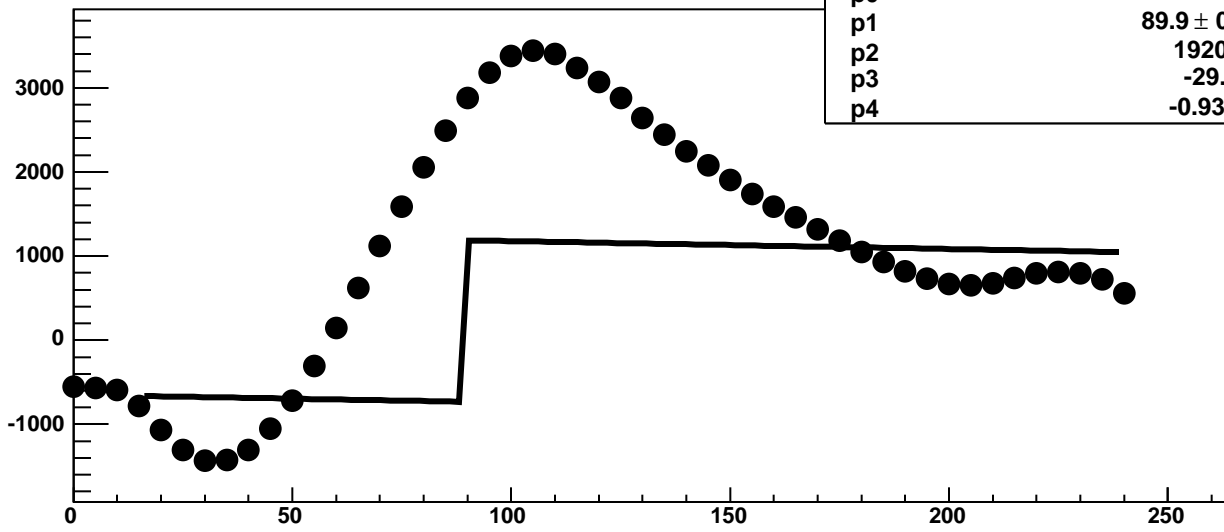
Chip 1, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold

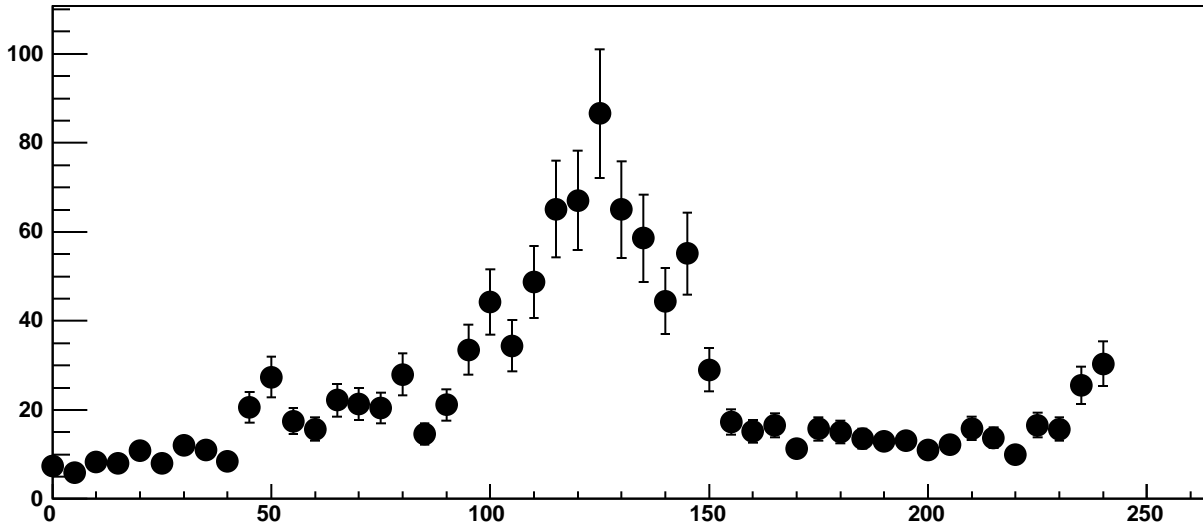


Chip 1, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

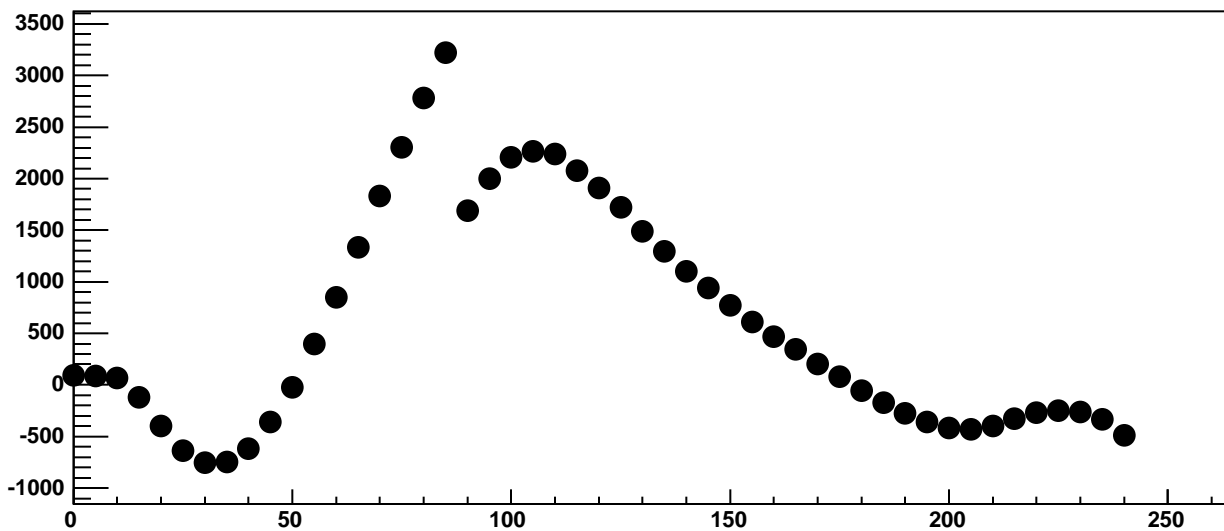


χ^2 / ndf	2.684e+06 / 41
p0	-732.6 ± 1.154
p1	89.9 ± 0.007602
p2	1920 ± 1.442
p3	-29.15 ± 1
p4	-0.9348 ± 1

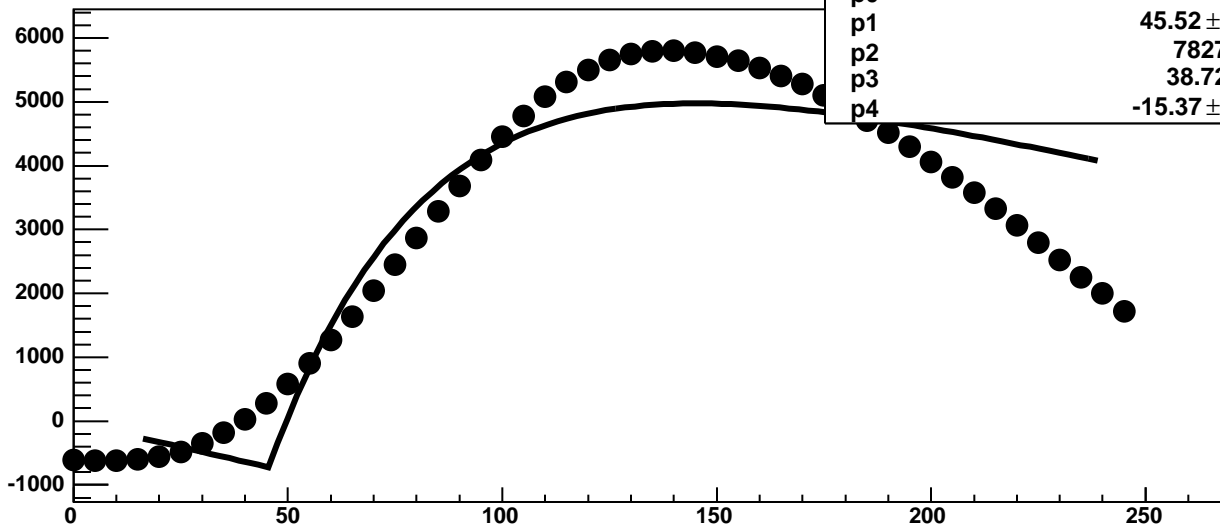
Chip 1, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

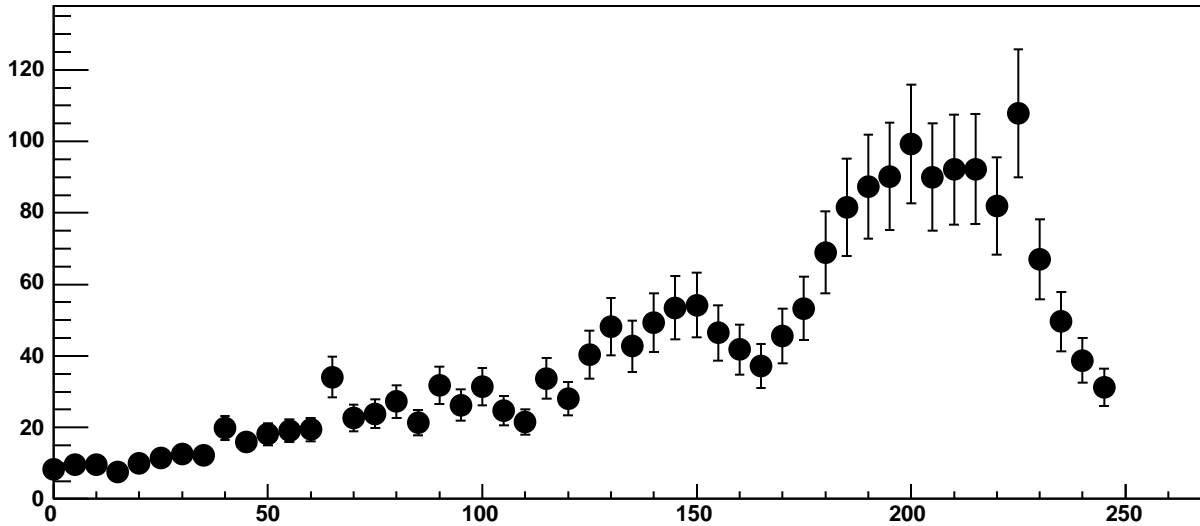


Chip 1, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

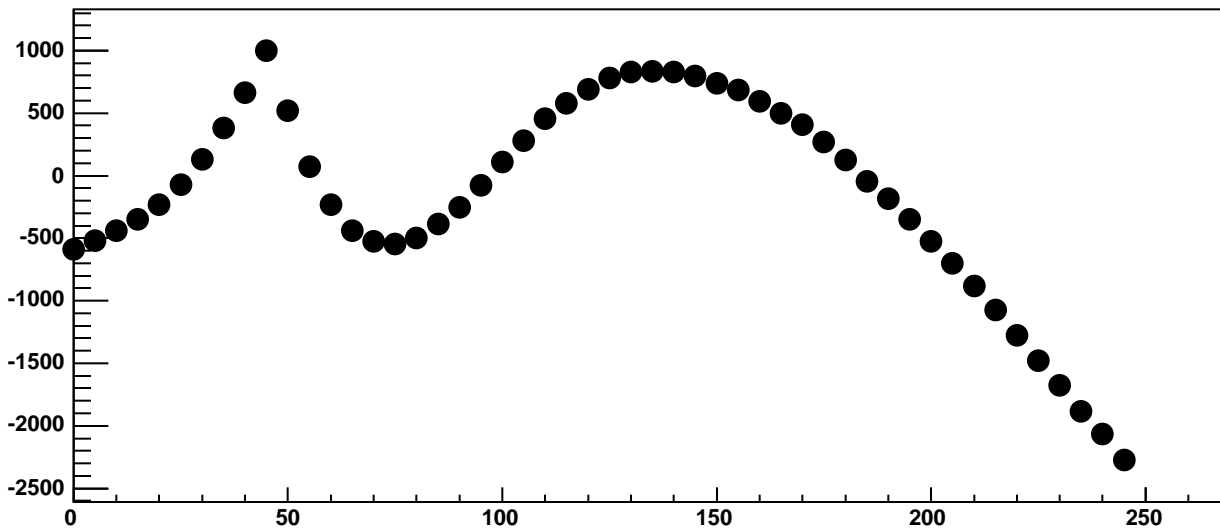


χ^2 / ndf	4.097e+05 / 41
p0	-722.5 ± 1.506
p1	45.52 ± 0.02088
p2	7827 ± 10.94
p3	38.72 ± 0.066
p4	-15.37 ± 0.06154

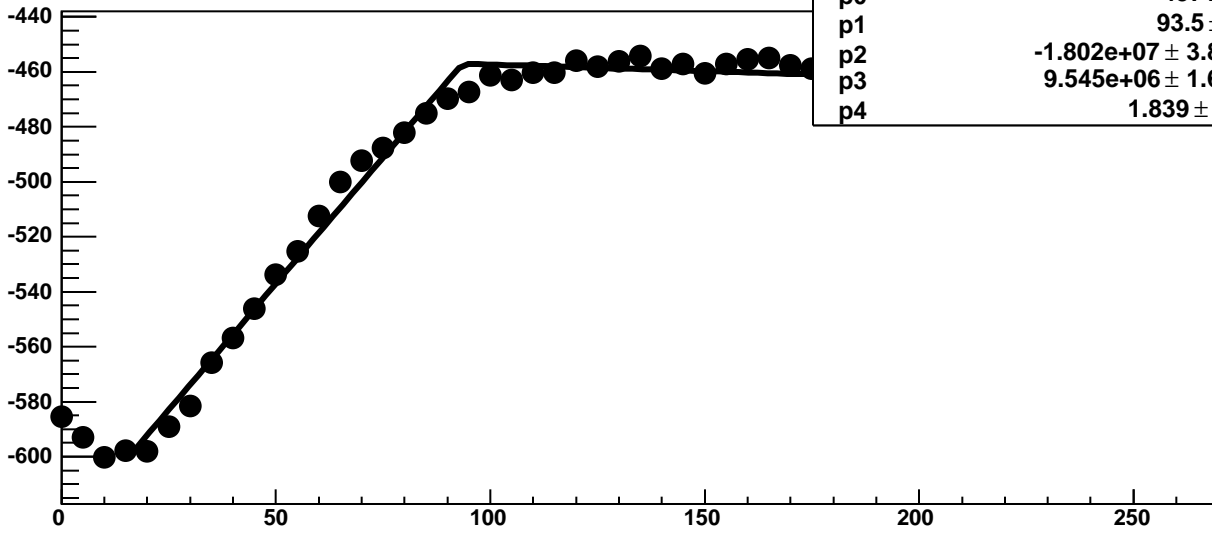
Chip 1, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold

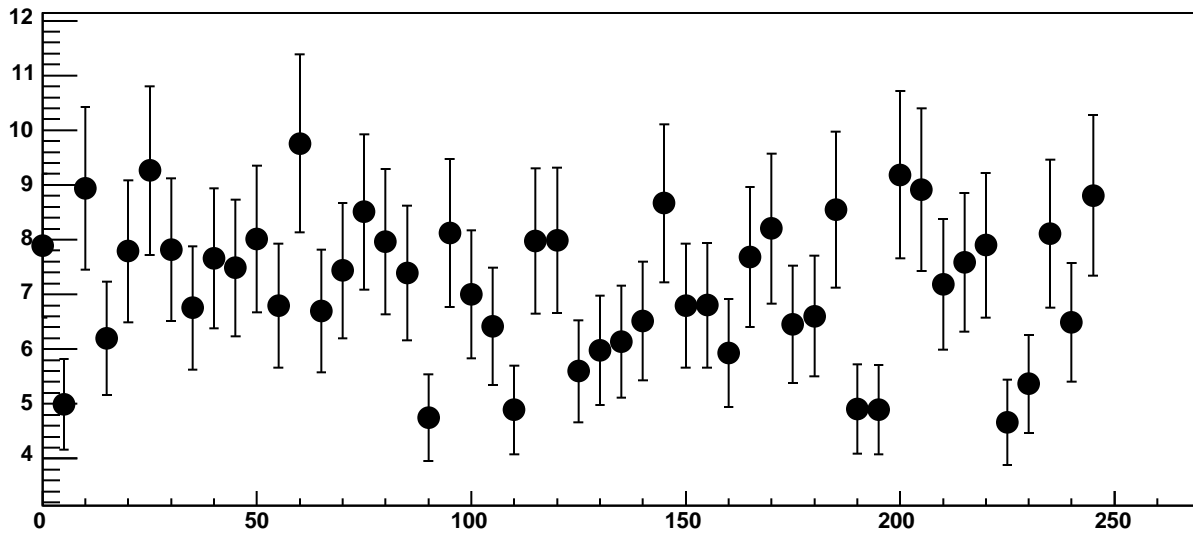


Chip 1, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

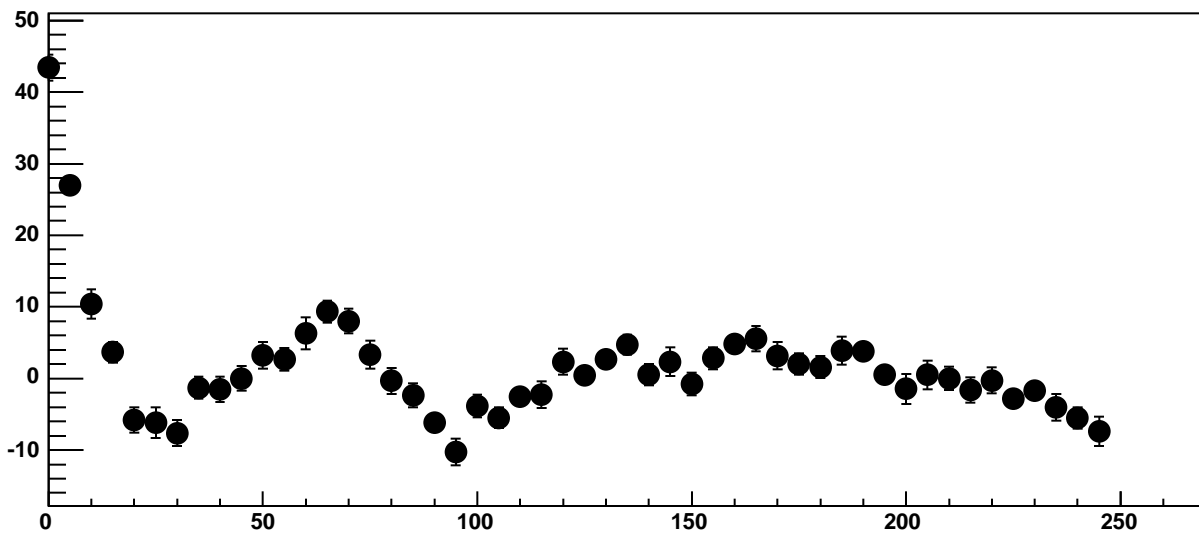


χ^2 / ndf	306.8 / 41
p0	-457 ± 0.5402
p1	93.5 ± 0.4979
p2	$-1.802\text{e}+07 \pm 3.805\text{e}+05$
p3	$9.545\text{e}+06 \pm 1.614\text{e}+05$
p4	1.839 ± 0.01667

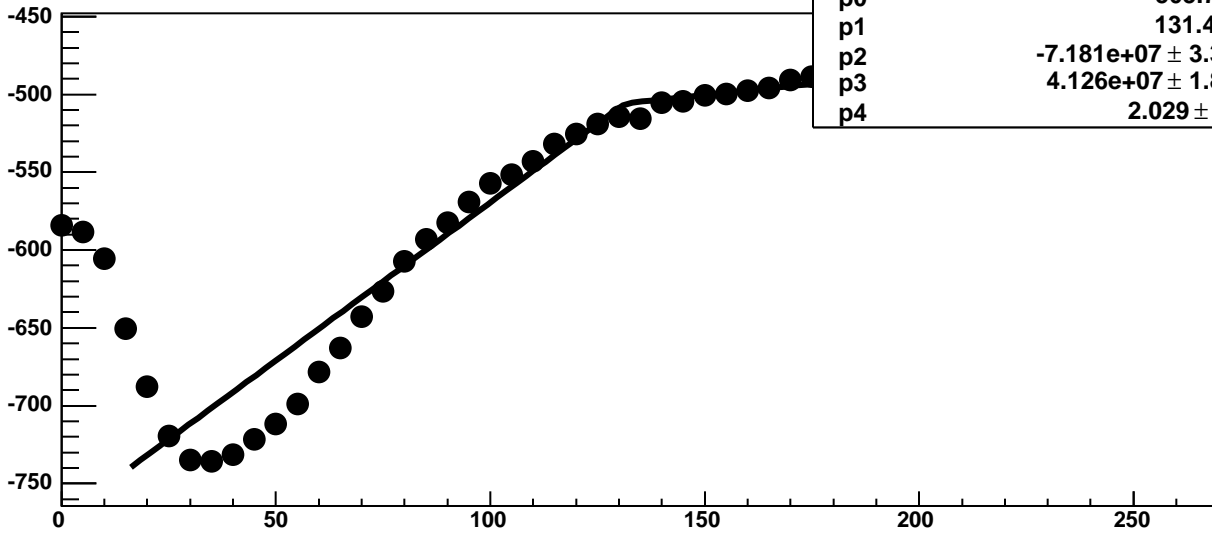
Chip 1, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

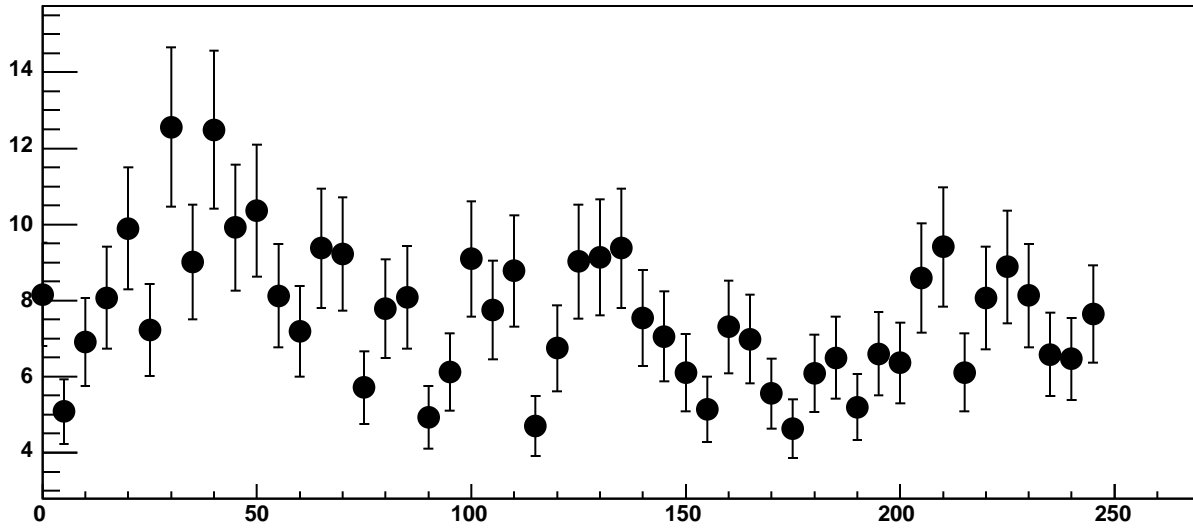


Chip 1, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold

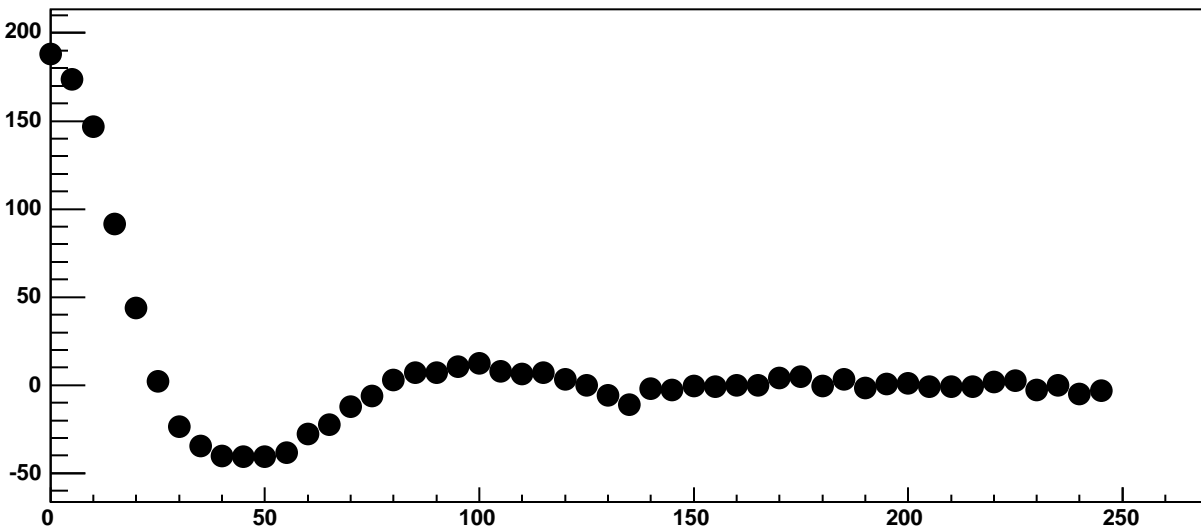


χ^2 / ndf	5178 / 41
p0	-505.7 ± 0.788
p1	131.4 ± 0.545
p2	$-7.181\text{e}+07 \pm 3.318\text{e}+06$
p3	$4.126\text{e}+07 \pm 1.884\text{e}+06$
p4	2.029 ± 0.01092

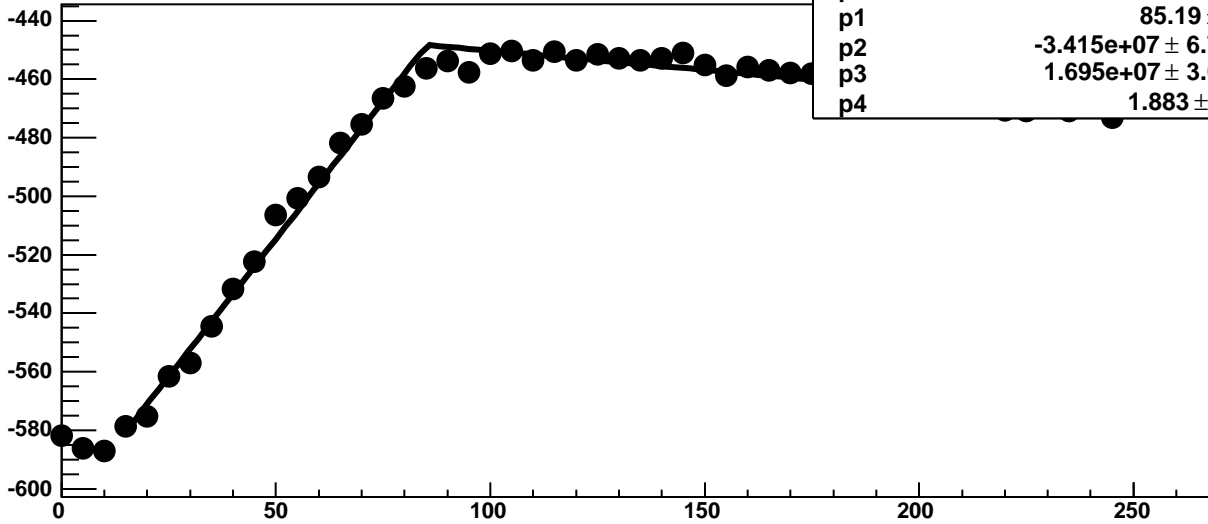
Chip 1, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold

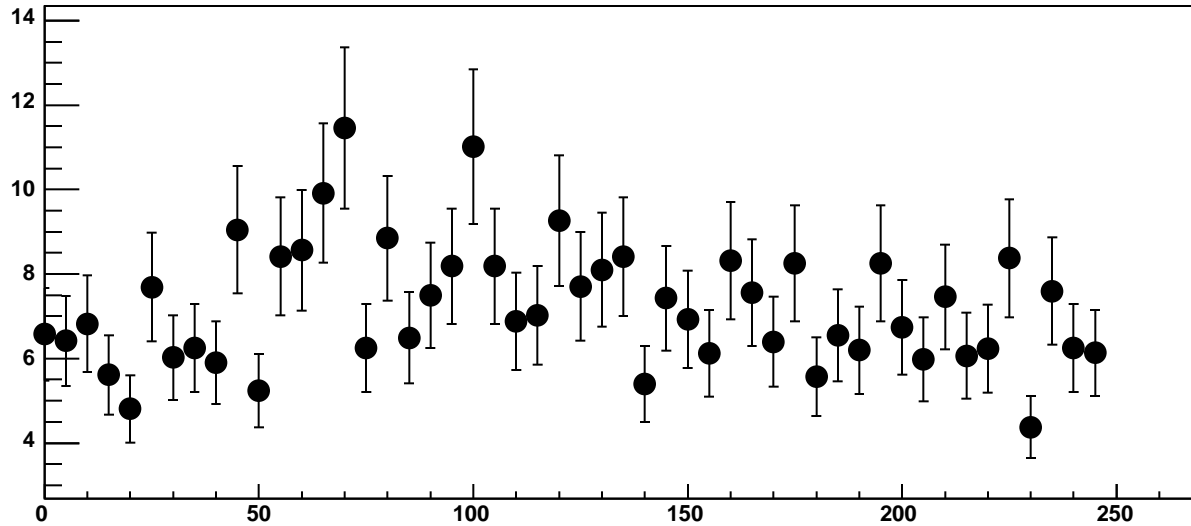


Chip 1, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

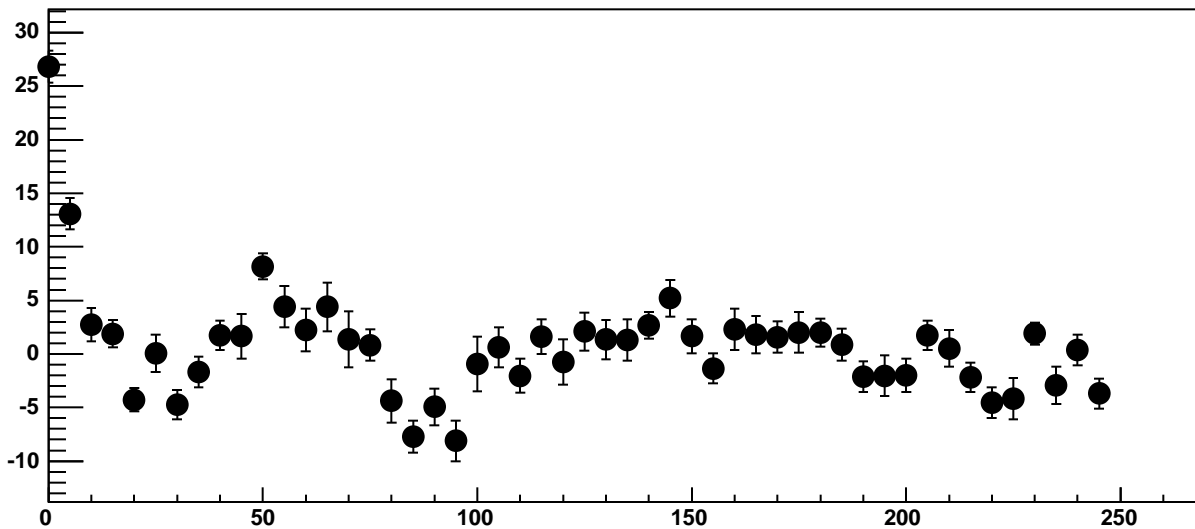


χ^2 / ndf	207.6 / 41
p0	-448.3 ± 0.5945
p1	85.19 ± 0.5184
p2	$-3.415\text{e}+07 \pm 6.707\text{e}+05$
p3	$1.695\text{e}+07 \pm 3.017\text{e}+05$
p4	1.883 ± 0.01797

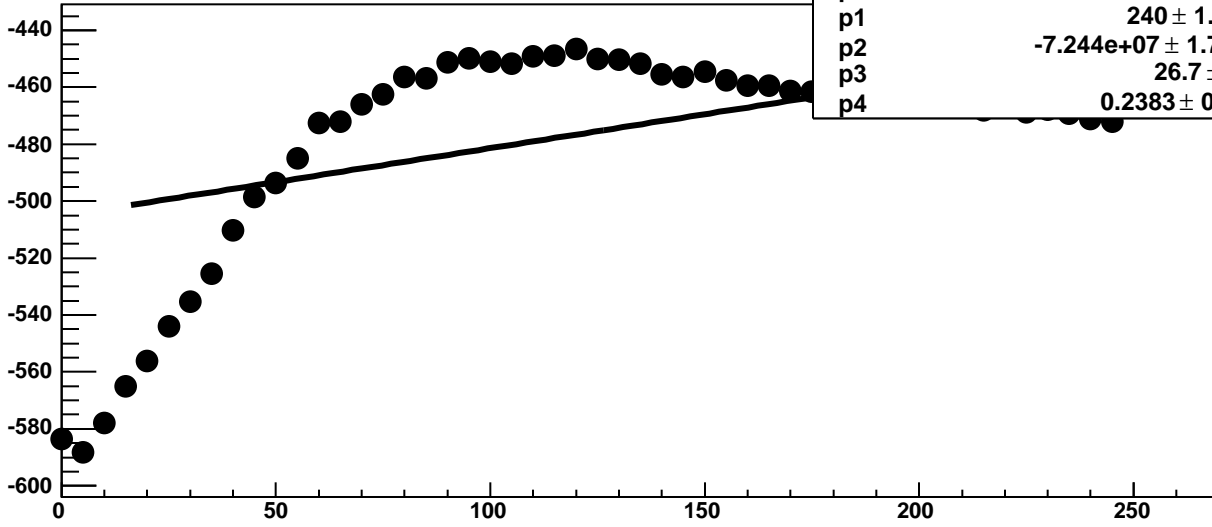
Chip 1, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

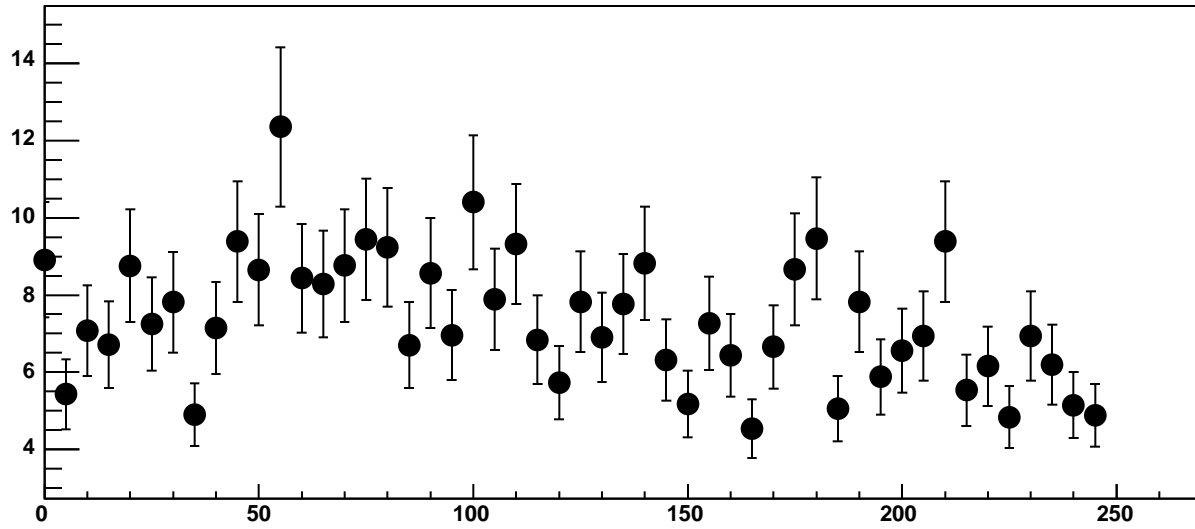


Chip 1, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

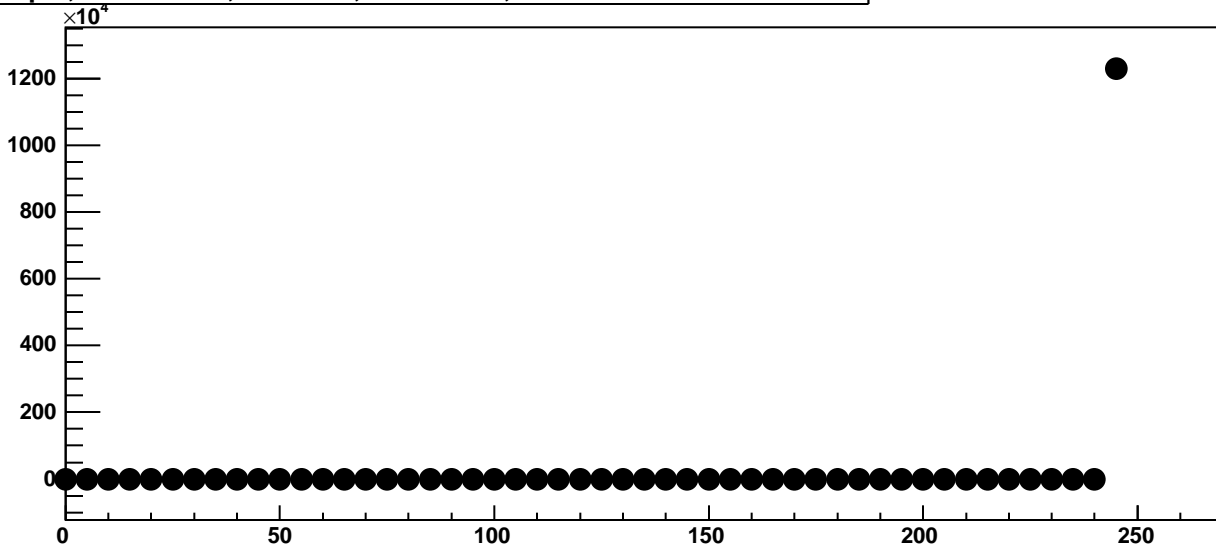


χ^2 / ndf	9752 / 41
p0	-448 ± 0.4156
p1	$240 \pm 1.599\text{e-}09$
p2	$-7.244\text{e+}07 \pm 1.705\text{e+}05$
p3	26.7 ± 0.6146
p4	0.2383 ± 0.003449

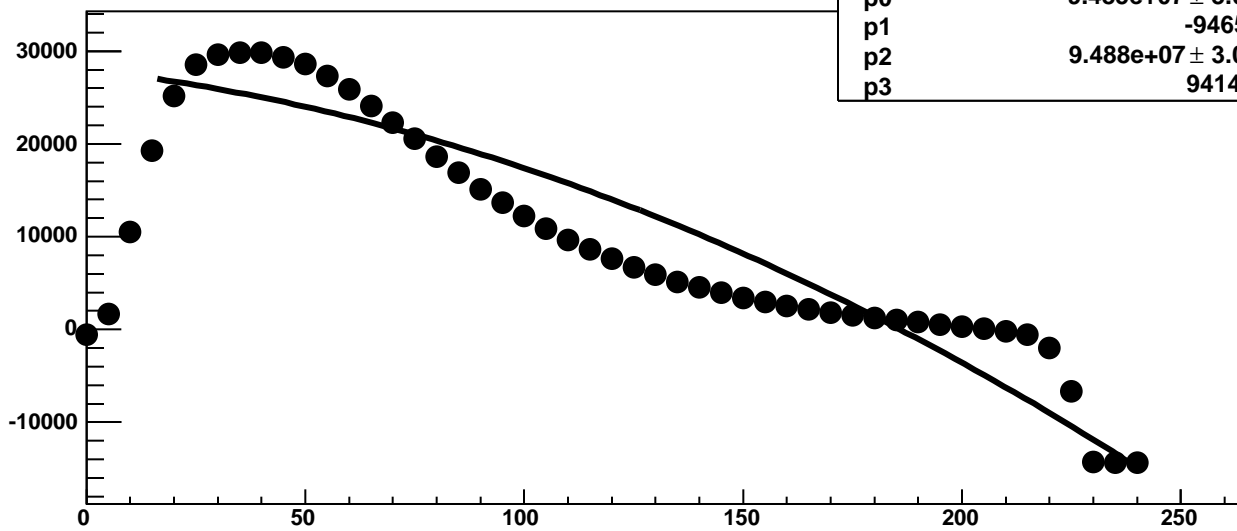
Chip 1, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

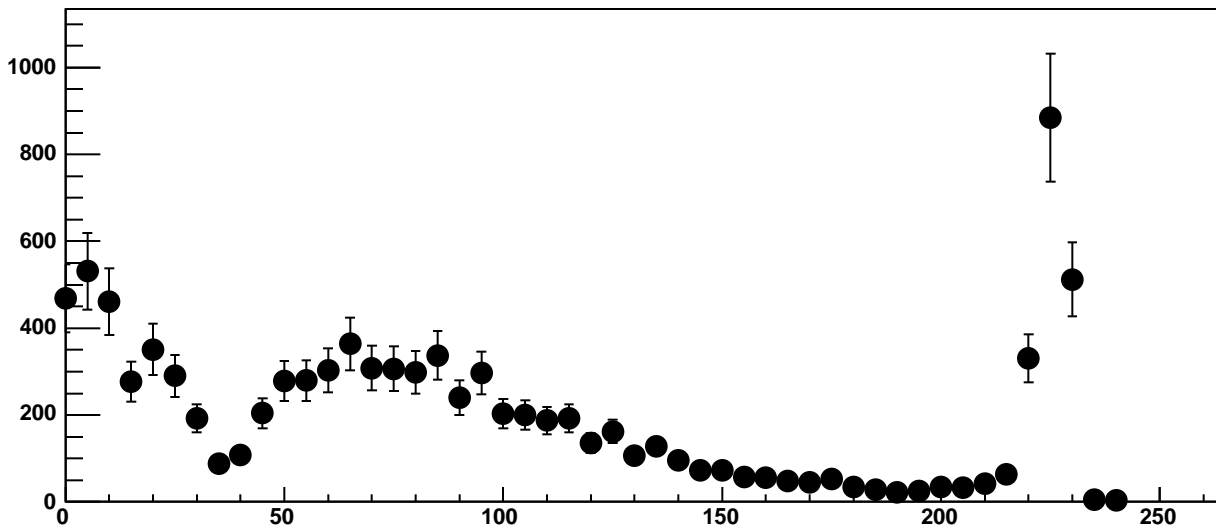


Chip 1, Channel 15, Enable 5!, DAC=4000, ADC Mean vs Hold

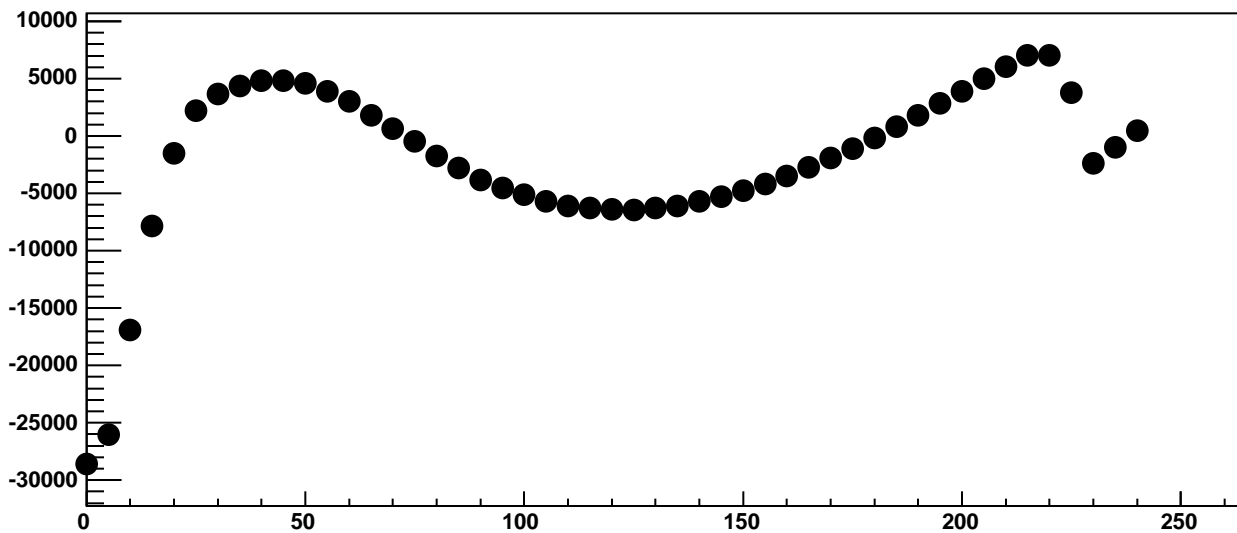


χ^2 / ndf	3.733e+06 / 42
p0	-9.485e+07 ± 3.014e+05
p1	-9465 ± 17.7
p2	9.488e+07 ± 3.015e+05
p3	9414 ± 17.55

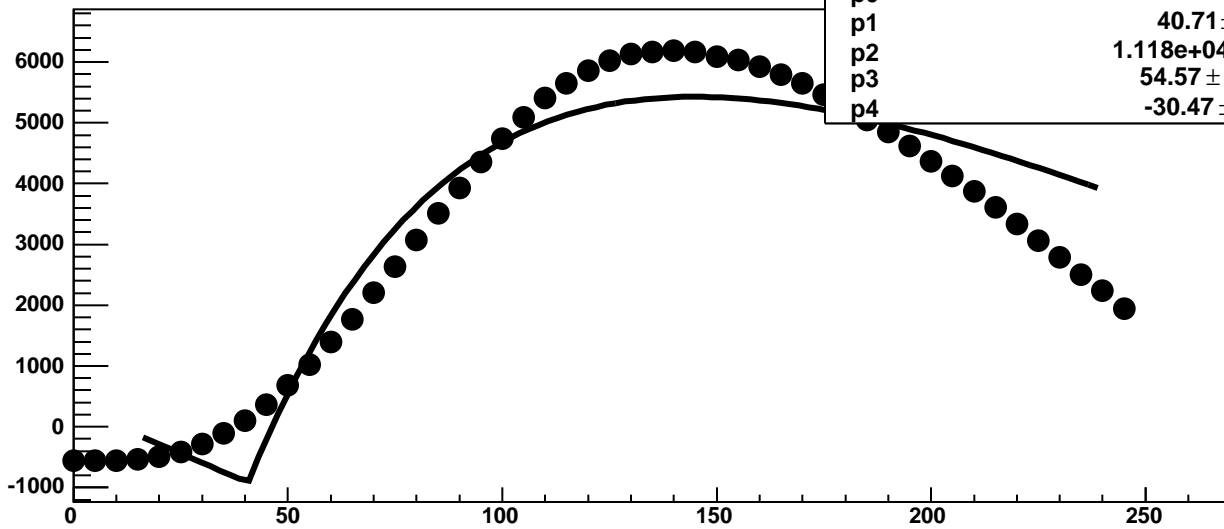
Chip 1, Channel 15, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 15, Enable 5!, DAC=4000, ADC Residuals vs Hold

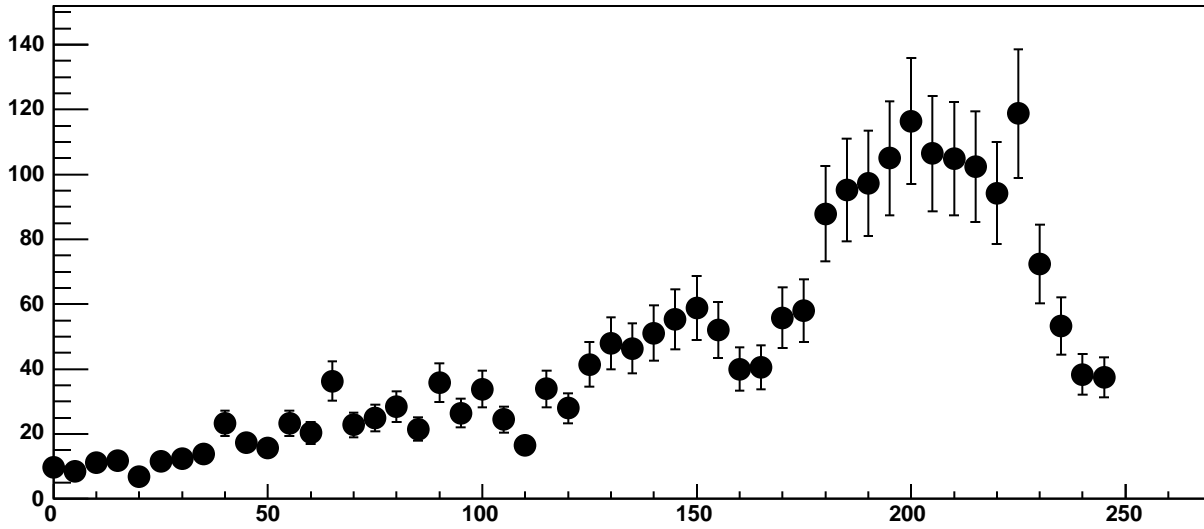


Chip 1, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

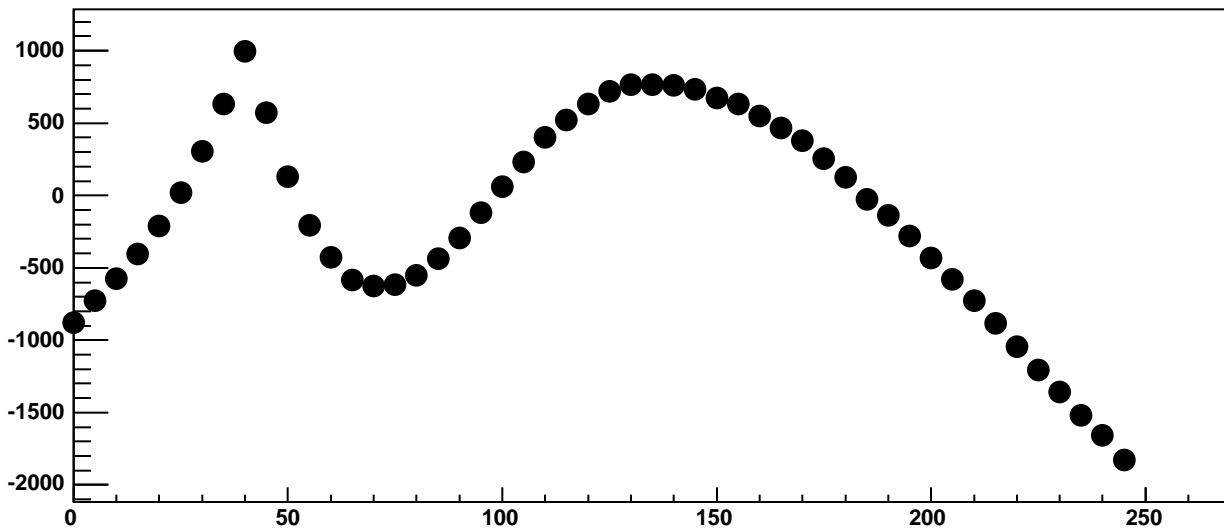


χ^2 / ndf	3.342e+05 / 41
p0	-918.1 ± 1.596
p1	40.71 ± 0.0187
p2	1.118e+04 ± 18.53
p3	54.57 ± 0.09195
p4	-30.47 ± 0.0886

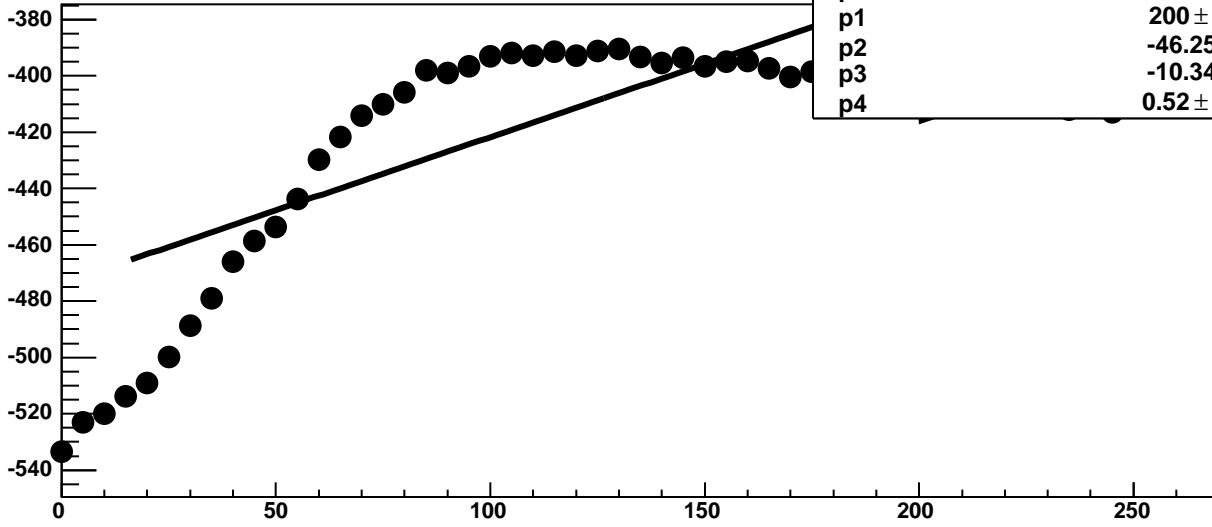
Chip 1, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

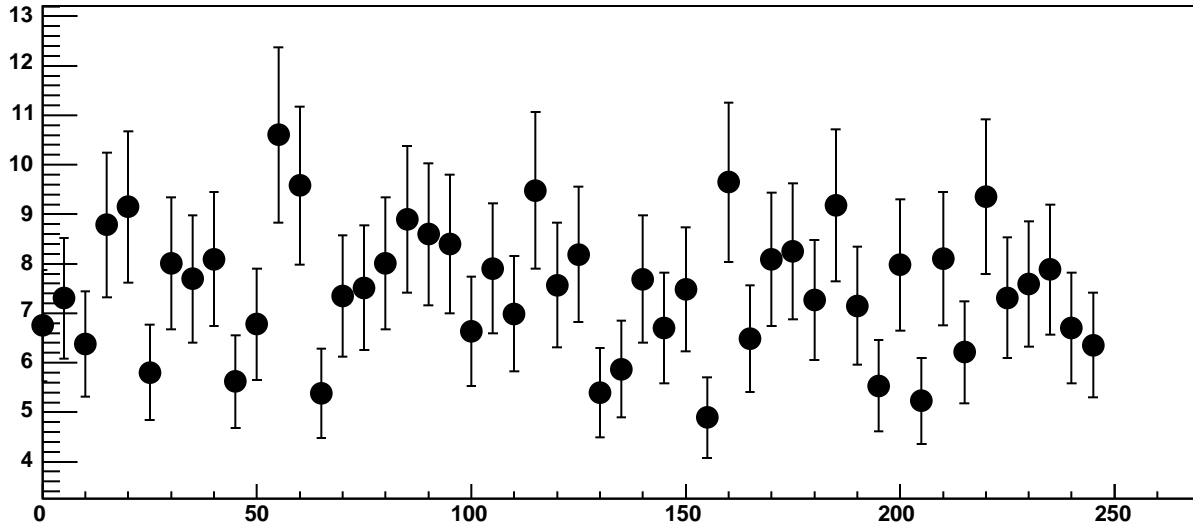


Chip 1, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold

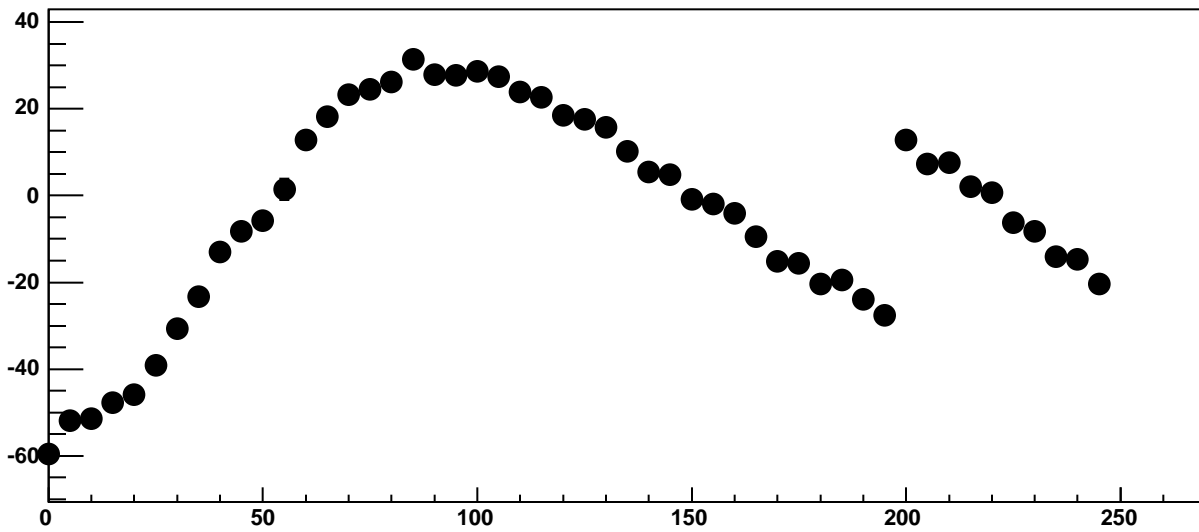


χ^2 / ndf	6692 / 41
p0	-369.8 ± 2.85
p1	200 ± 0.01329
p2	-46.25 ± 2.124
p3	-10.34 ± 13.42
p4	0.52 ± 0.01914

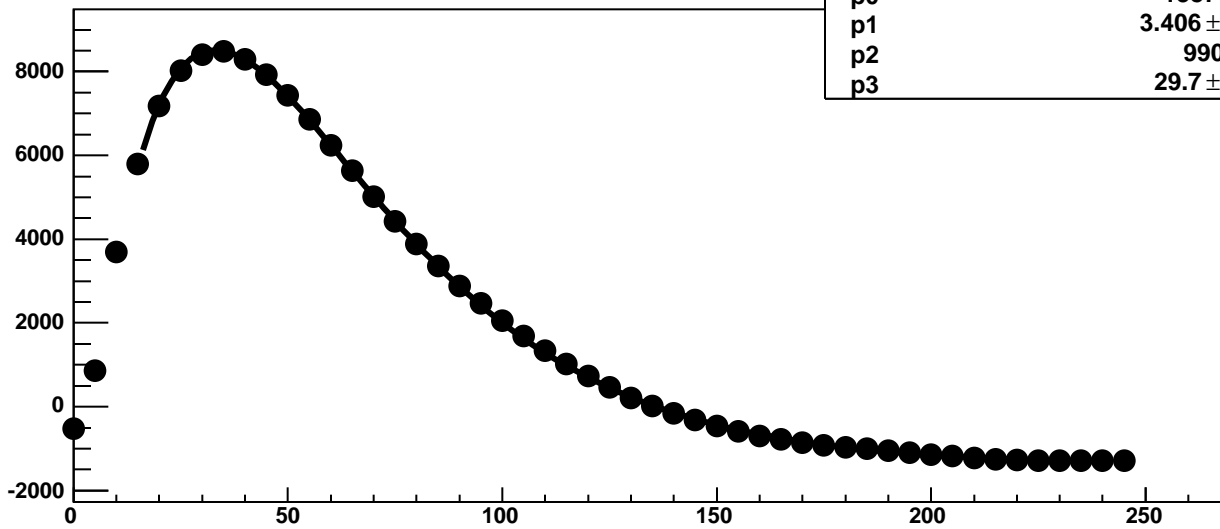
Chip 1, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold

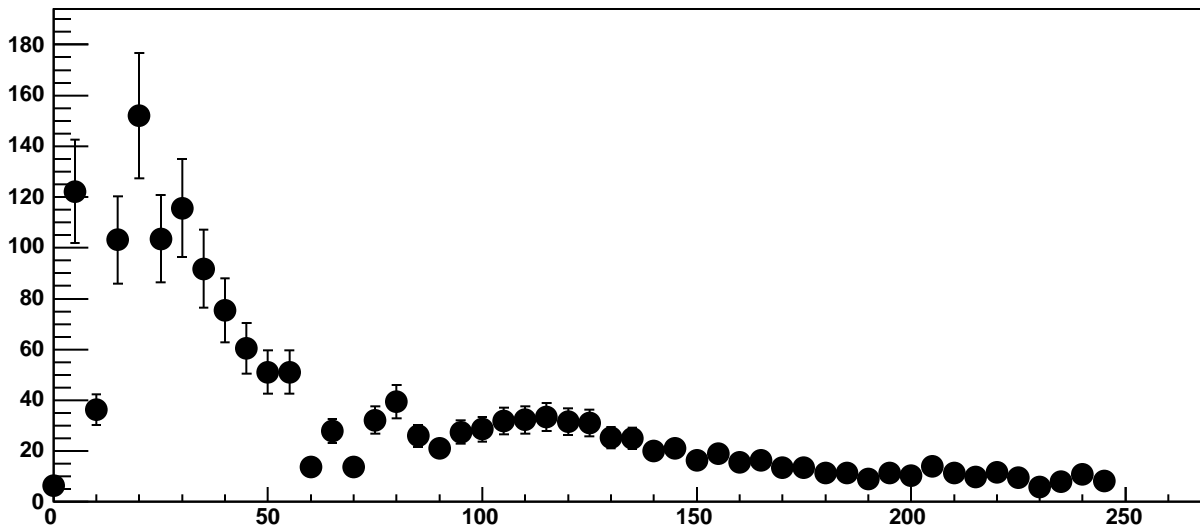


Chip 1, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold

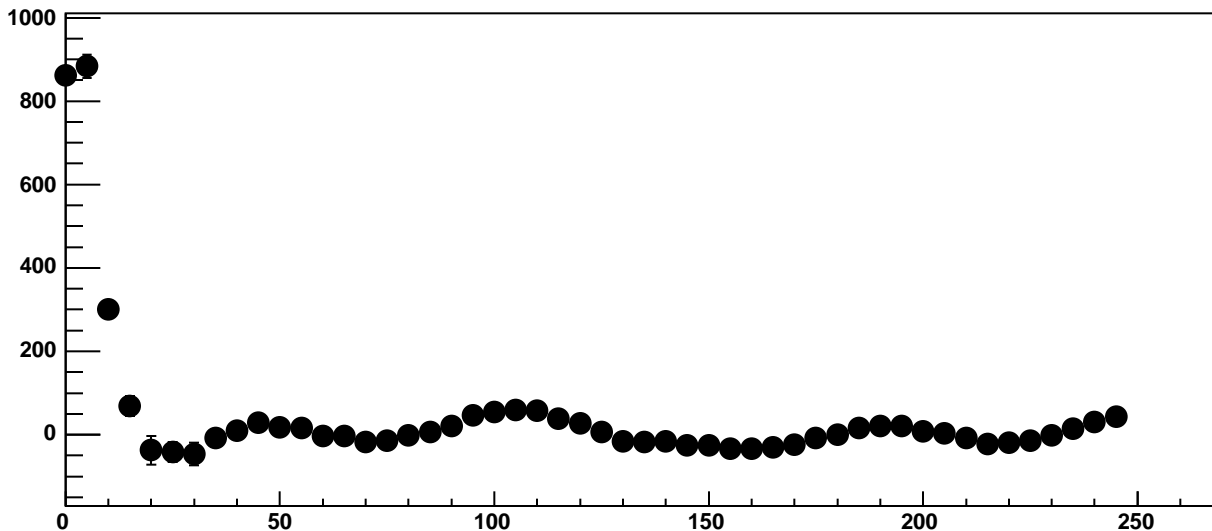


χ^2 / ndf	1365 / 42
p0	-1387 ± 0.8599
p1	3.406 ± 0.04654
p2	9903 ± 5.61
p3	29.7 ± 0.01783

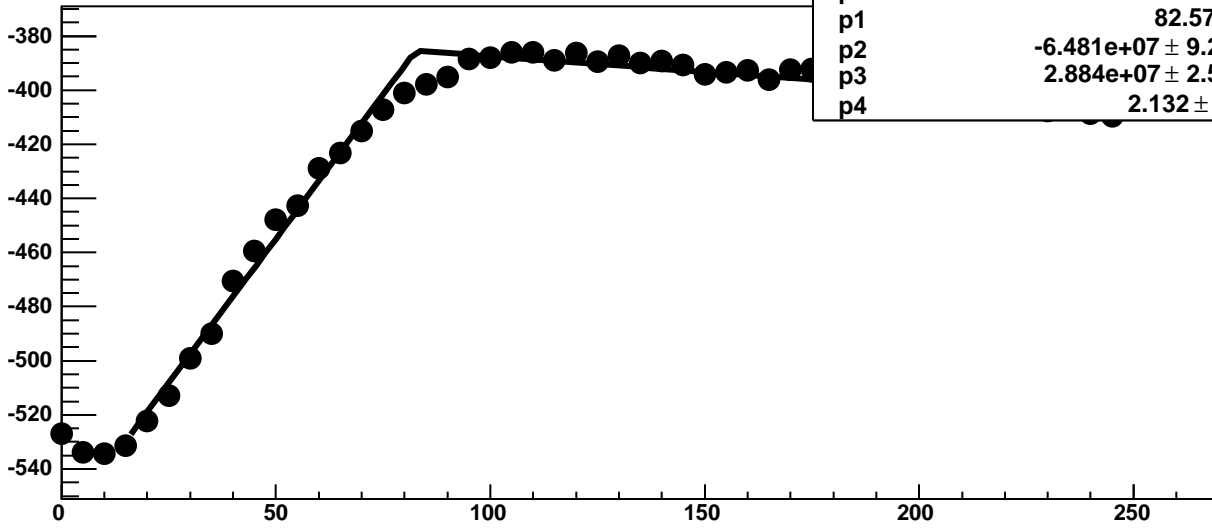
Chip 1, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold

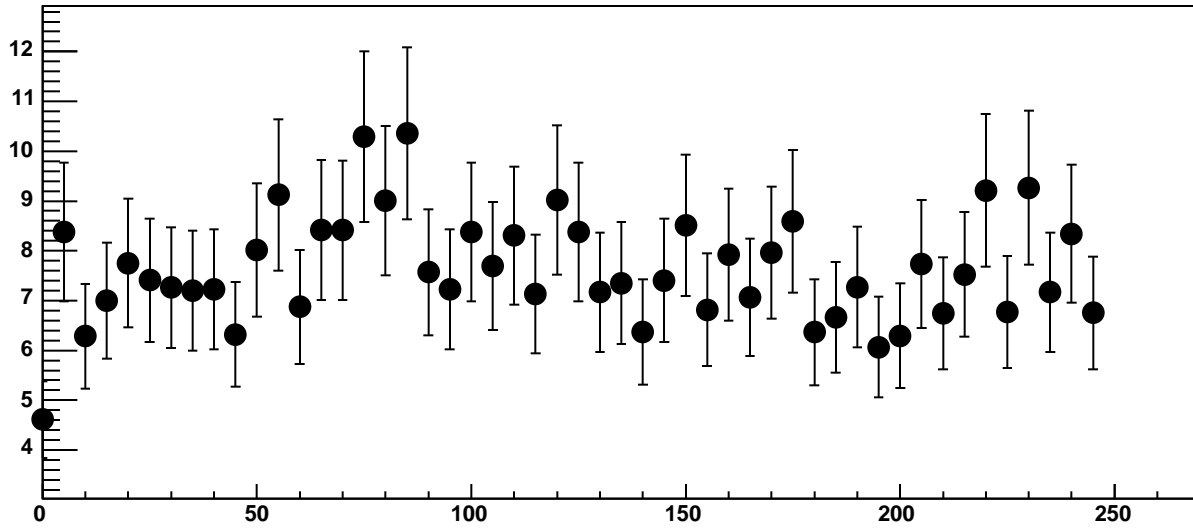


Chip 1, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

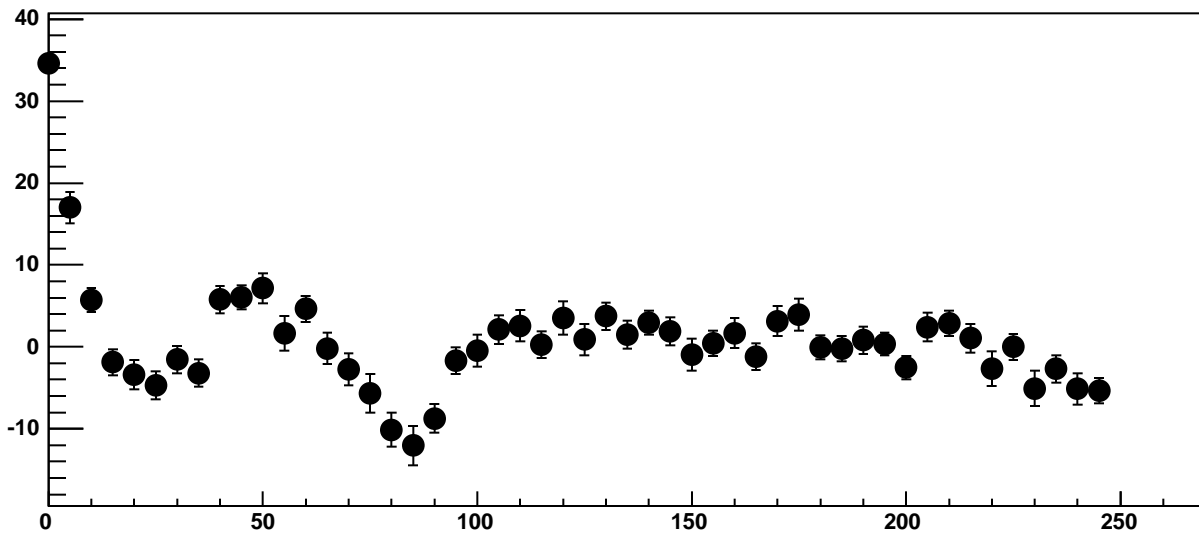


χ^2 / ndf	207.5 / 41
p0	-385.5 ± 0.6099
p1	82.57 ± 0.546
p2	$-6.481\text{e}+07 \pm 9.229\text{e}+05$
p3	$2.884\text{e}+07 \pm 2.558\text{e}+05$
p4	2.132 ± 0.02458

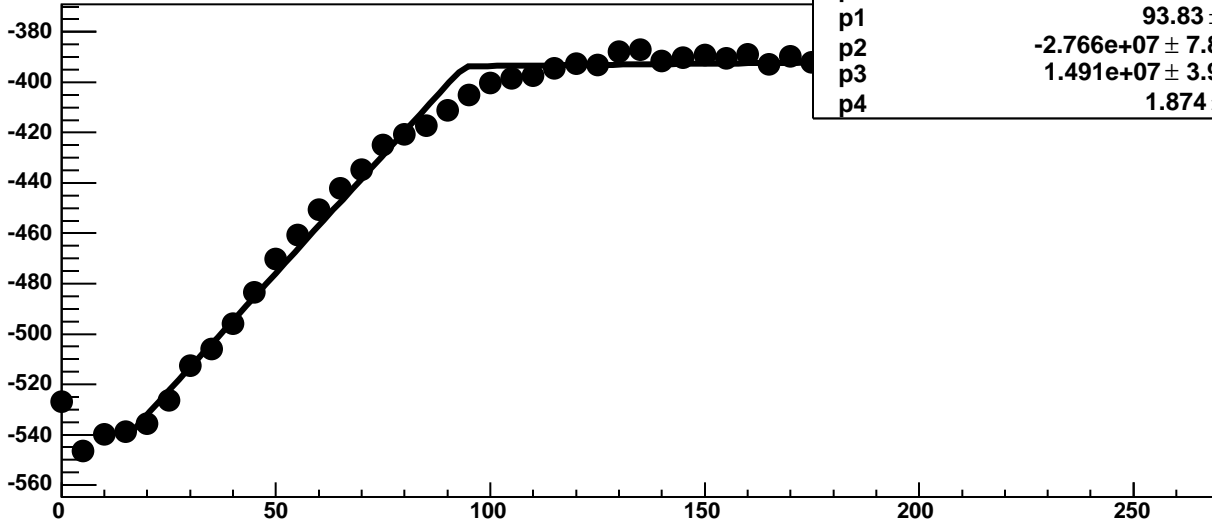
Chip 1, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

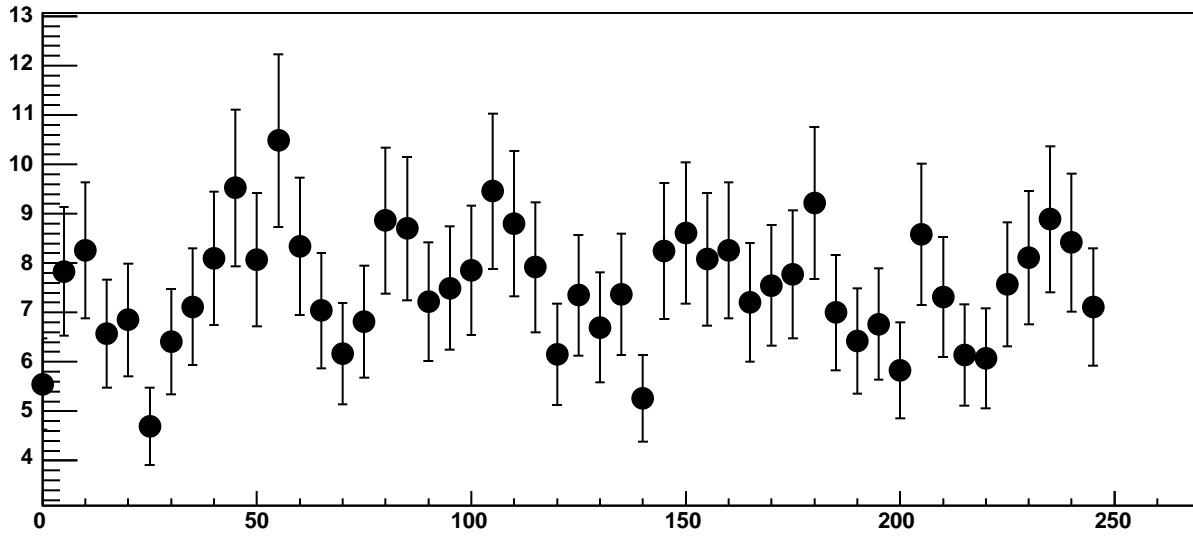


Chip 1, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

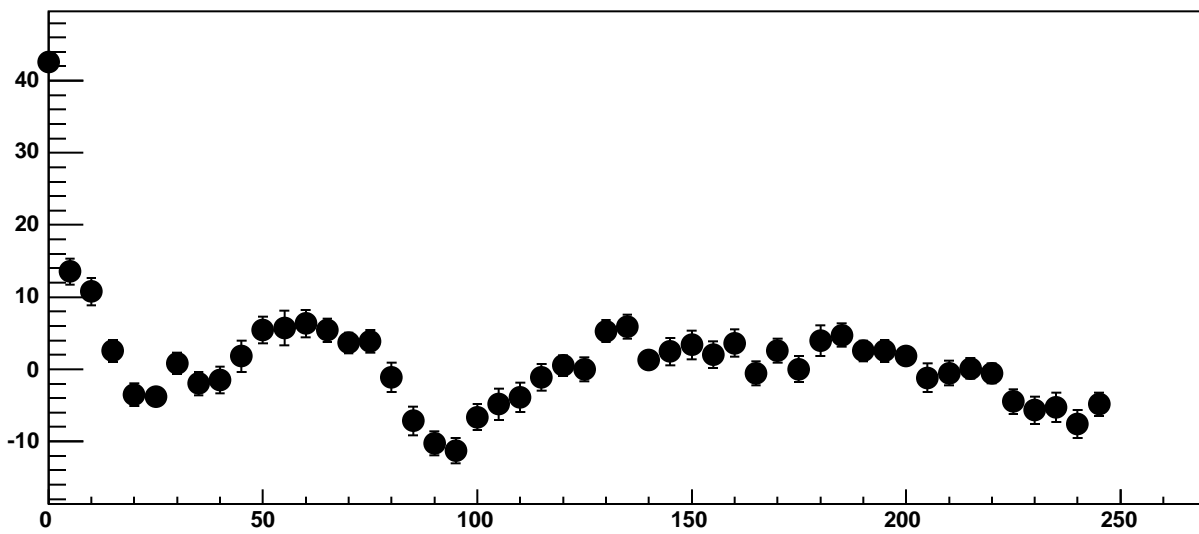


χ^2 / ndf	286 / 41
p0	-393.7 ± 0.6311
p1	93.83 ± 0.5793
p2	$-2.766\text{e}+07 \pm 7.829\text{e}+05$
p3	$1.491\text{e}+07 \pm 3.988\text{e}+05$
p4	1.874 ± 0.0171

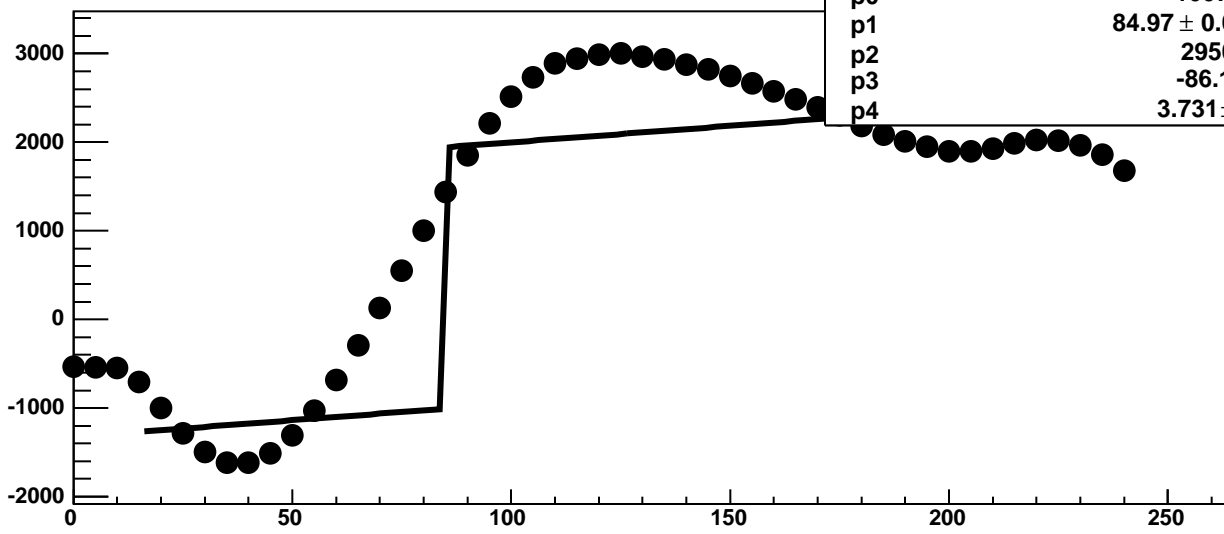
Chip 1, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

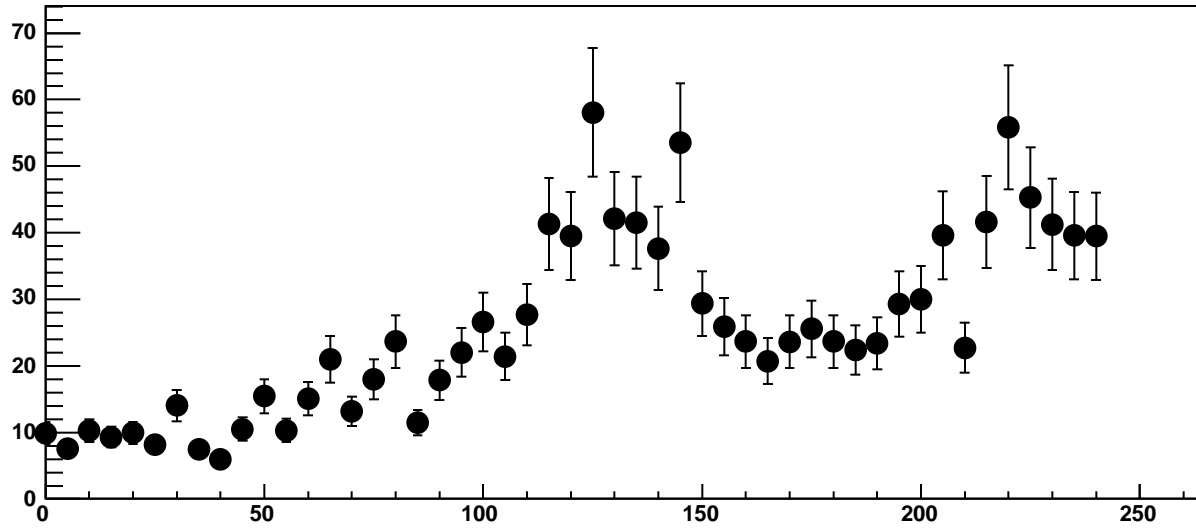


Chip 1, Channel 16, Enable 5, DAC=4000, ADC Mean vs Hold

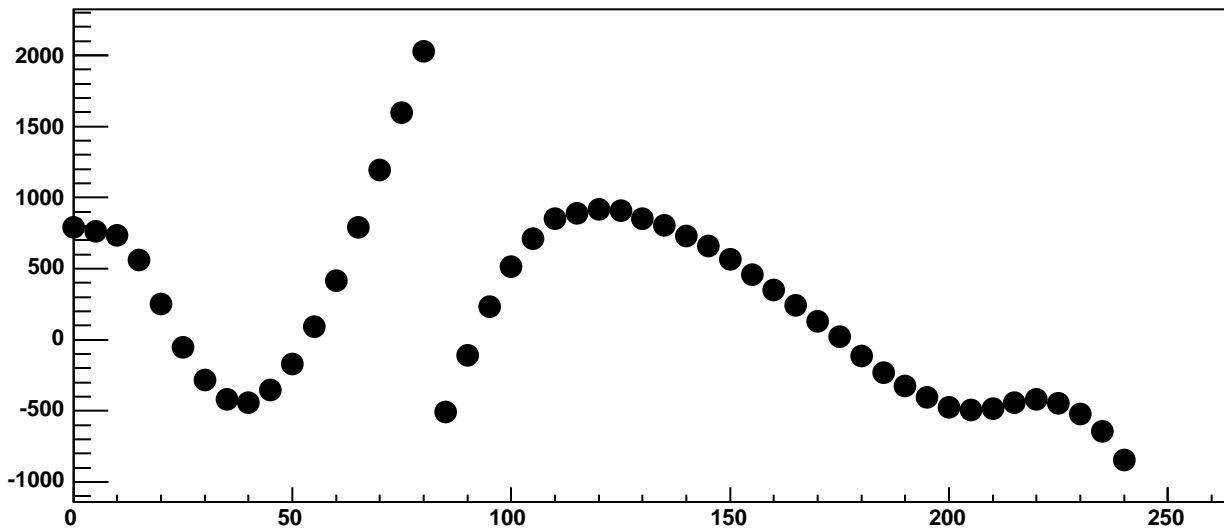


χ^2 / ndf	9.705e+05 / 41
p0	-1007 ± 6.647
p1	84.97 ± 0.0003389
p2	2950 ± 6.69
p3	-86.1 ± 2.663
p4	3.731 ± 0.1003

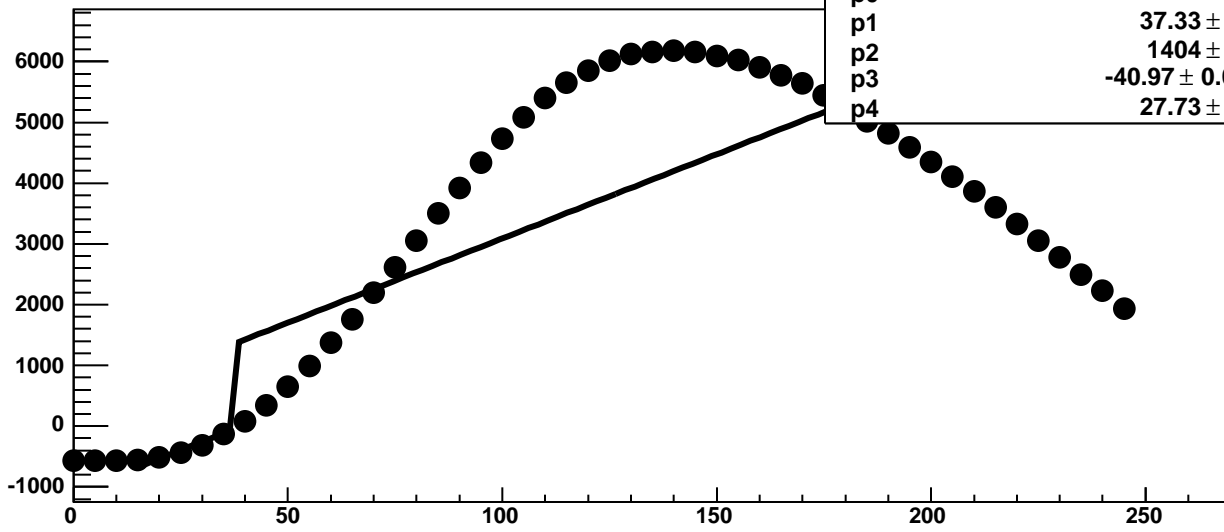
Chip 1, Channel 16, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 16, Enable 5, DAC=4000, ADC Residuals vs Hold

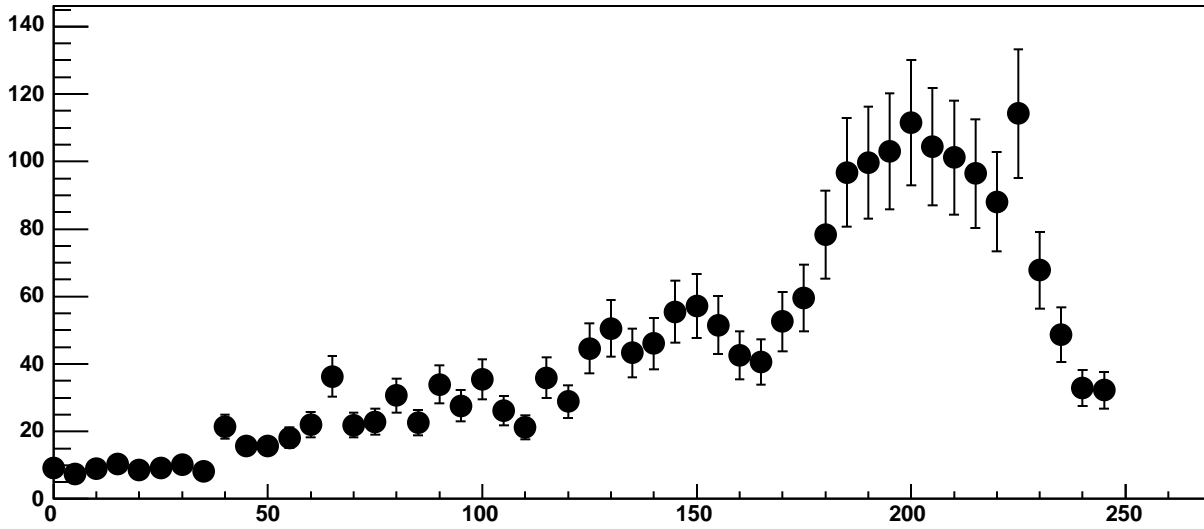


Chip 1, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

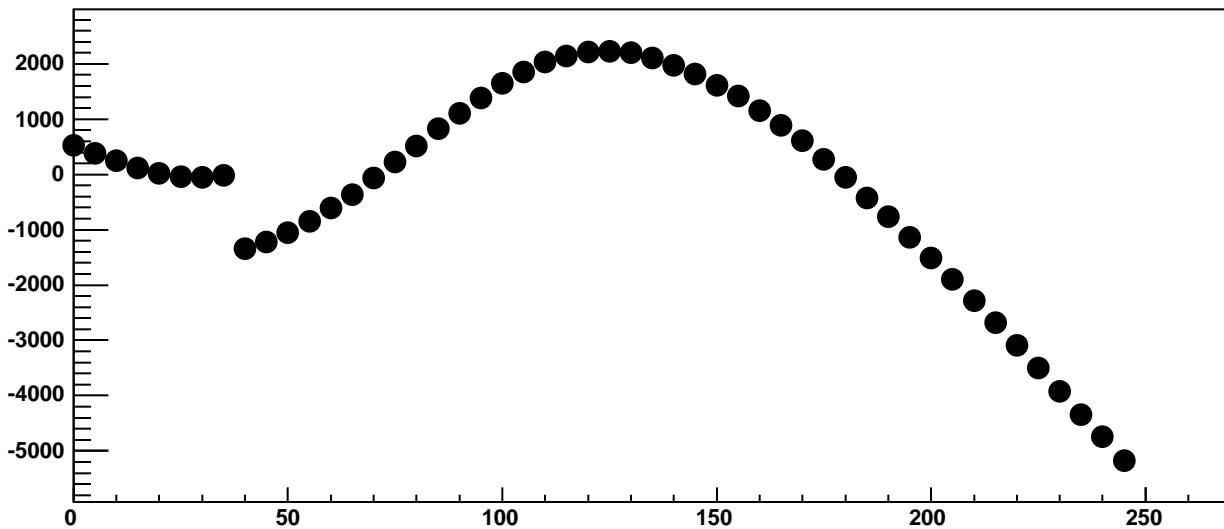


χ^2 / ndf	1.877e+06 / 41
p0	-52.24 ± 0.07138
p1	37.33 ± 0.01209
p2	1404 ± 0.07308
p3	-40.97 ± 0.0005095
p4	27.73 ± 0.01619

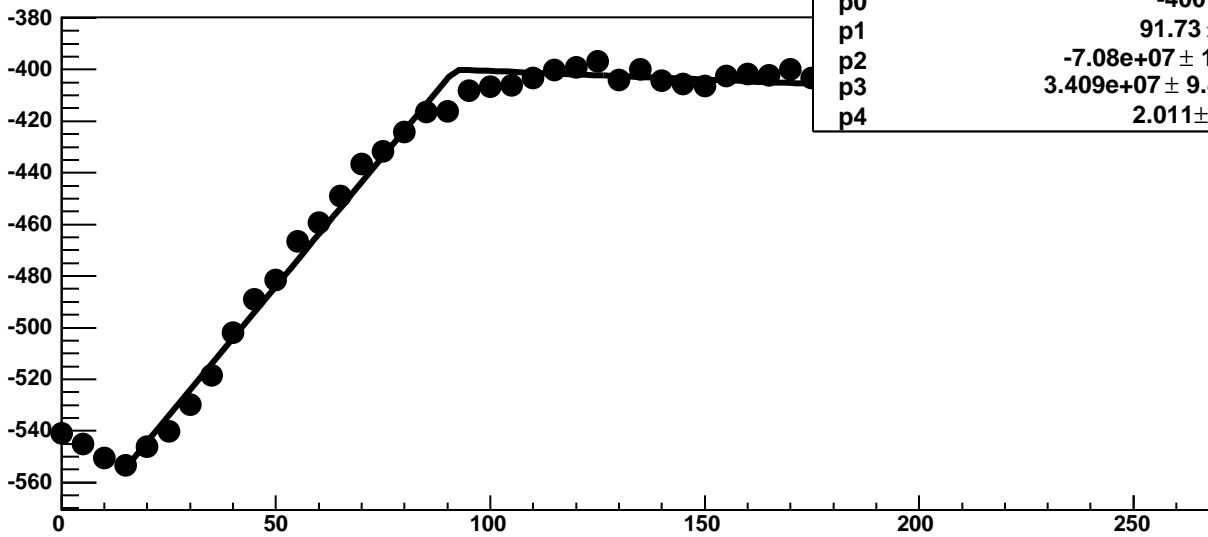
Chip 1, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

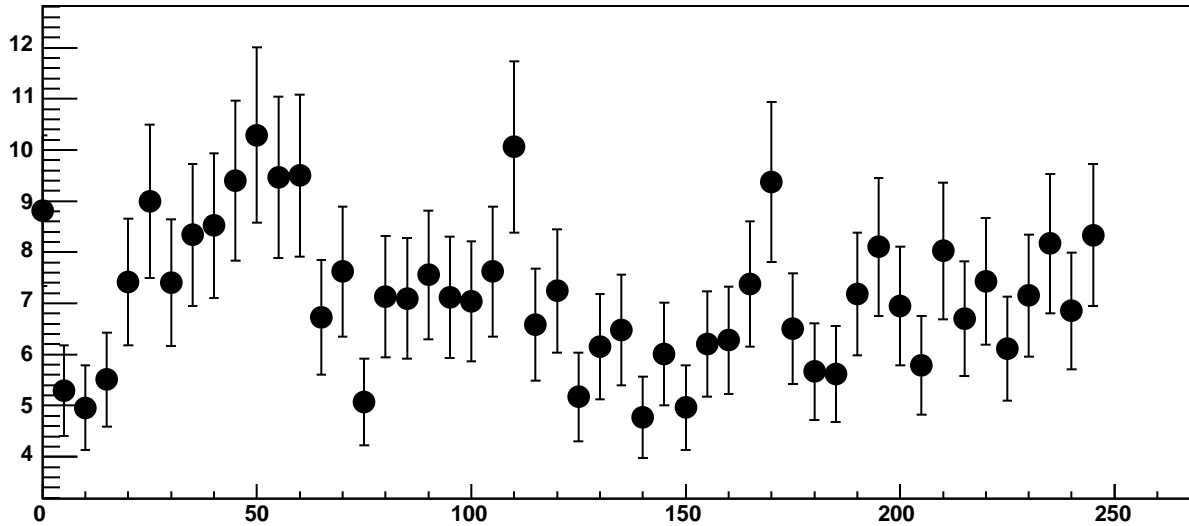


Chip 1, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

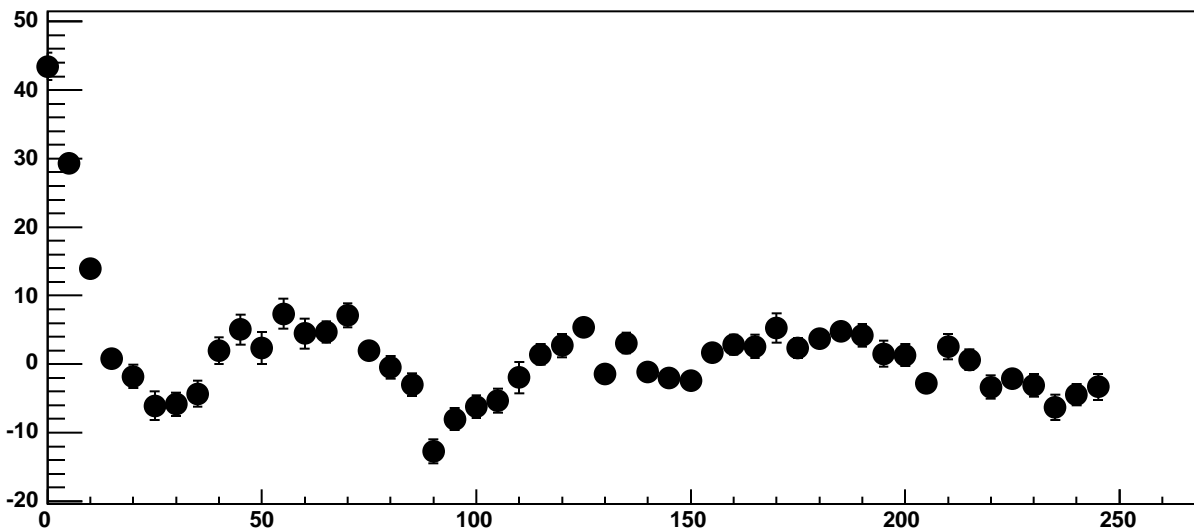


χ^2 / ndf	303.7 / 41
p0	-400 ± 0.5601
p1	91.73 ± 0.4926
p2	$-7.08\text{e}+07 \pm 1.97\text{e}+06$
p3	$3.409\text{e}+07 \pm 9.475\text{e}+05$
p4	2.011 ± 0.01756

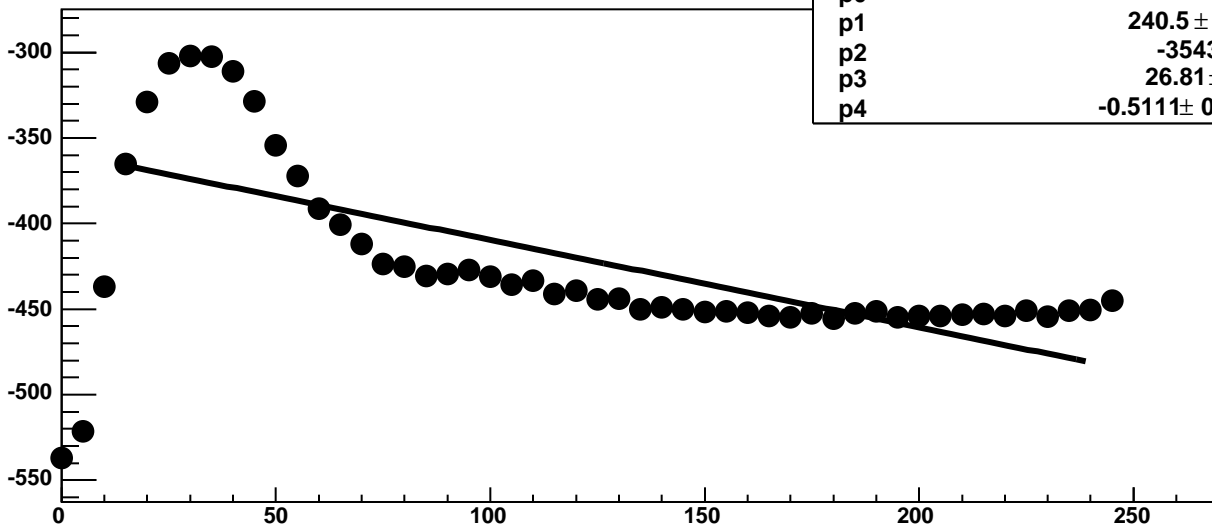
Chip 1, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



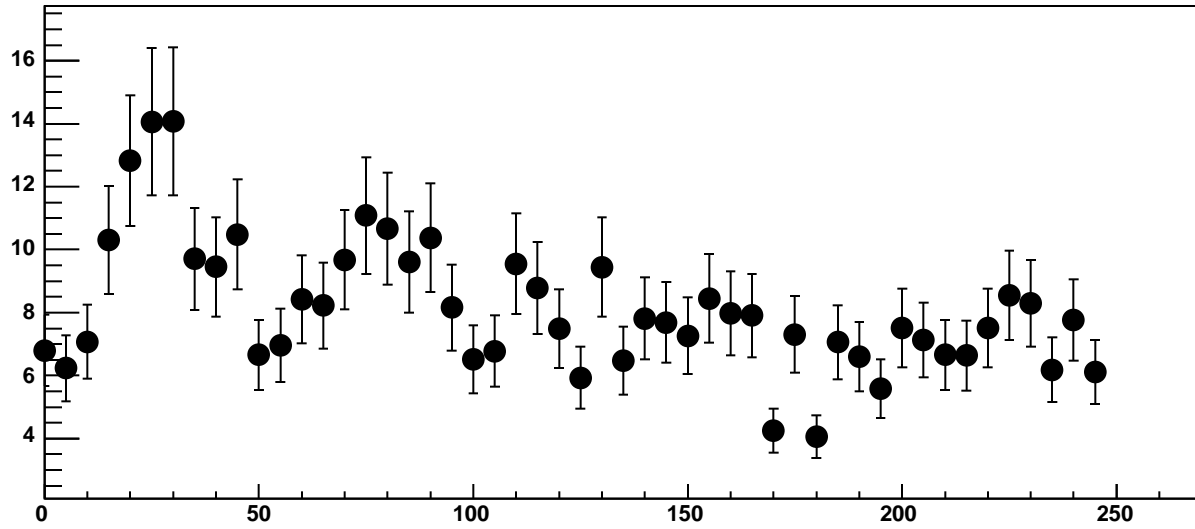
Chip 1, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold



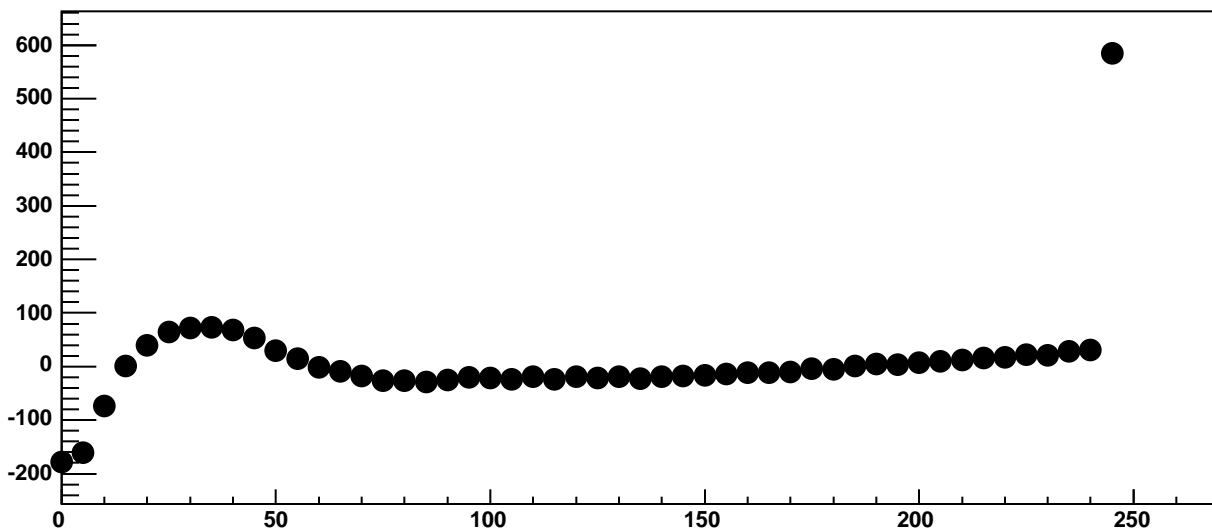
Chip 1, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold



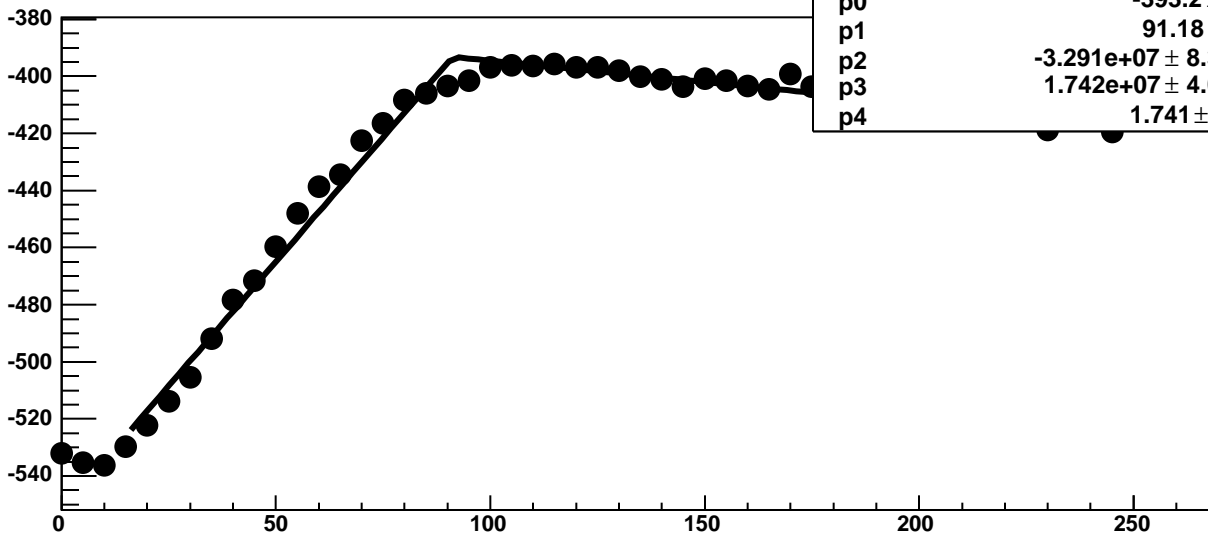
Chip 1, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold

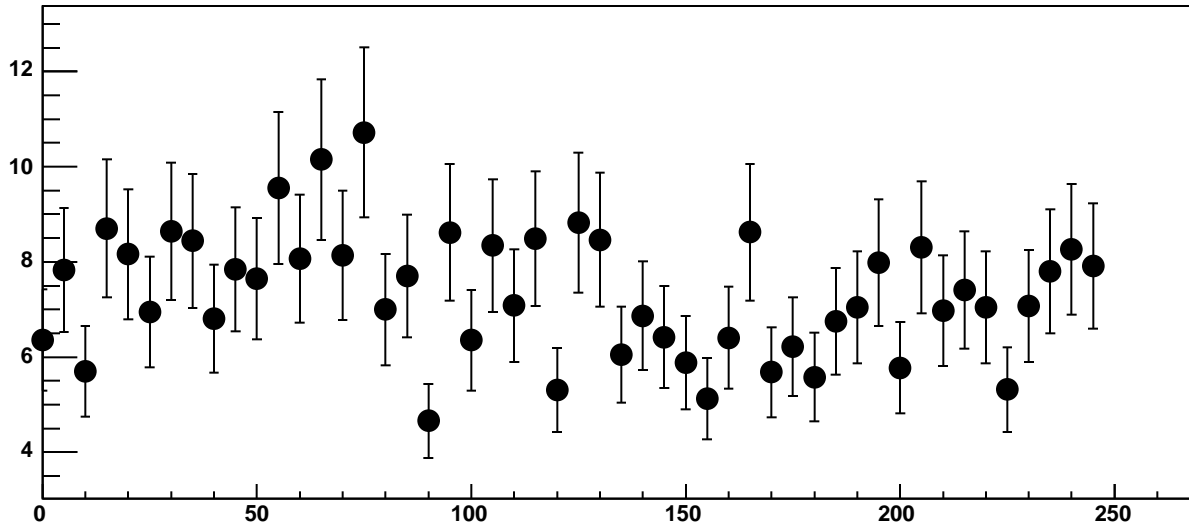


Chip 1, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold

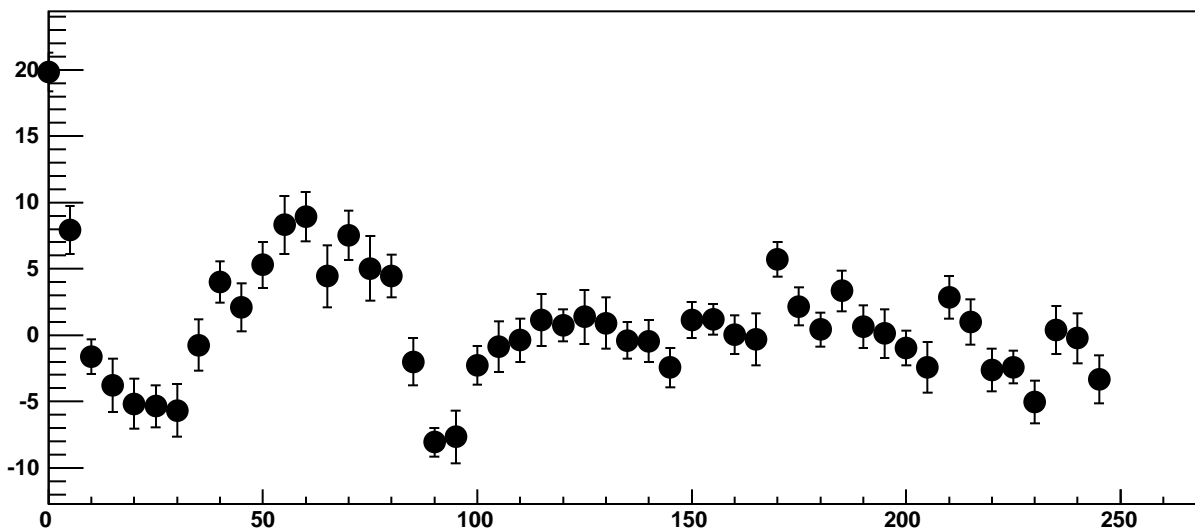


χ^2 / ndf	248.1 / 41
p0	-393.2 ± 0.5555
p1	91.18 ± 0.5093
p2	$-3.291\text{e}+07 \pm 8.396\text{e}+05$
p3	$1.742\text{e}+07 \pm 4.085\text{e}+05$
p4	1.741 ± 0.01753

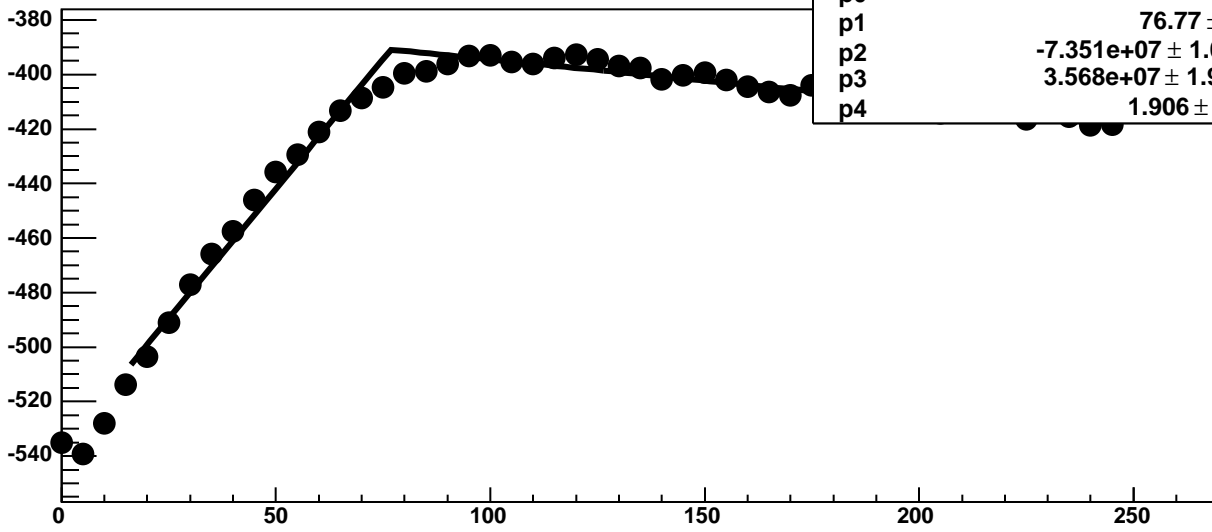
Chip 1, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold

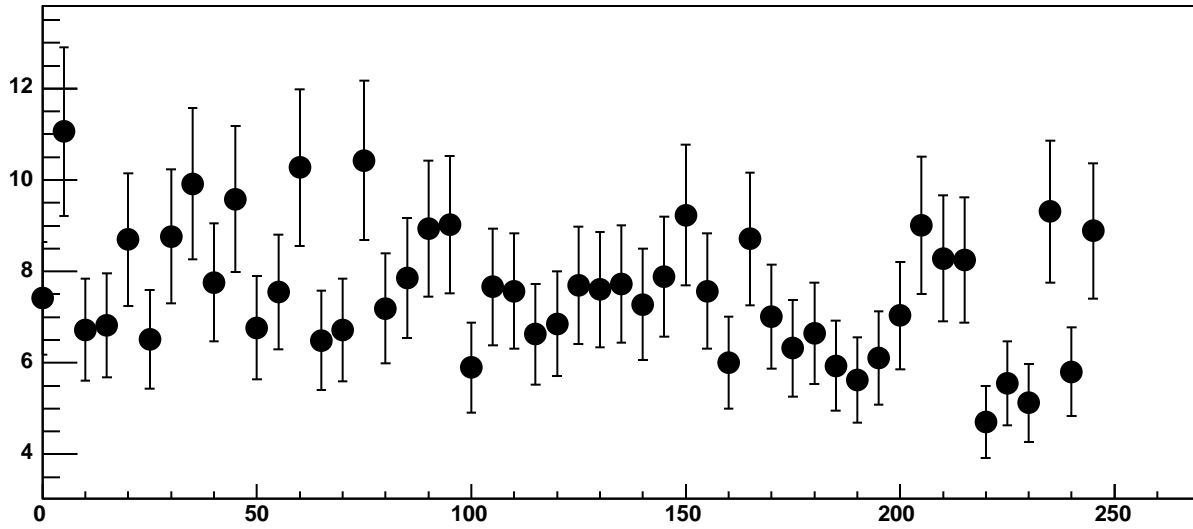


Chip 1, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold

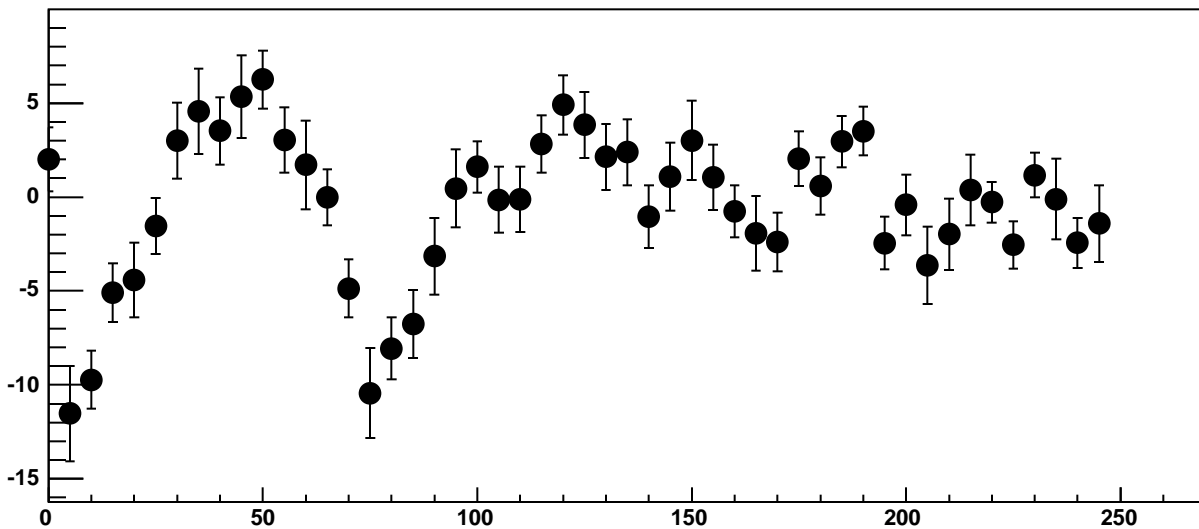


χ^2 / ndf	181.3 / 41
p0	-390.9 ± 0.5479
p1	76.77 ± 0.5536
p2	$-7.351\text{e}+07 \pm 1.031\text{e}+06$
p3	$3.568\text{e}+07 \pm 1.988\text{e}+05$
p4	1.906 ± 0.02609

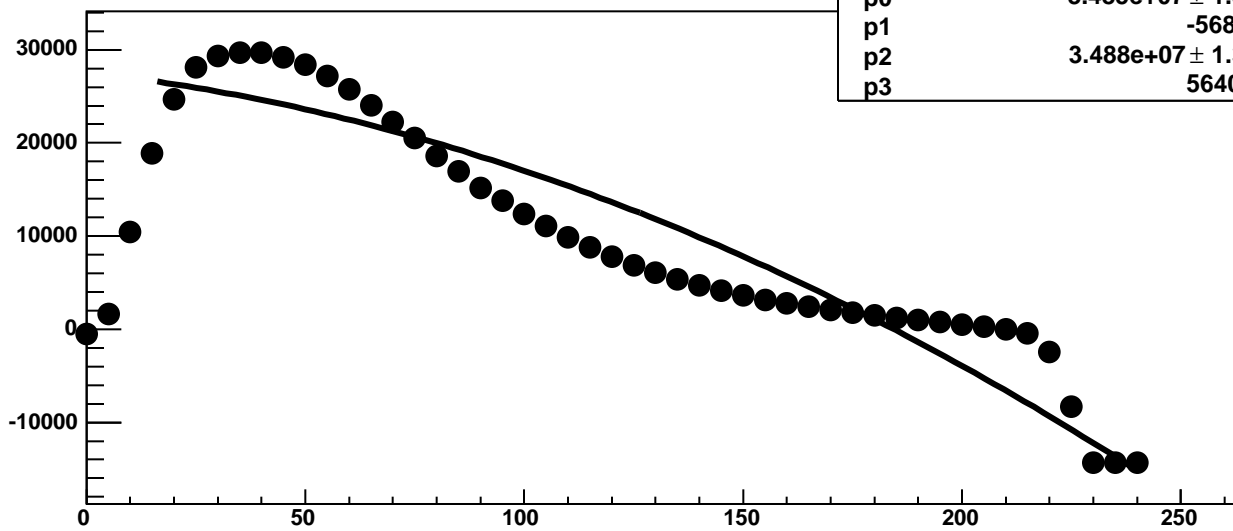
Chip 1, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

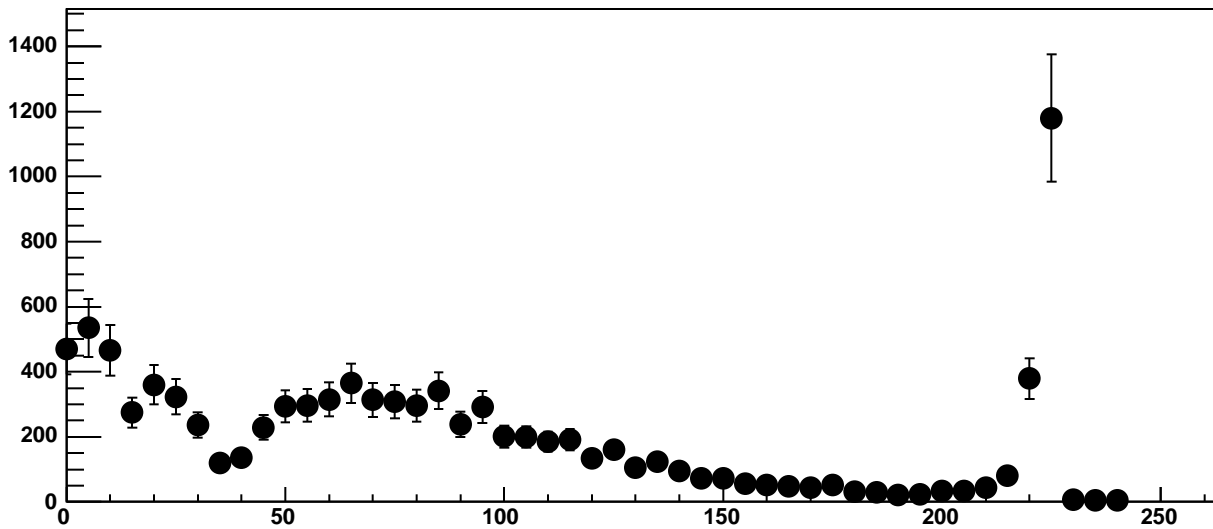


Chip 1, Channel 17, Enable 5!, DAC=4000, ADC Mean vs Hold

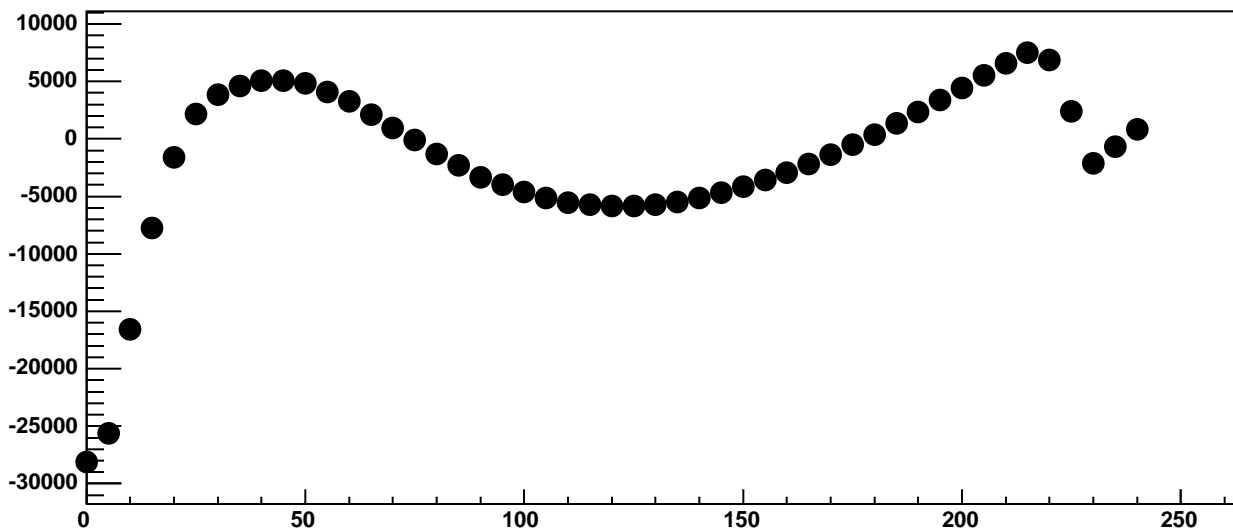


χ^2 / ndf	5.365e+06 / 42
p0	-3.485e+07 ± 1.345e+05
p1	-5688 ± 12.6
p2	3.488e+07 ± 1.345e+05
p3	5640 ± 12.47

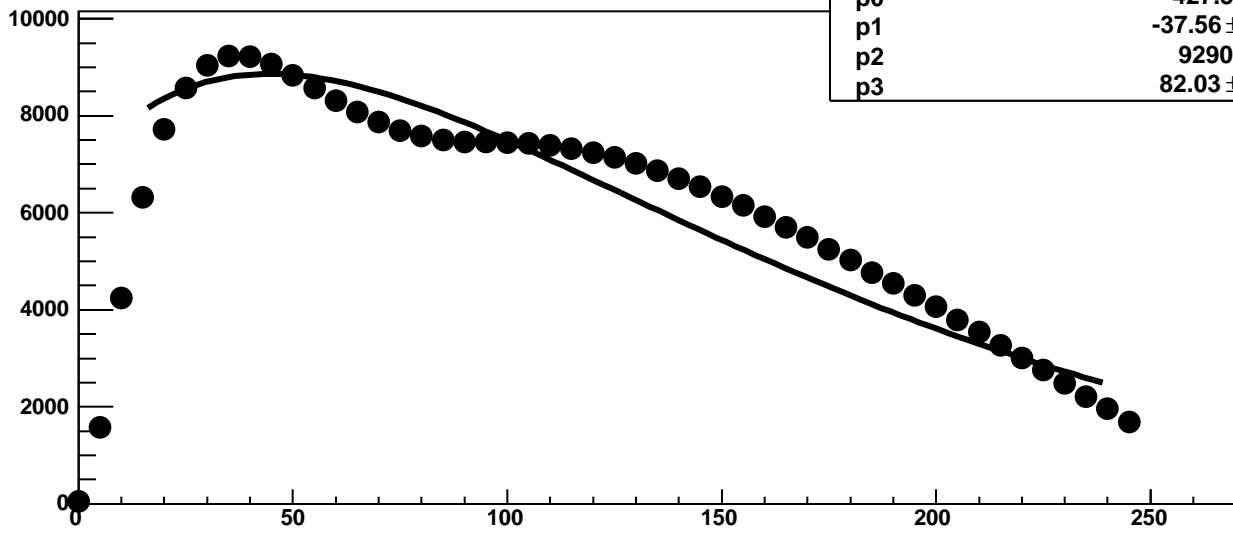
Chip 1, Channel 17, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 1, Channel 17, Enable 5!, DAC=4000, ADC Residuals vs Hold

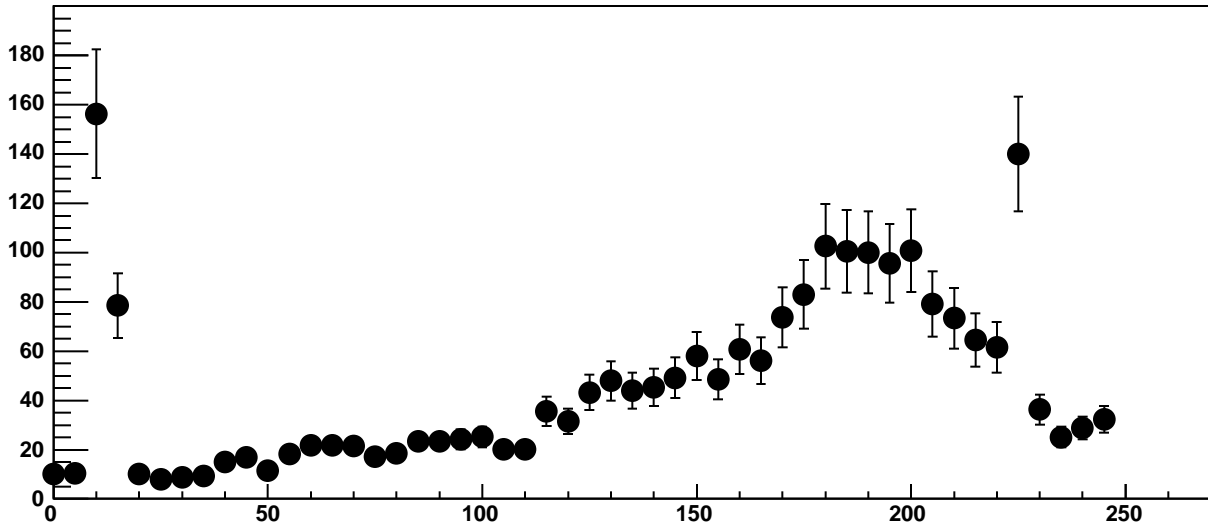


Chip 2, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold

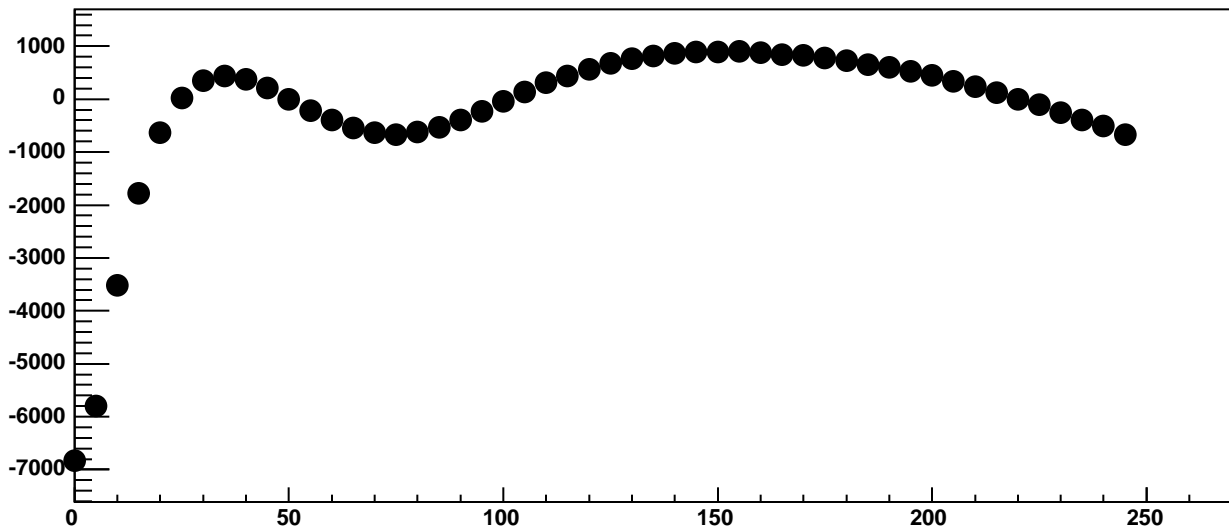


χ^2 / ndf	3.59e+05 / 42
p0	-427.5 ± 20.87
p1	-37.56 ± 0.2128
p2	9290 ± 20.04
p3	82.03 ± 0.2012

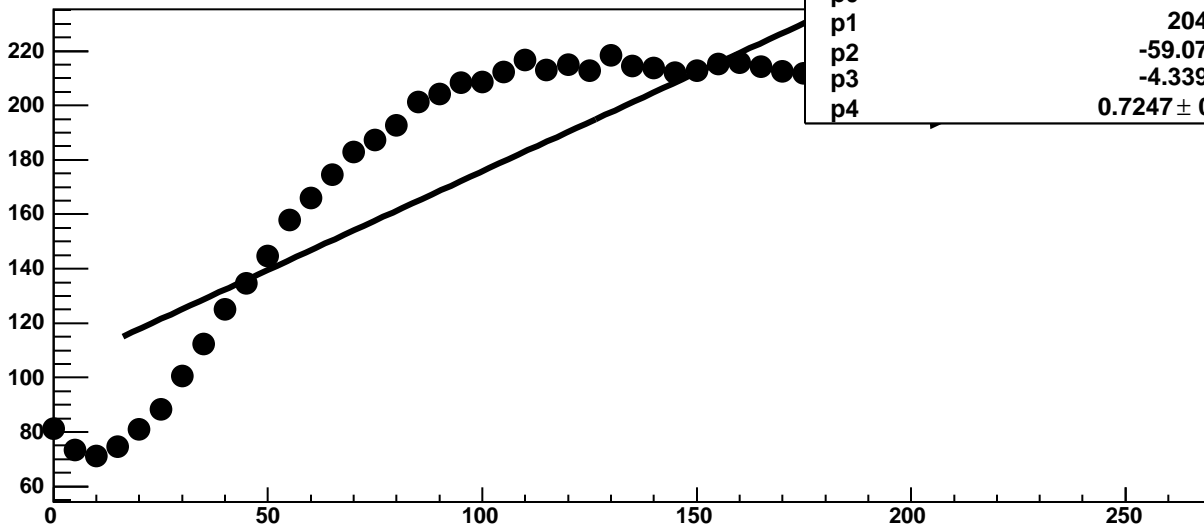
Chip 2, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold



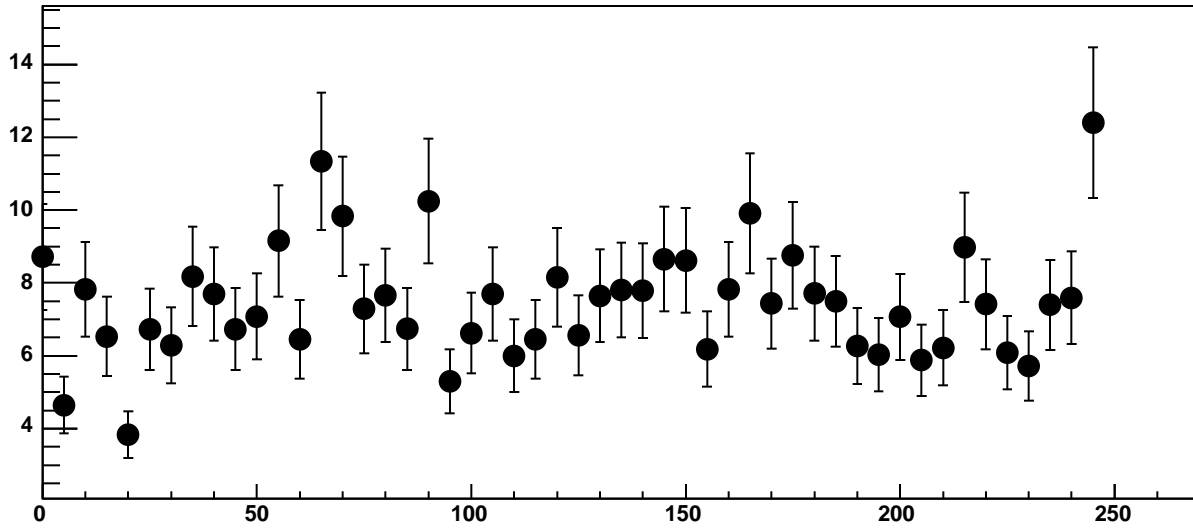
Chip 2, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold



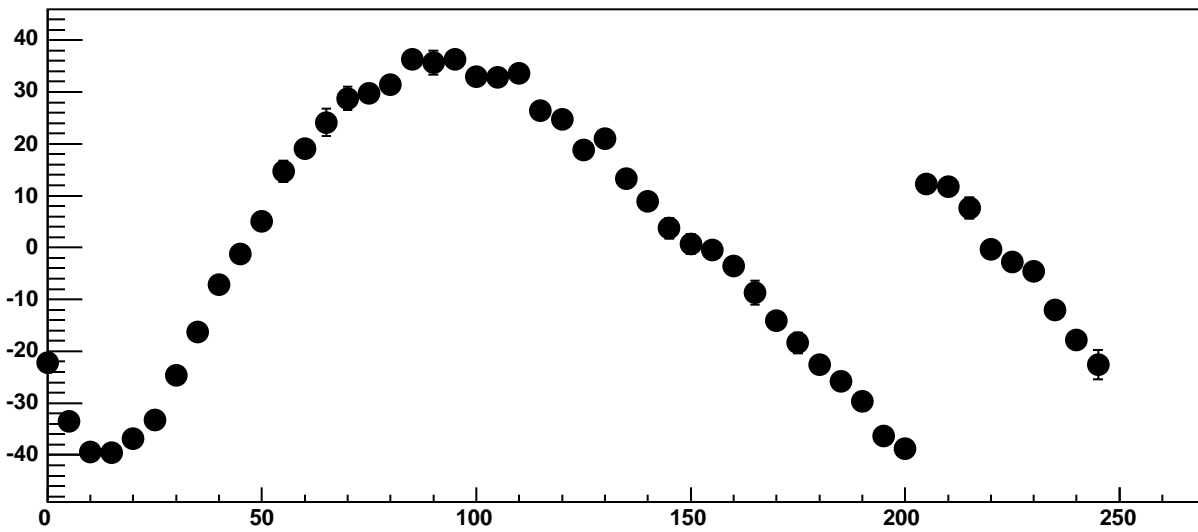
Chip 2, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold



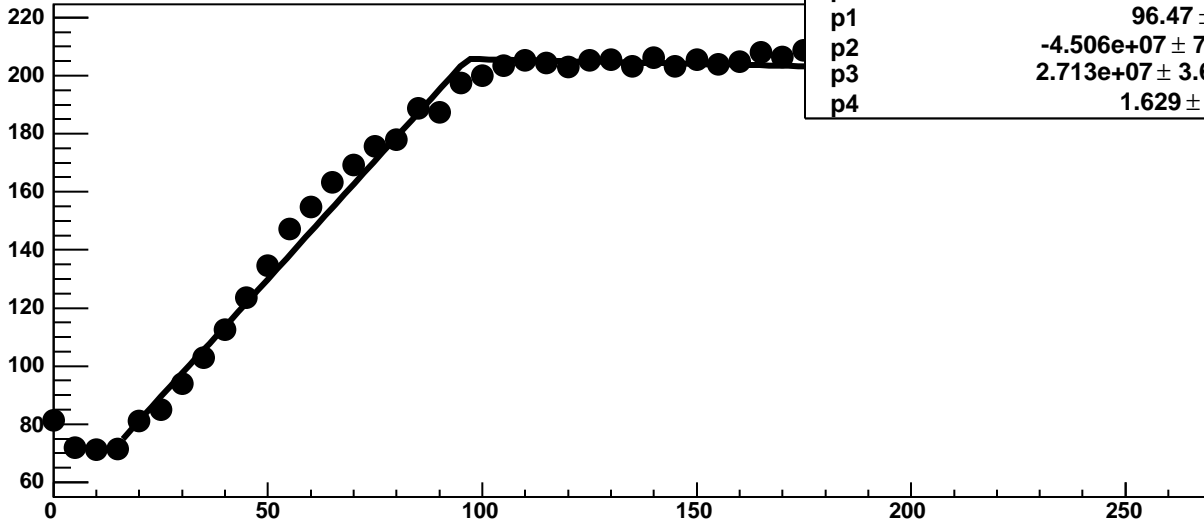
Chip 2, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold

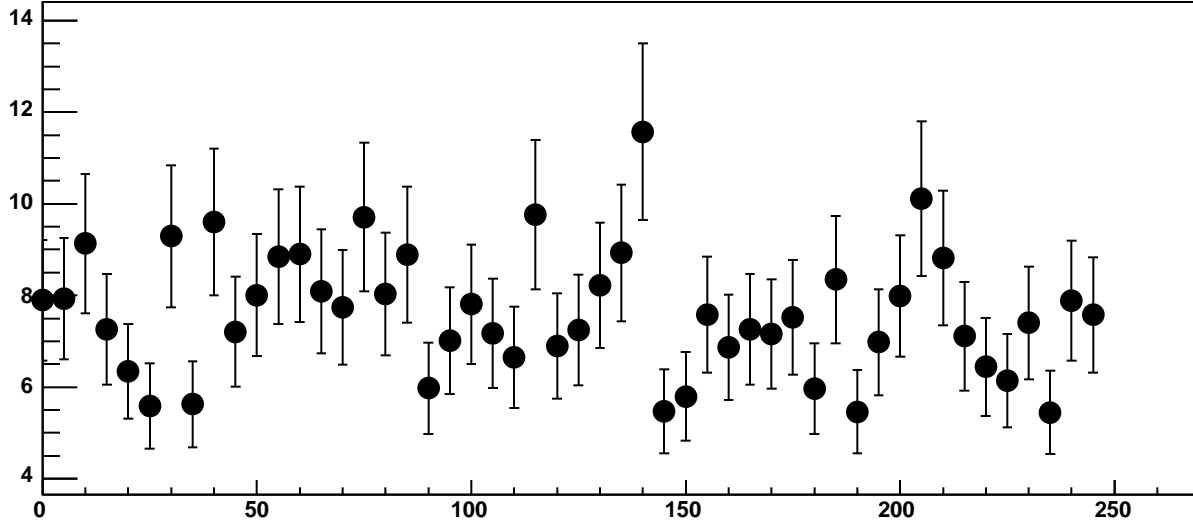


Chip 2, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

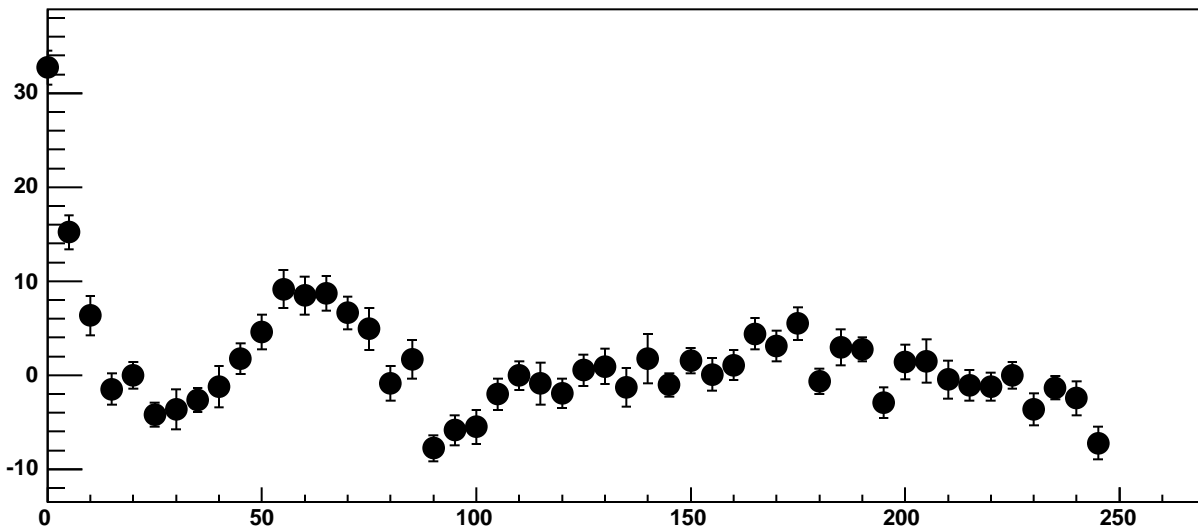


χ^2 / ndf	209.2 / 41
p0	205.7 ± 0.6199
p1	96.47 ± 0.6144
p2	$-4.506e+07 \pm 7.86e+05$
p3	$2.713e+07 \pm 3.668e+05$
p4	1.629 ± 0.01574

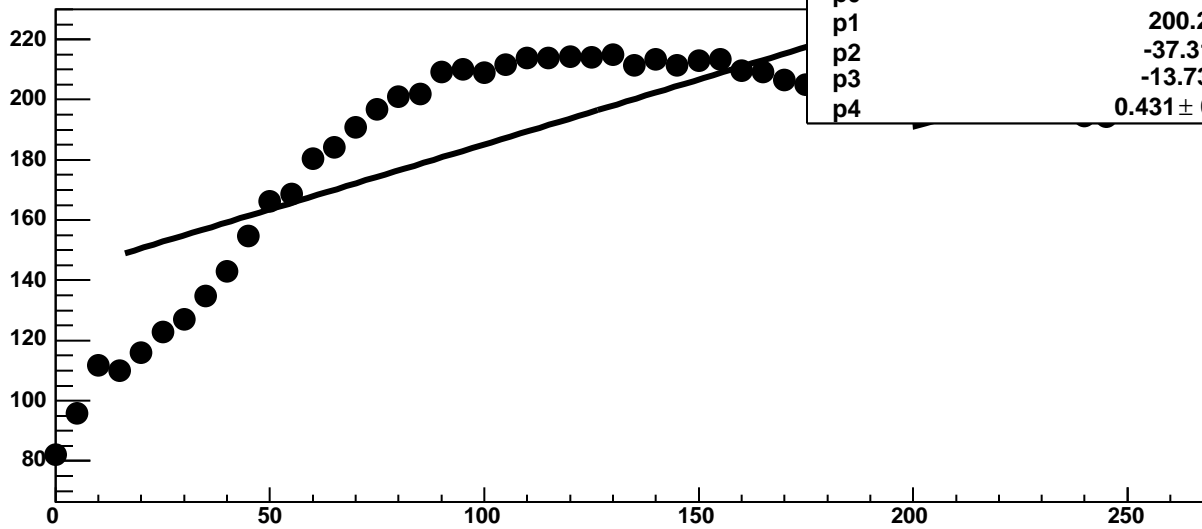
Chip 2, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

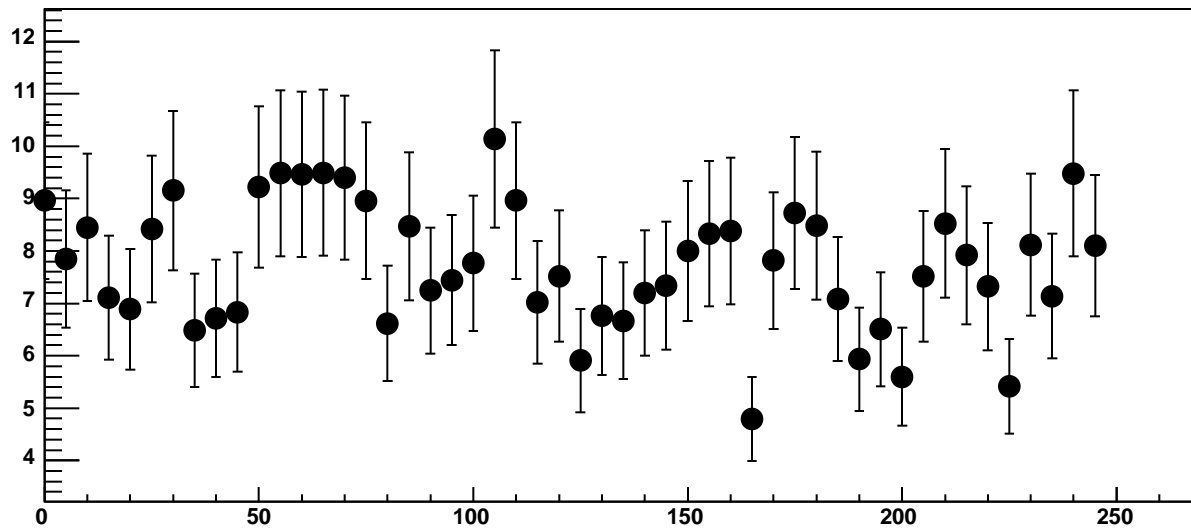


Chip 2, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold

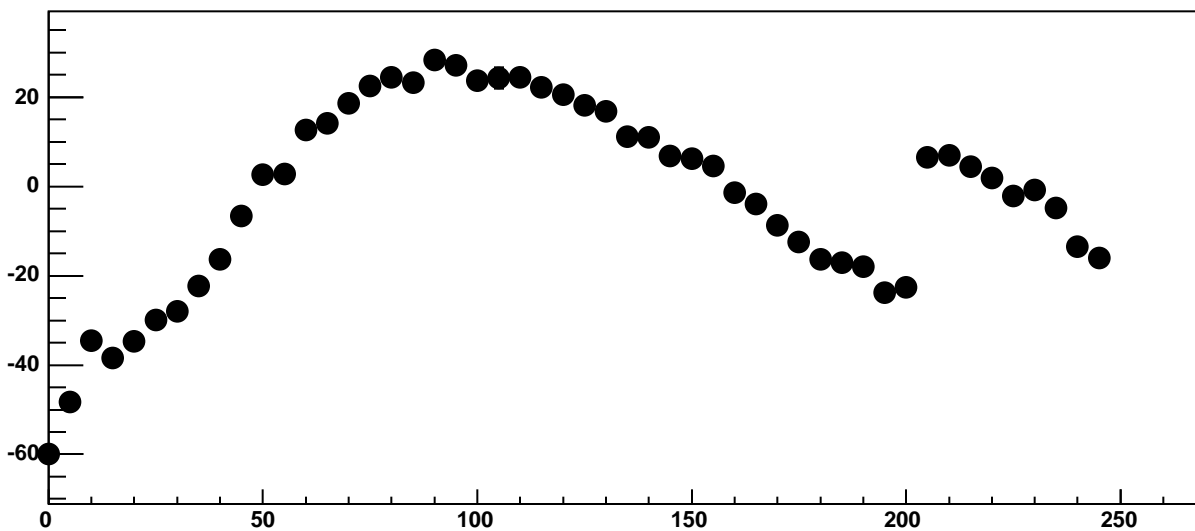


χ^2 / ndf	5297 / 41
p0	228.3 ± 2.066
p1	200.2 ± 6.366
p2	-37.31 ± 4.217
p3	-13.73 ± 26.89
p4	0.431 ± 0.00351

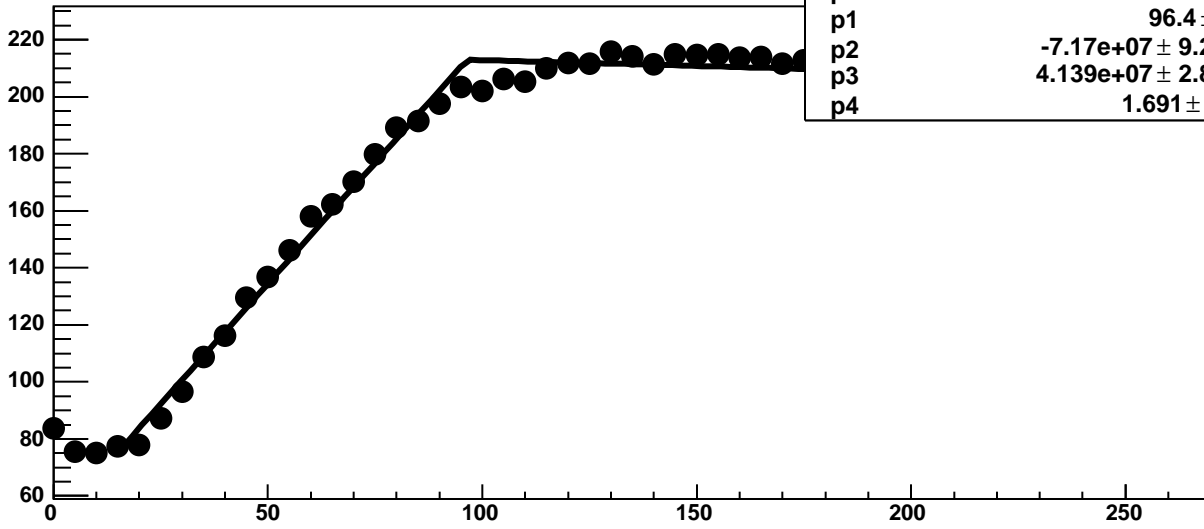
Chip 2, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold

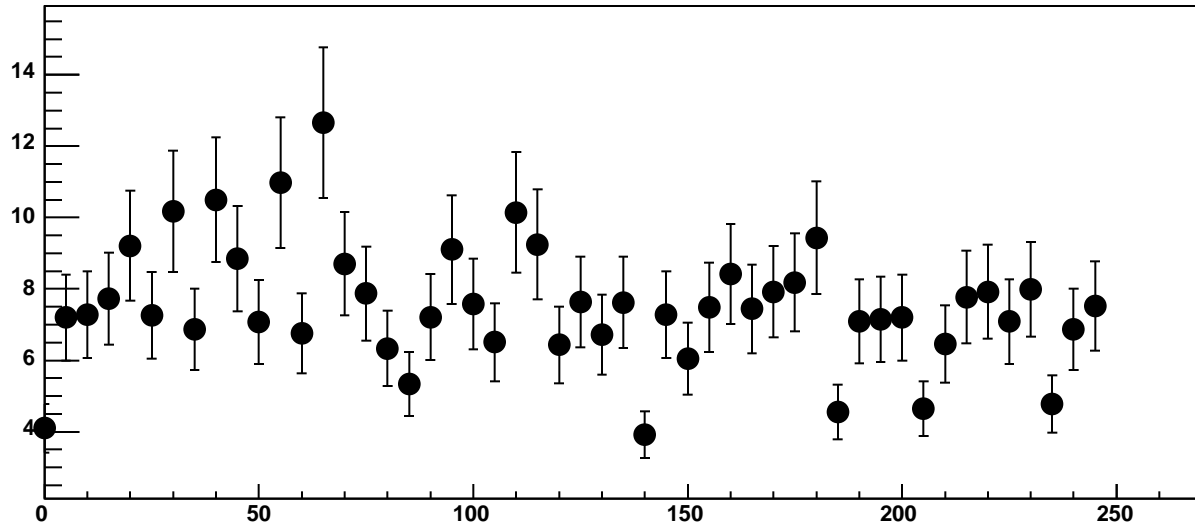


Chip 2, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

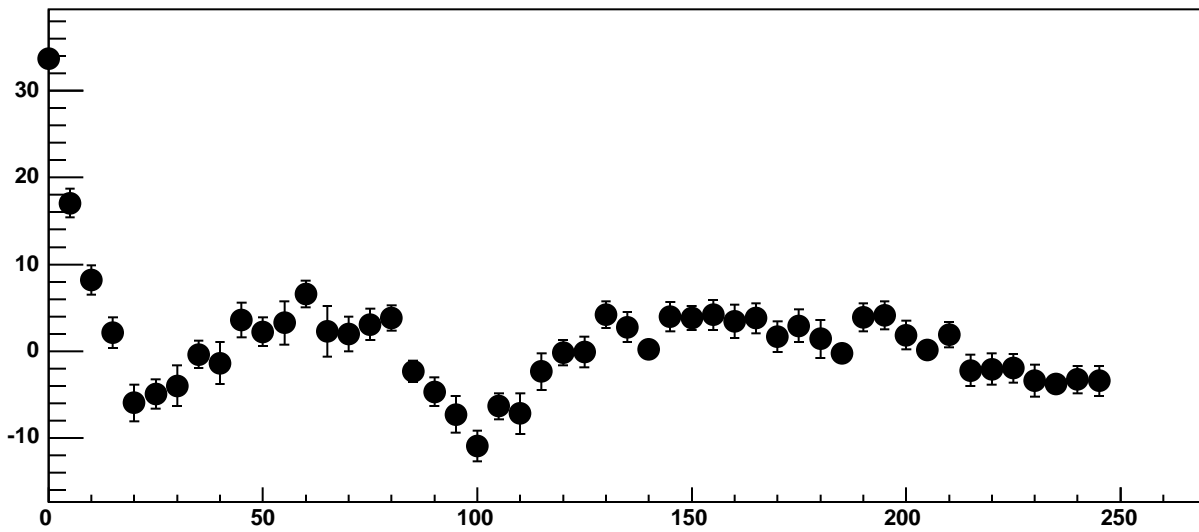


χ^2 / ndf	229.6 / 41
p0	213 ± 0.5838
p1	96.4 ± 0.5743
p2	-7.17e+07 ± 9.241e+05
p3	4.139e+07 ± 2.894e+05
p4	1.691 ± 0.01736

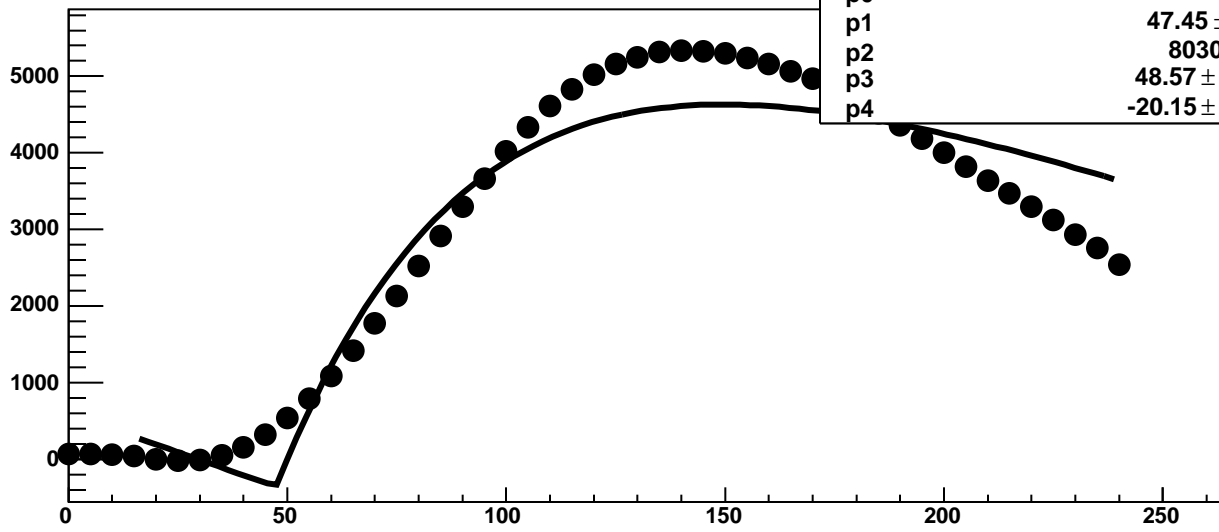
Chip 2, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



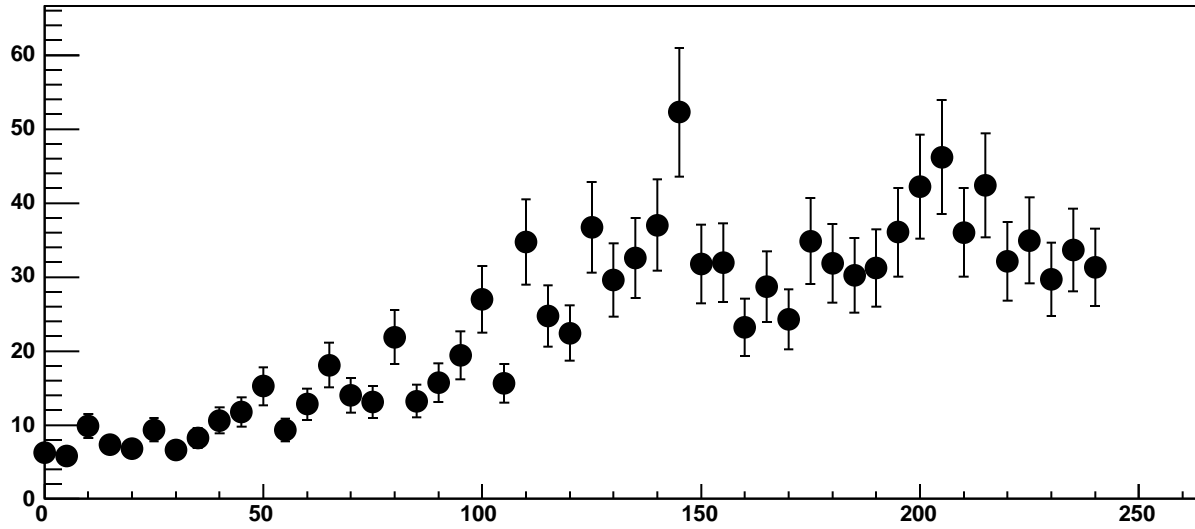
Chip 2, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold



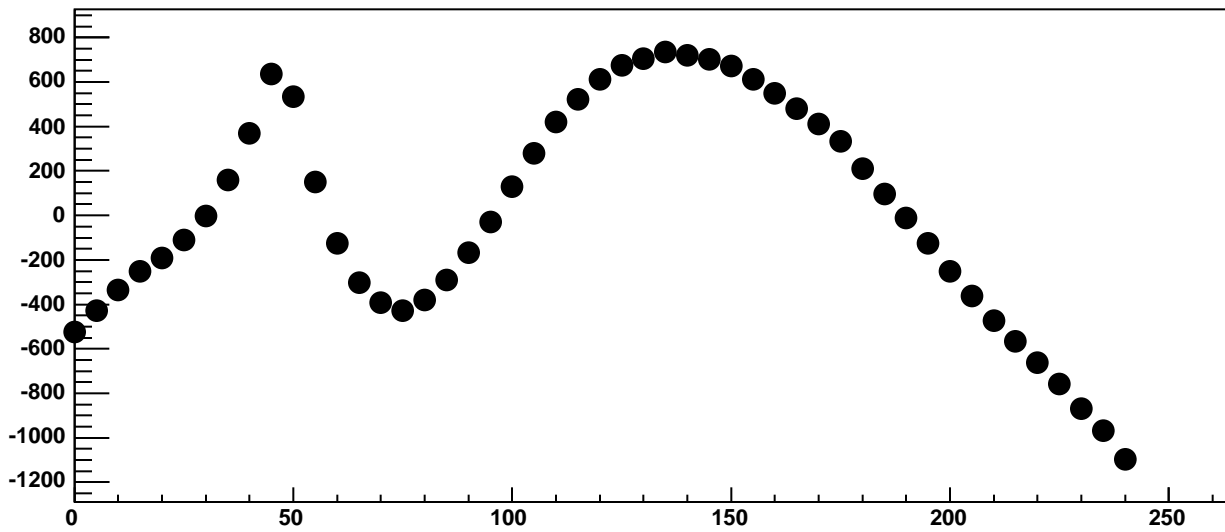
Chip 2, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold



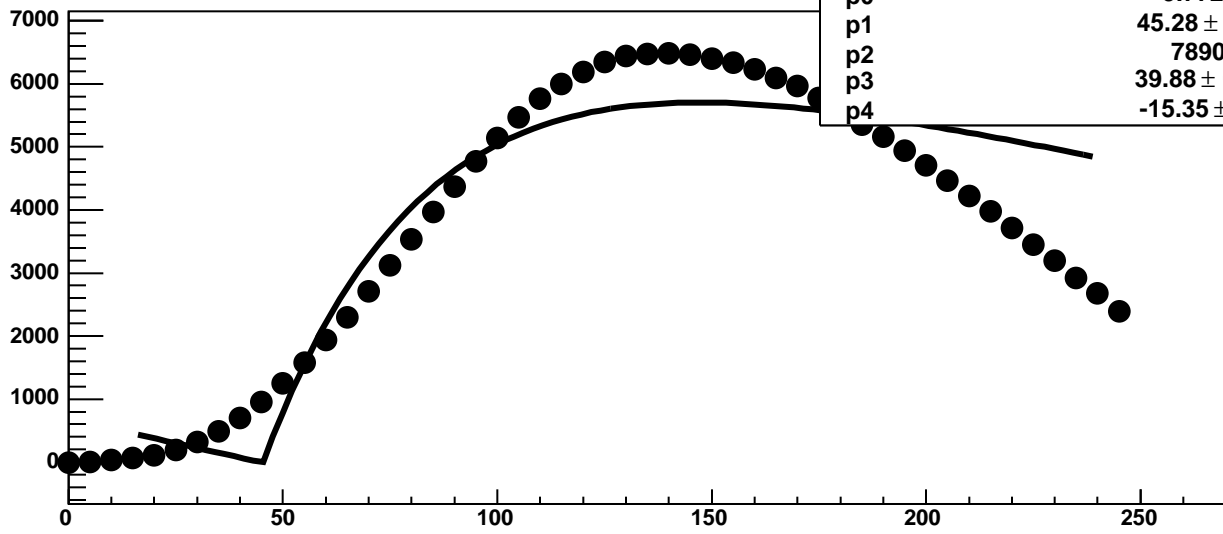
Chip 2, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold

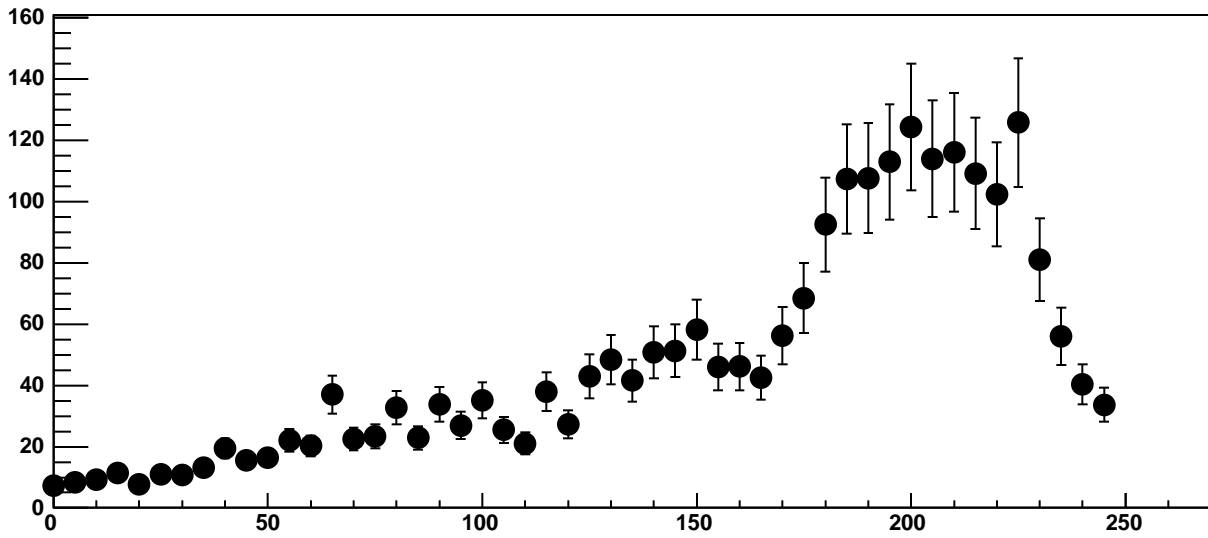


Chip 2, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold

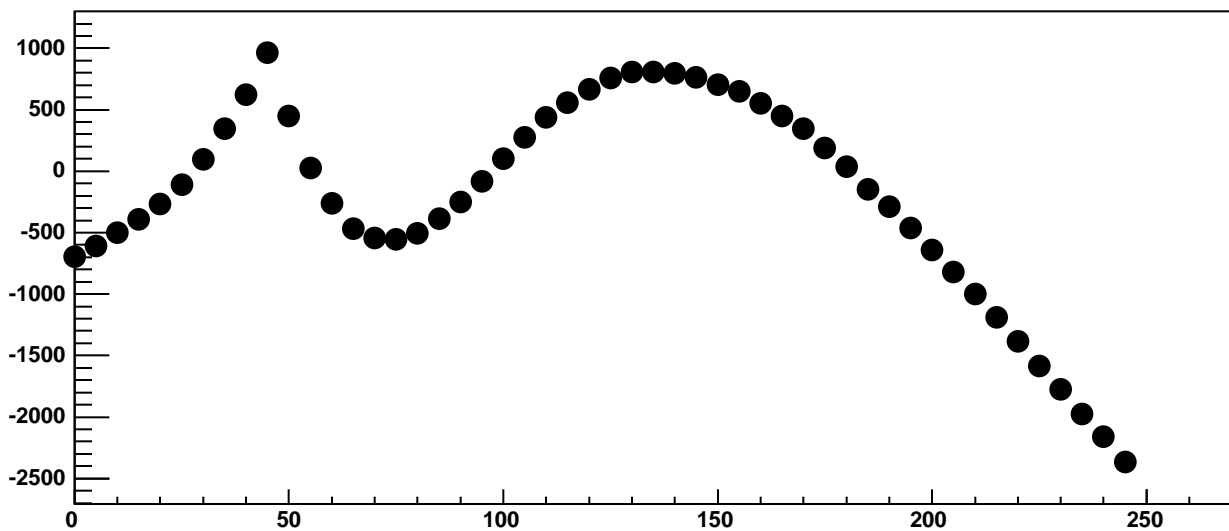


χ^2 / ndf	3.752e+05 / 41
p0	-9.772 ± 1.519
p1	45.28 ± 0.02112
p2	7890 ± 11.87
p3	39.88 ± 0.07174
p4	-15.35 ± 0.0674

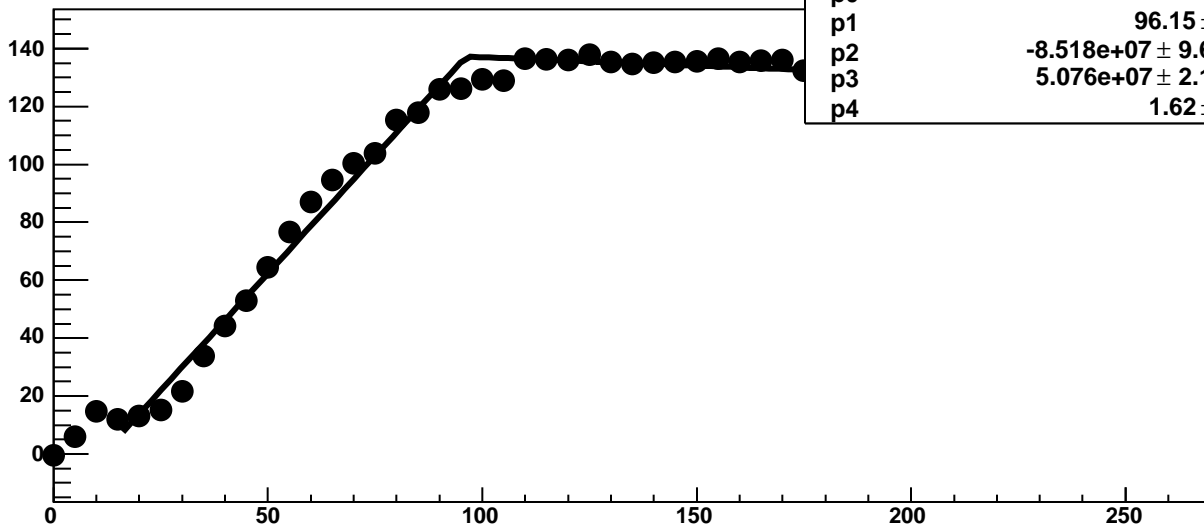
Chip 2, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold

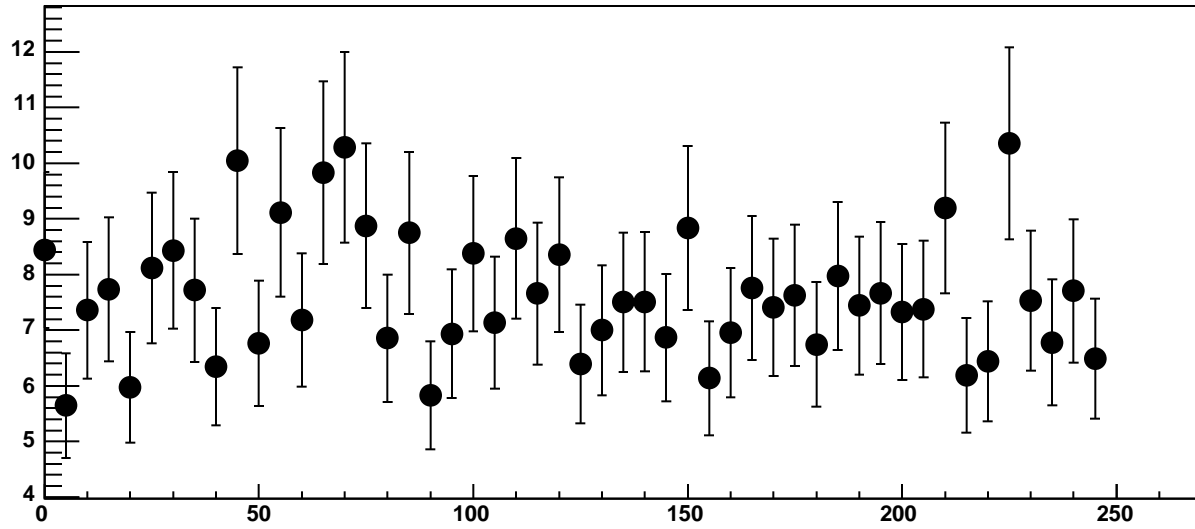


Chip 2, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

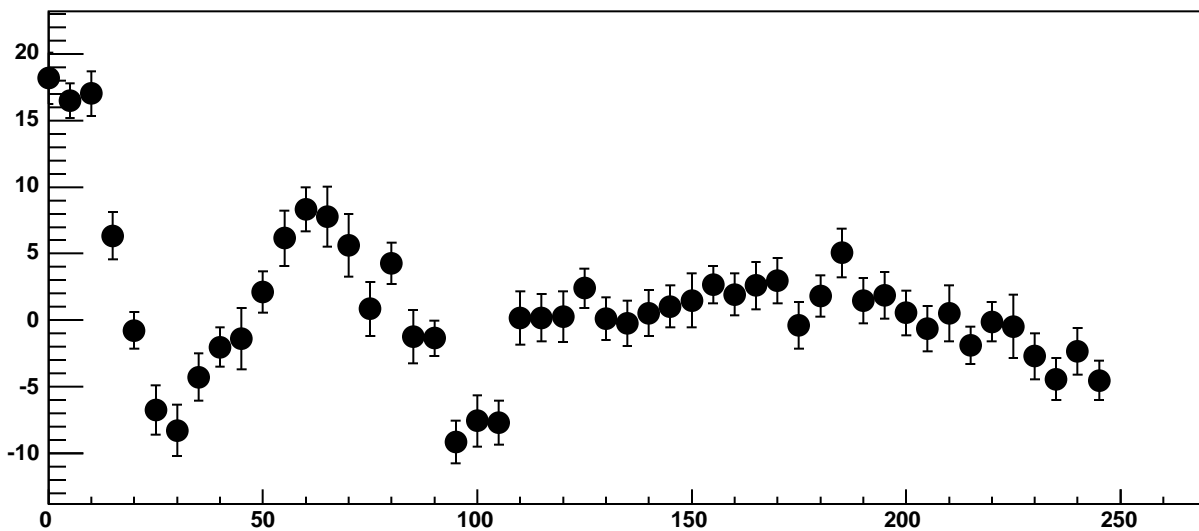


χ^2 / ndf	225.3 / 41
p0	137.2 ± 0.6293
p1	96.15 ± 0.6092
p2	$-8.518e+07 \pm 9.698e+05$
p3	$5.076e+07 \pm 2.104e+05$
p4	1.62 ± 0.0162

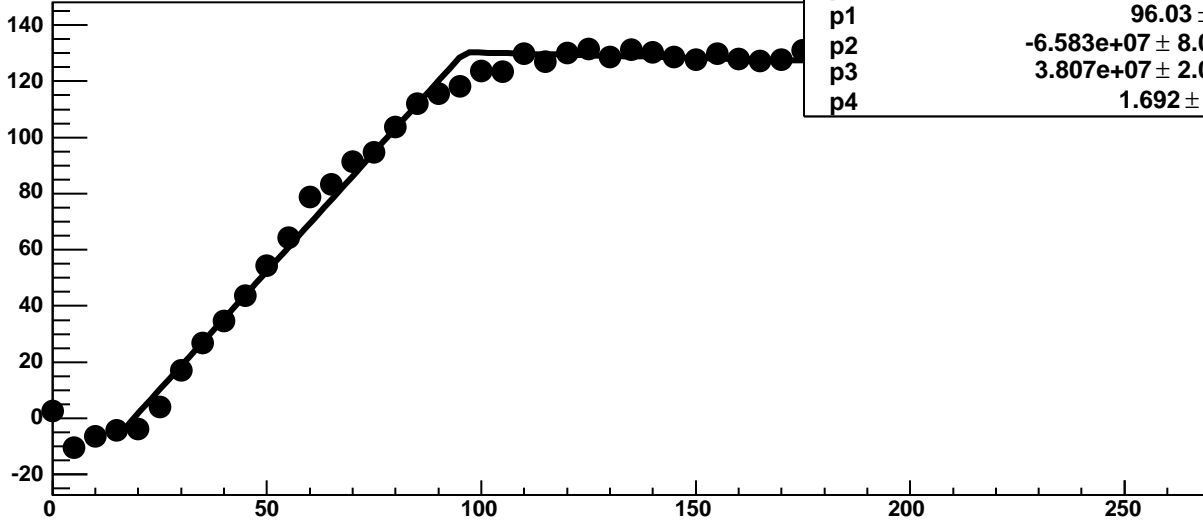
Chip 2, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

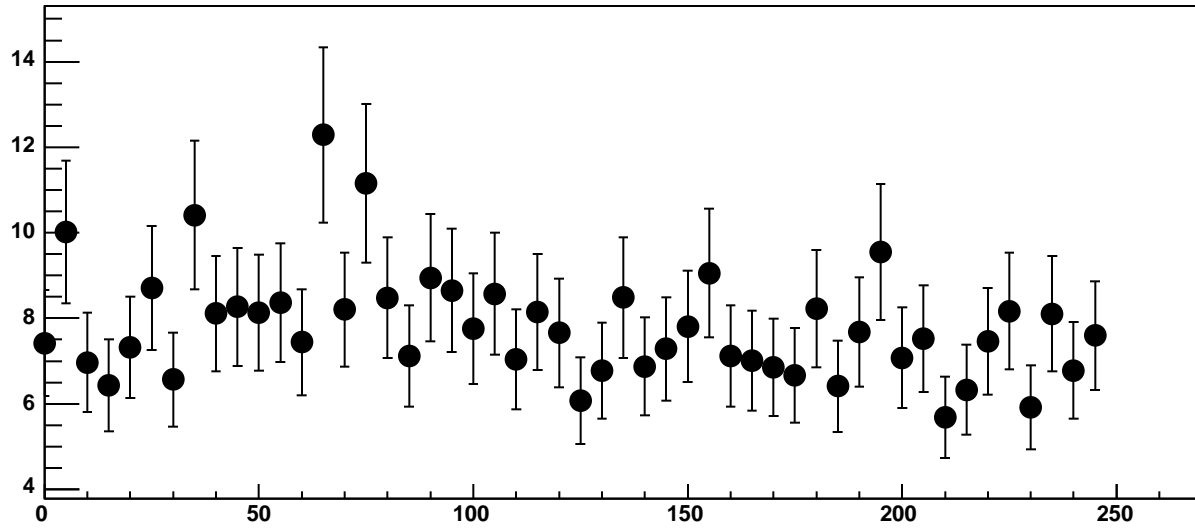


Chip 2, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

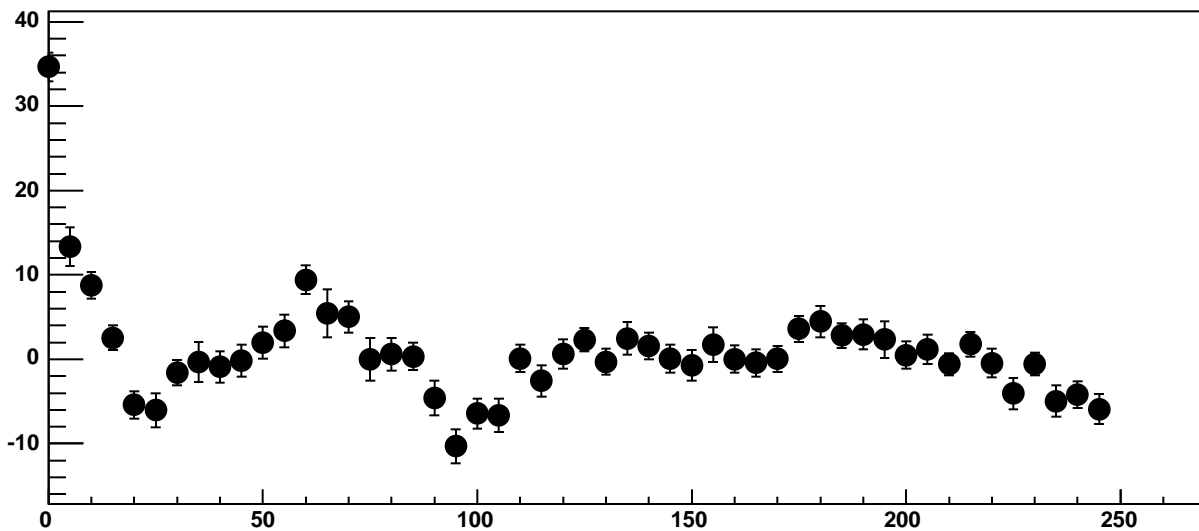


χ^2 / ndf	175.4 / 41
p0	130.3 ± 0.6271
p1	96.03 ± 0.6414
p2	$-6.583\text{e}+07 \pm 8.095\text{e}+05$
p3	$3.807\text{e}+07 \pm 2.036\text{e}+05$
p4	1.692 ± 0.01777

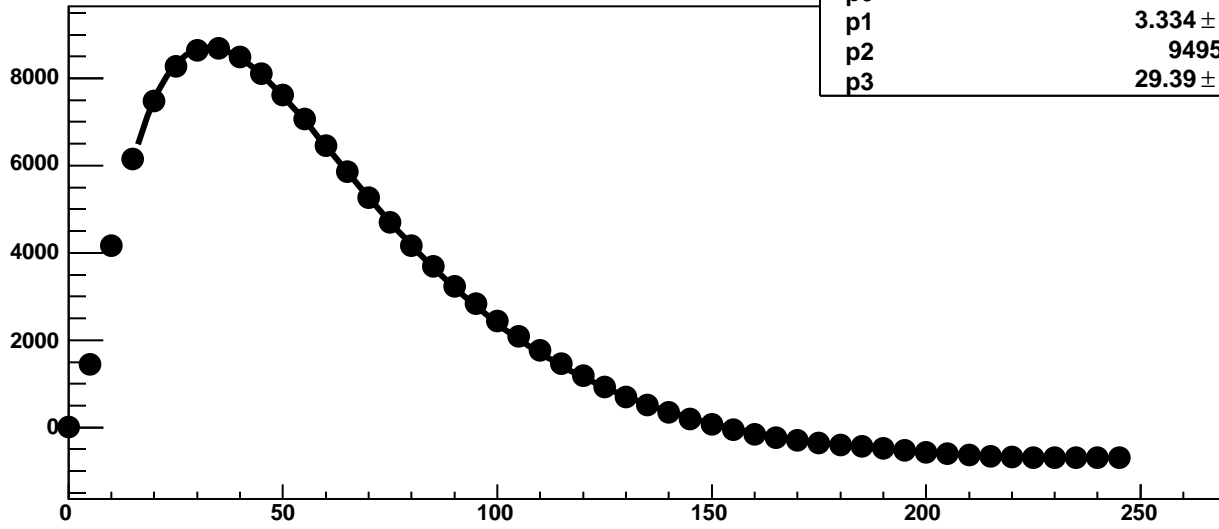
Chip 2, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

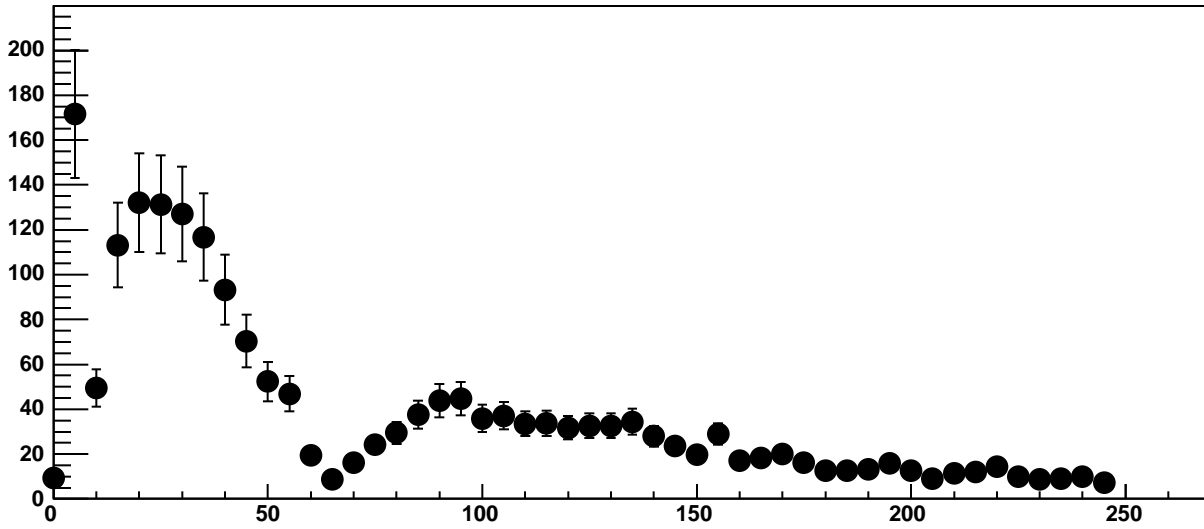


Chip 2, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold

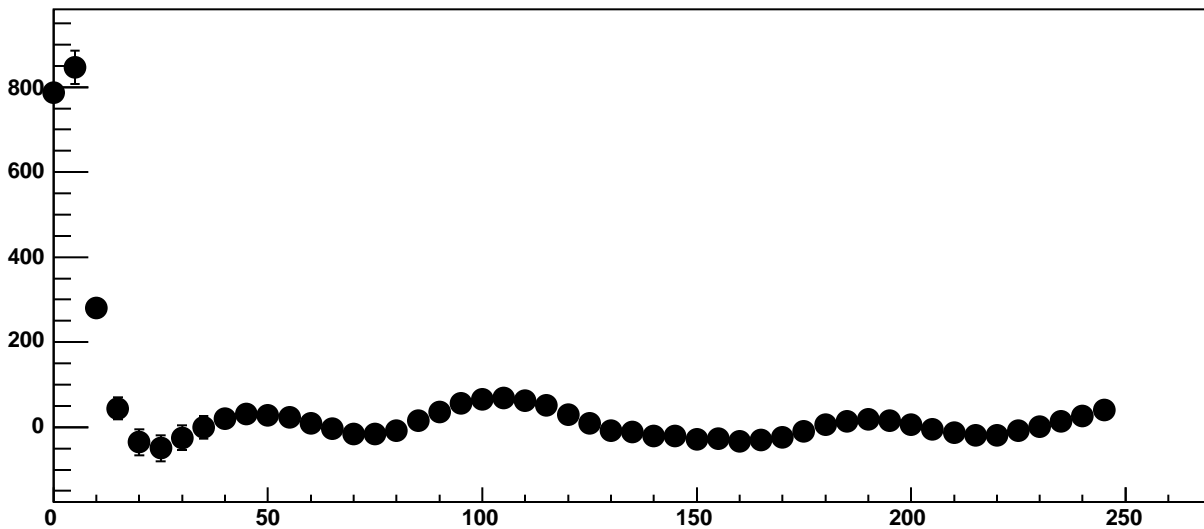


χ^2 / ndf	998.2 / 42
p0	-782.7 ± 0.9856
p1	3.334 ± 0.05352
p2	9495 ± 6.307
p3	29.39 ± 0.02185

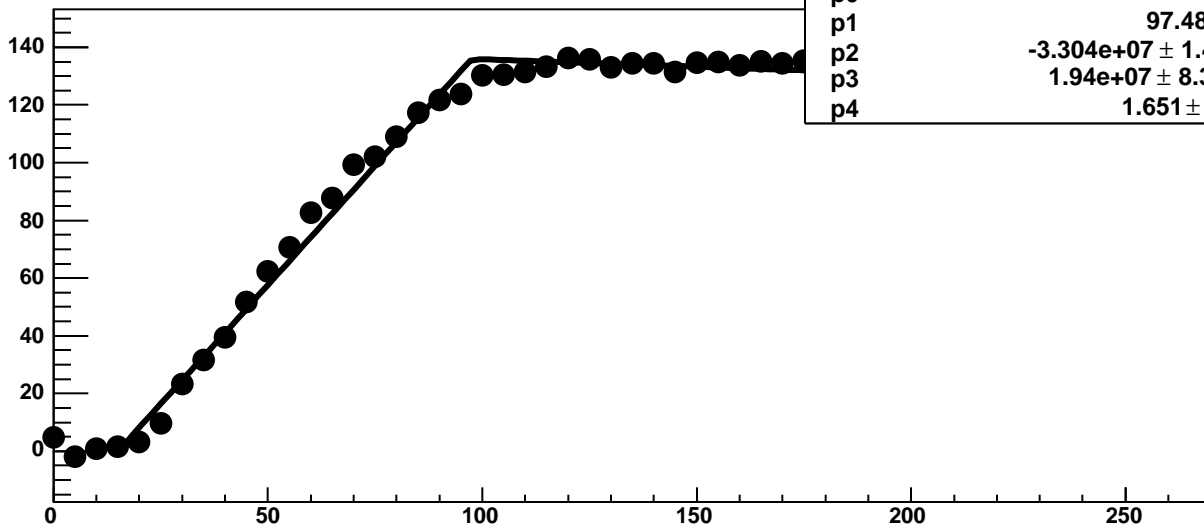
Chip 2, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold

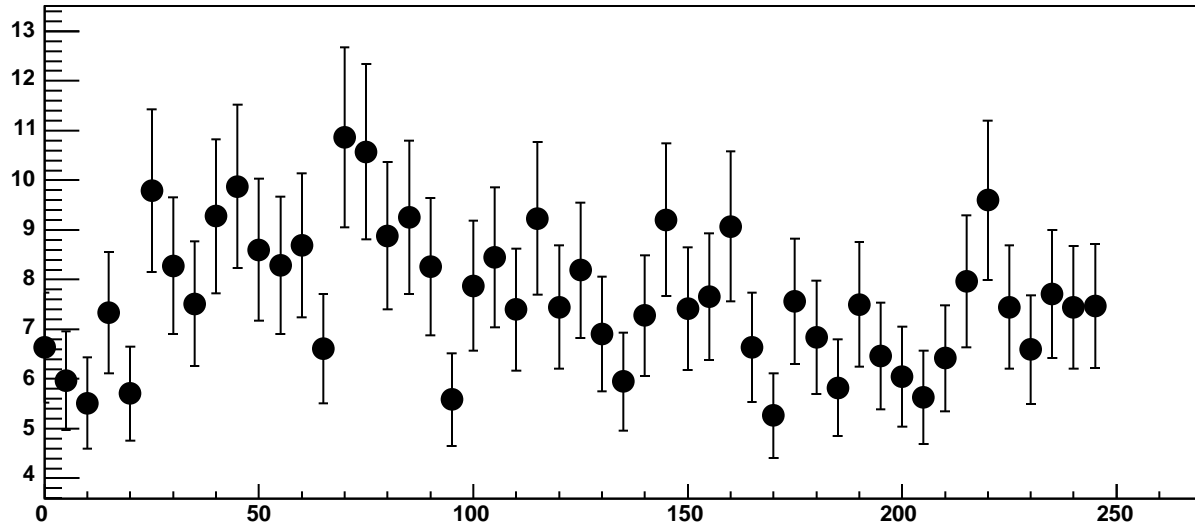


Chip 2, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

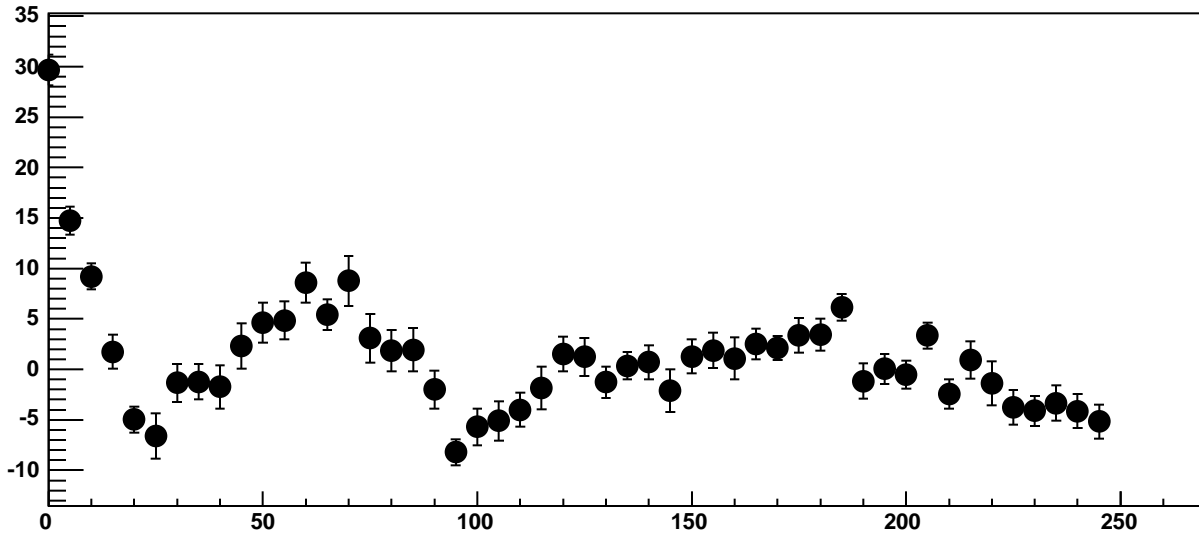


χ^2 / ndf	224.4 / 41
p0	136 ± 0.6303
p1	97.48 ± 0.622
p2	$-3.304\text{e}+07 \pm 1.465\text{e}+06$
p3	$1.94\text{e}+07 \pm 8.357\text{e}+05$
p4	1.651 ± 0.01652

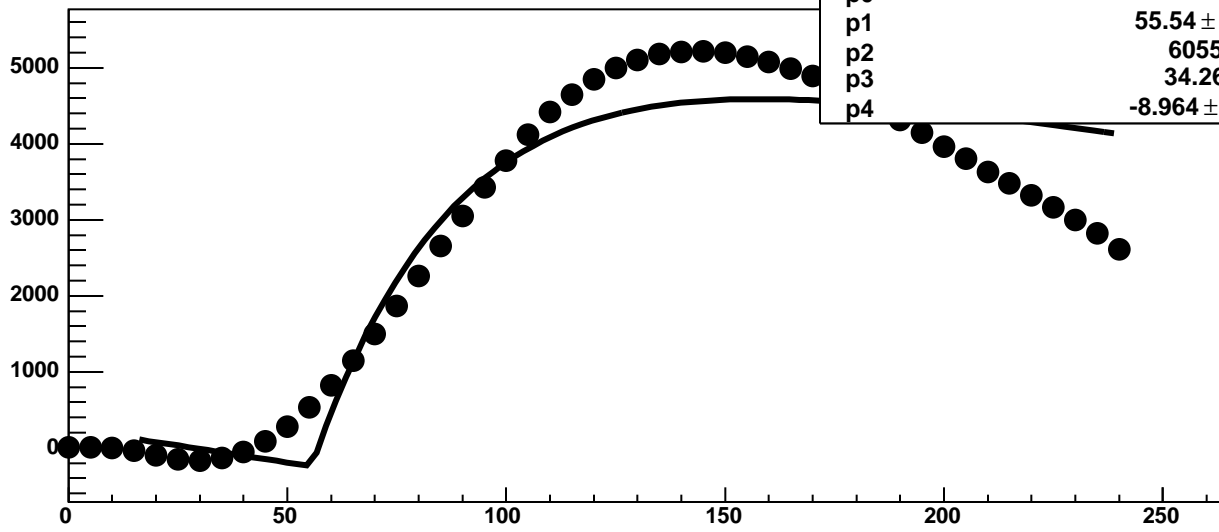
Chip 2, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

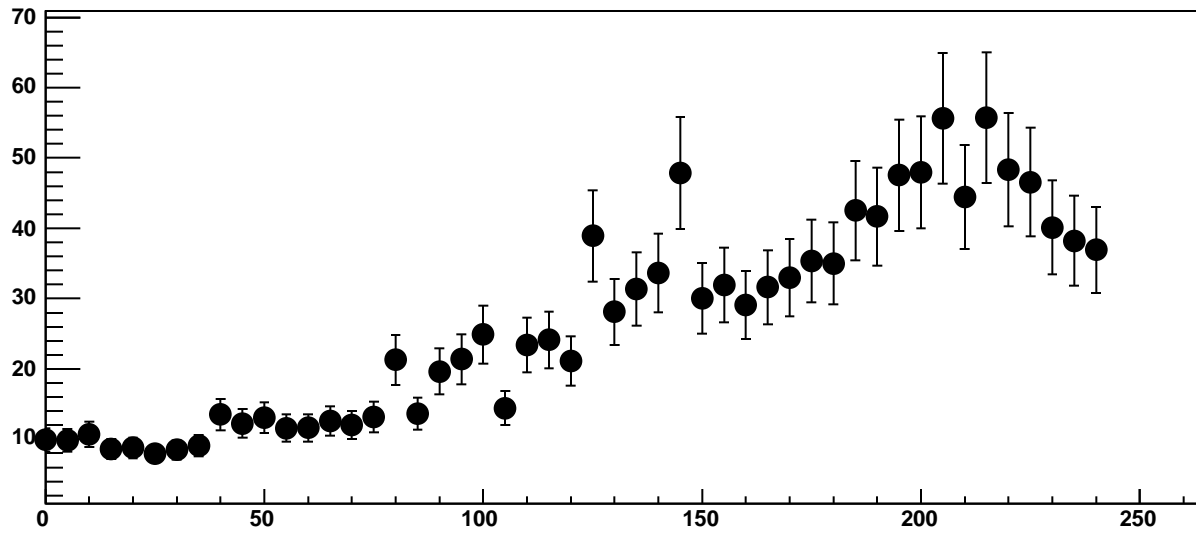


Chip 2, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

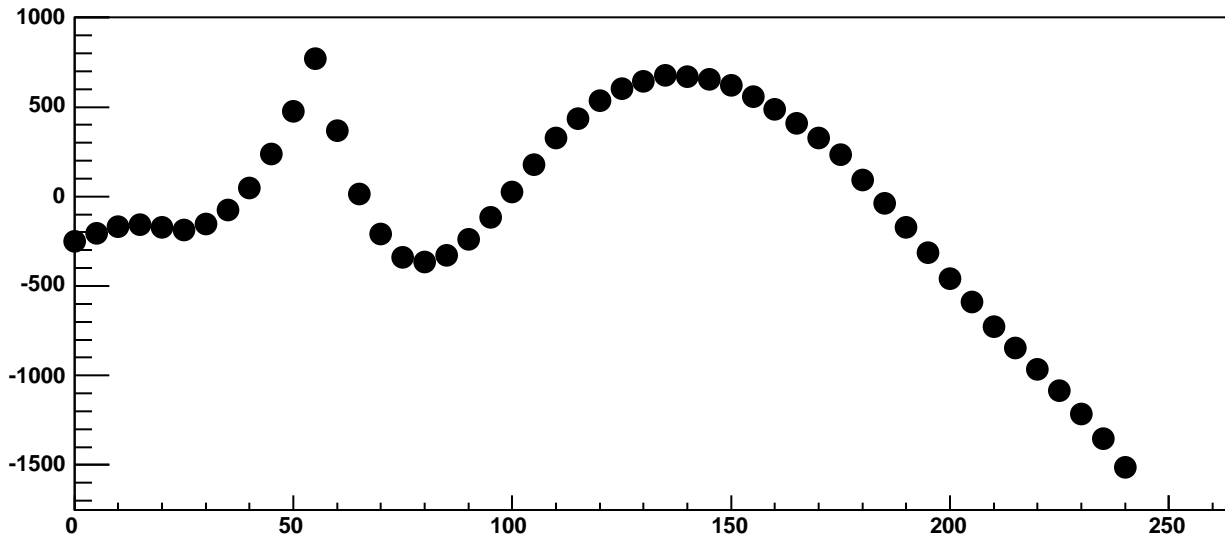


χ^2 / ndf	3.956e+05 / 41
p0	-243.7 ± 1.206
p1	55.54 ± 0.01719
p2	6055 ± 7.172
p3	34.26 ± 0.051
p4	-8.964 ± 0.04201

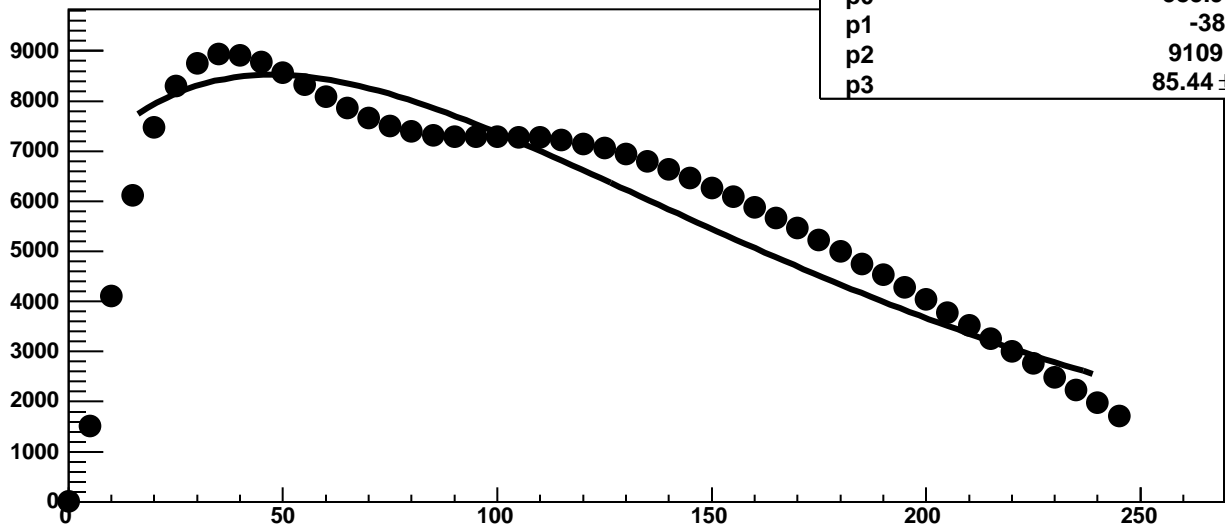
Chip 2, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold

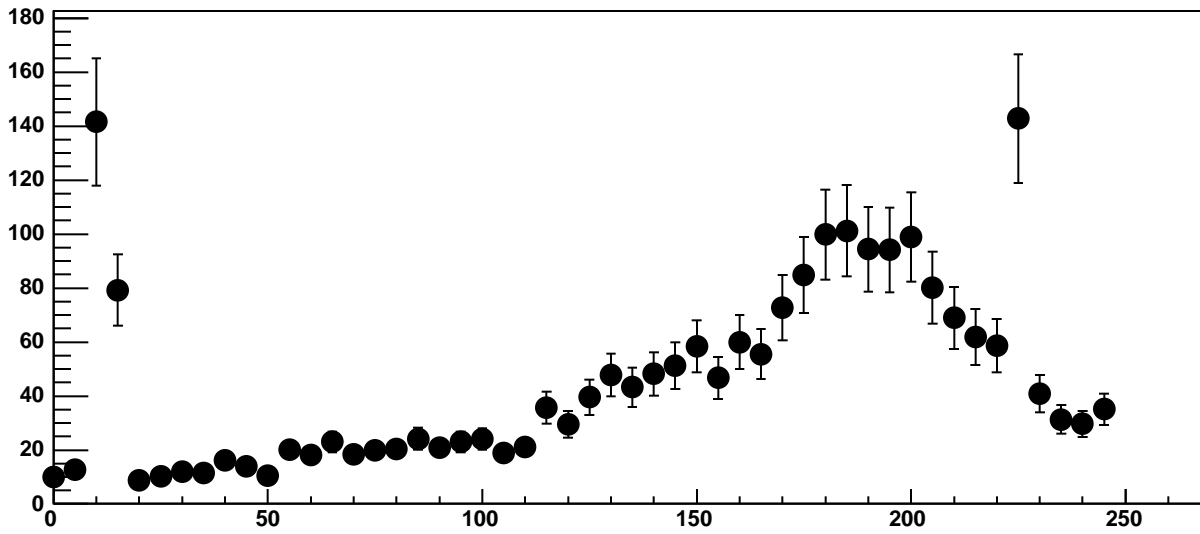


Chip 2, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold

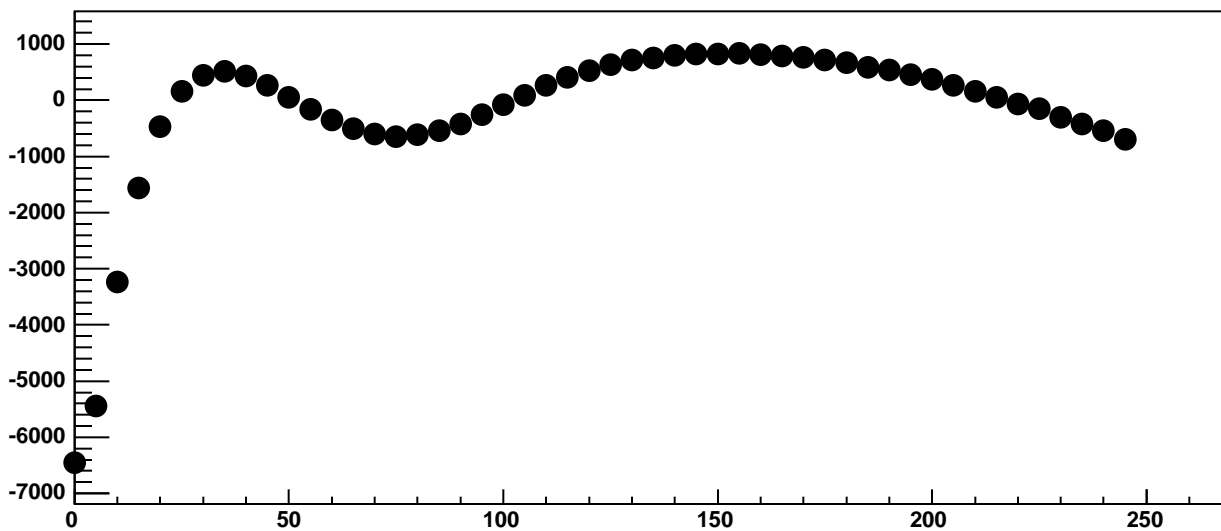


χ^2 / ndf	3.166e+05 / 42
p0	-585.9 ± 23.47
p1	-38 ± 0.221
p2	9109 ± 22.67
p3	85.44 ± 0.2191

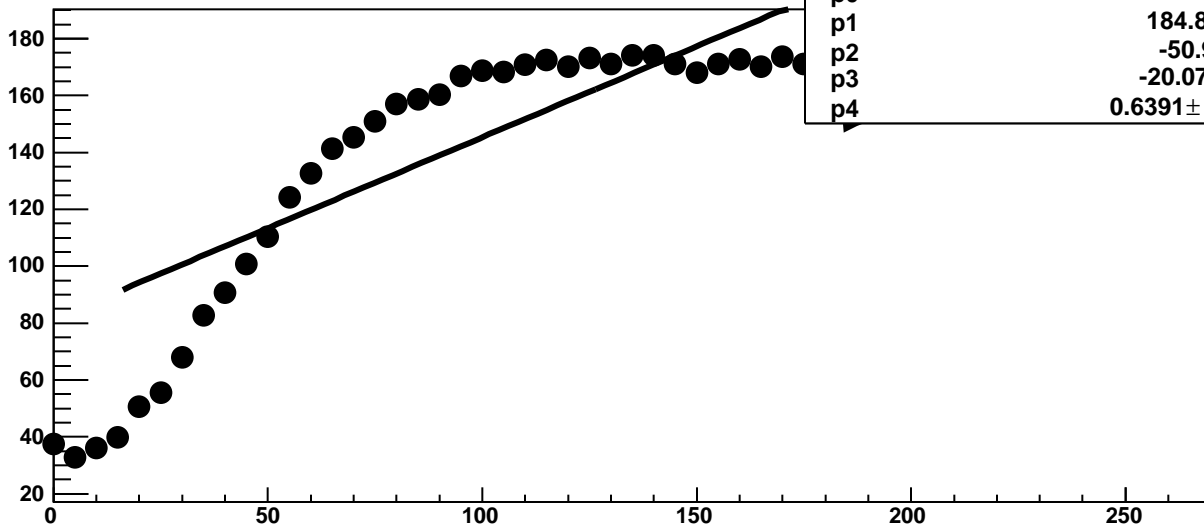
Chip 2, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold

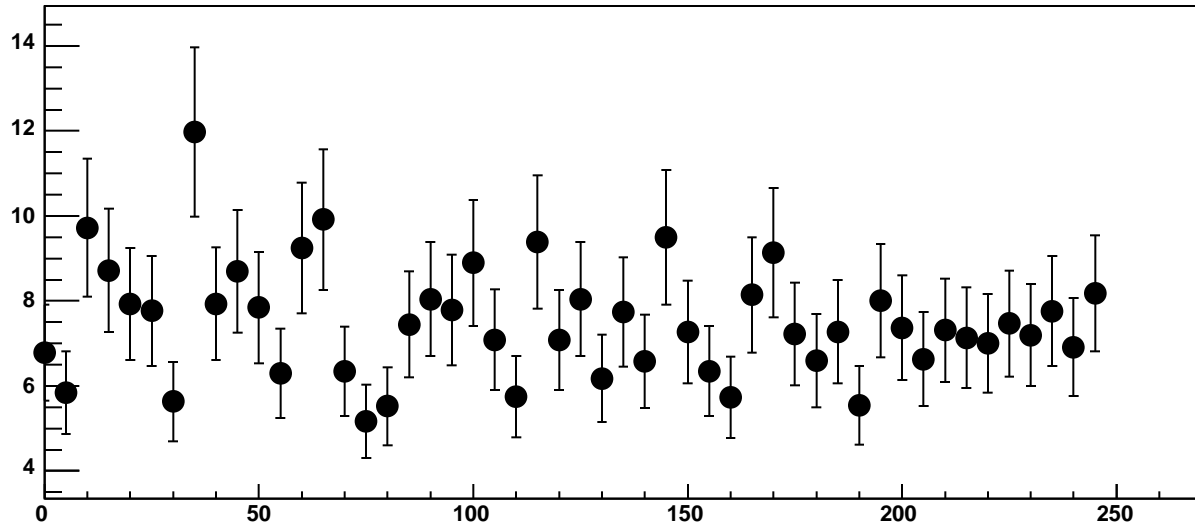


Chip 2, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

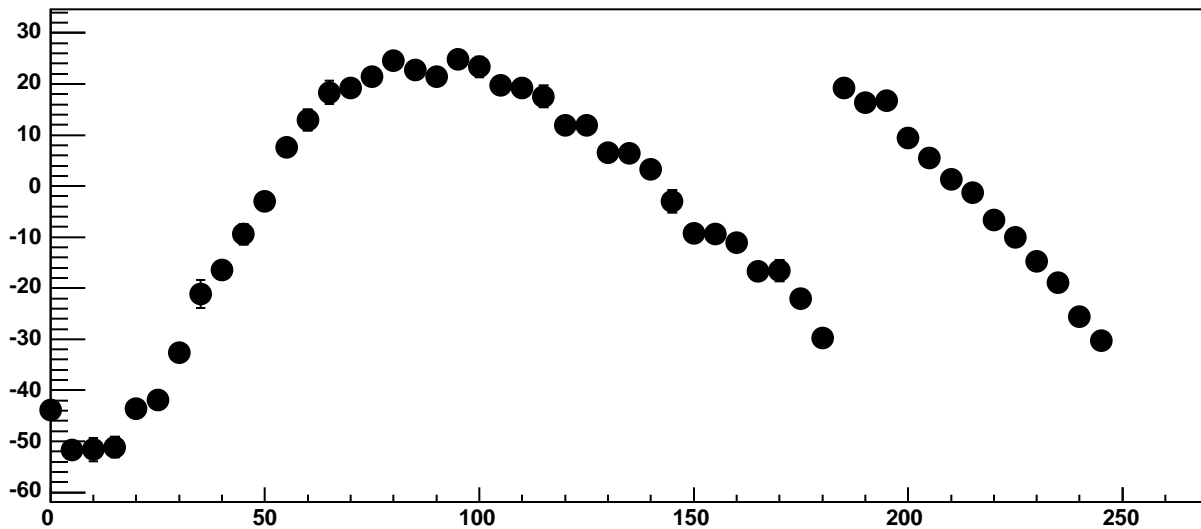


χ^2 / ndf	6576 / 41
p0	199.6 ± 1.277
p1	184.8 ± 2.982
p2	-50.9 ± 2.66
p3	-20.07 ± 40.28
p4	0.6391 ± 0.00155

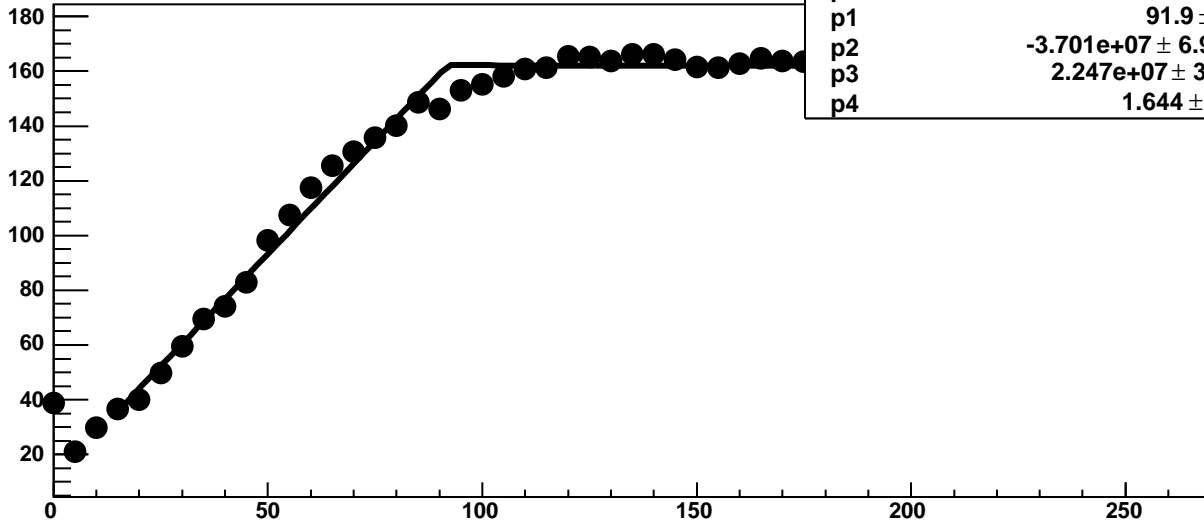
Chip 2, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold

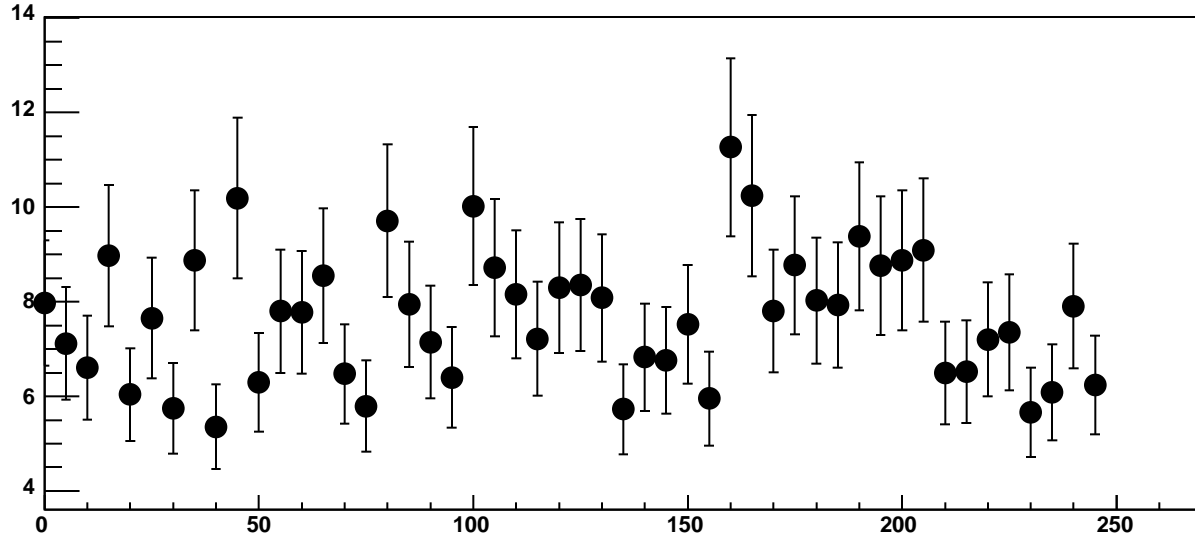


Chip 2, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

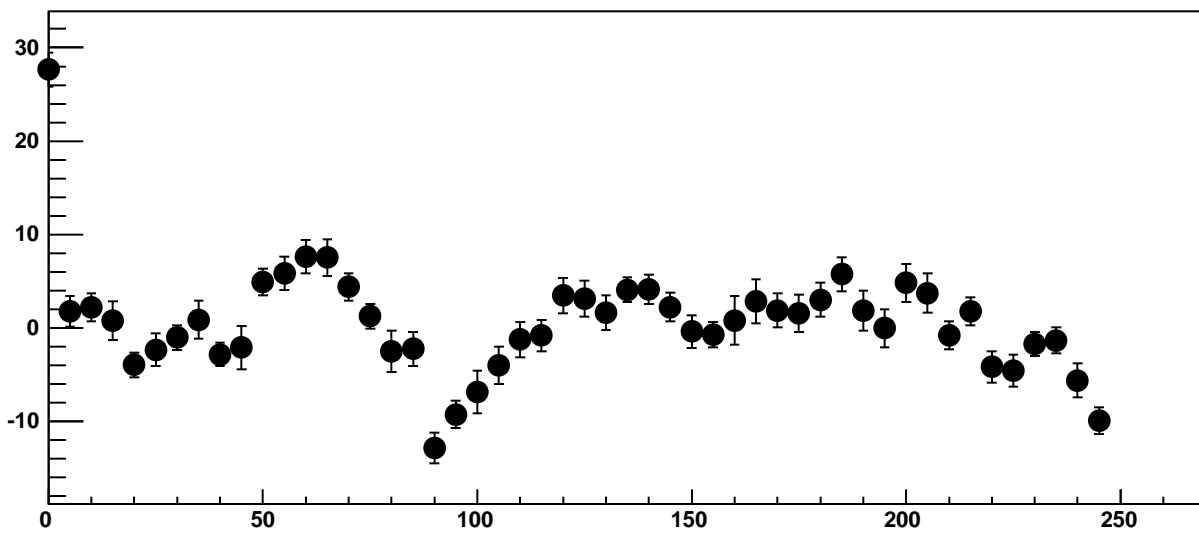


χ^2 / ndf	279.1 / 41
p0	162.2 ± 0.6317
p1	91.9 ± 0.6345
p2	$-3.701\text{e}+07 \pm 6.953\text{e}+05$
p3	$2.247\text{e}+07 \pm 3.34\text{e}+05$
p4	1.644 ± 0.01791

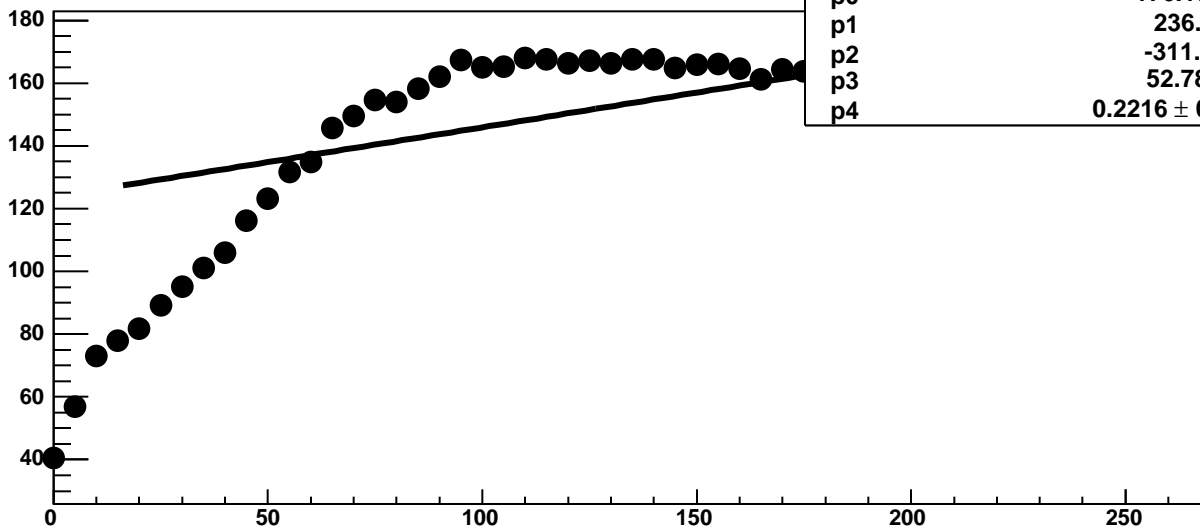
Chip 2, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

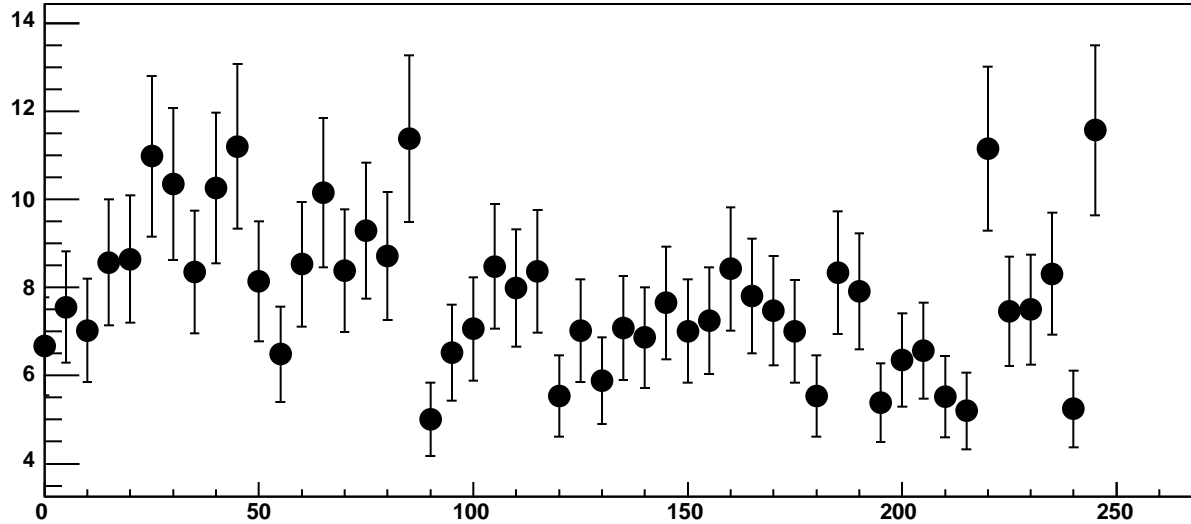


Chip 2, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold

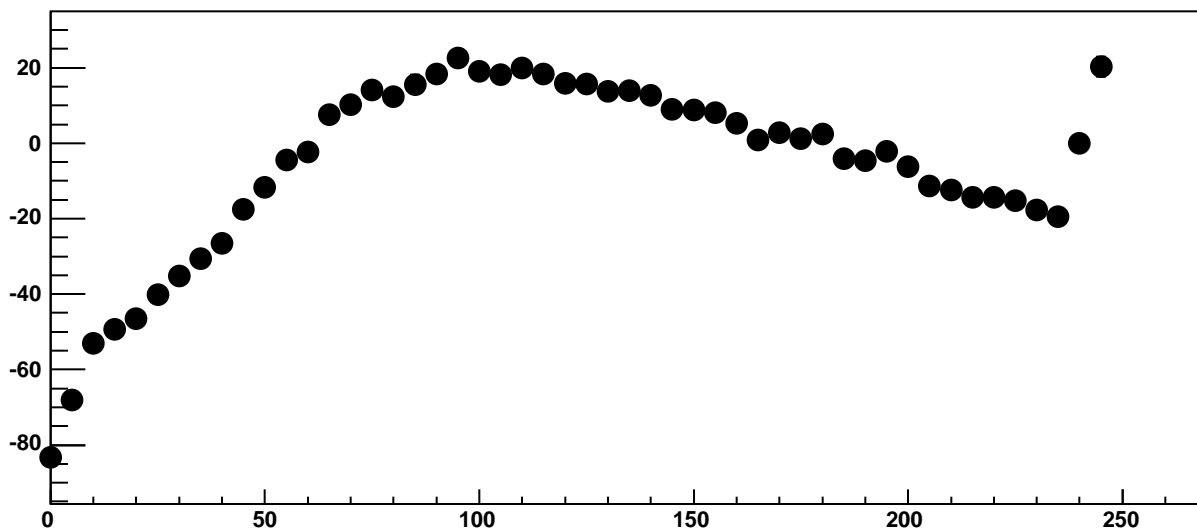


χ^2 / ndf	4444 / 41
p0	176.1 ± 0.7935
p1	236.2 ± 2.88
p2	-311.1 ± 224.6
p3	52.78 ± 32.55
p4	0.2216 ± 0.004113

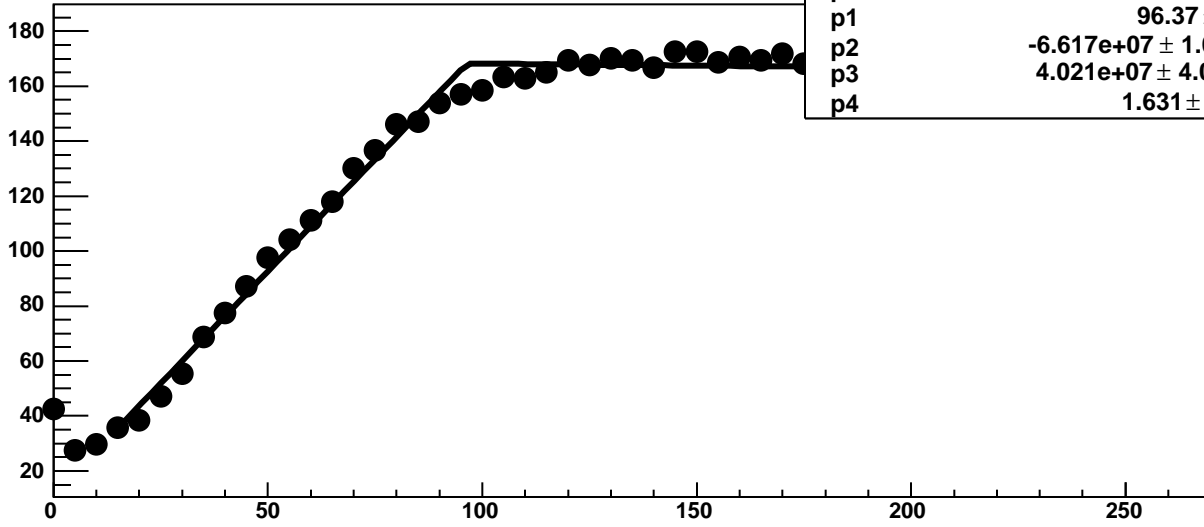
Chip 2, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold

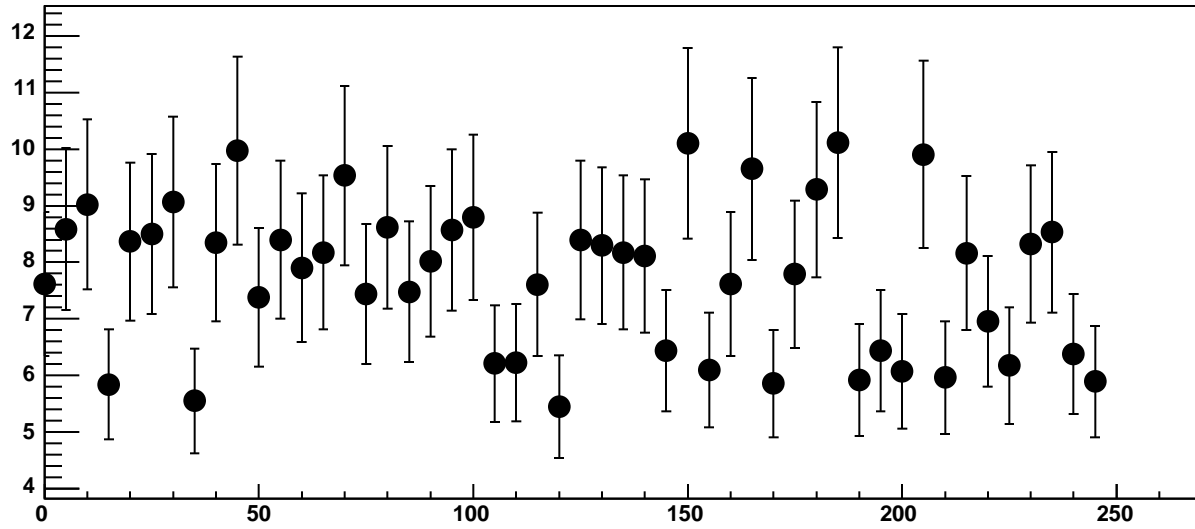


Chip 2, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold

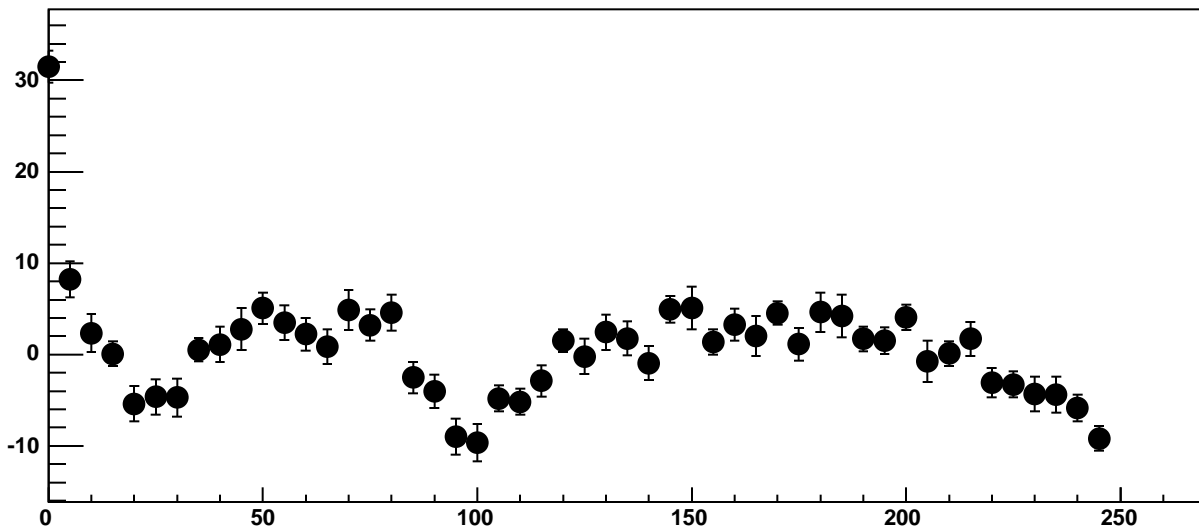


χ^2 / ndf	219.4 / 41
p0	168.3 ± 0.6119
p1	96.37 ± 0.6661
p2	$-6.617\text{e}+07 \pm 1.062\text{e}+06$
p3	$4.021\text{e}+07 \pm 4.054\text{e}+05$
p4	1.631 ± 0.01748

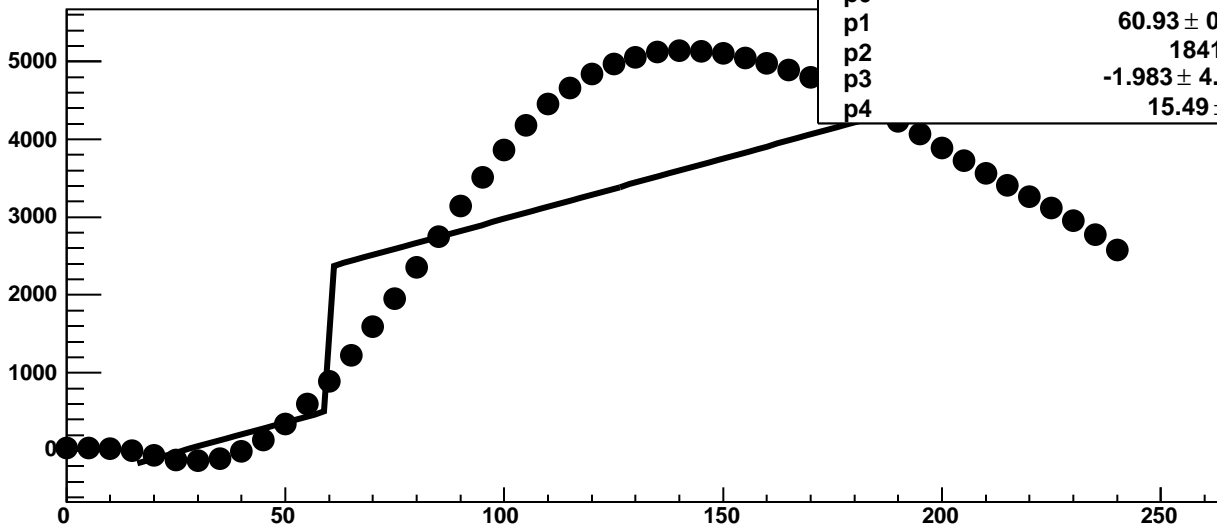
Chip 2, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

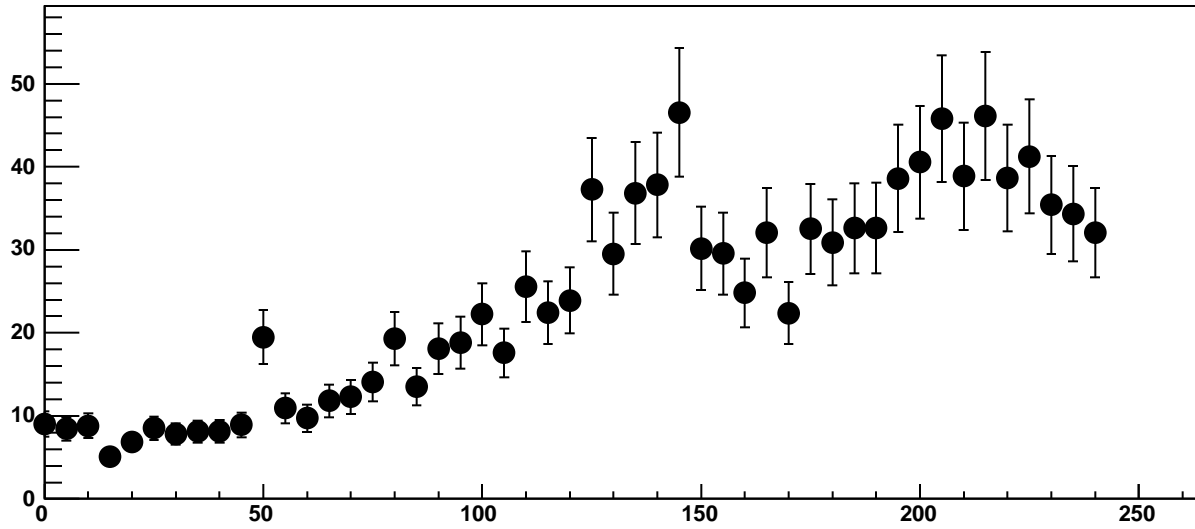


Chip 2, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

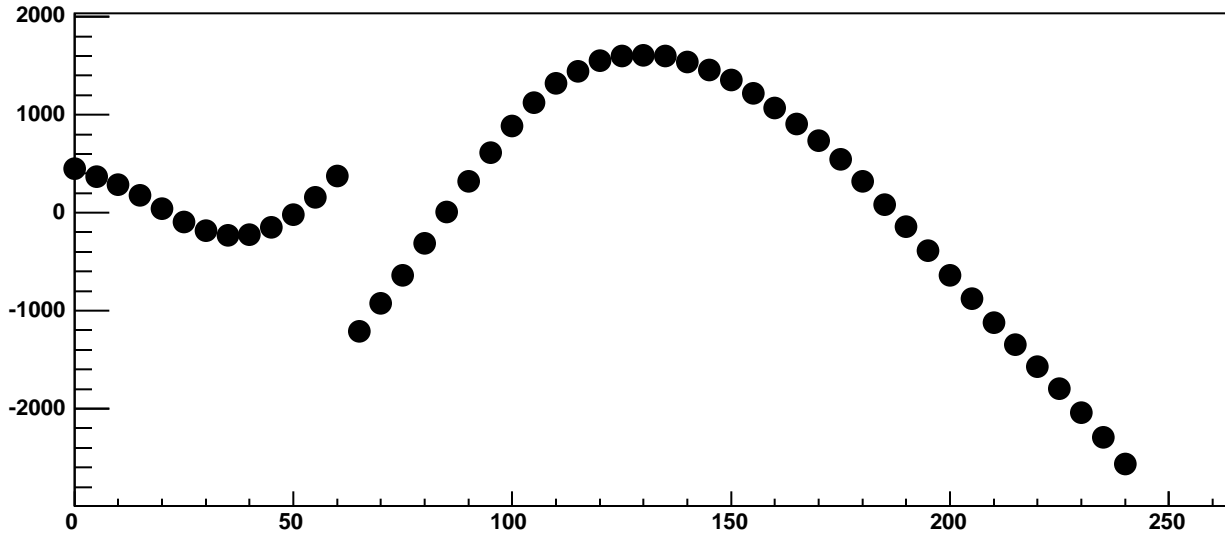


χ^2 / ndf	1.507e+06 / 41
p0	532.4 ± 0.8099
p1	60.93 ± 0.001064
p2	1841 ± 1.806
p3	-1.983 ± 4.813e-07
p4	15.49 ± 0.0188

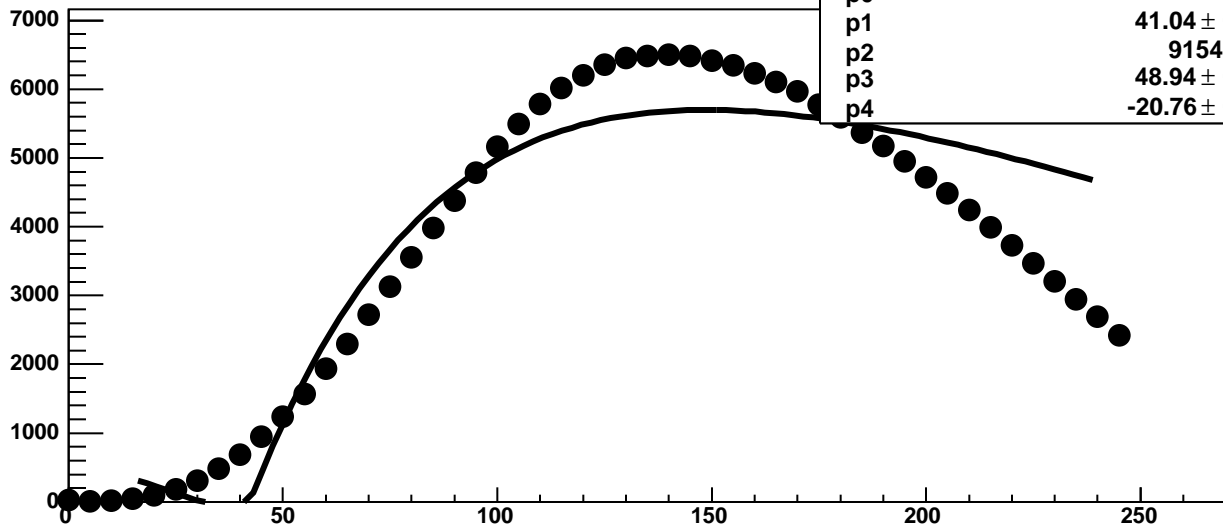
Chip 2, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

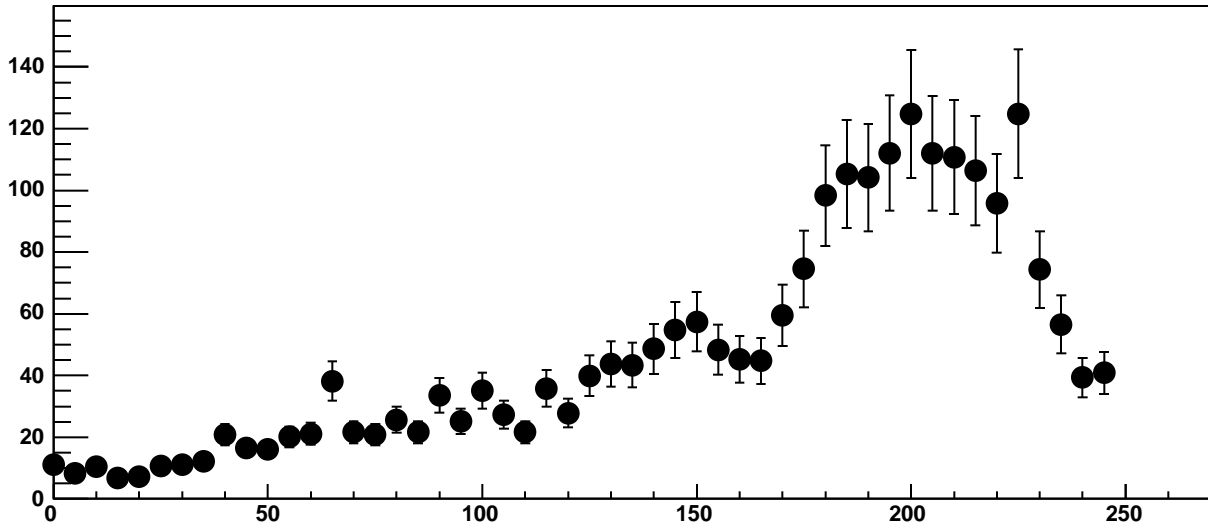


Chip 2, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold

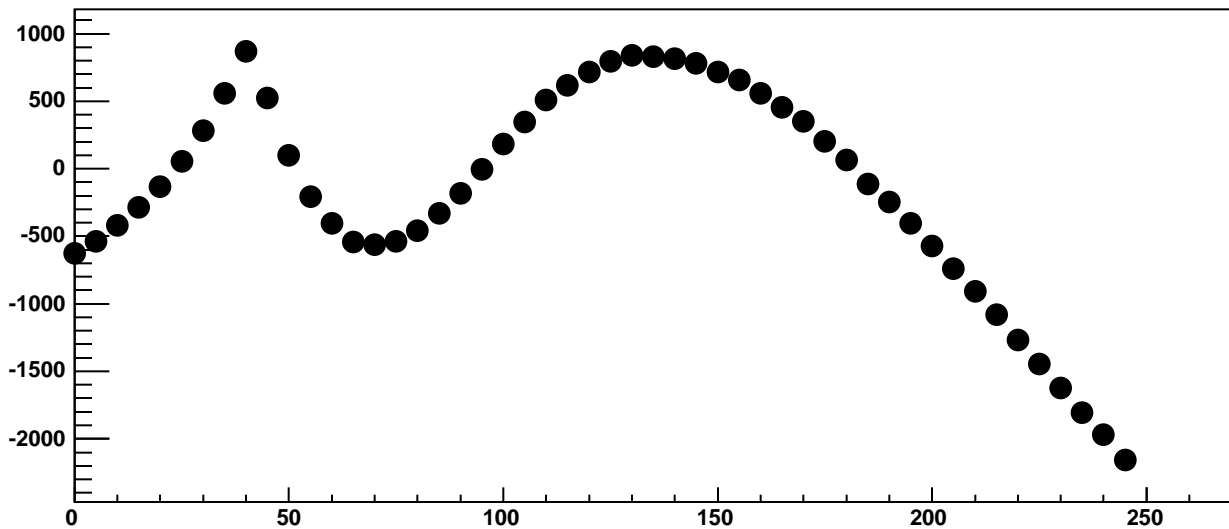


χ^2 / ndf	3.581e+05 / 41
p0	-205.7 ± 1.499
p1	41.04 ± 0.01944
p2	9154 ± 14.67
p3	48.94 ± 0.08328
p4	-20.76 ± 0.07483

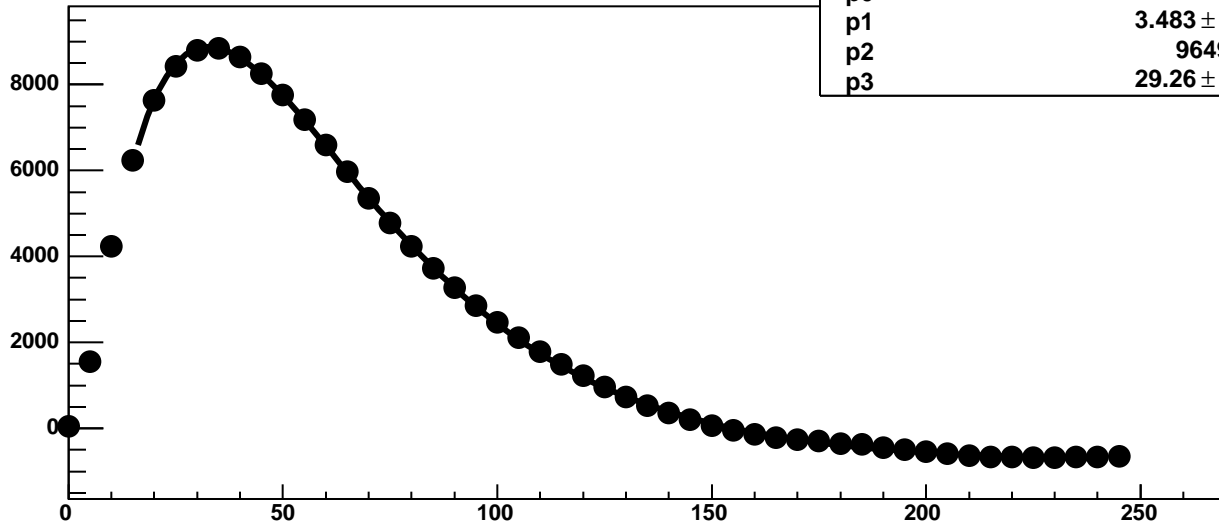
Chip 2, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

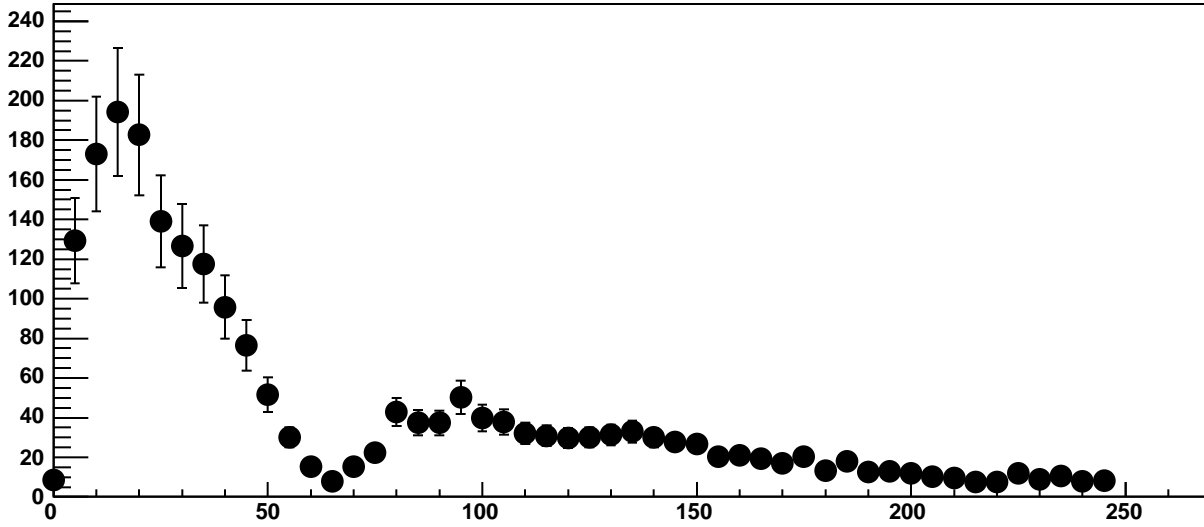


Chip 2, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold

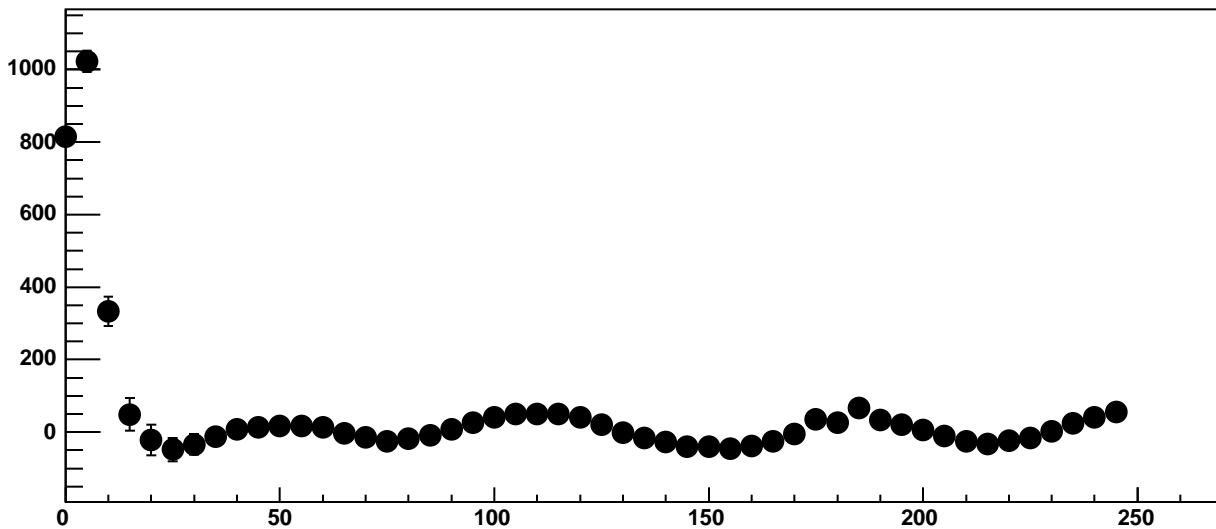


χ^2 / ndf	2461 / 42
p0	-768.6 ± 0.9052
p1	3.483 ± 0.06909
p2	9649 ± 7.86
p3	29.26 ± 0.02255

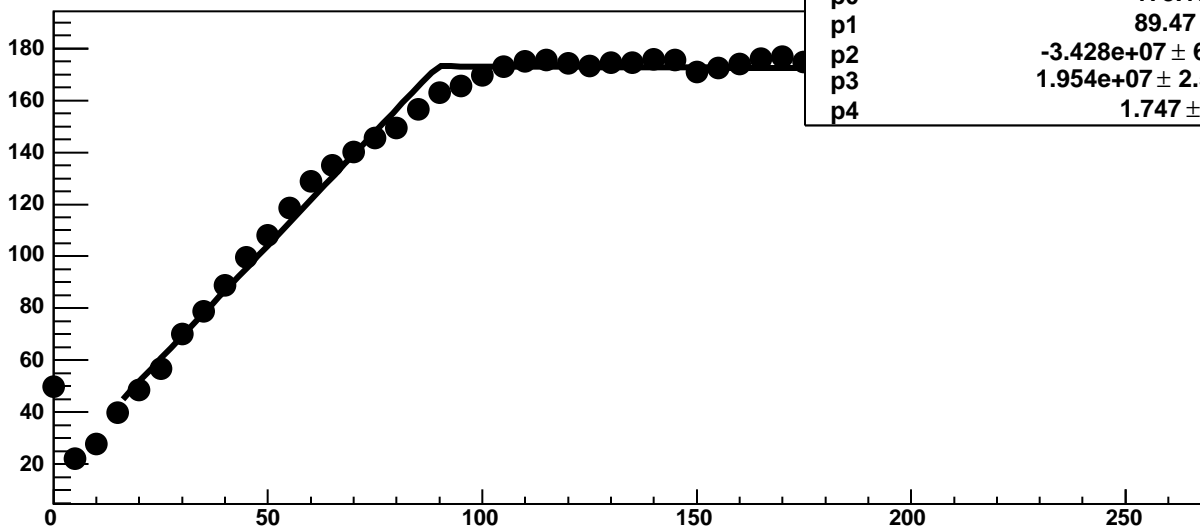
Chip 2, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold

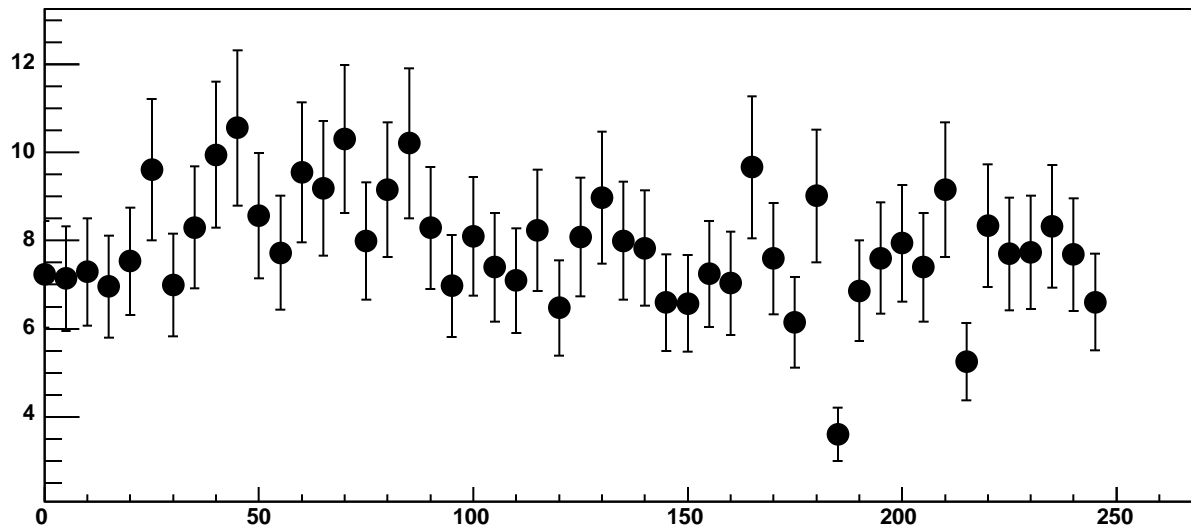


Chip 2, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

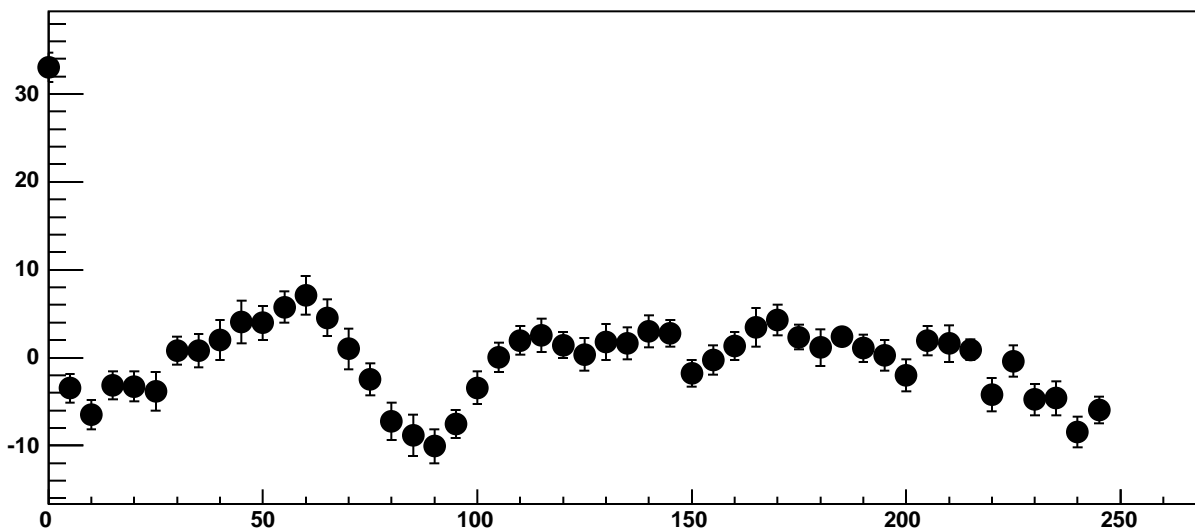


χ^2 / ndf	204.8 / 41
p0	173.1 ± 0.6084
p1	89.47 ± 0.7188
p2	$-3.428\text{e}+07 \pm 6.83\text{e}+05$
p3	$1.954\text{e}+07 \pm 2.502\text{e}+05$
p4	1.747 ± 0.02297

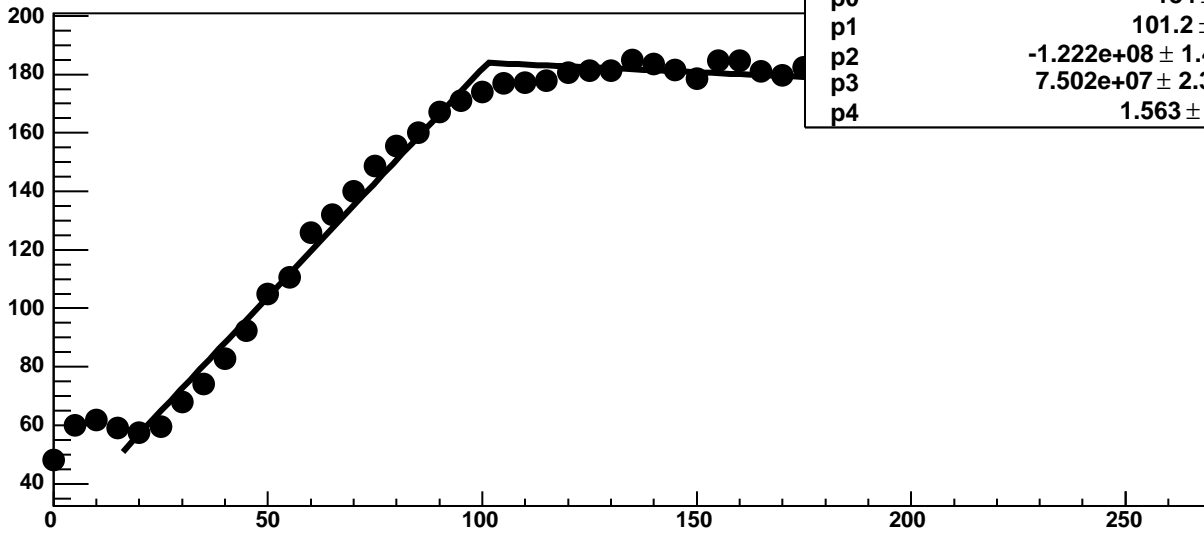
Chip 2, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

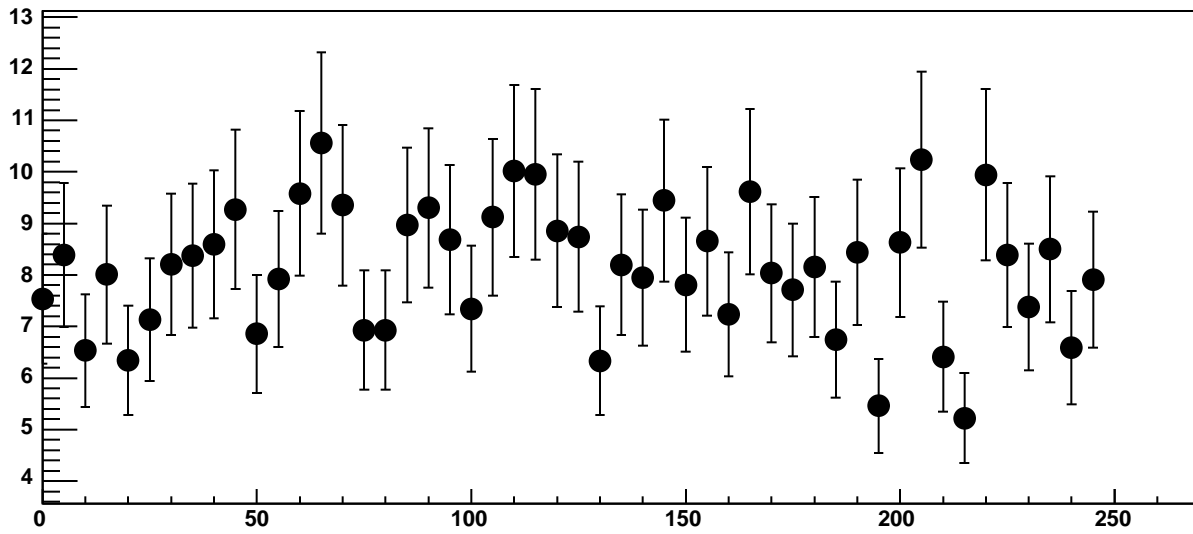


Chip 2, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

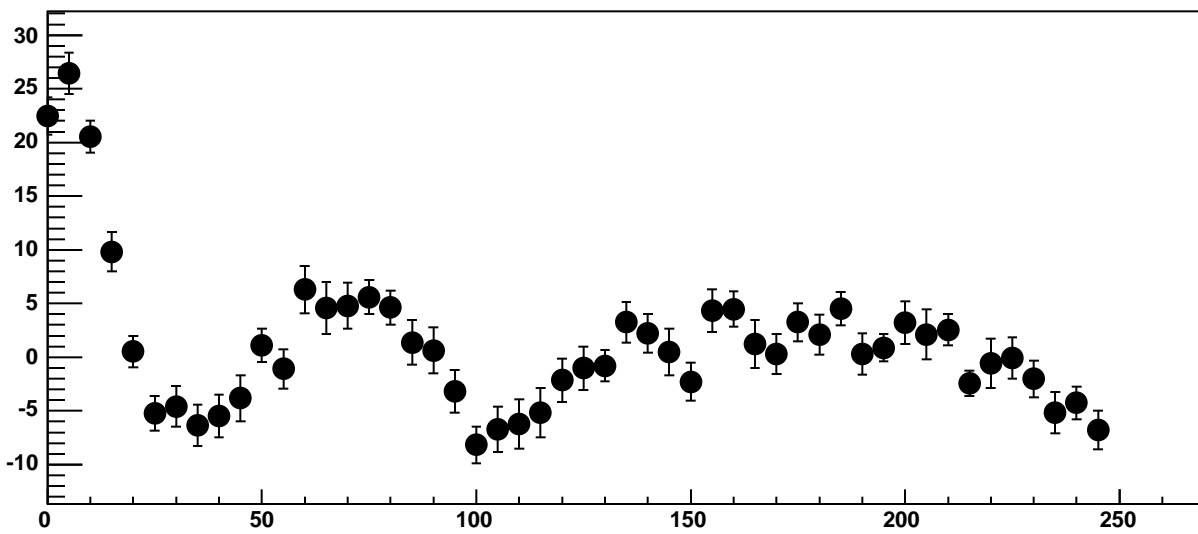


χ^2 / ndf	215.8 / 41
p0	184 ± 0.7133
p1	101.2 ± 0.6945
p2	$-1.222\text{e}+08 \pm 1.424\text{e}+06$
p3	$7.502\text{e}+07 \pm 2.304\text{e}+05$
p4	1.563 ± 0.01617

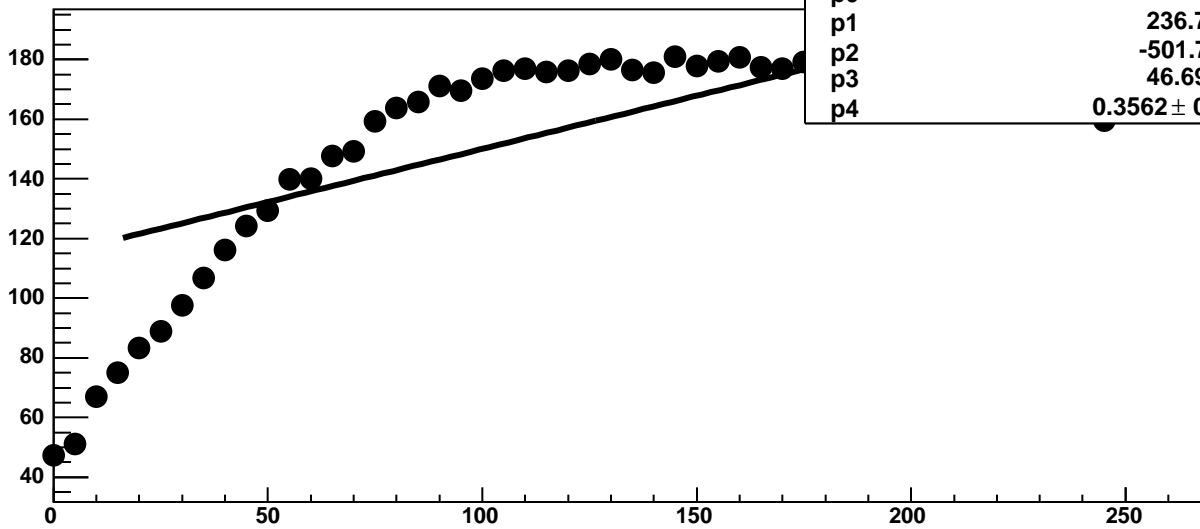
Chip 2, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

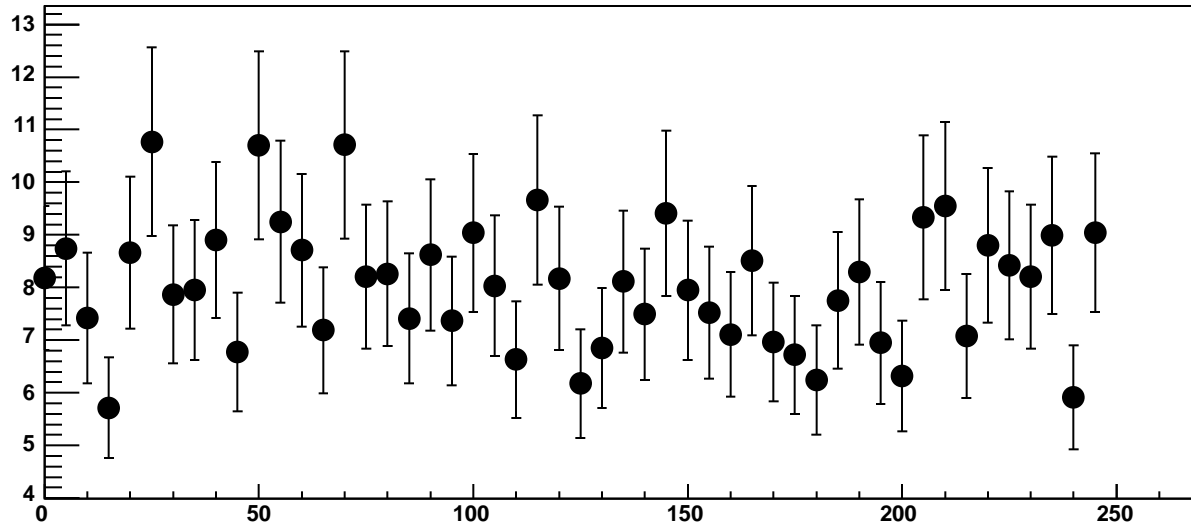


Chip 2, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold

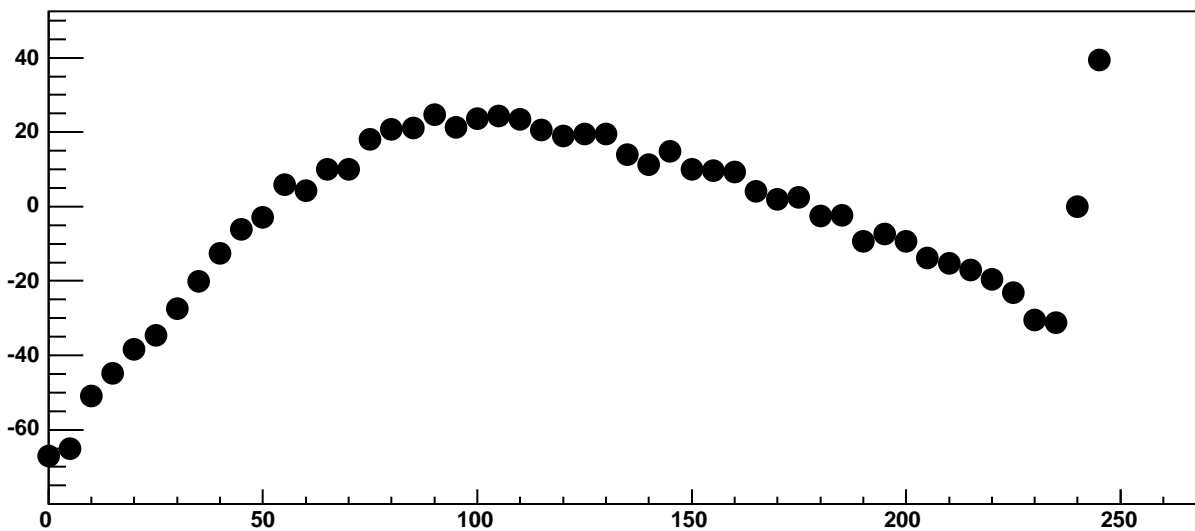


χ^2 / ndf	5230 / 41
p0	198.7 ± 0.8917
p1	236.7 ± 2.028
p2	-501.7 ± 292.8
p3	46.69 ± 23.25
p4	0.3562 ± 0.004173

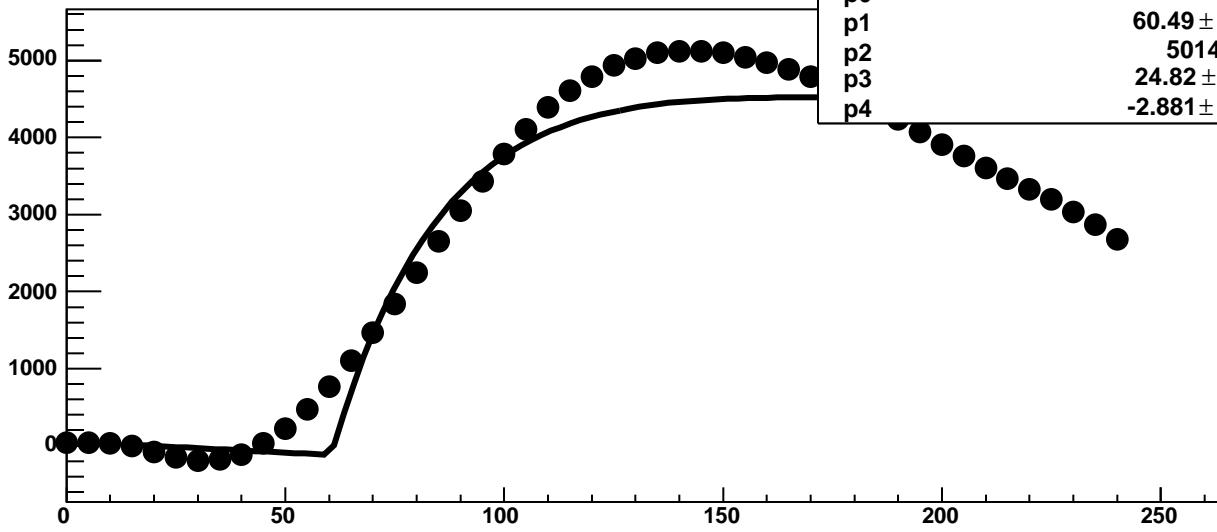
Chip 2, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold

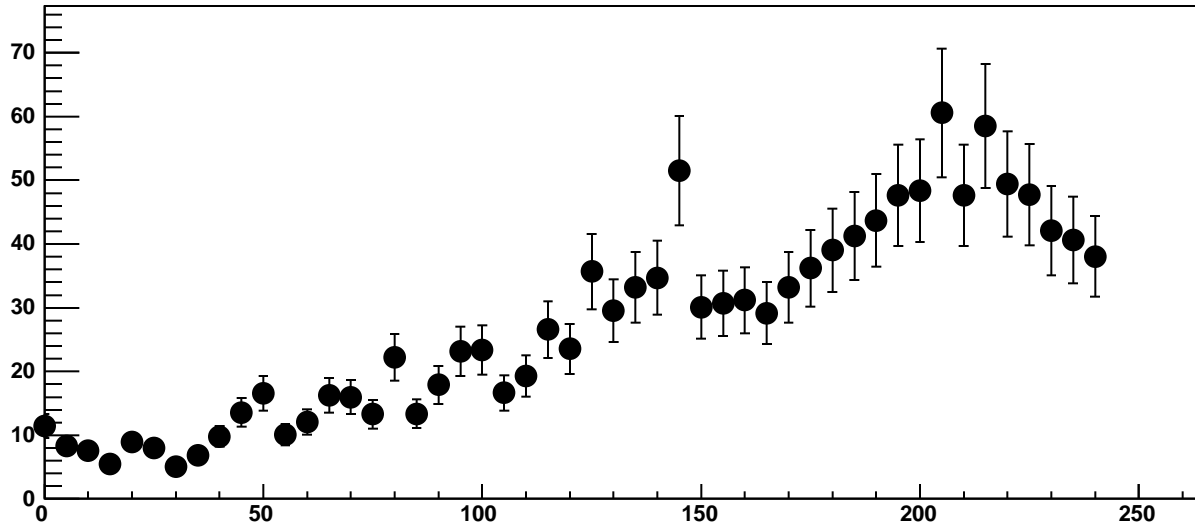


Chip 2, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold

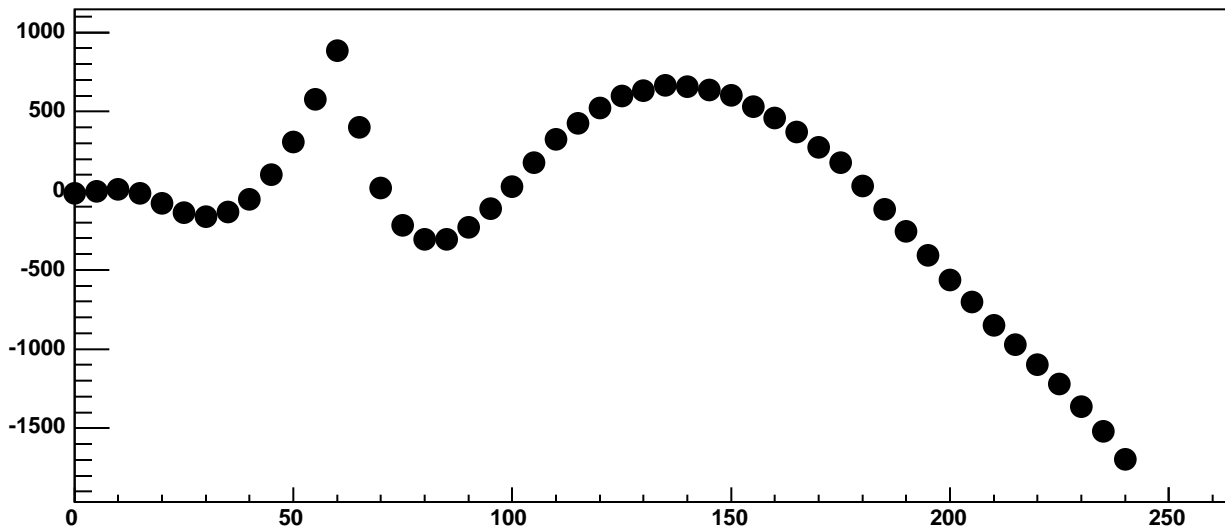


χ^2 / ndf	4.411e+05 / 41
p0	-118.8 ± 1.131
p1	60.49 ± 0.02079
p2	5014 ± 5.237
p3	24.82 ± 0.04201
p4	-2.881 ± 0.03319

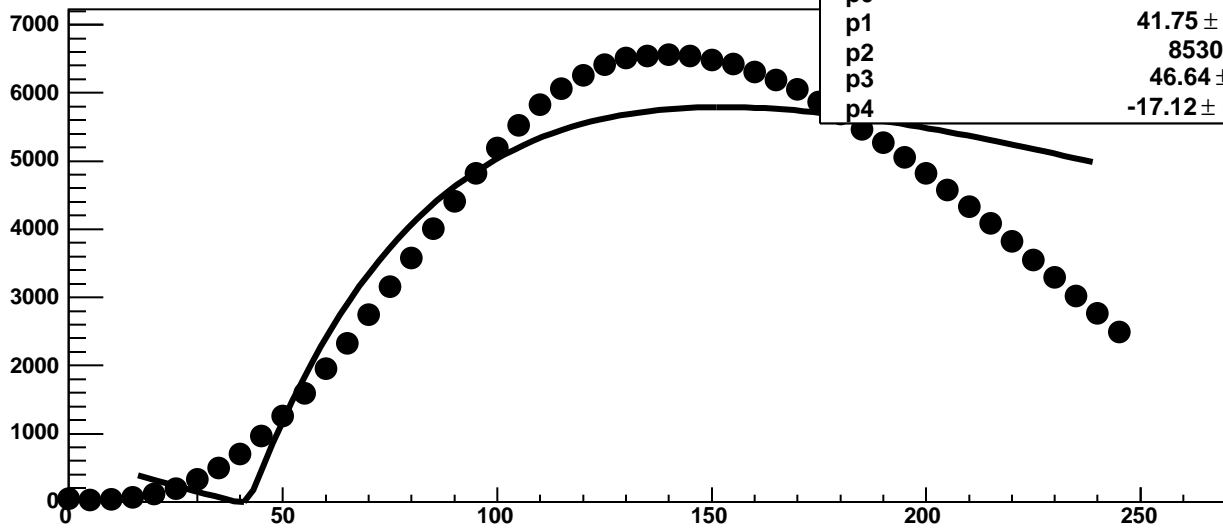
Chip 2, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold

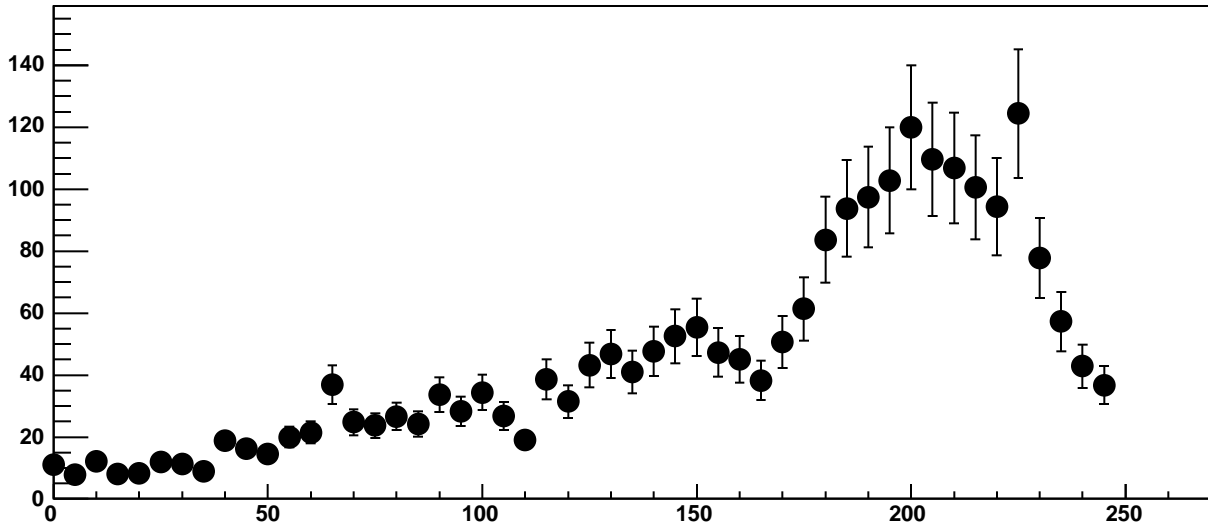


Chip 2, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

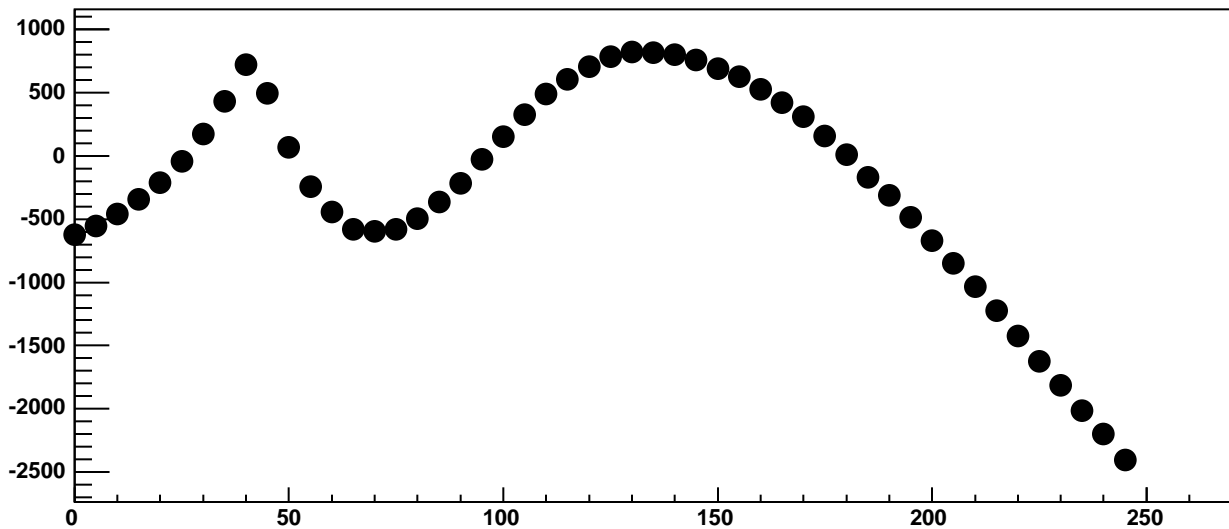


χ^2 / ndf	3.645e+05 / 41
p0	-48.97 ± 1.421
p1	41.75 ± 0.01851
p2	8530 ± 13.47
p3	46.64 ± 0.0798
p4	-17.12 ± 0.07178

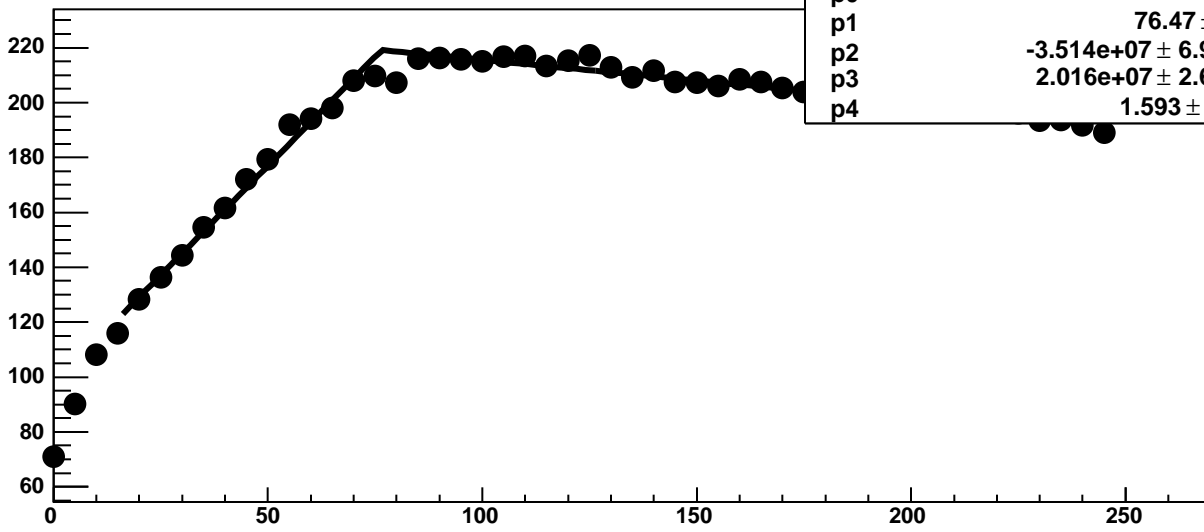
Chip 2, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

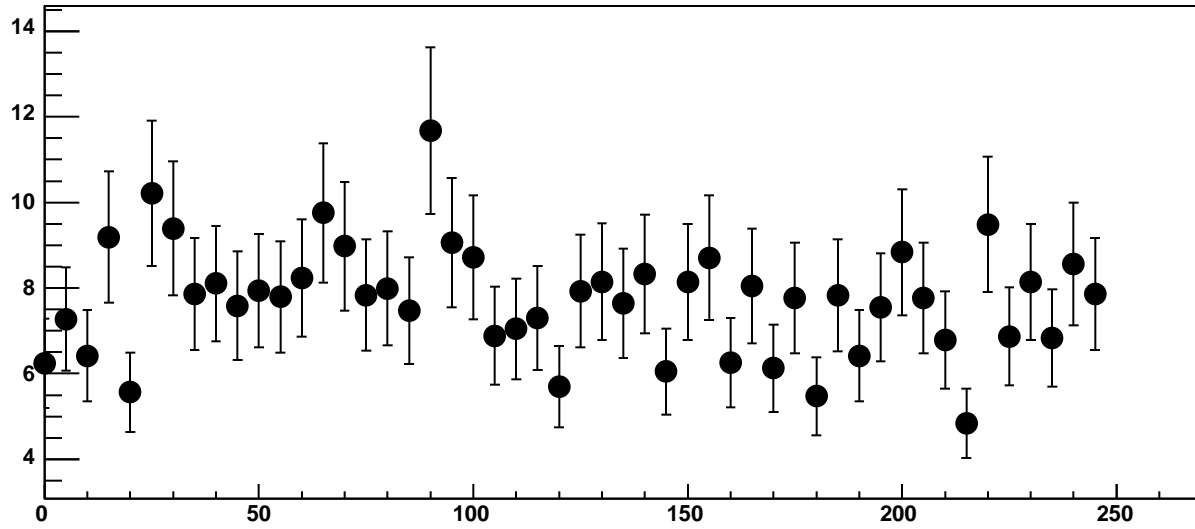


Chip 2, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold

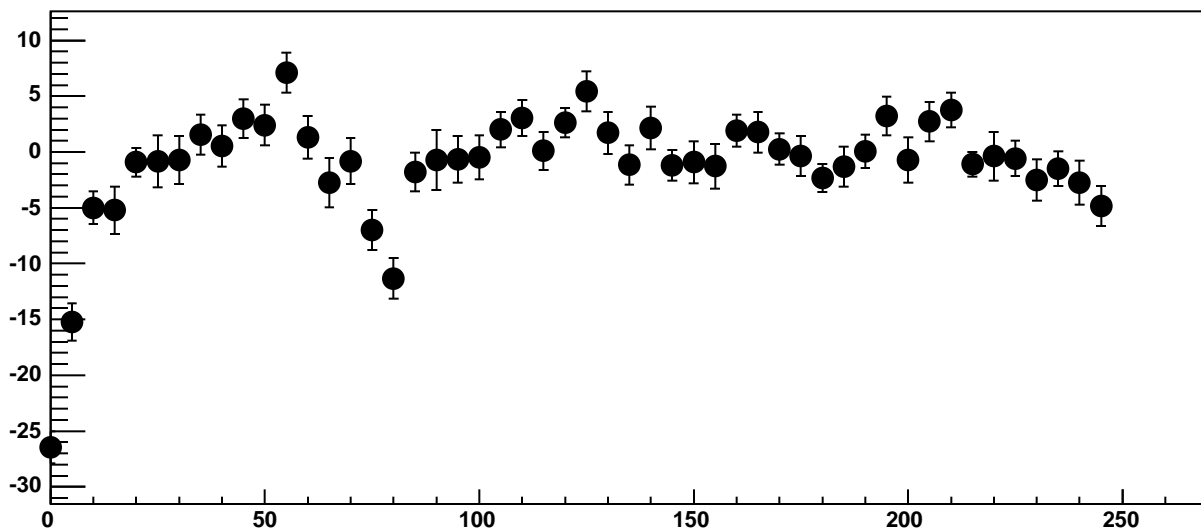


χ^2 / ndf	131.5 / 41
p0	219.1 ± 0.5777
p1	76.47 ± 0.6939
p2	$-3.514e+07 \pm 6.953e+05$
p3	$2.016e+07 \pm 2.626e+05$
p4	1.593 ± 0.02729

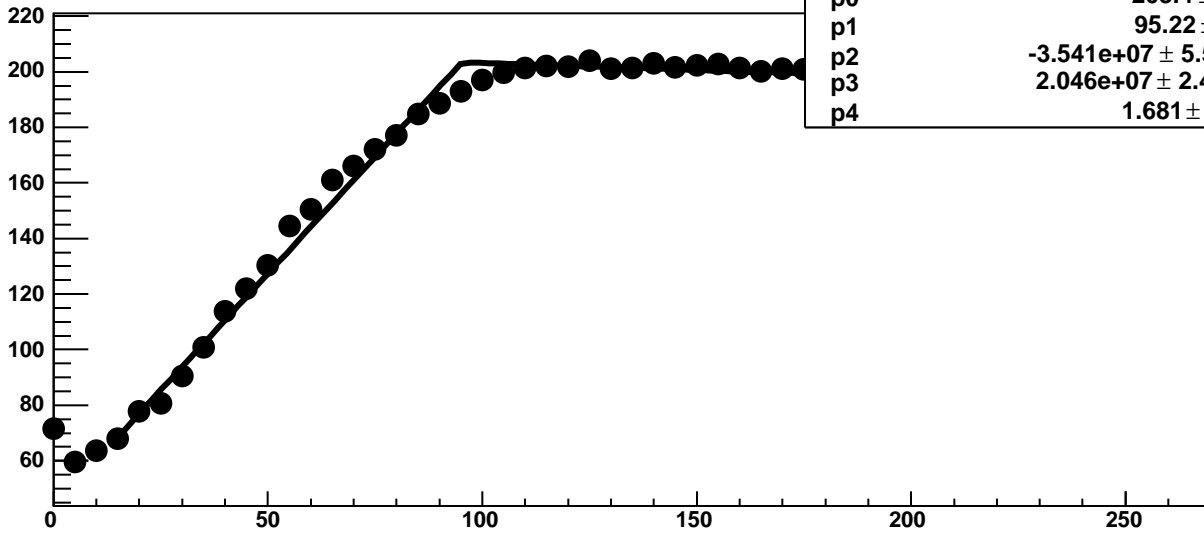
Chip 2, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold

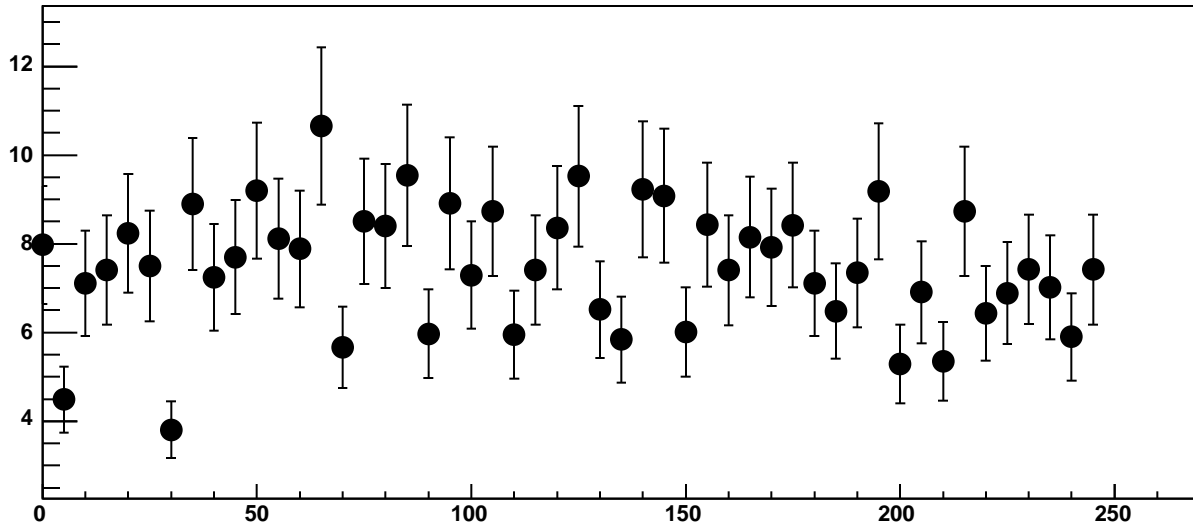


Chip 2, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

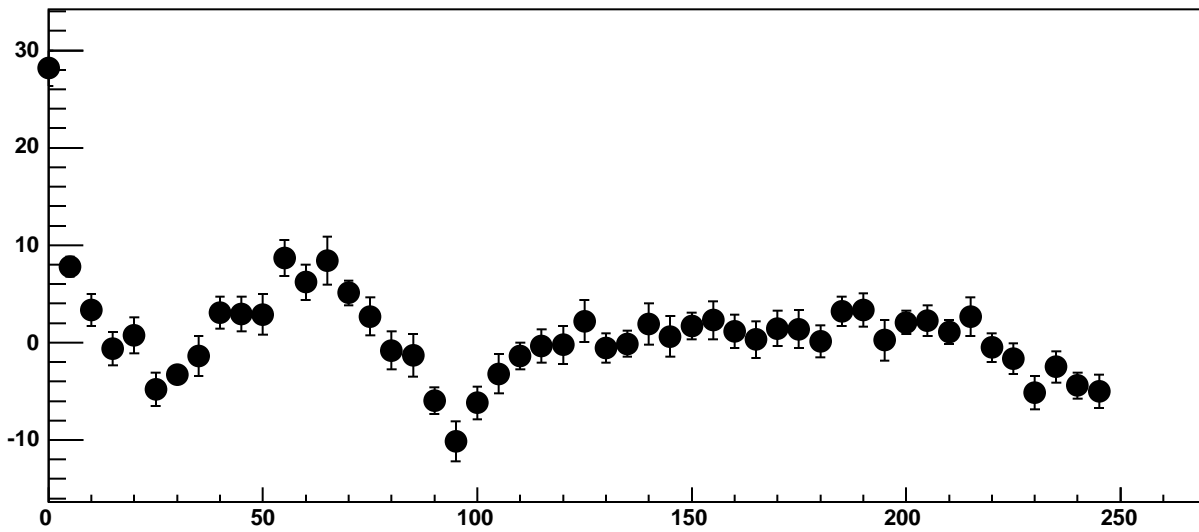


χ^2 / ndf	202.2 / 41
p0	203.4 ± 0.6224
p1	95.22 ± 0.5999
p2	$-3.541\text{e}+07 \pm 5.511\text{e}+05$
p3	$2.046\text{e}+07 \pm 2.493\text{e}+05$
p4	1.681 ± 0.01617

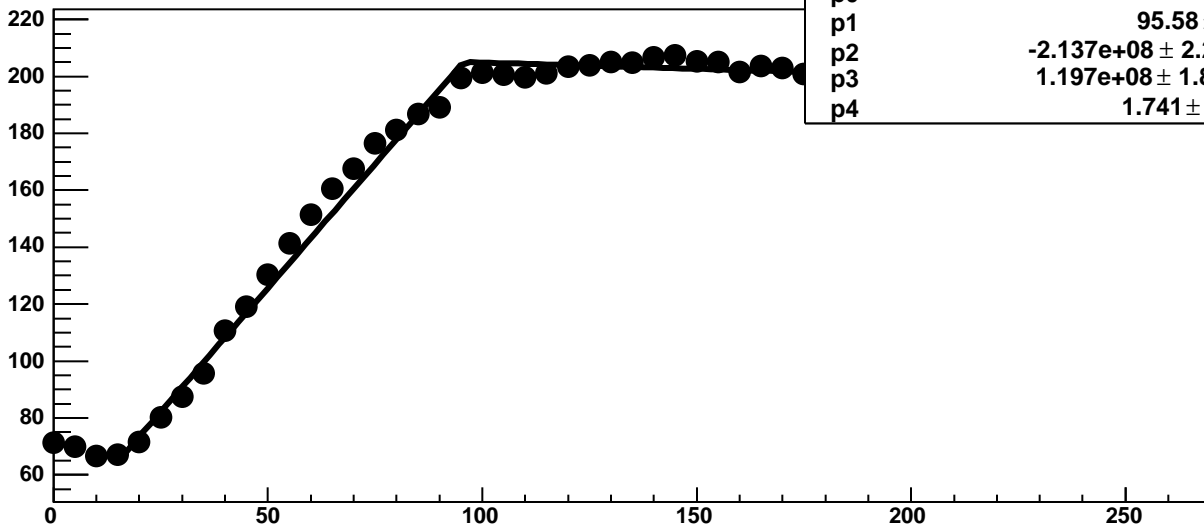
Chip 2, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

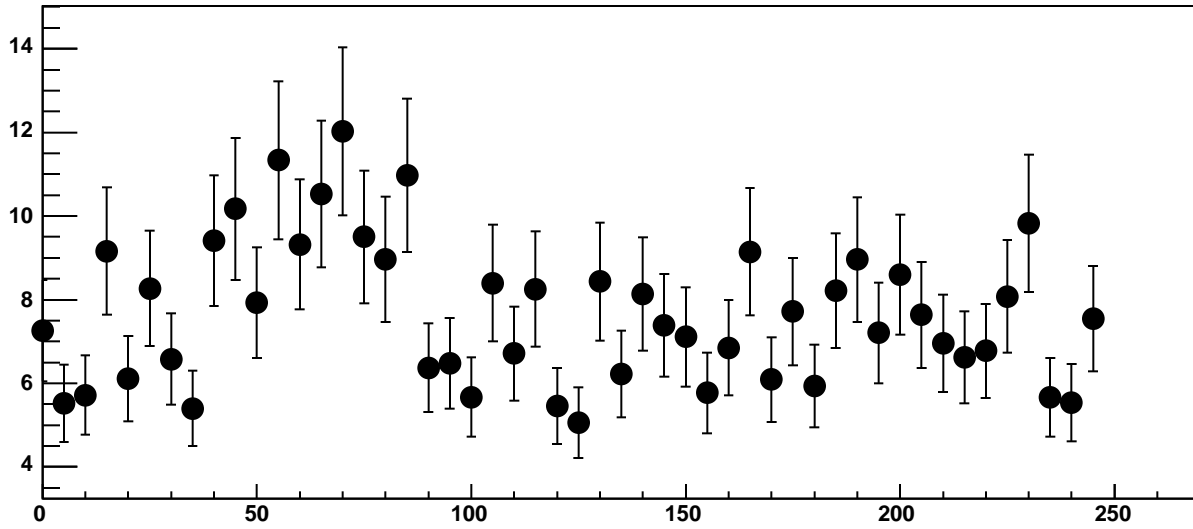


Chip 2, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

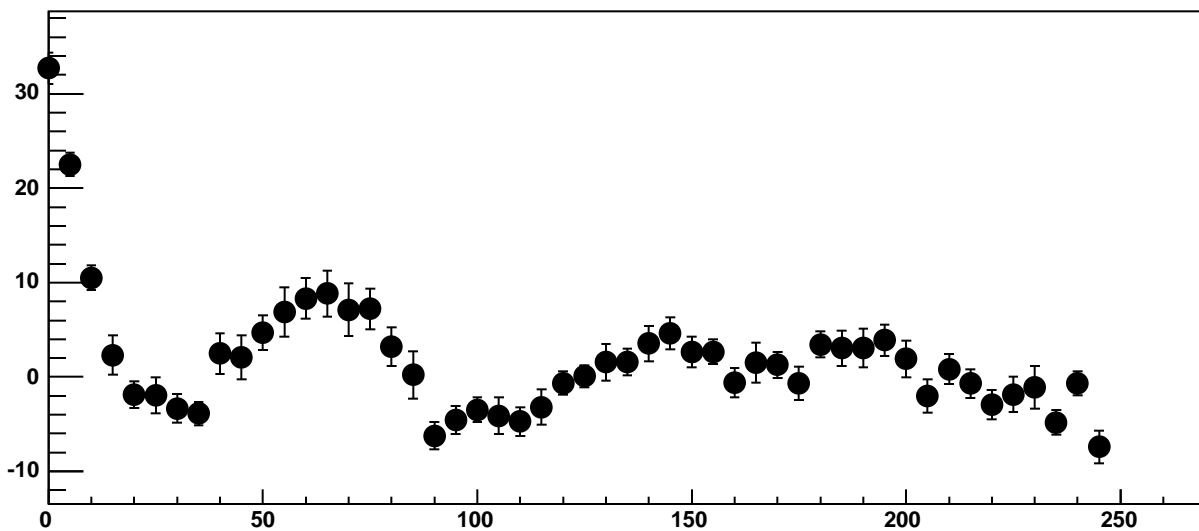


χ^2 / ndf	194.3 / 41
p0	205 ± 0.5577
p1	95.58 ± 0.5821
p2	-2.137e+08 ± 2.204e+06
p3	1.197e+08 ± 1.808e+05
p4	1.741 ± 0.01693

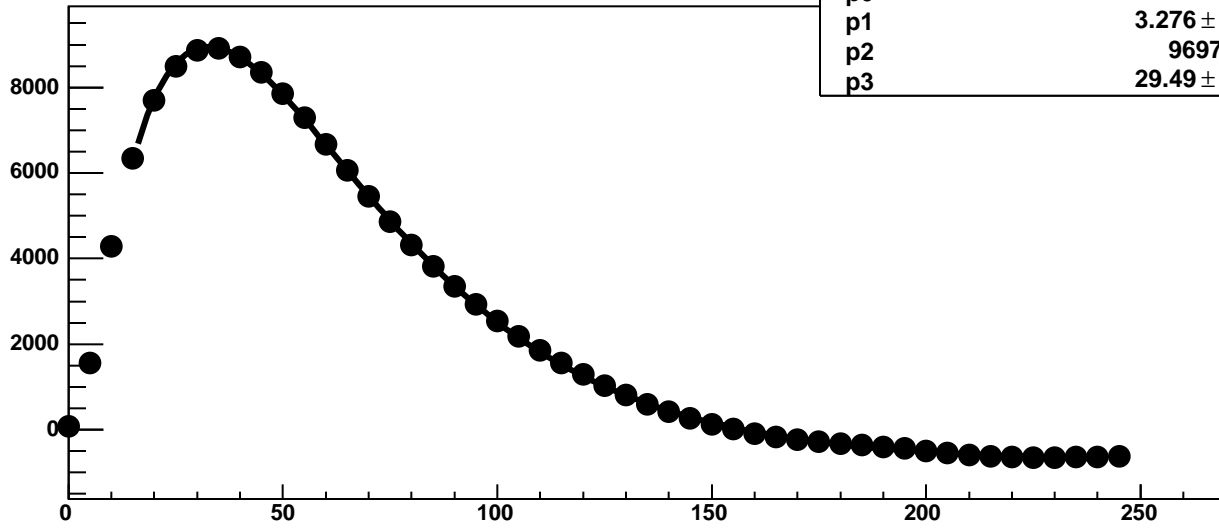
Chip 2, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

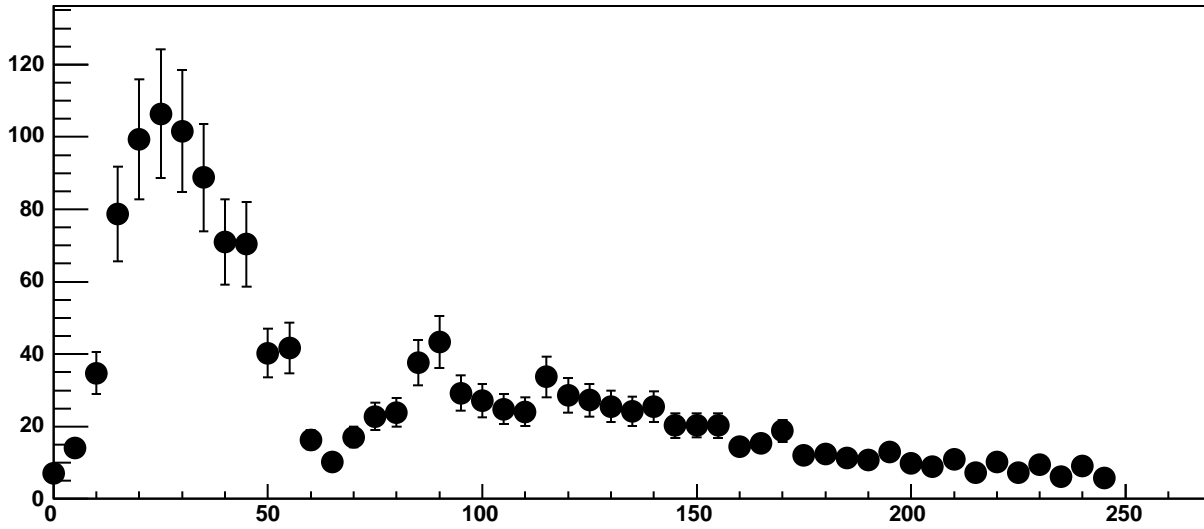


Chip 2, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold

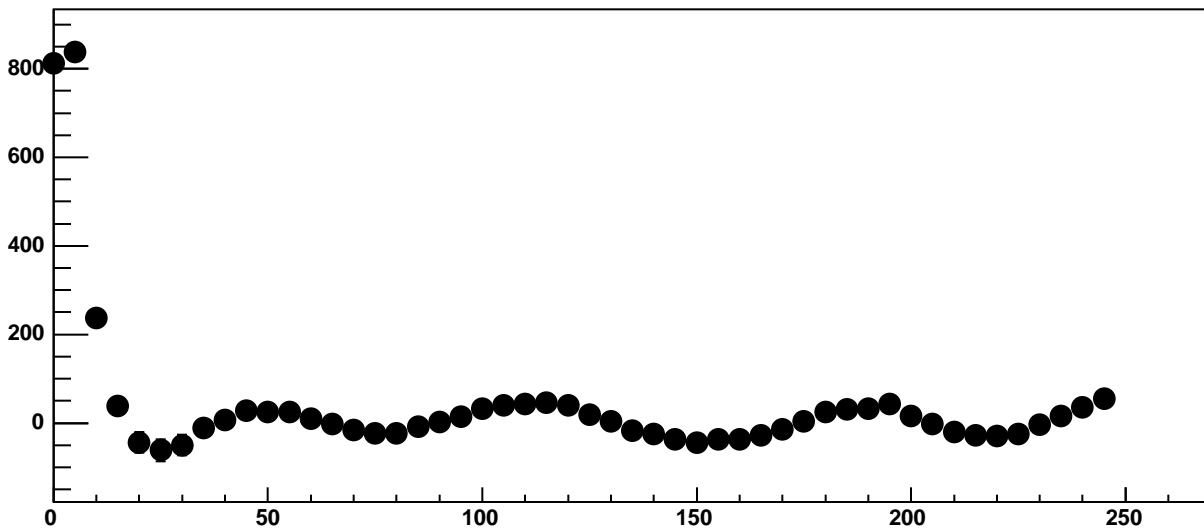


χ^2 / ndf	2520 / 42
p0	-739.7 ± 0.7974
p1	3.276 ± 0.03903
p2	9697 ± 4.856
p3	29.49 ± 0.01726

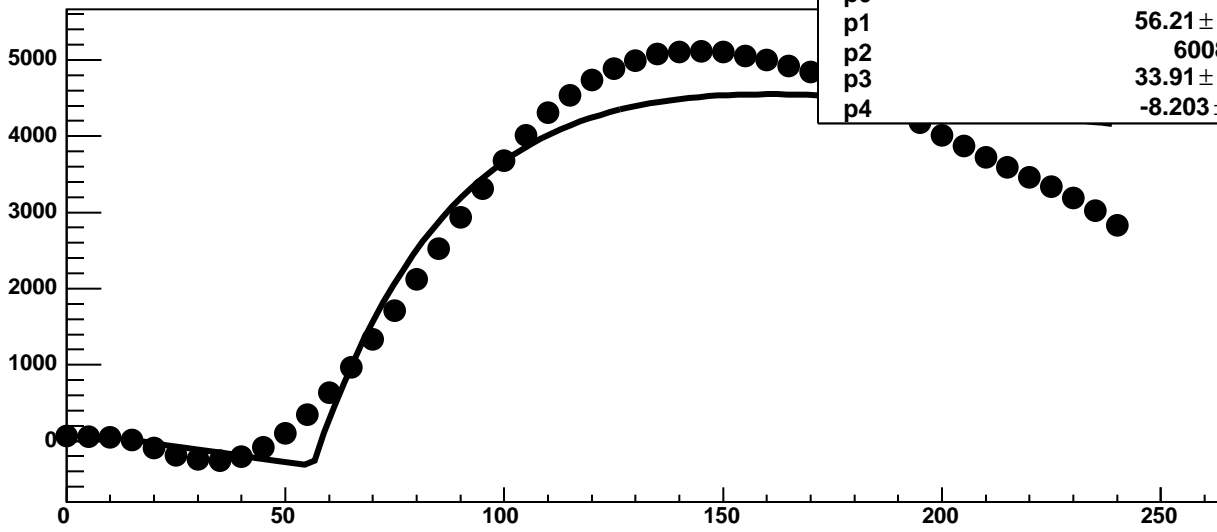
Chip 2, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold

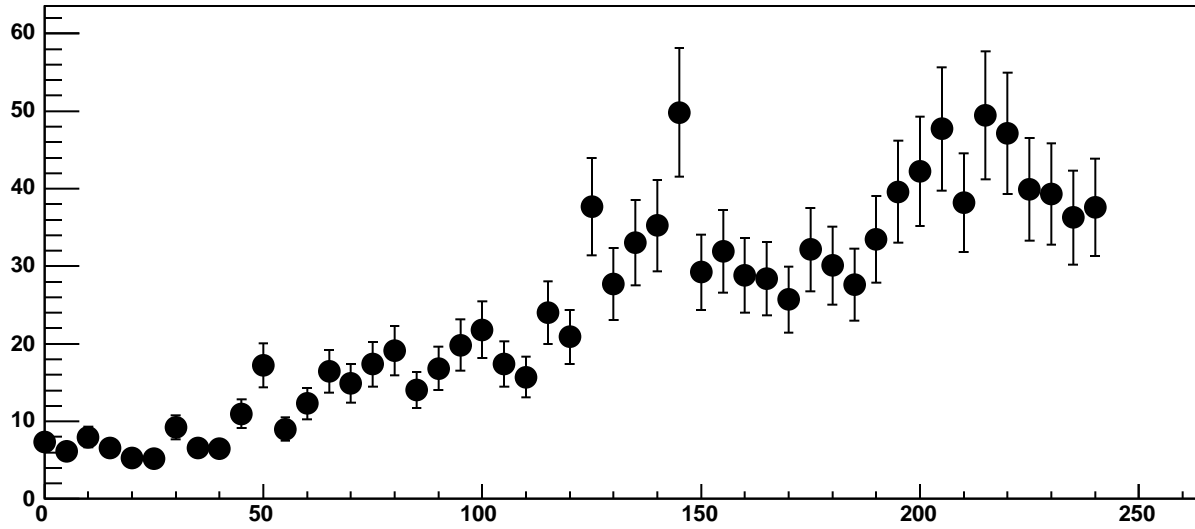


Chip 2, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold

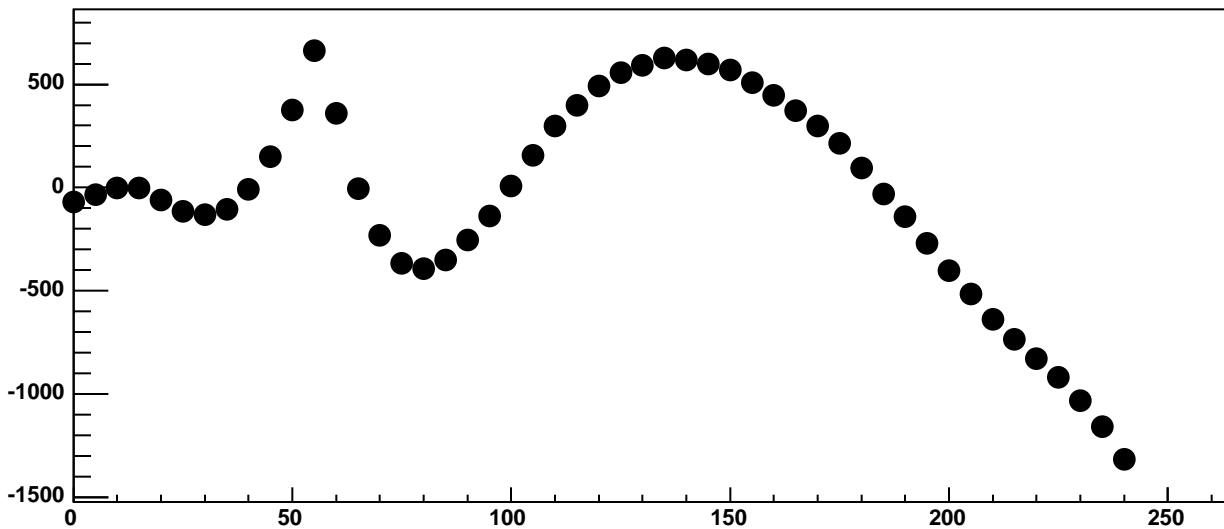


χ^2 / ndf	3.523e+05 / 41
p0	-325.4 ± 1.056
p1	56.21 ± 0.01737
p2	6008 ± 6.36
p3	33.91 ± 0.04784
p4	-8.203 ± 0.0363

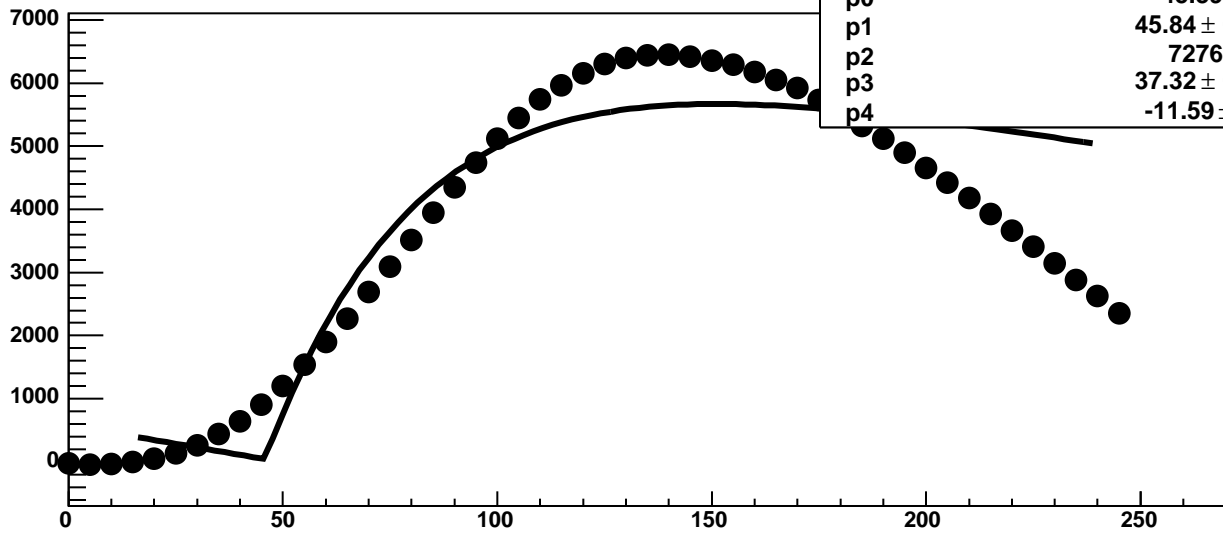
Chip 2, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold

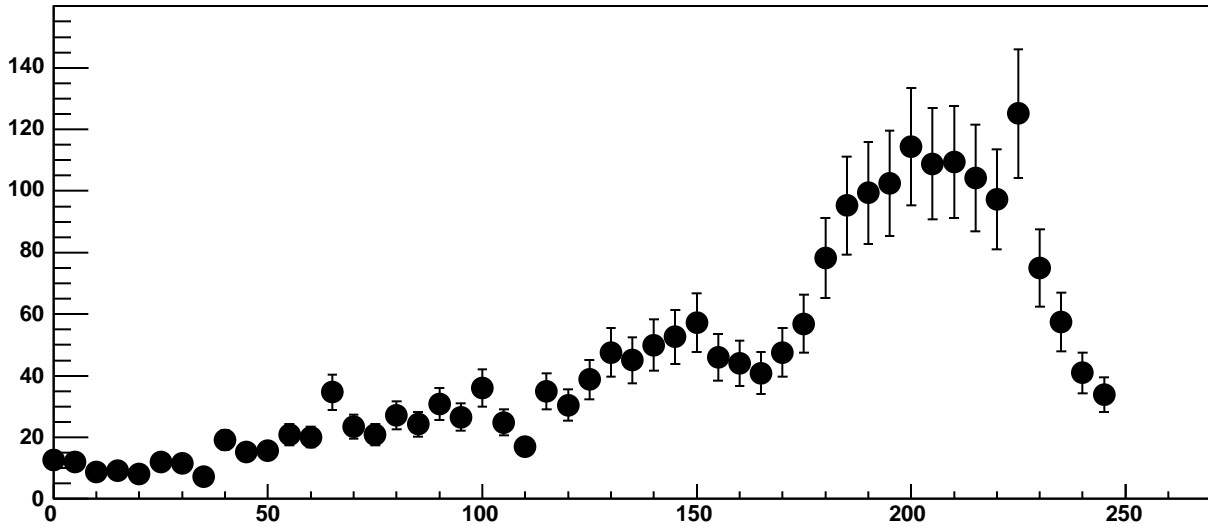


Chip 2, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold

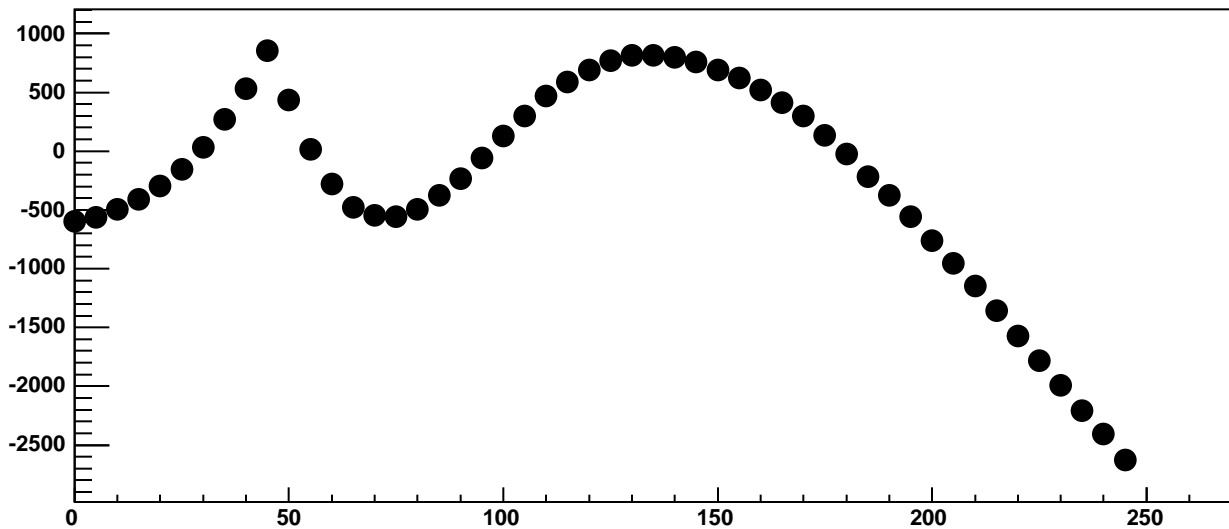


χ^2 / ndf	4.24e+05 / 41
p0	45.39 ± 1.348
p1	45.84 ± 0.01998
p2	7276 ± 10.13
p3	37.32 ± 0.06446
p4	-11.59 ± 0.0601

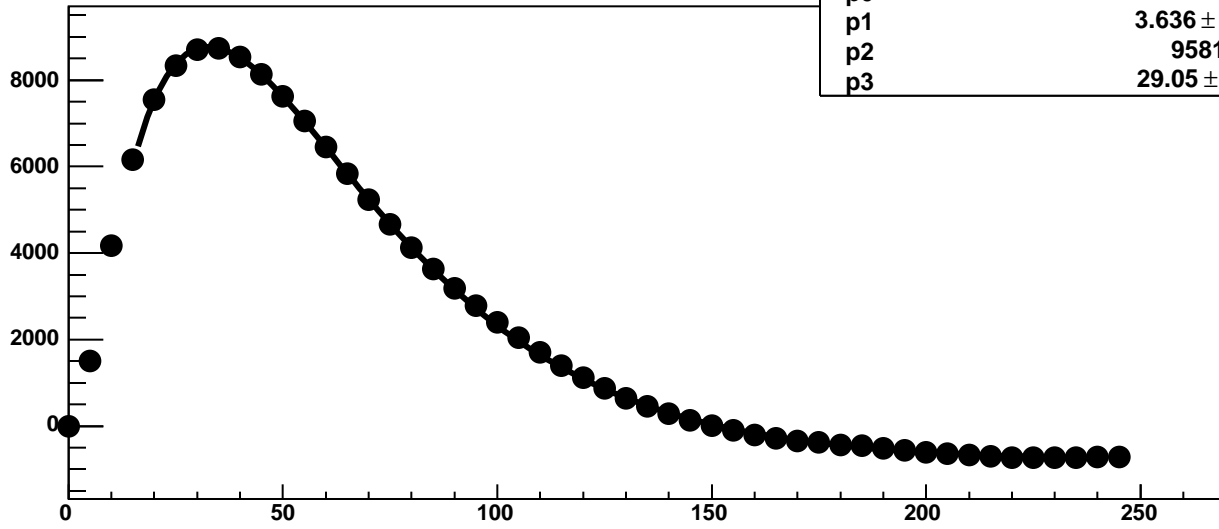
Chip 2, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

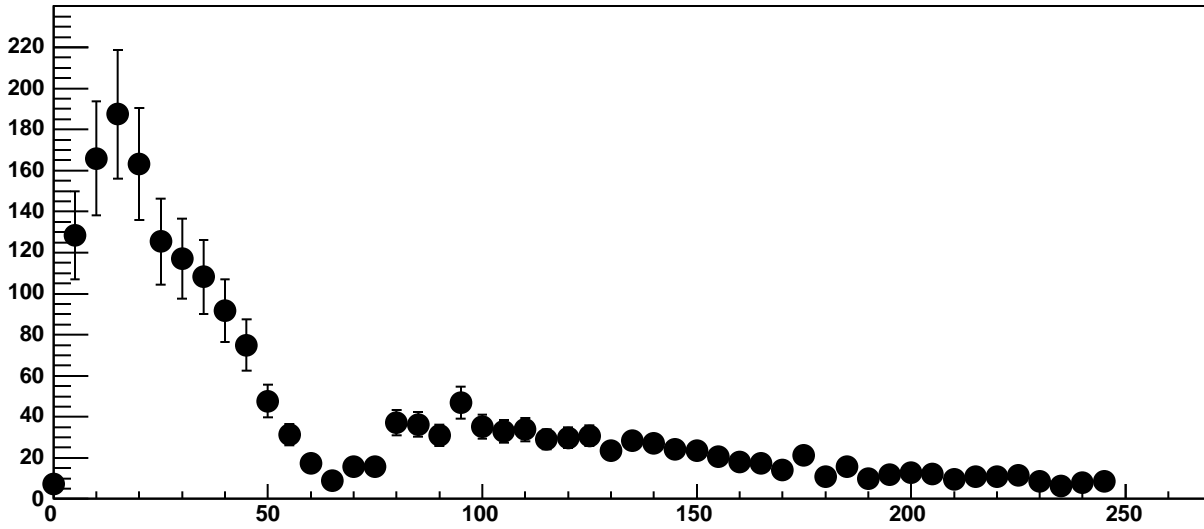


Chip 2, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold

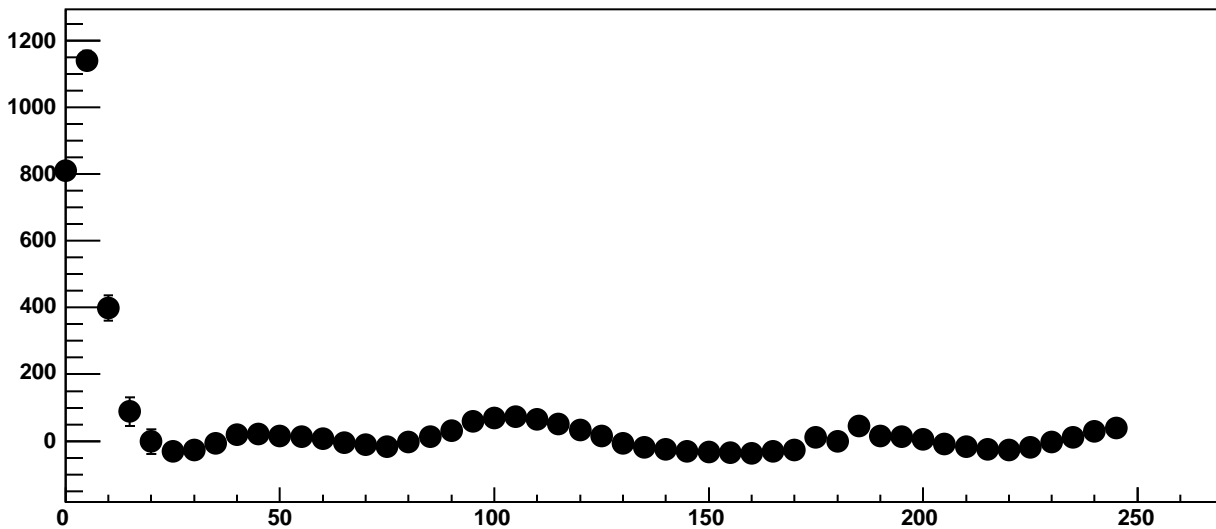


χ^2 / ndf	1668 / 42
p0	-810.1 ± 0.8602
p1	3.636 ± 0.06422
p2	9581 ± 7.379
p3	29.05 ± 0.02091

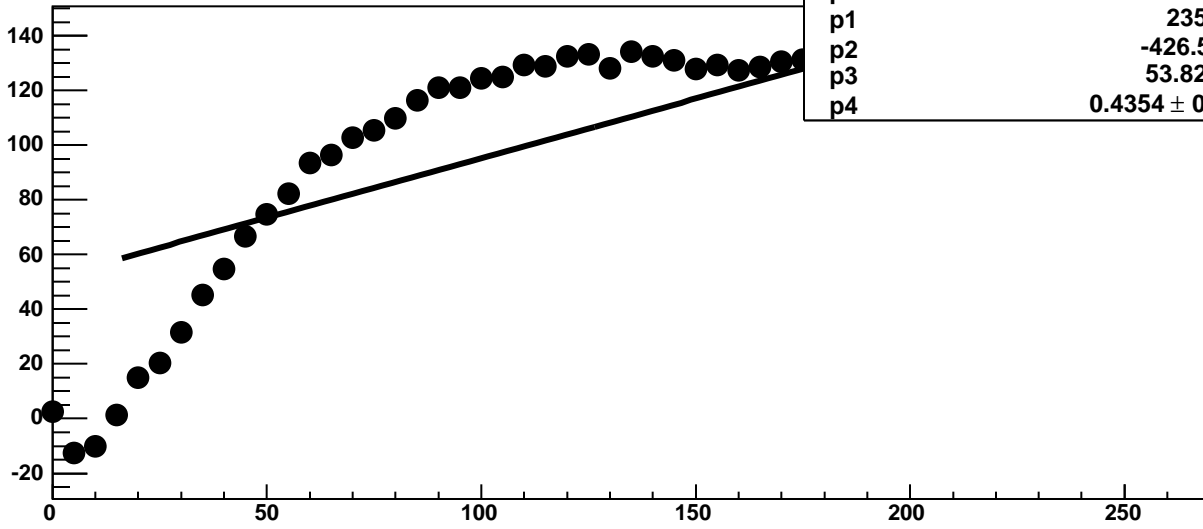
Chip 2, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold

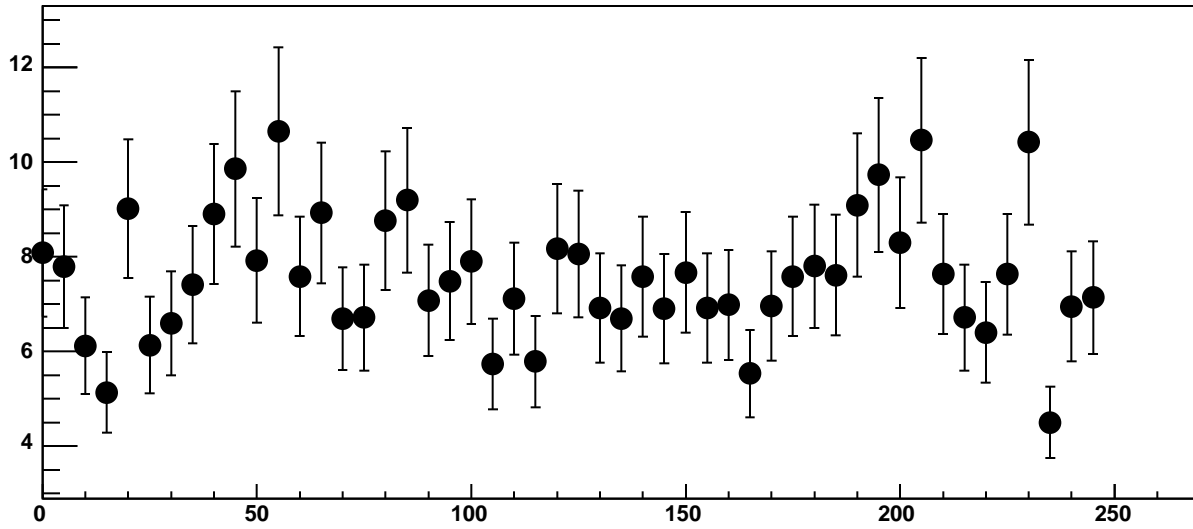


Chip 2, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

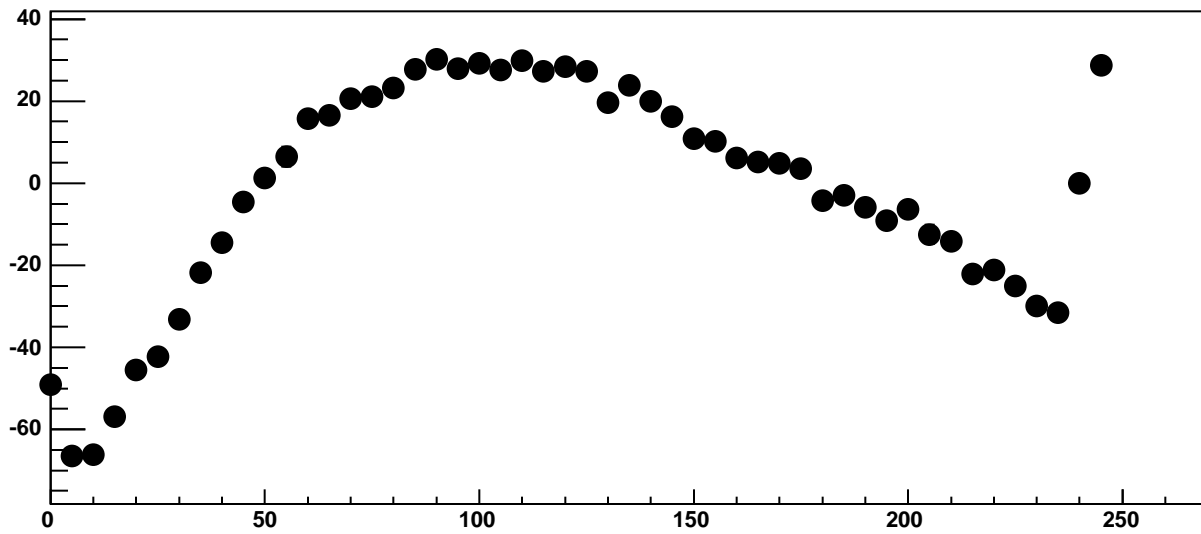


χ^2 / ndf	1.038e+04 / 41
p0	154 ± 1.805
p1	235 ± 4.008
p2	-426.5 ± 268.1
p3	53.82 ± 25.89
p4	0.4354 ± 0.003709

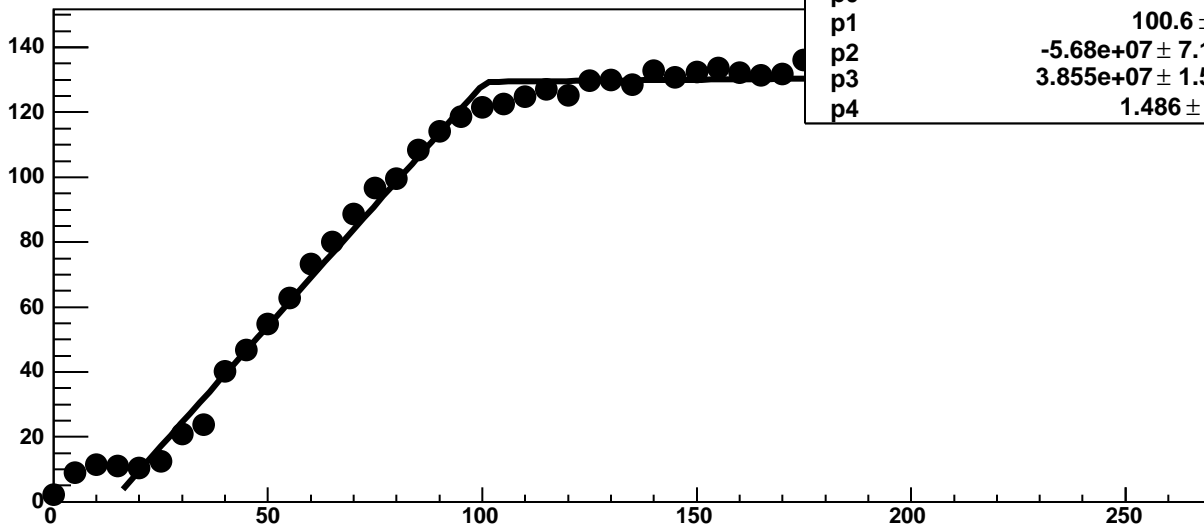
Chip 2, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

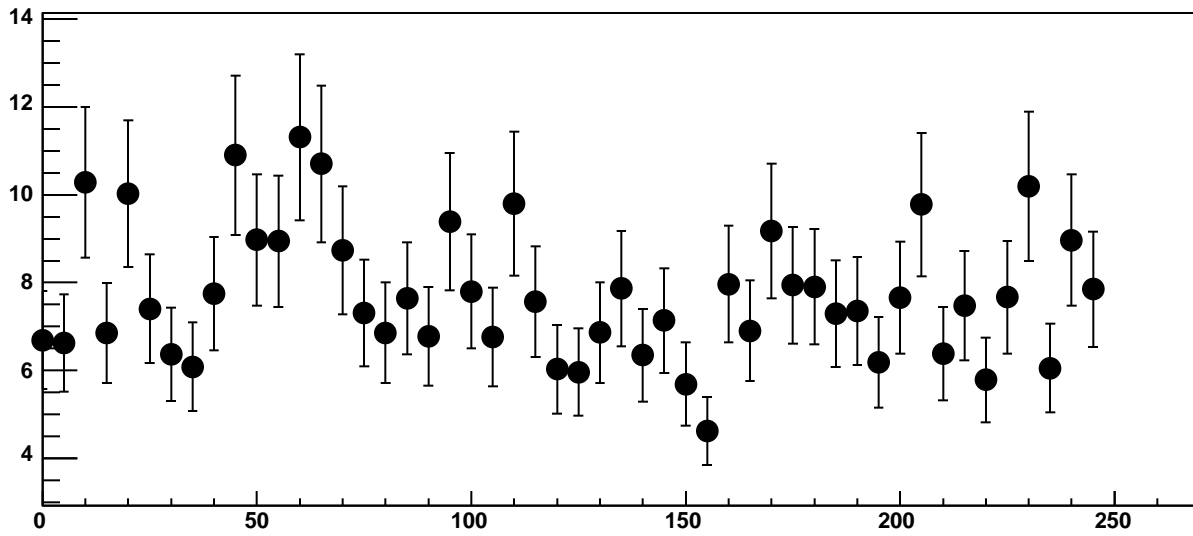


Chip 2, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

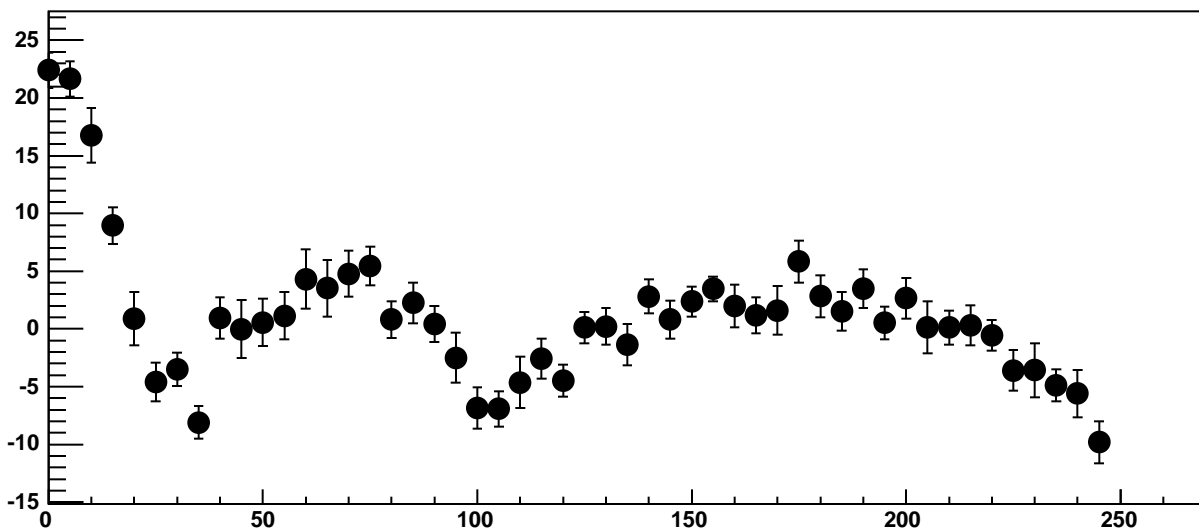


χ^2 / ndf	223.7 / 41
p0	129.4 ± 0.6176
p1	100.6 ± 0.6956
p2	$-5.68\text{e}+07 \pm 7.108\text{e}+05$
p3	$3.855\text{e}+07 \pm 1.587\text{e}+05$
p4	1.486 ± 0.01579

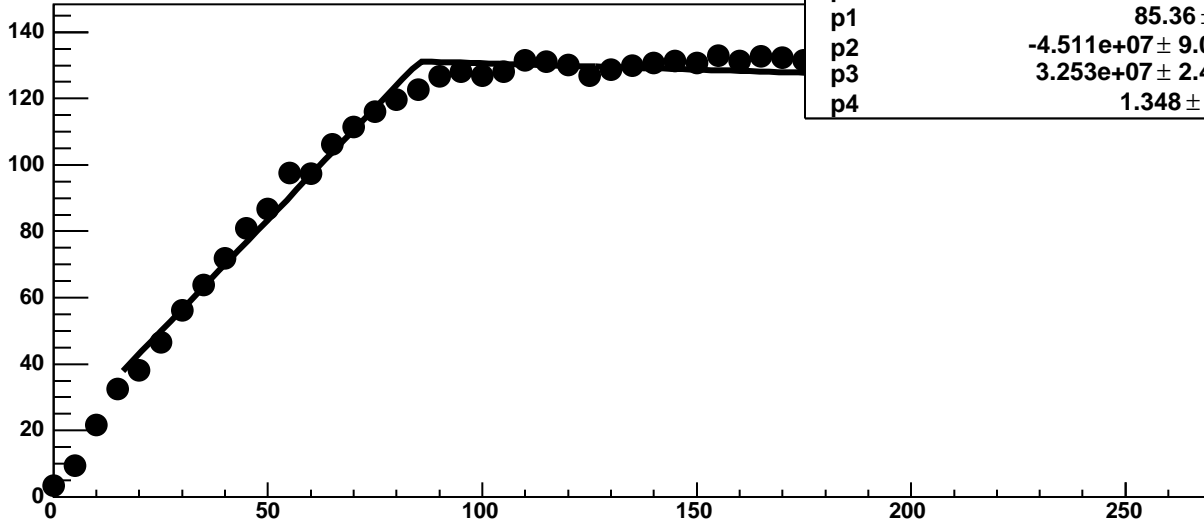
Chip 2, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

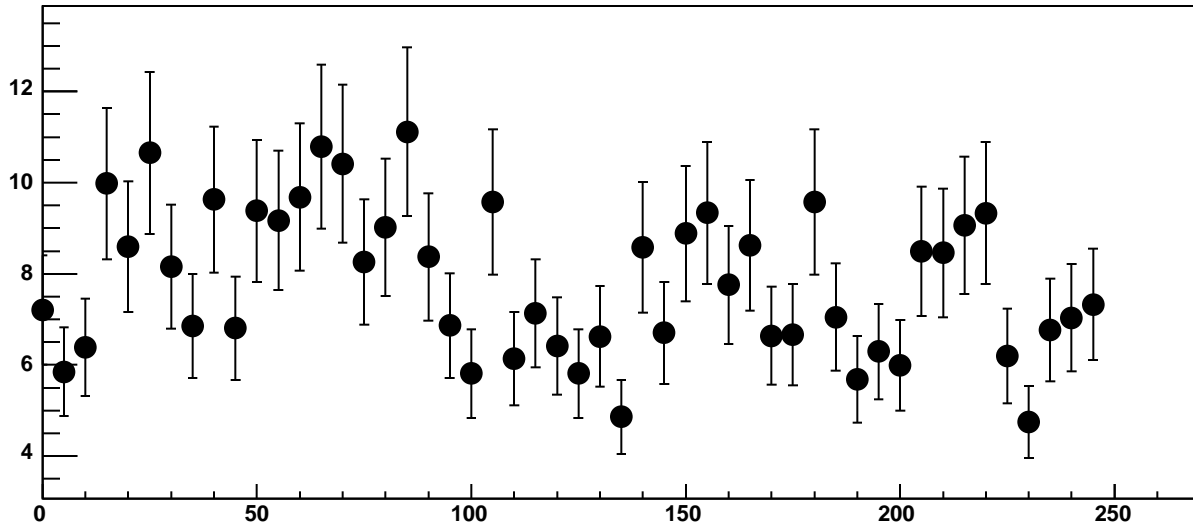


Chip 2, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold

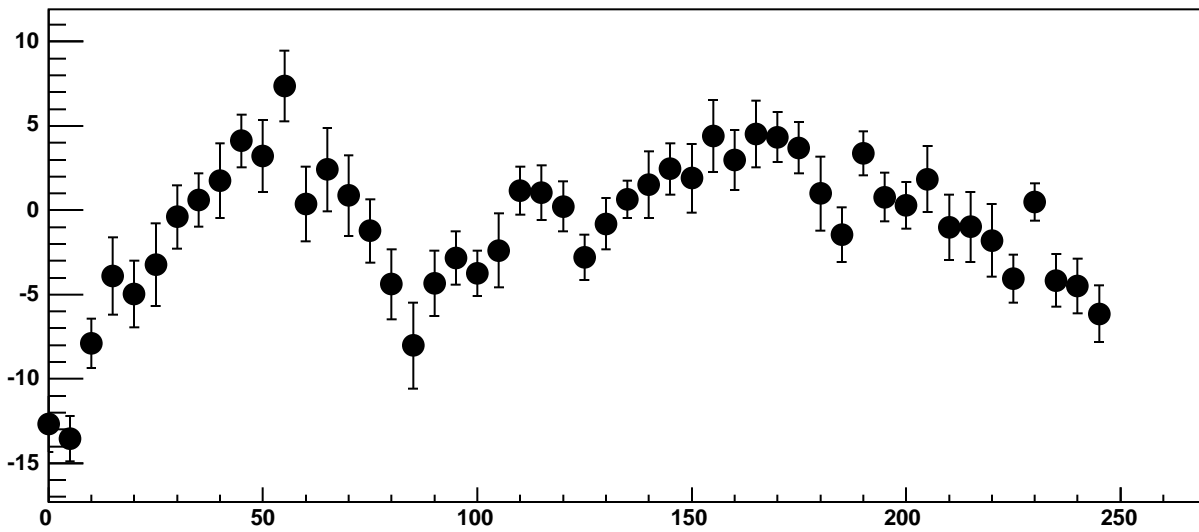


χ^2 / ndf	136.9 / 41
p0	131.2 ± 0.5585
p1	85.36 ± 0.8909
p2	$-4.511\text{e}+07 \pm 9.053\text{e}+05$
p3	$3.253\text{e}+07 \pm 2.468\text{e}+05$
p4	1.348 ± 0.02584

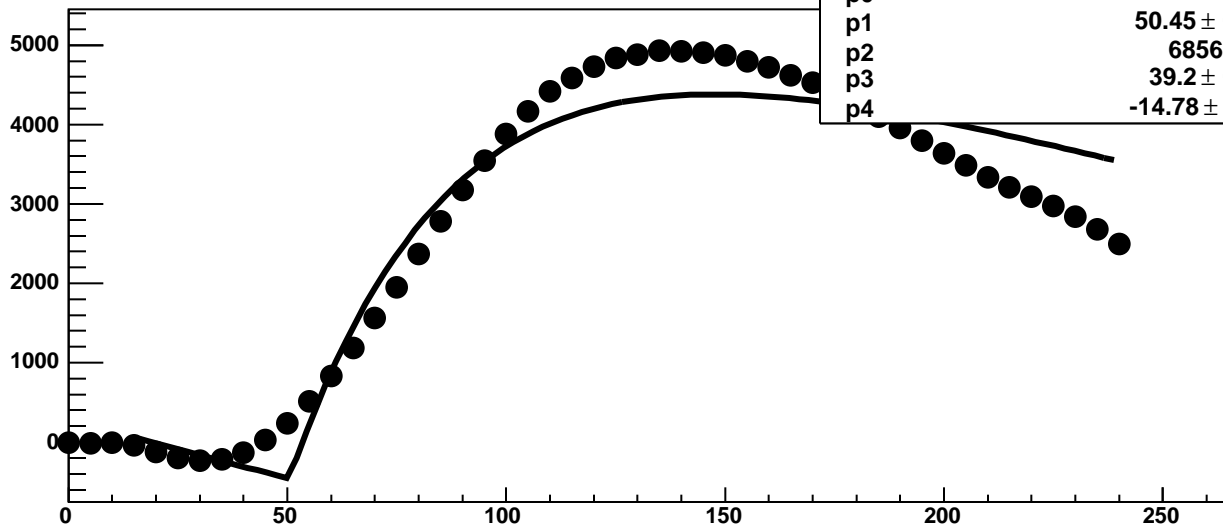
Chip 2, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold

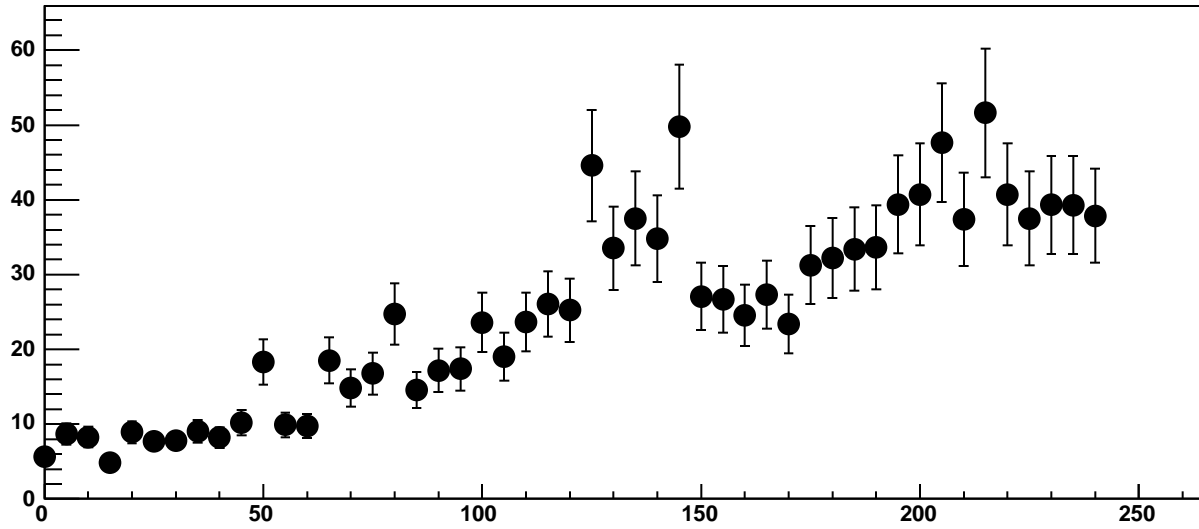


Chip 2, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

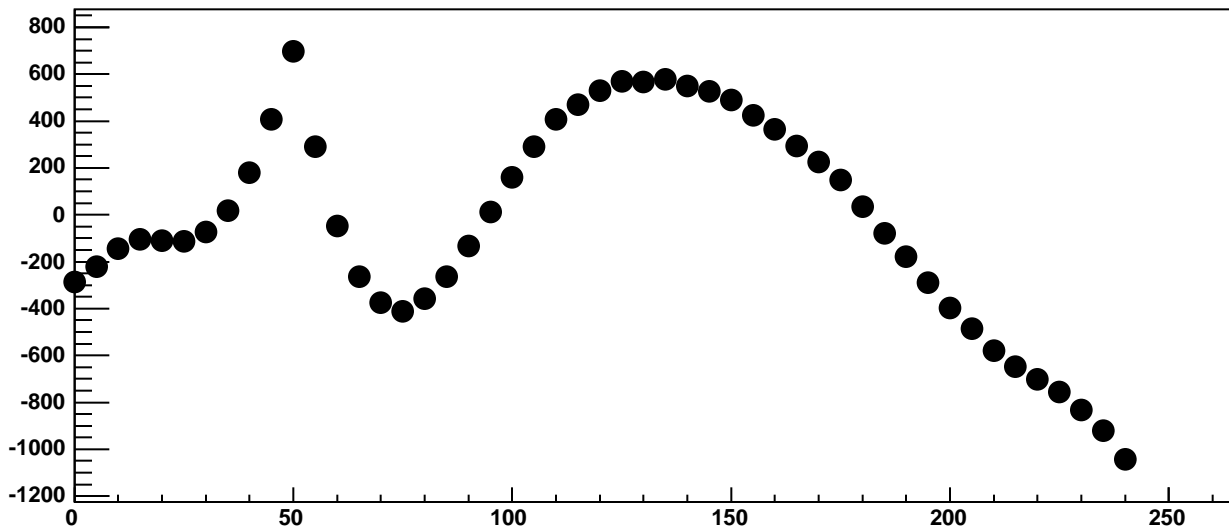


χ^2 / ndf	2.647e+05 / 41
p0	-463.9 ± 1.119
p1	50.45 ± 0.01549
p2	6856 ± 8.047
p3	39.2 ± 0.05559
p4	-14.78 ± 0.04351

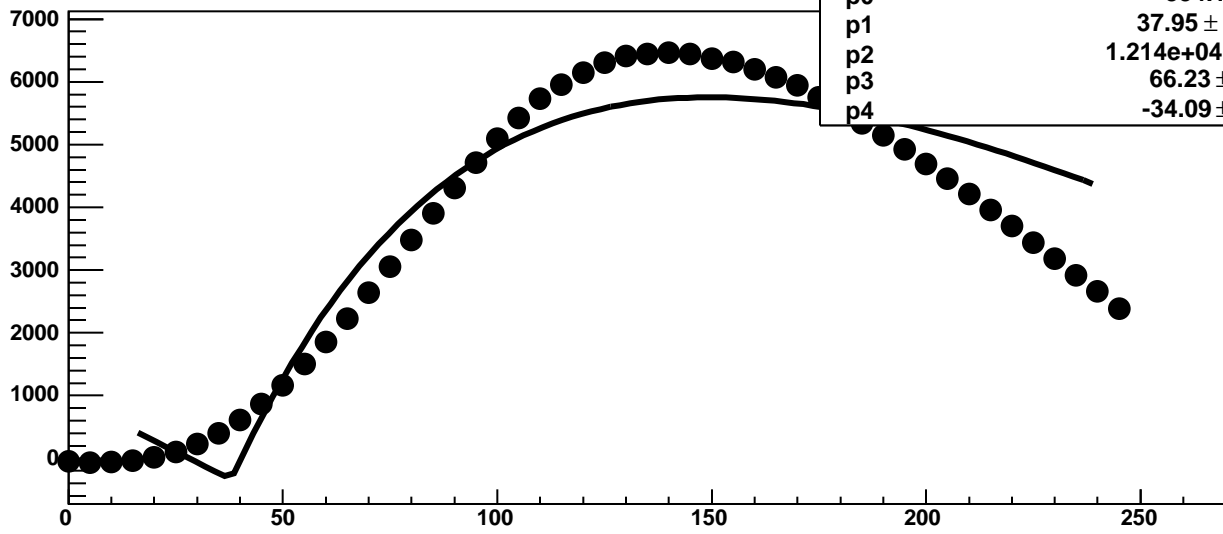
Chip 2, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

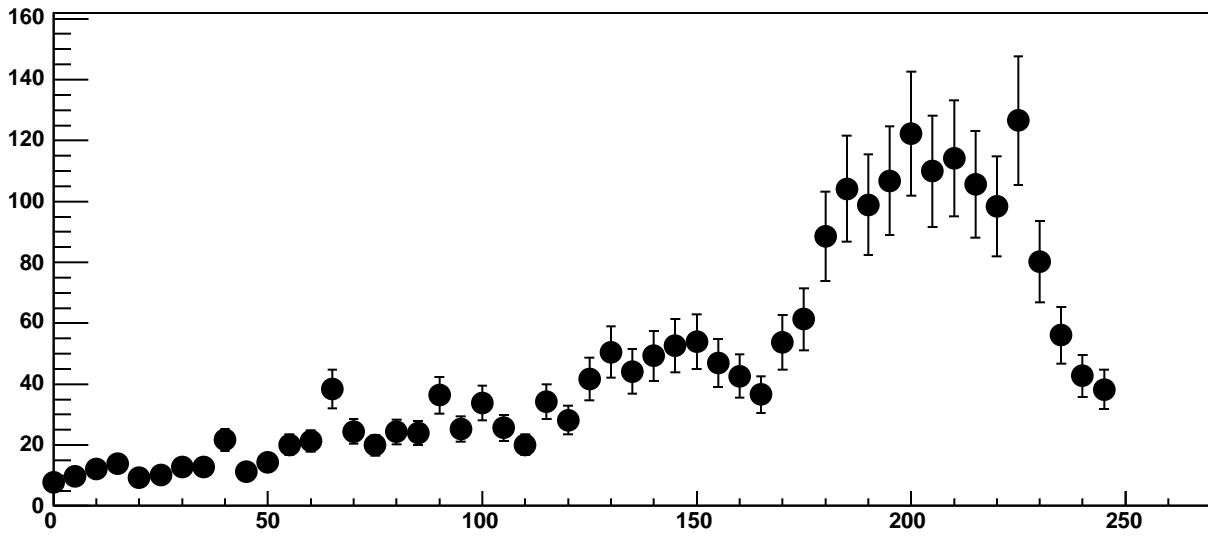


Chip 2, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

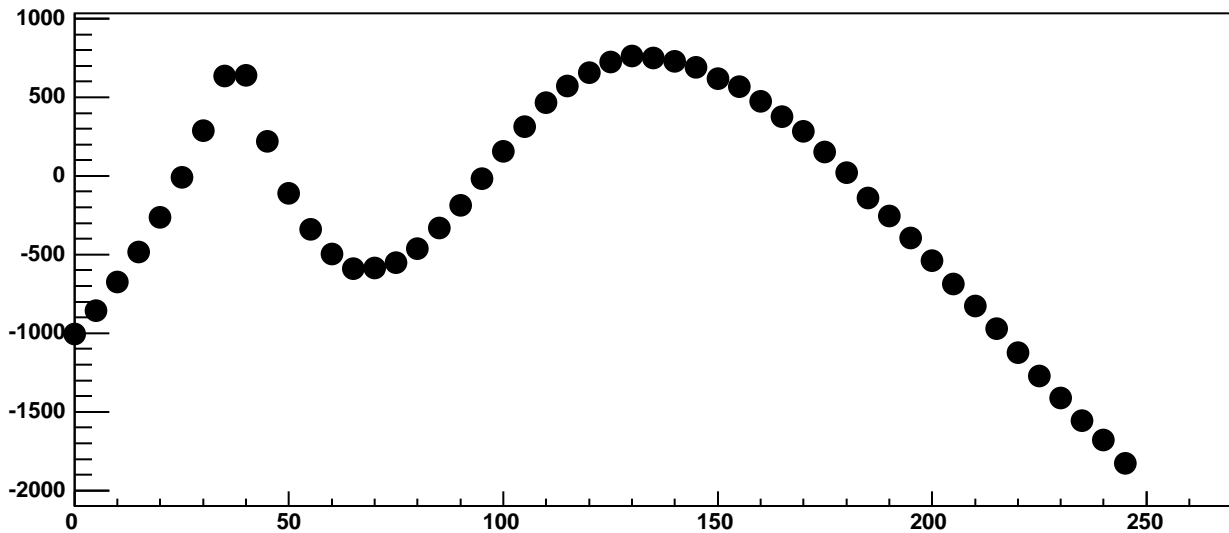


χ^2 / ndf	2.996e+05 / 41
p0	-334.1 ± 1.555
p1	37.95 ± 0.01741
p2	1.214e+04 ± 24.38
p3	66.23 ± 0.1203
p4	-34.09 ± 0.1078

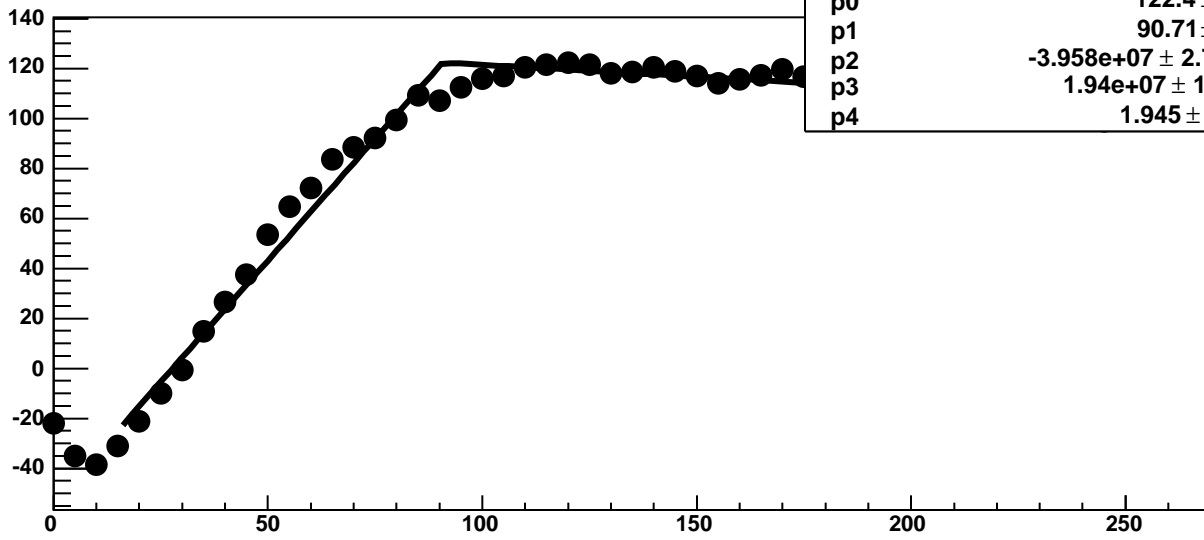
Chip 2, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold

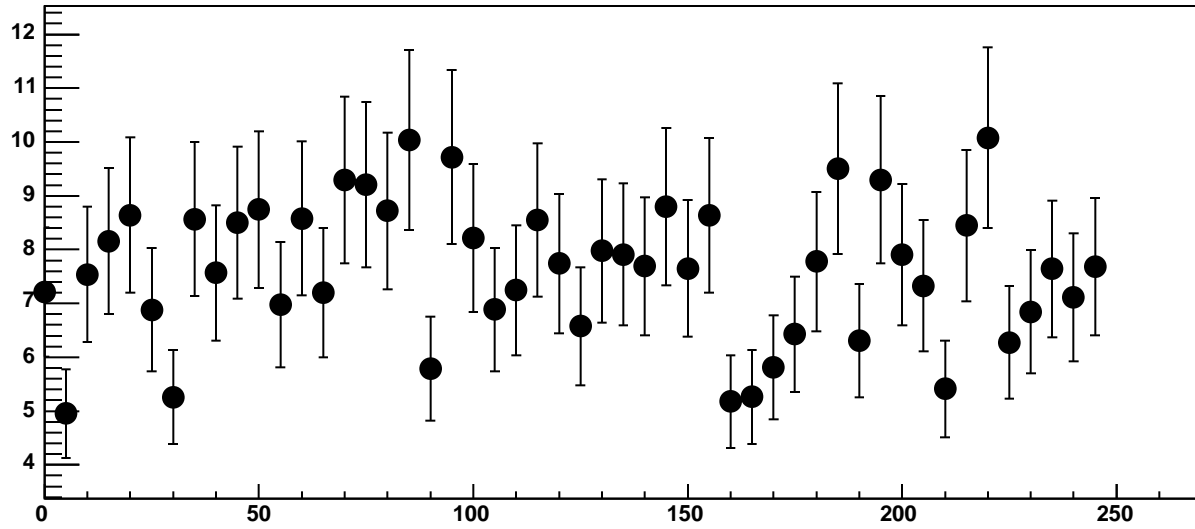


Chip 2, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold

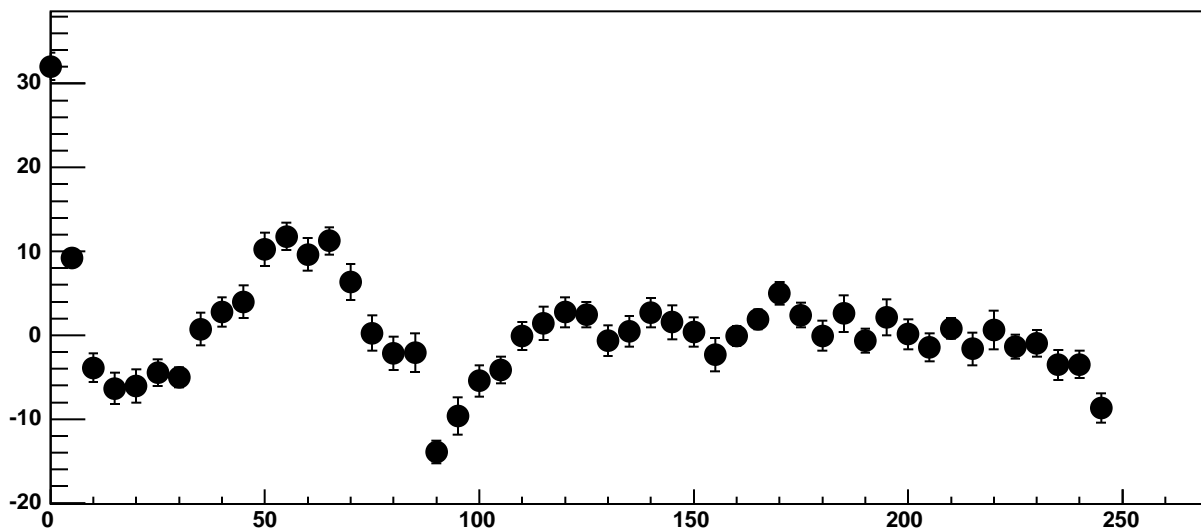


χ^2 / ndf	401.1 / 41
p0	122.4 ± 0.6219
p1	90.71 ± 0.5262
p2	$-3.958e+07 \pm 2.713e+06$
p3	$1.94e+07 \pm 1.32e+06$
p4	1.945 ± 0.01857

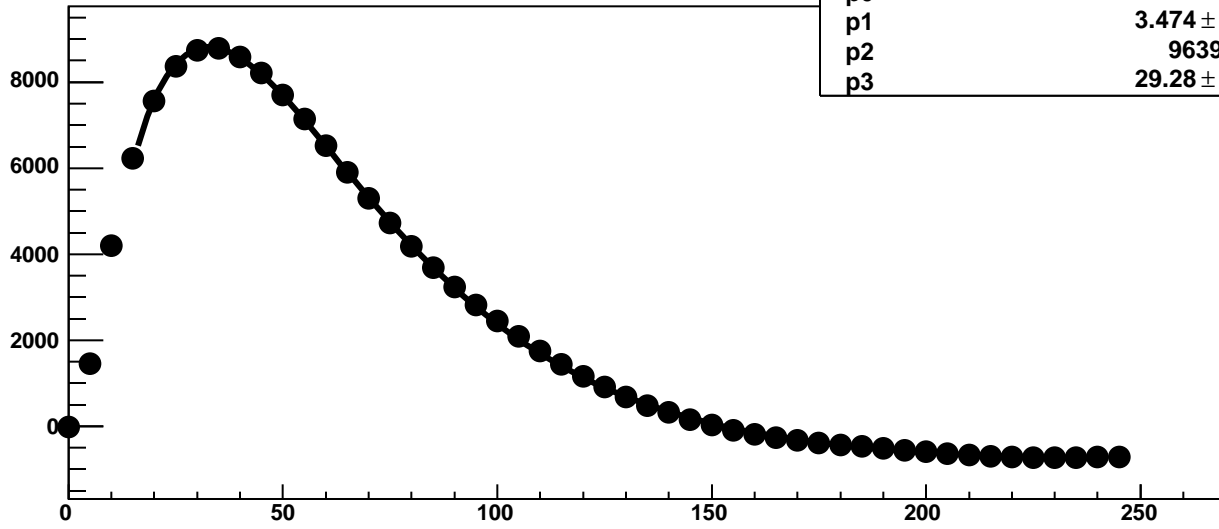
Chip 2, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold

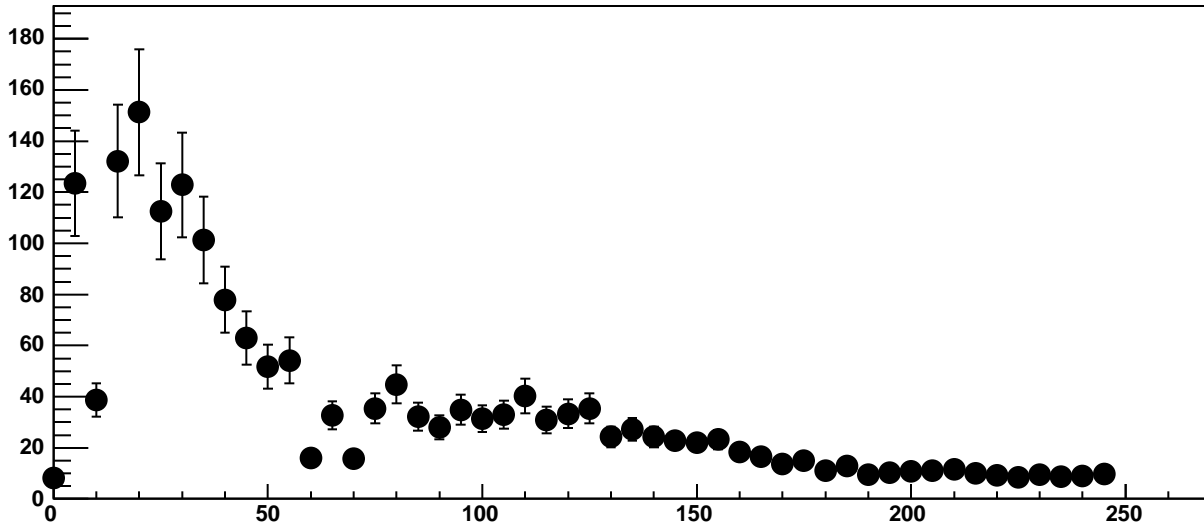


Chip 2, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold

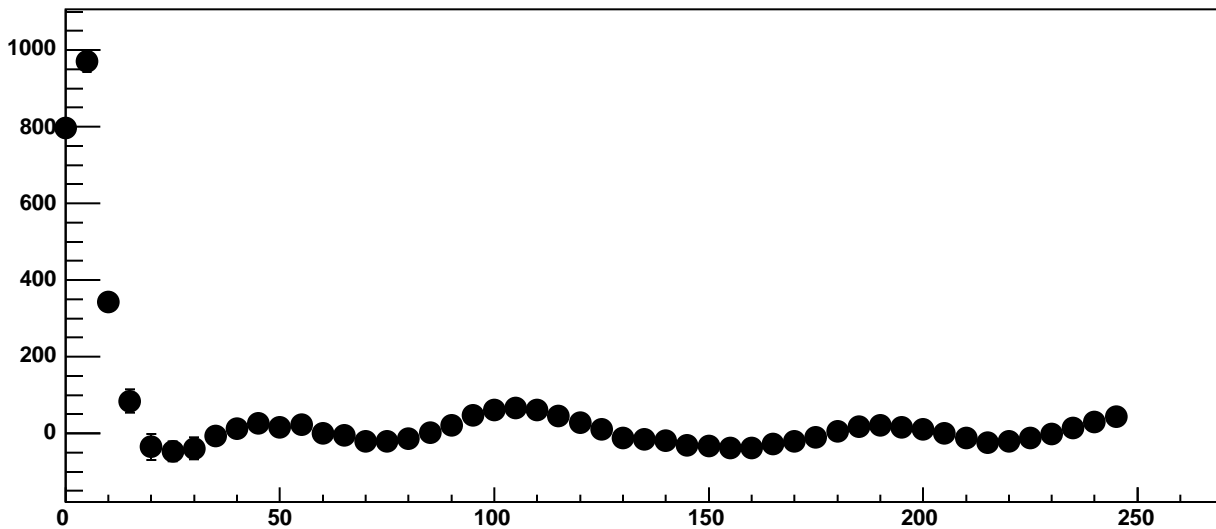


χ^2 / ndf	1427 / 42
p0	-817.9 ± 0.9001
p1	3.474 ± 0.05594
p2	9639 ± 6.487
p3	29.28 ± 0.02063

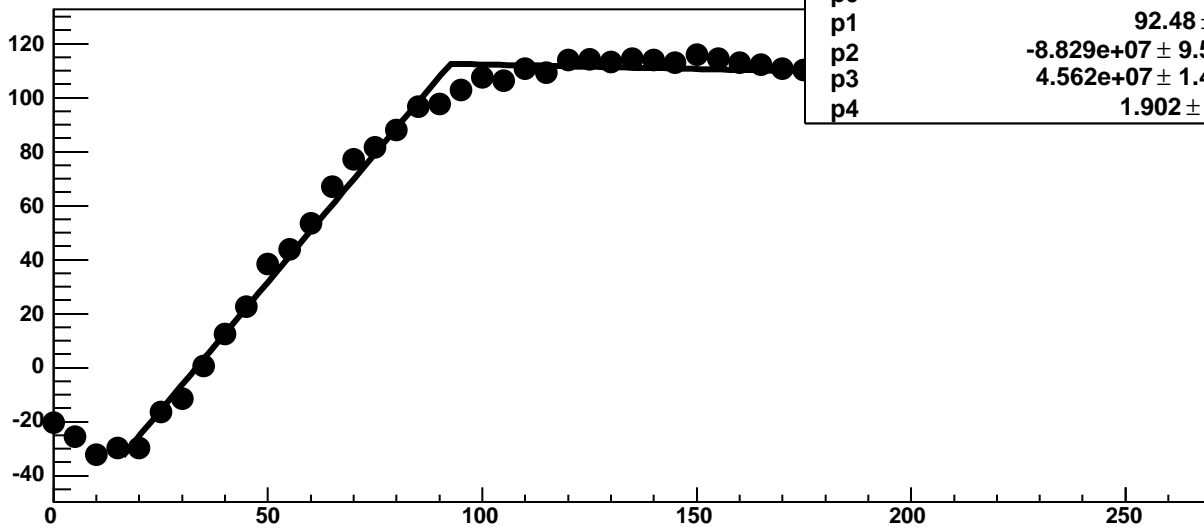
Chip 2, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold

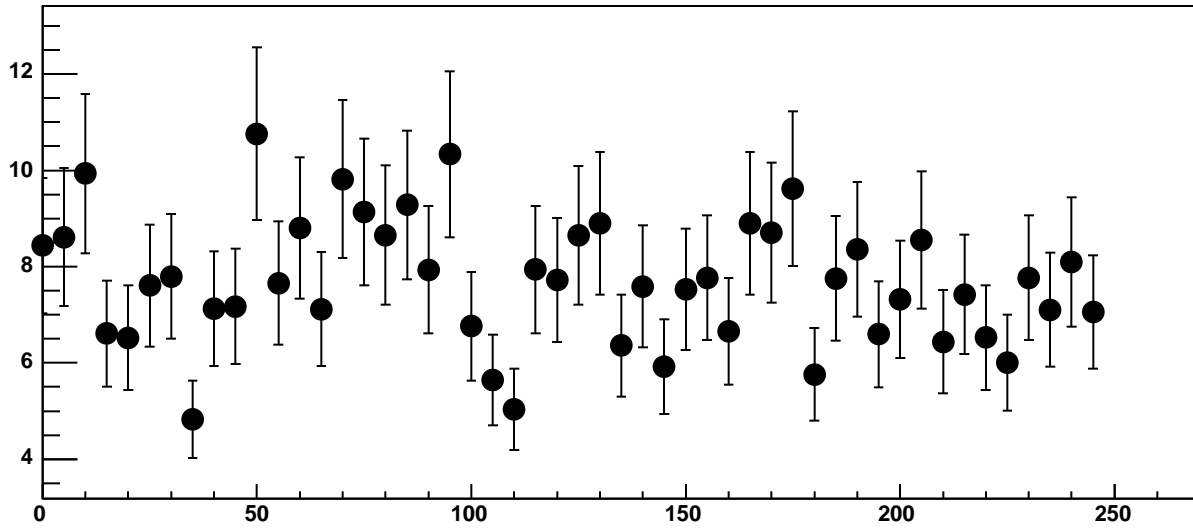


Chip 2, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

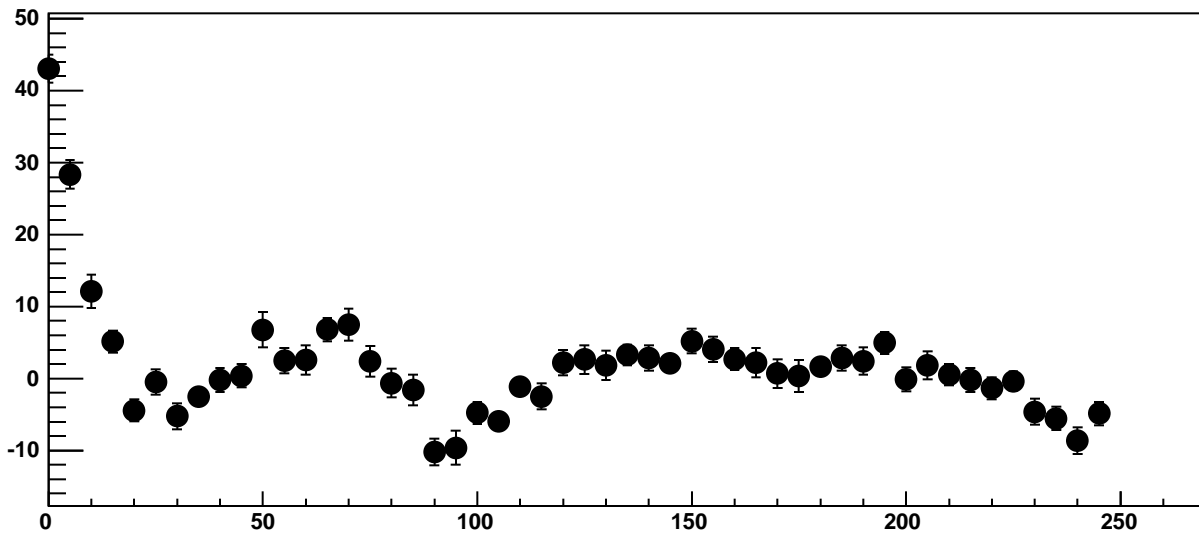


χ^2 / ndf	248.1 / 41
p0	112.5 ± 0.5752
p1	92.48 ± 0.5793
p2	$-8.829e+07 \pm 9.566e+05$
p3	$4.562e+07 \pm 1.464e+05$
p4	1.902 ± 0.01896

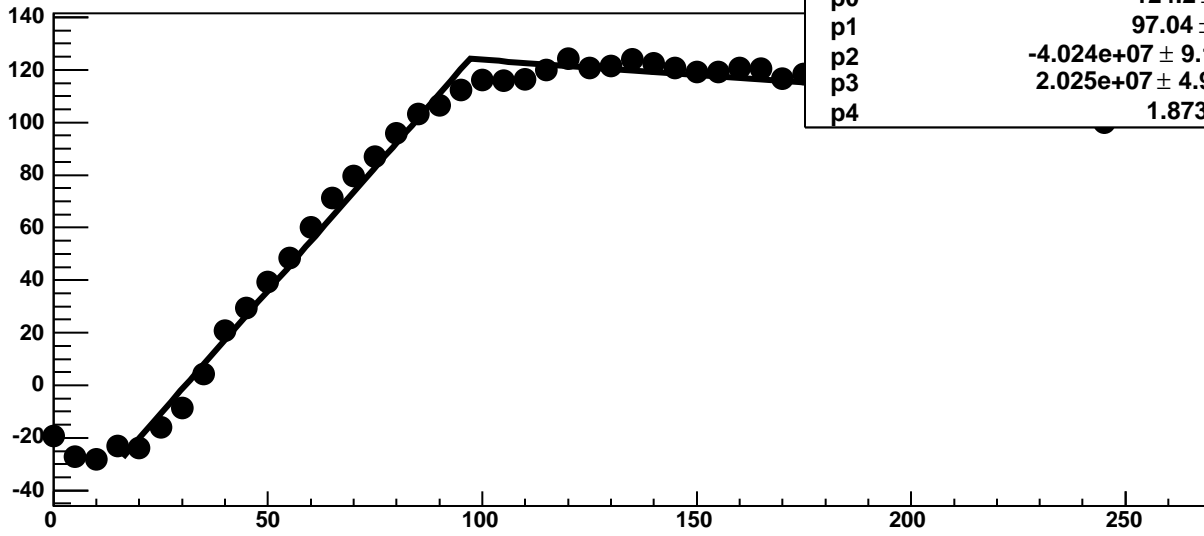
Chip 2, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

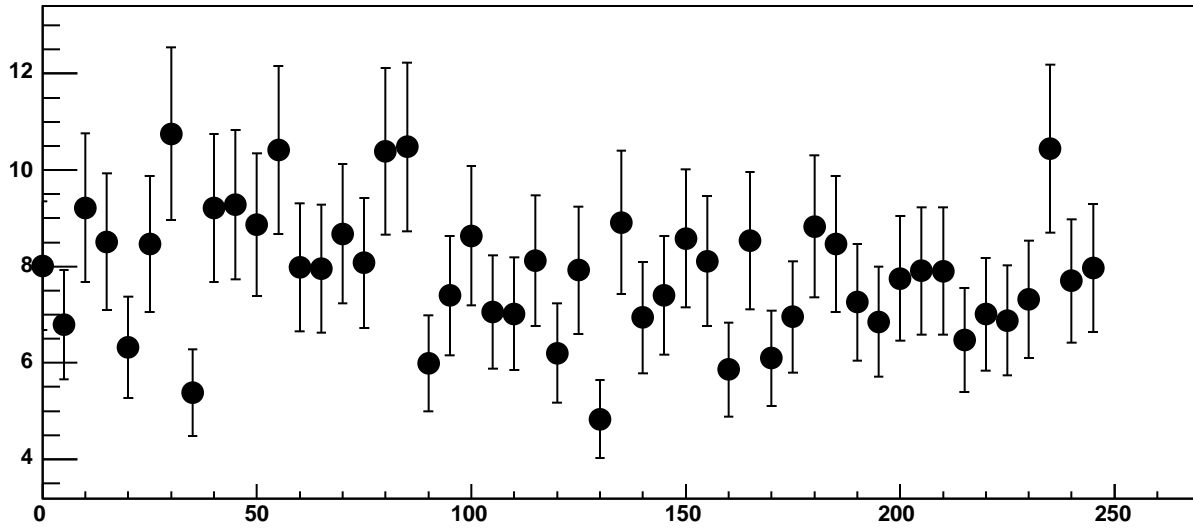


Chip 2, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

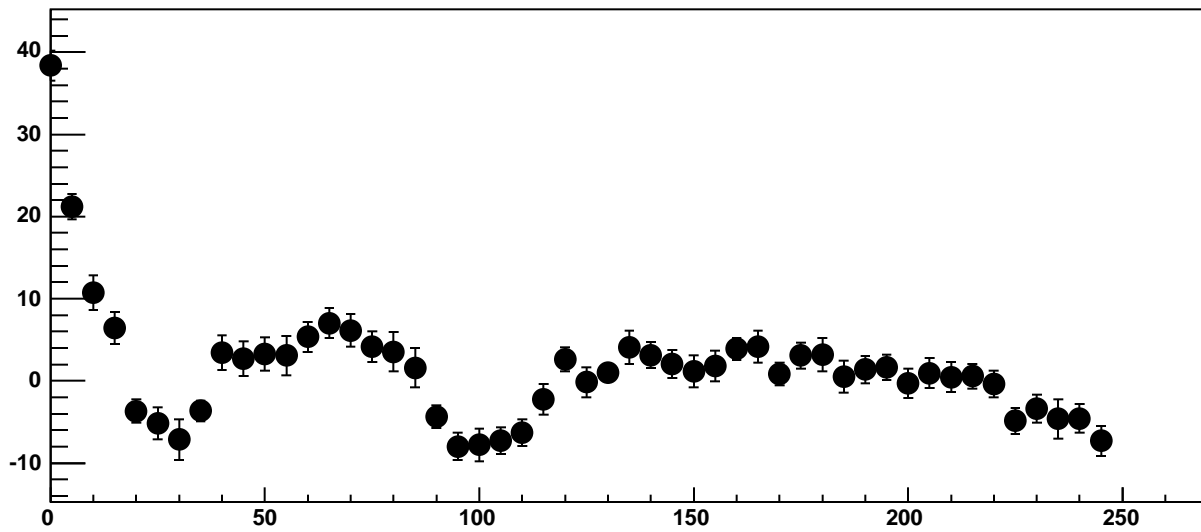


χ^2 / ndf	235.8 / 41
p0	124.2 ± 0.5678
p1	97.04 ± 0.5074
p2	$-4.024\text{e}+07 \pm 9.112\text{e}+05$
p3	$2.025\text{e}+07 \pm 4.906\text{e}+05$
p4	1.873 ± 0.016

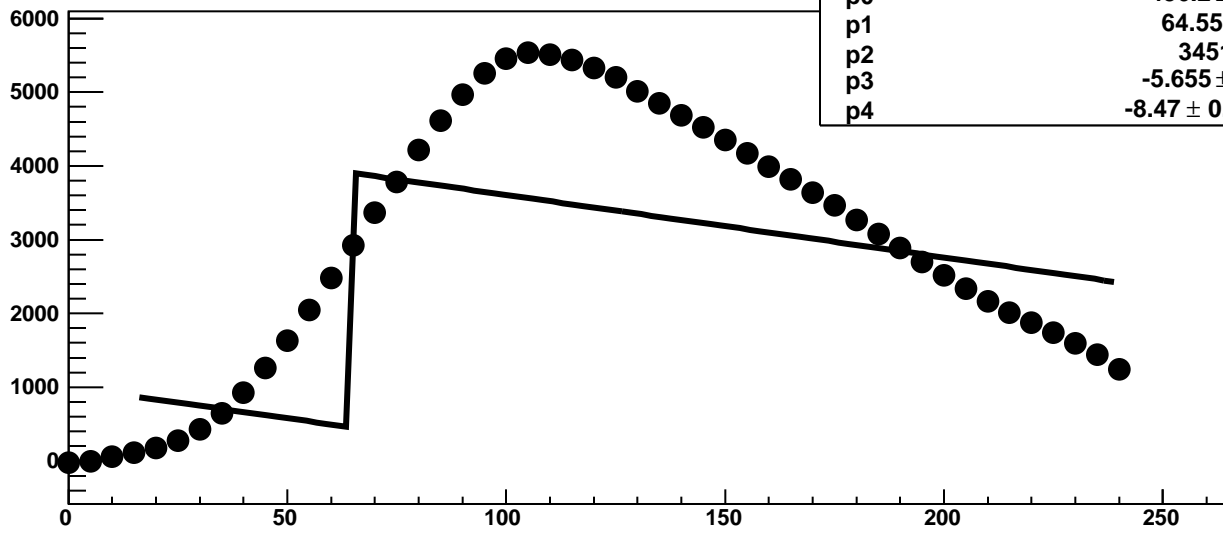
Chip 2, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

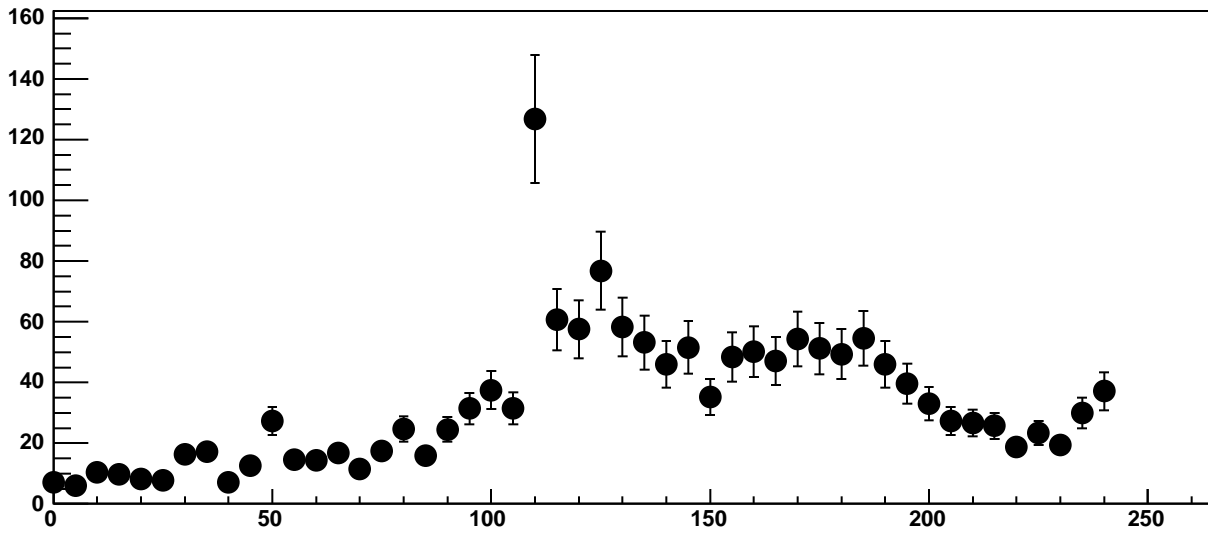


Chip 2, Channel 6, Enable 5, DAC=4000, ADC Mean vs Hold

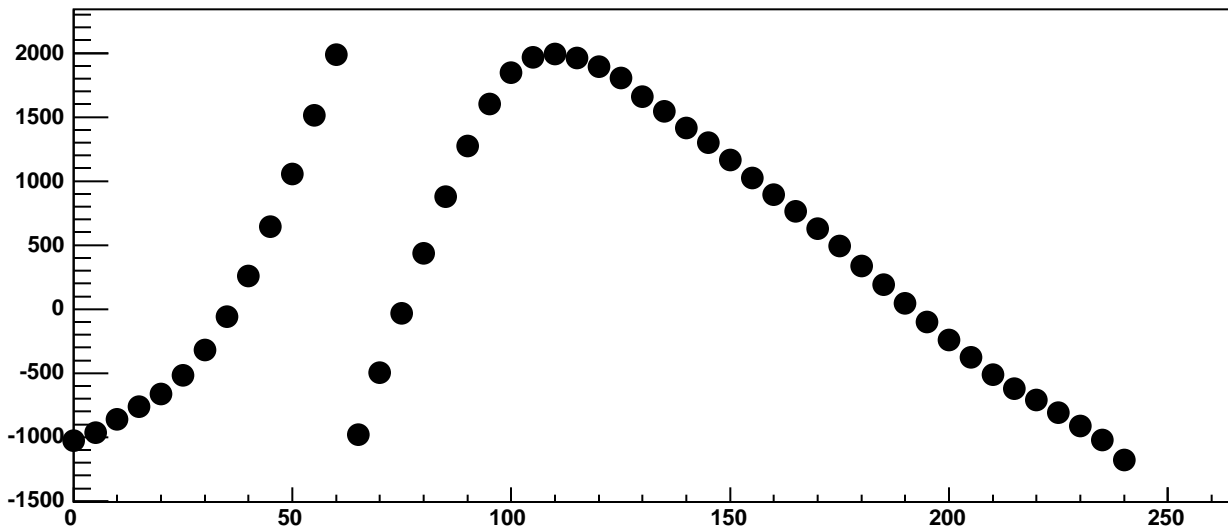


χ^2 / ndf	1.696e+06 / 41
p0	456.2 ± 0.1622
p1	64.55 ± 0.287
p2	3451 ± 0.24
p3	-5.655 ± 0.1139
p4	-8.47 ± 0.001743

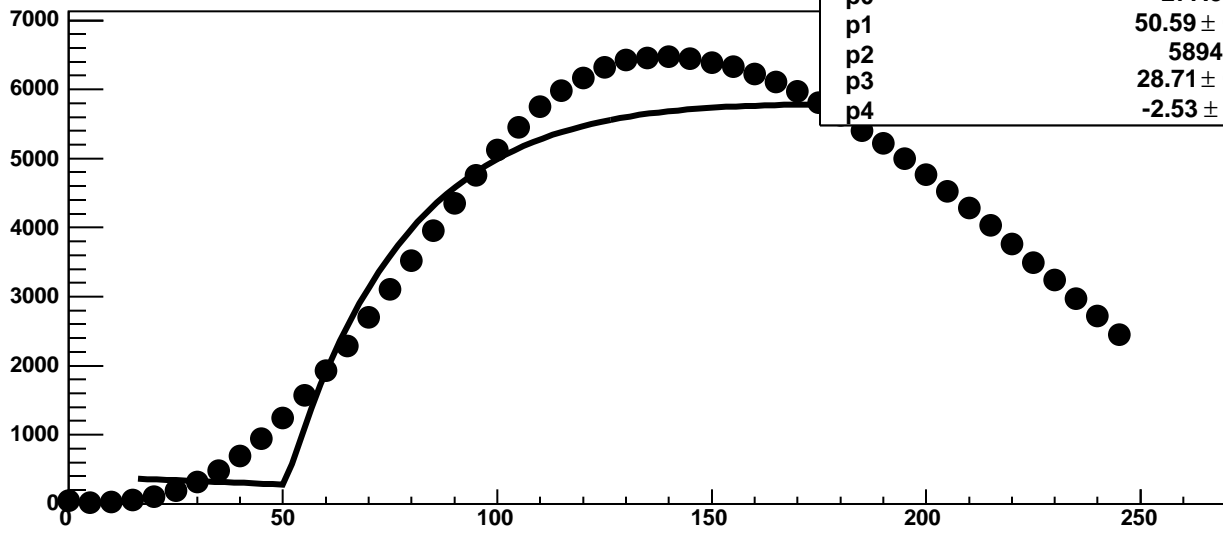
Chip 2, Channel 6, Enable 5, DAC=4000, ADC Noise vs Hold



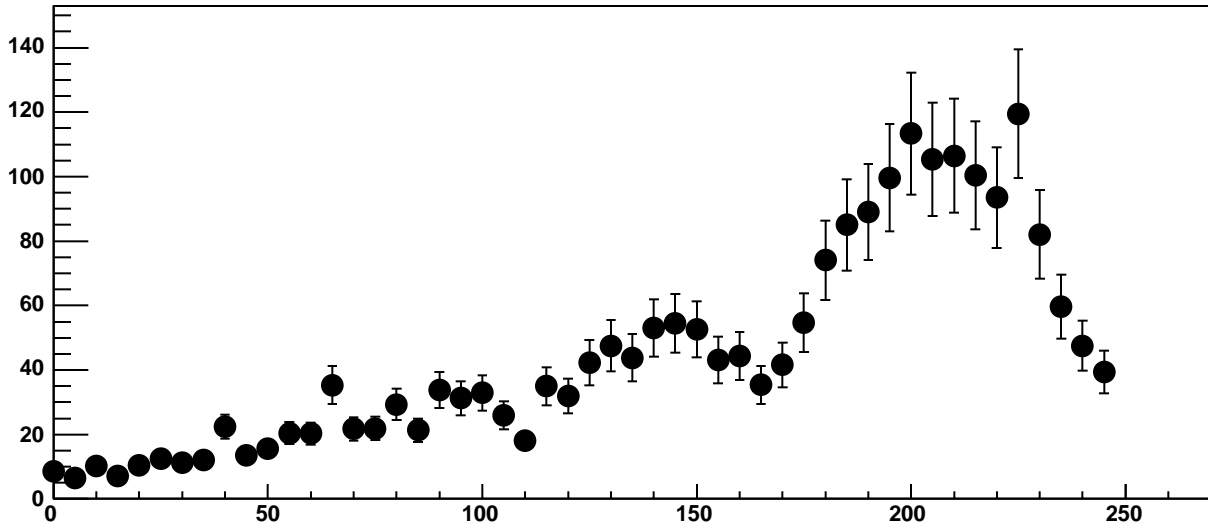
Chip 2, Channel 6, Enable 5, DAC=4000, ADC Residuals vs Hold



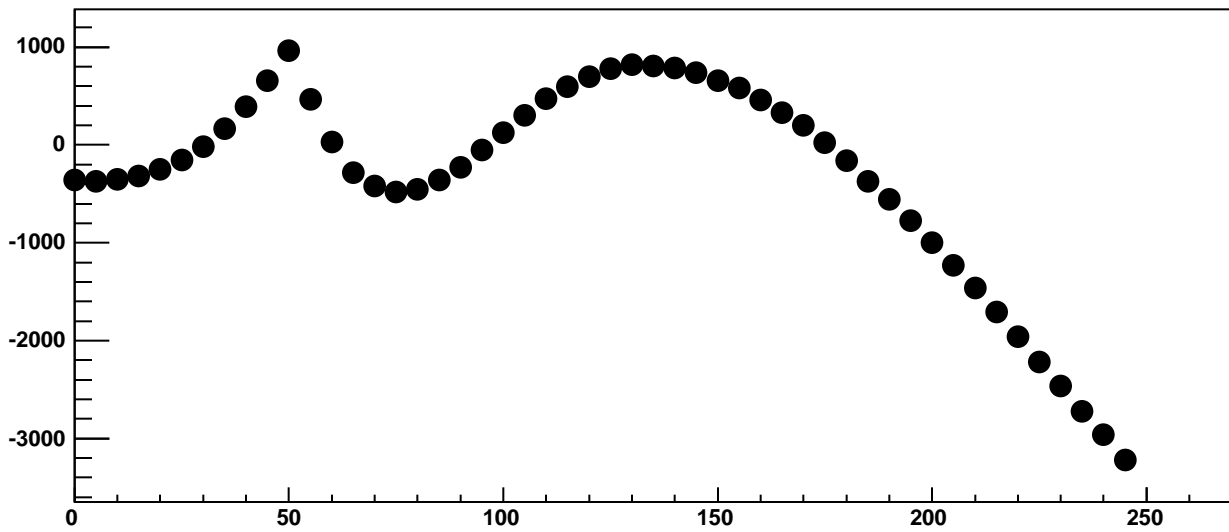
Chip 2, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold



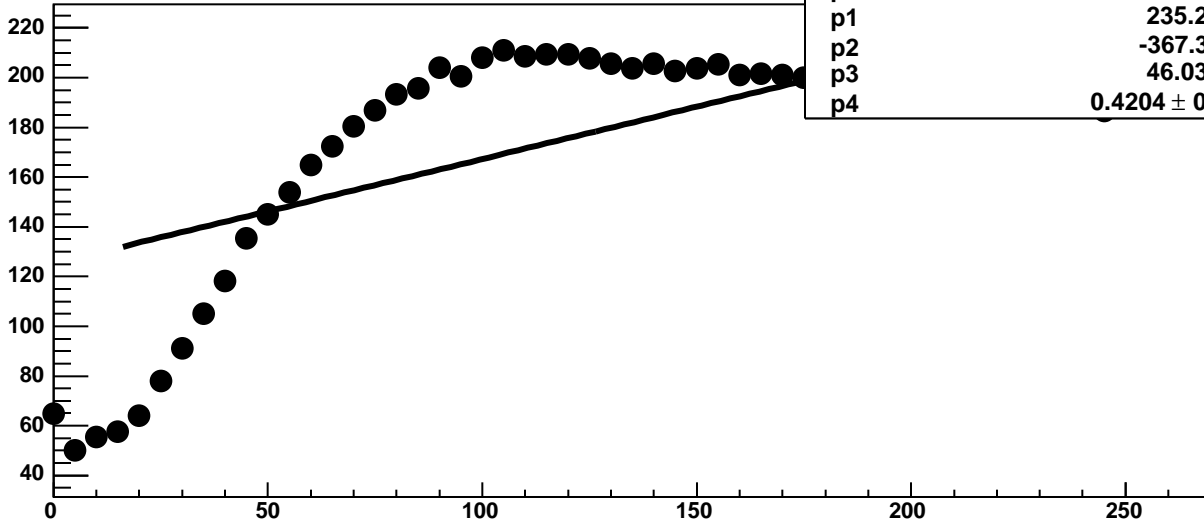
Chip 2, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

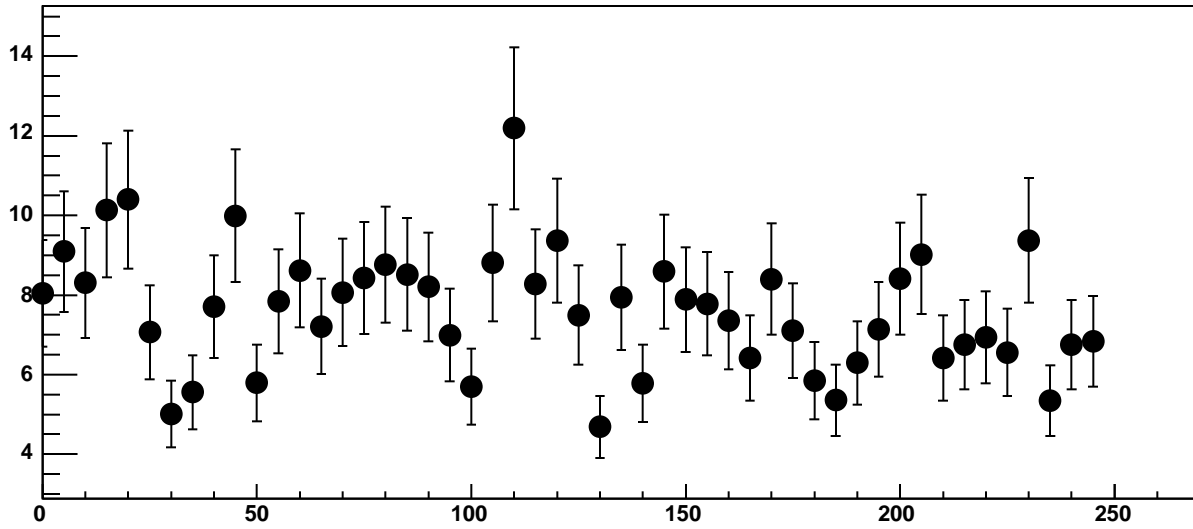


Chip 2, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold

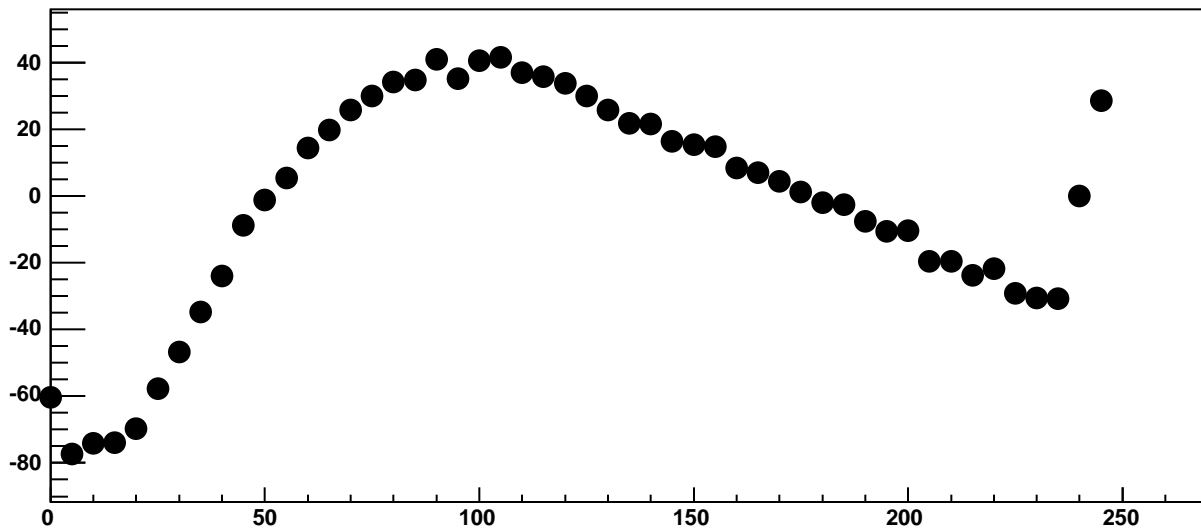


χ^2 / ndf	1.365e+04 / 41
p0	224.1 ± 1.148
p1	235.2 ± 2.516
p2	-367.3 ± 175.9
p3	46.03 ± 19.16
p4	0.4204 ± 0.003695

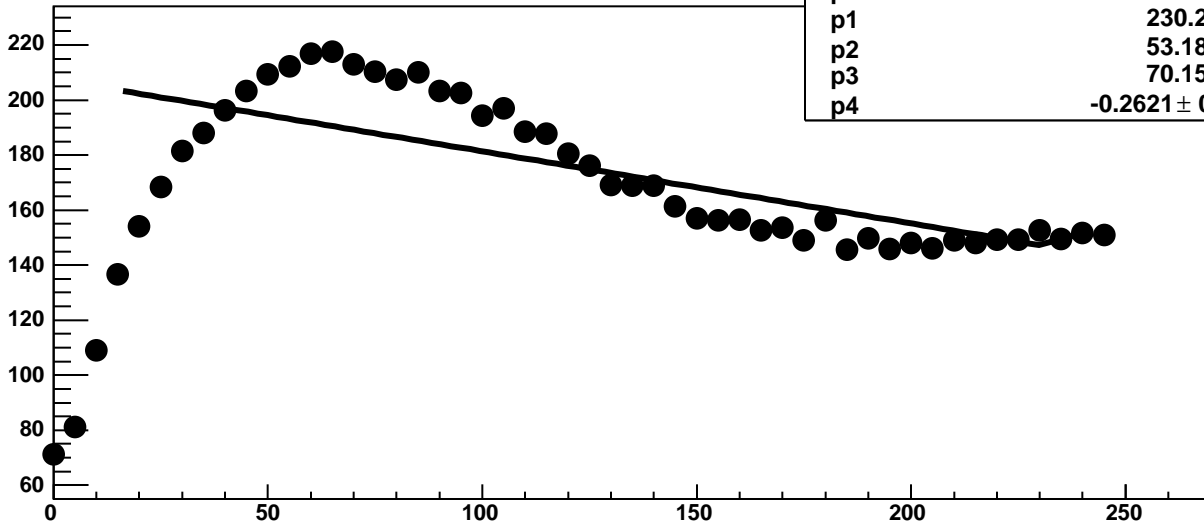
Chip 2, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

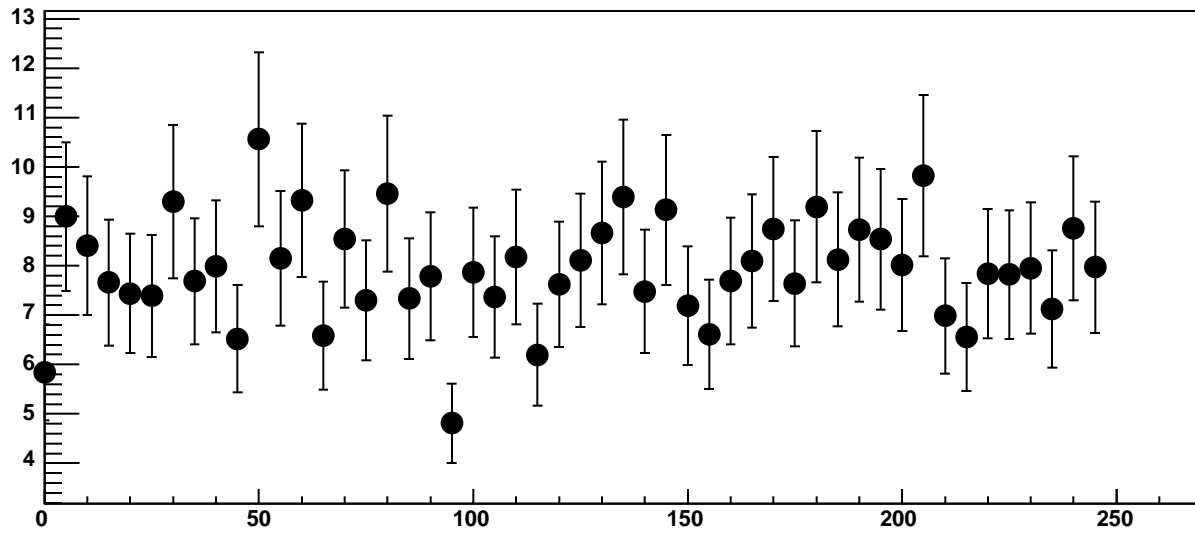


Chip 2, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold

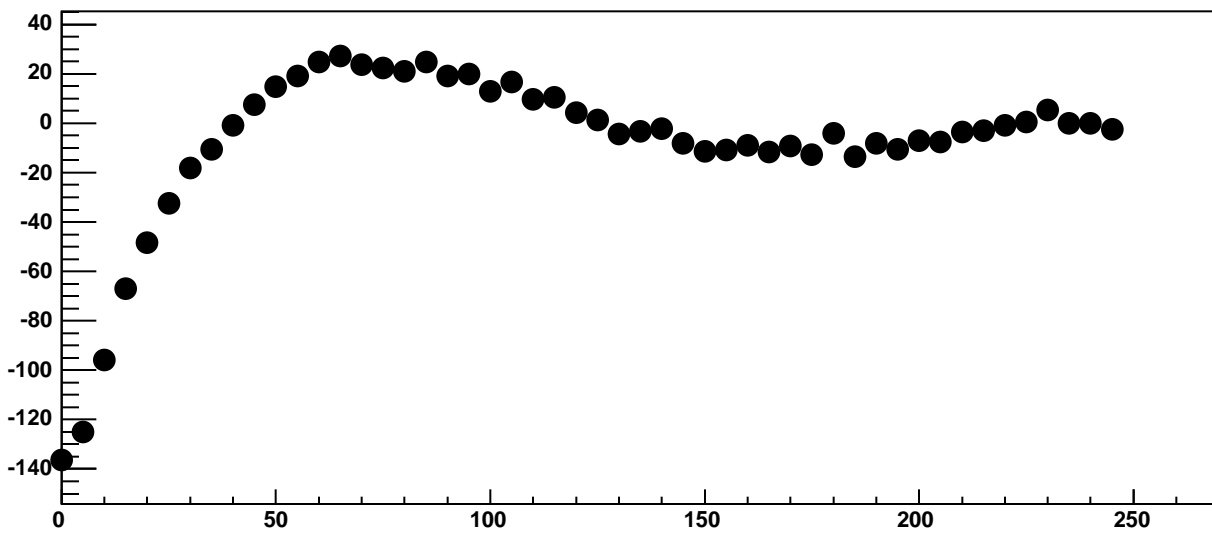


χ^2 / ndf	5126 / 41
p0	147.3 ± 1.417
p1	230.2 ± 5.264
p2	53.18 ± 29.37
p3	70.15 ± 12.84
p4	-0.2621 ± 0.004131

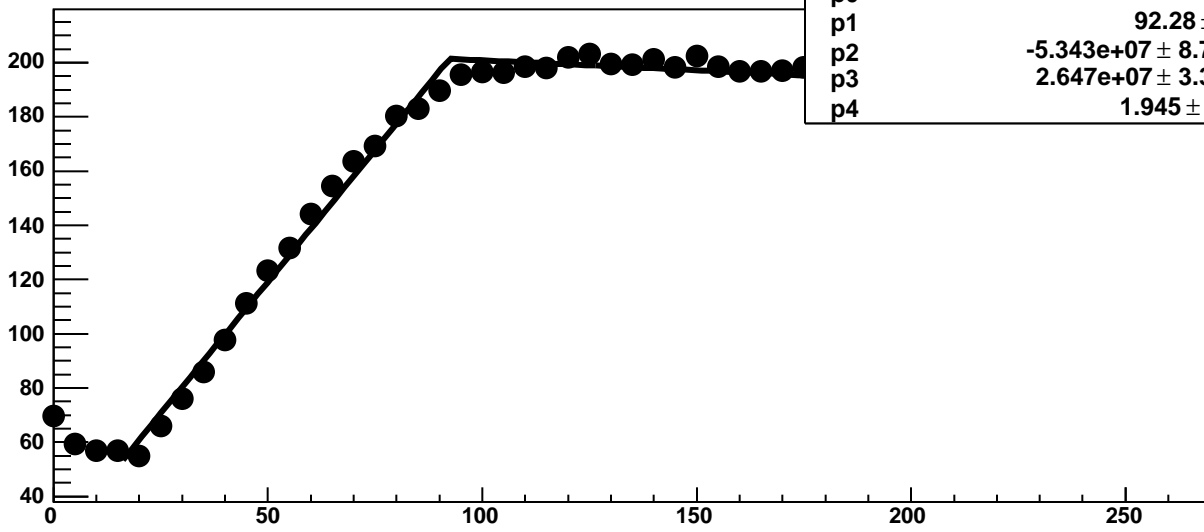
Chip 2, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold

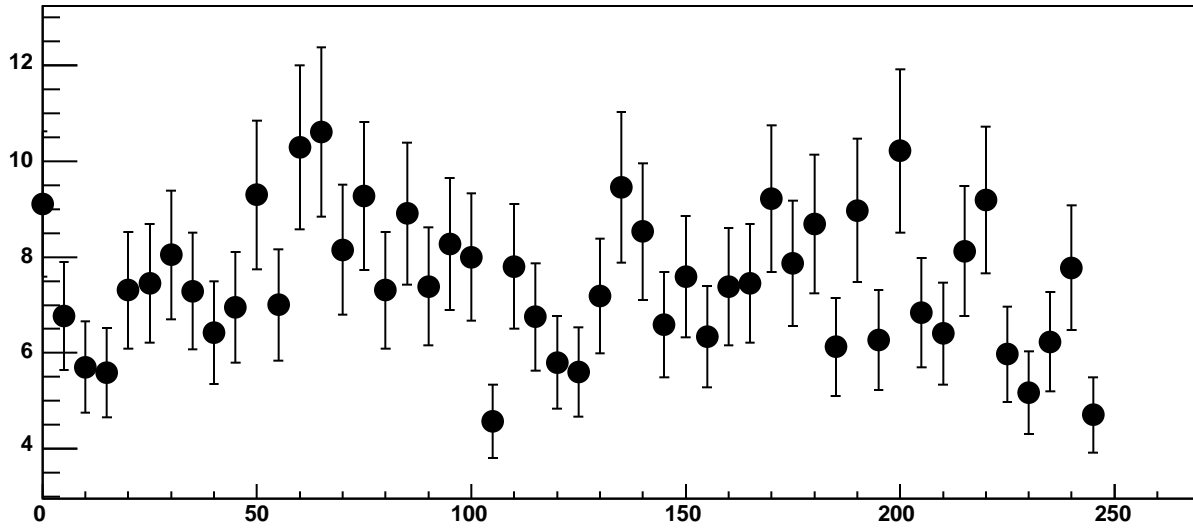


Chip 2, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

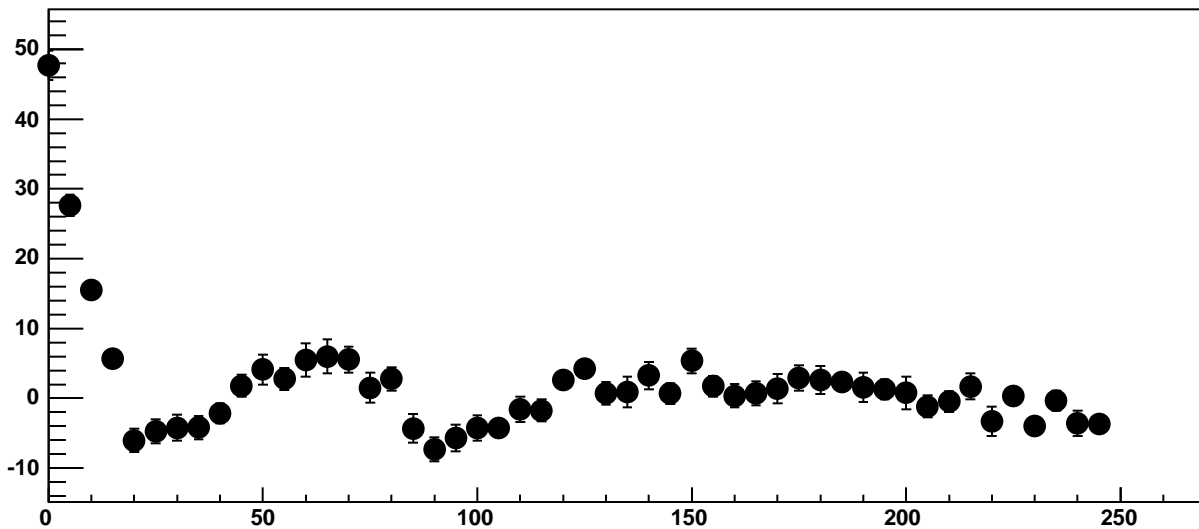


χ^2 / ndf	199 / 41
p0	201.4 ± 0.5441
p1	92.28 ± 0.5498
p2	$-5.343\text{e}+07 \pm 8.775\text{e}+05$
p3	$2.647\text{e}+07 \pm 3.394\text{e}+05$
p4	1.945 ± 0.01854

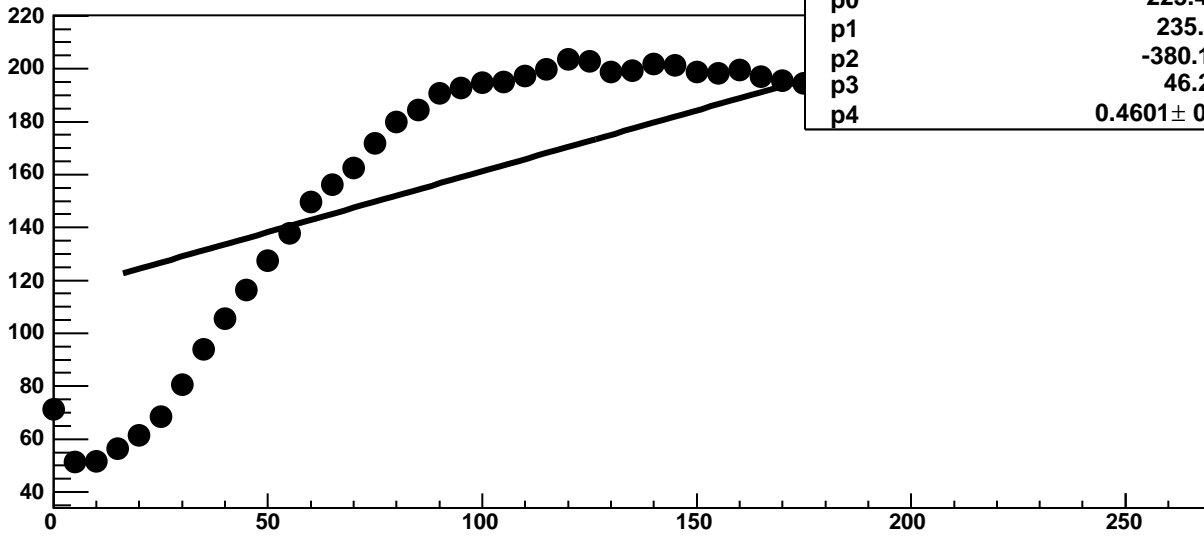
Chip 2, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

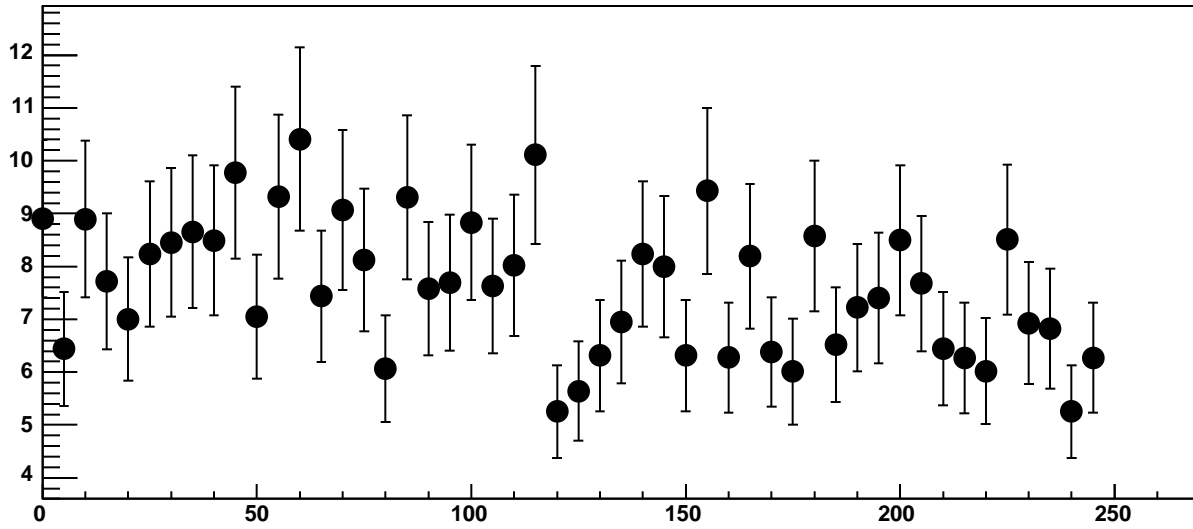


Chip 2, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

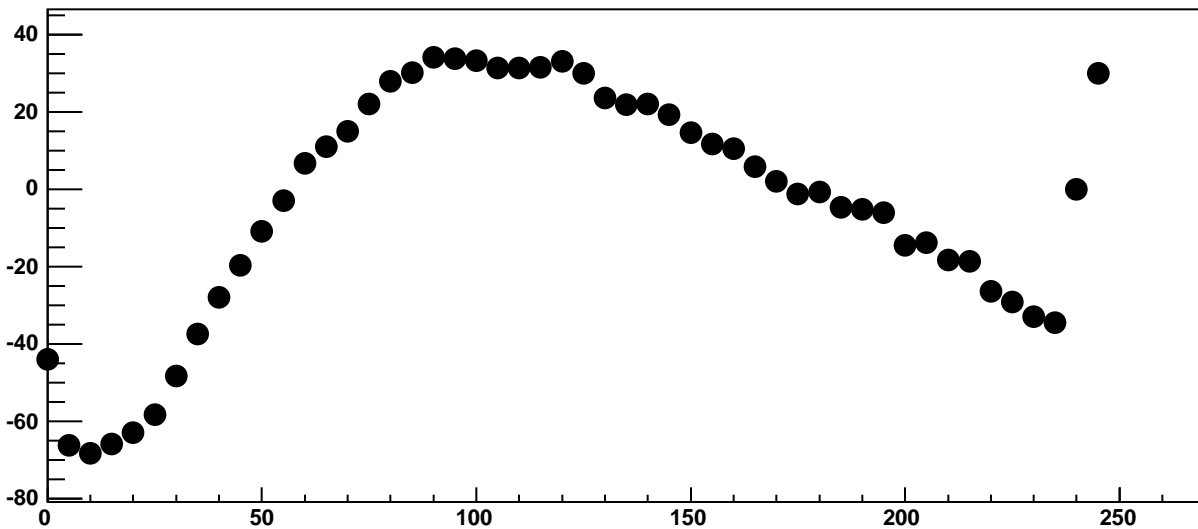


χ^2 / ndf	1.202e+04 / 41
p0	223.4 ± 1.146
p1	235.1 ± 2.28
p2	-380.1 ± 160.3
p3	46.21 ± 17
p4	0.4601 ± 0.003958

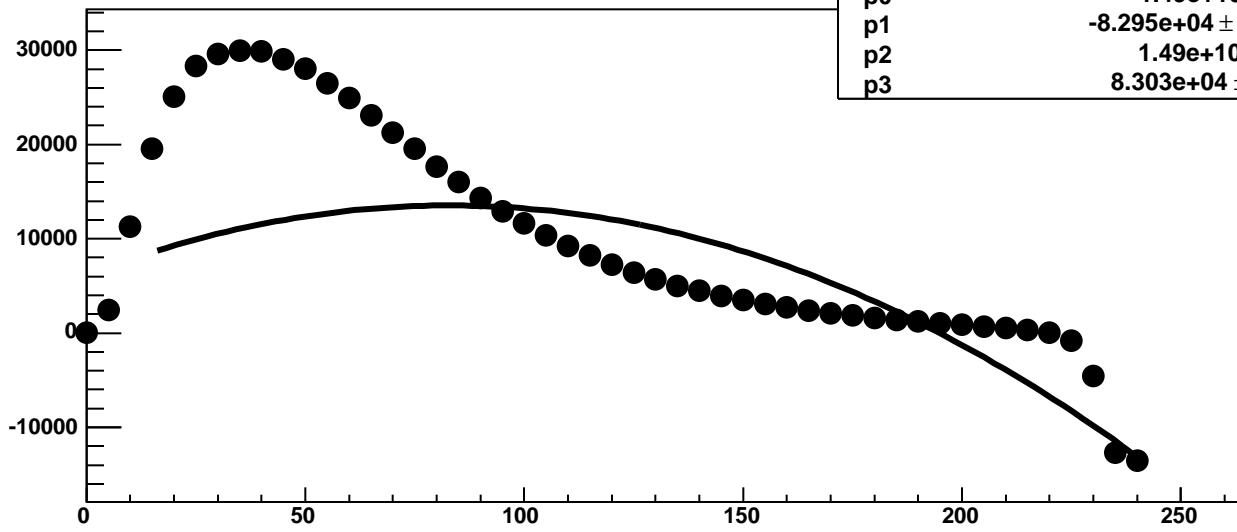
Chip 2, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

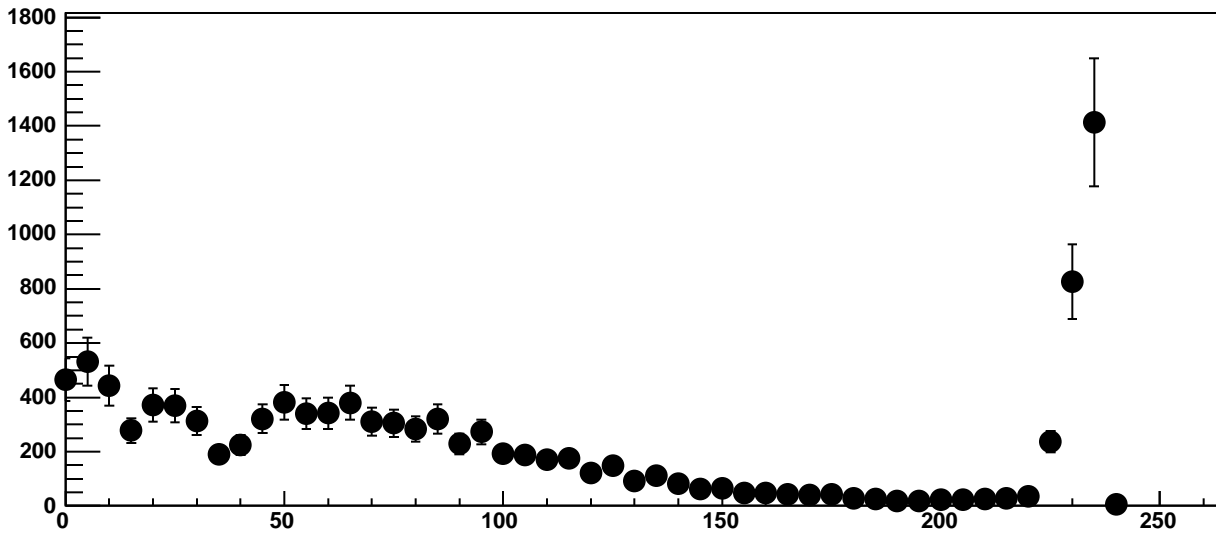


Chip 2, Channel 7, Enable 5!, DAC=4000, ADC Mean vs Hold

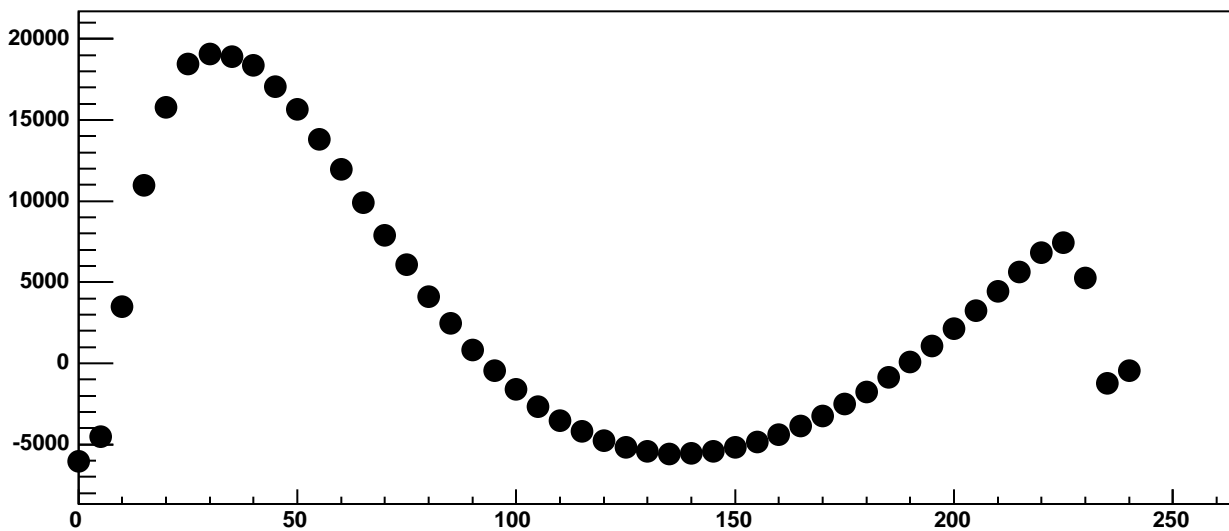


χ^2 / ndf	4.838e+06 / 42
p0	-1.49e+10 ± 10.89
p1	-8.295e+04 ± 0.03238
p2	1.49e+10 ± 10.89
p3	8.303e+04 ± 0.0323

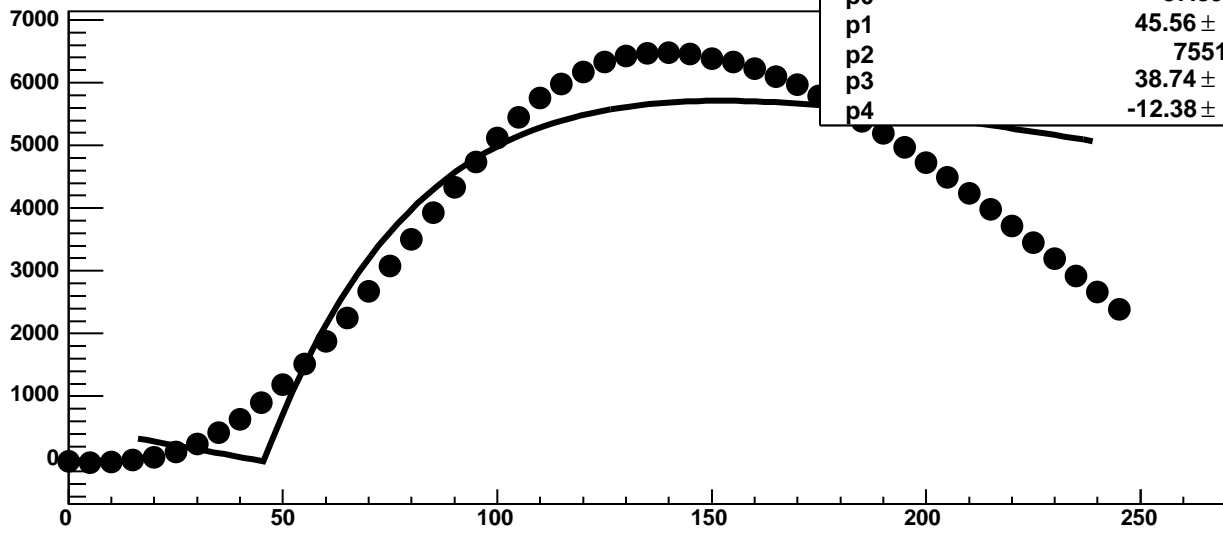
Chip 2, Channel 7, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 7, Enable 5!, DAC=4000, ADC Residuals vs Hold

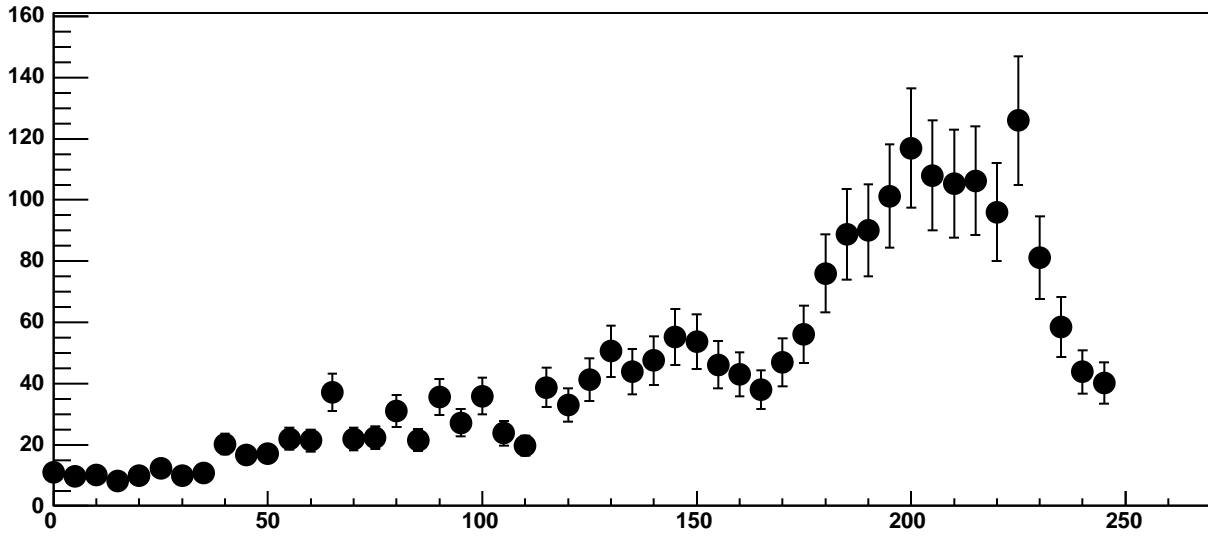


Chip 2, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

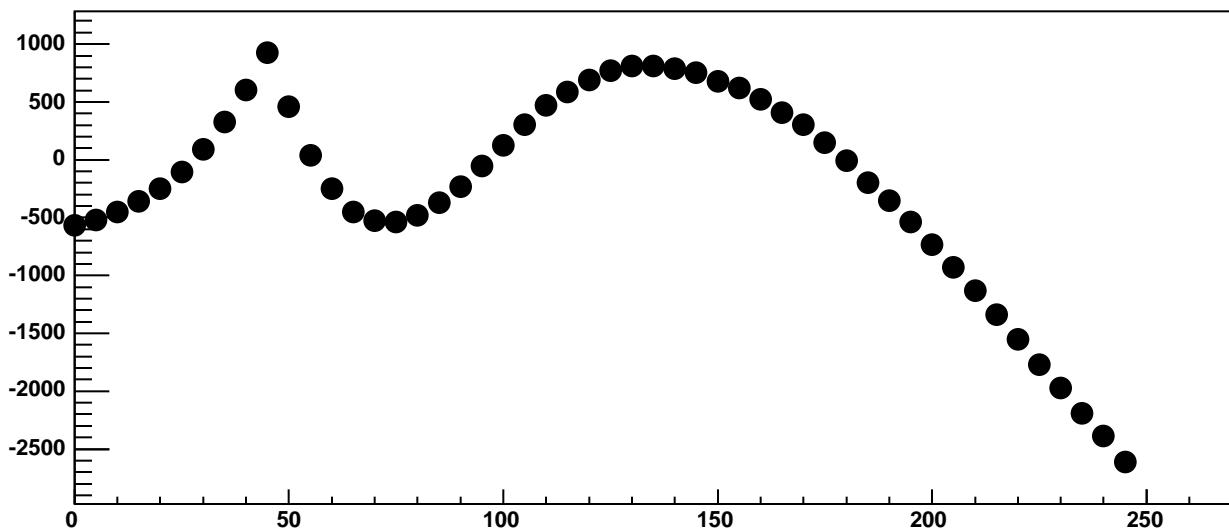


χ^2 / ndf	3.776e+05 / 41
p0	-37.89 ± 1.513
p1	45.56 ± 0.02094
p2	7551 ± 11.18
p3	38.74 ± 0.06754
p4	-12.38 ± 0.06456

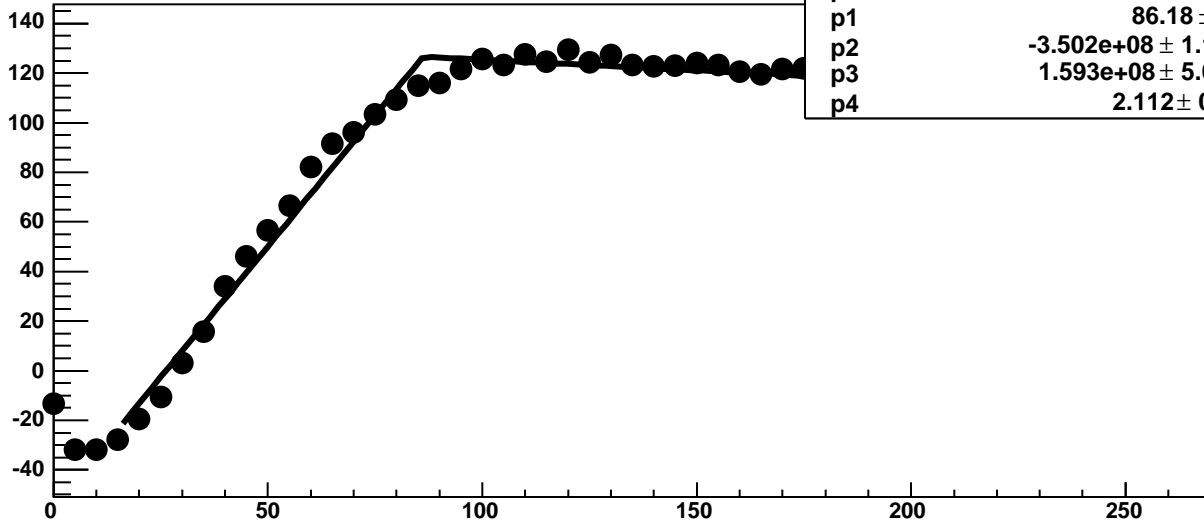
Chip 2, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

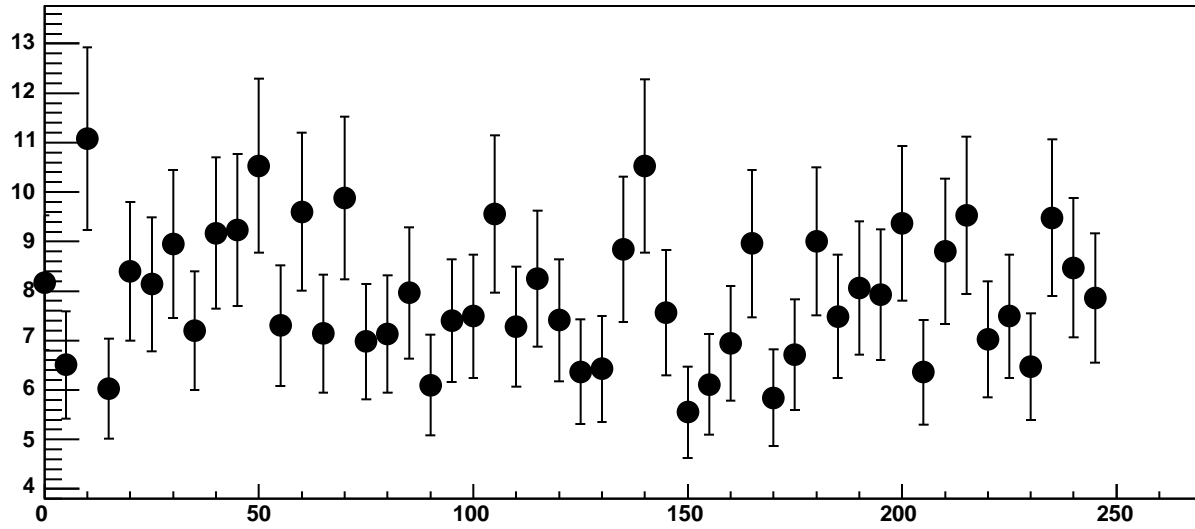


Chip 2, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

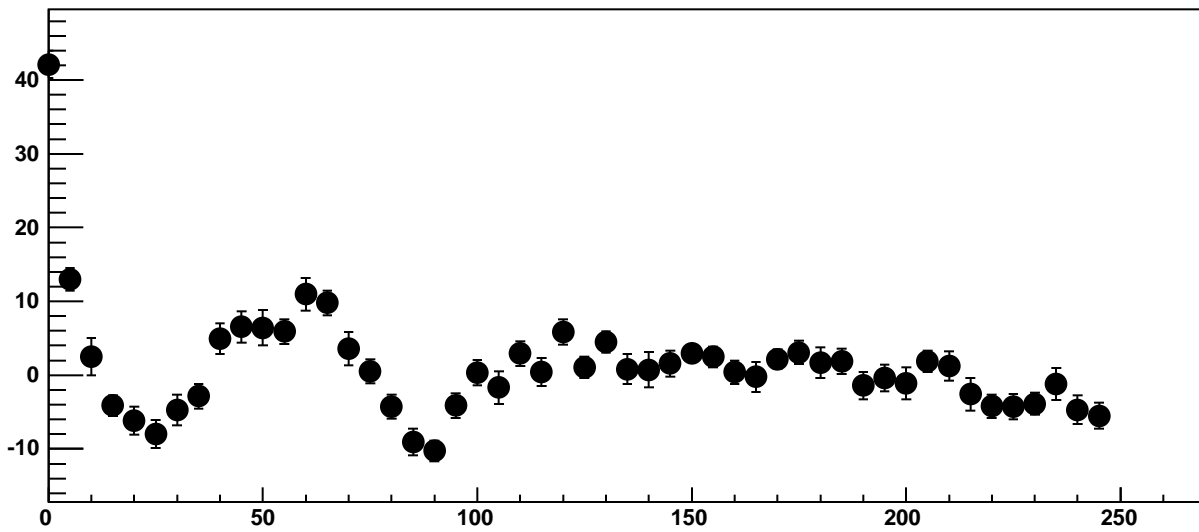


χ^2 / ndf	308.6 / 41
p0	126.6 ± 0.5441
p1	86.18 ± 0.4194
p2	-3.502e+08 ± 1.119e+09
p3	1.593e+08 ± 5.092e+08
p4	2.112 ± 0.006471

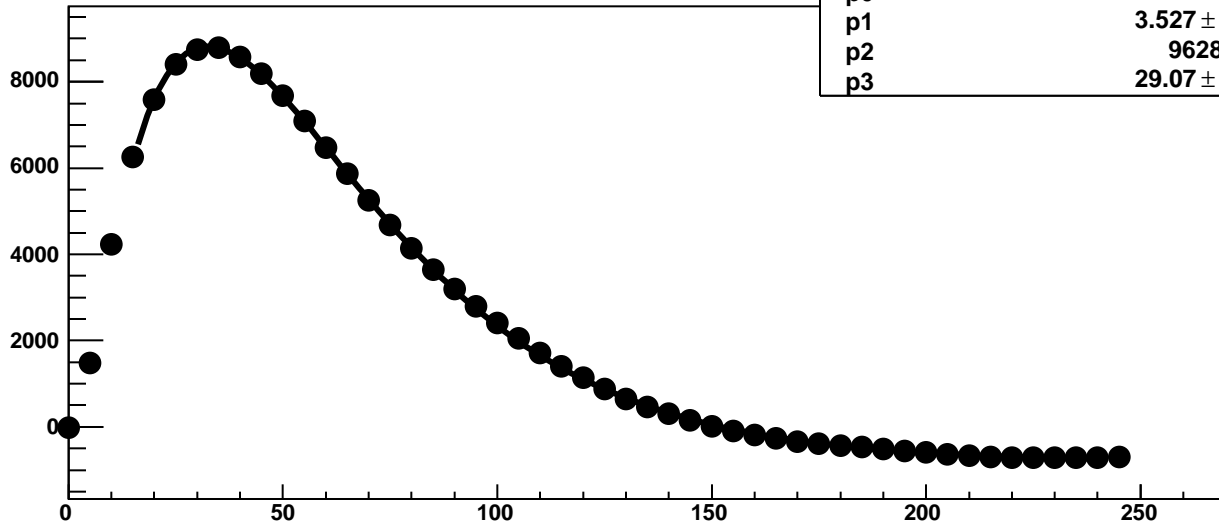
Chip 2, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

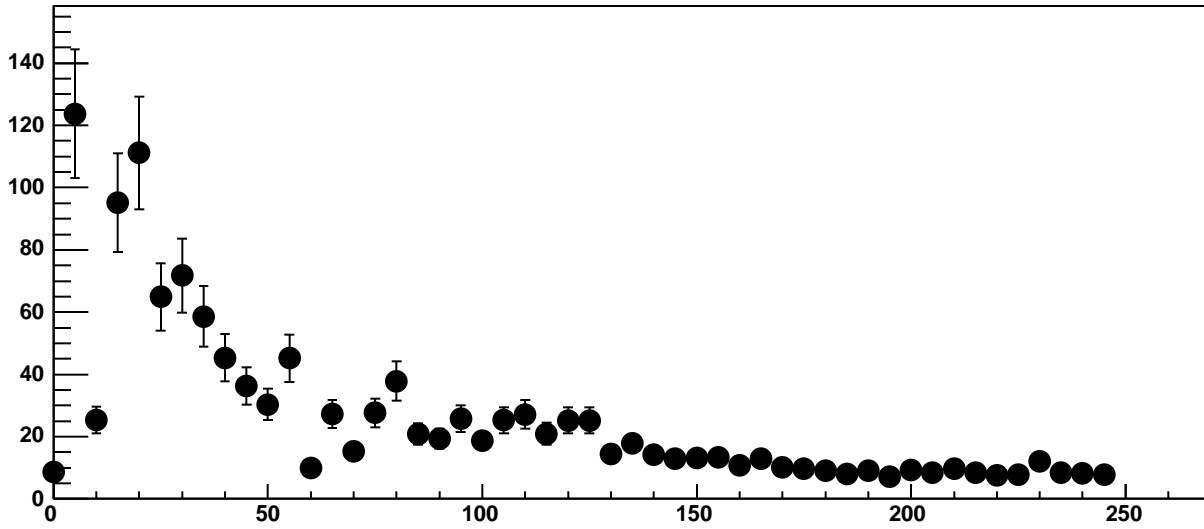


Chip 2, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold

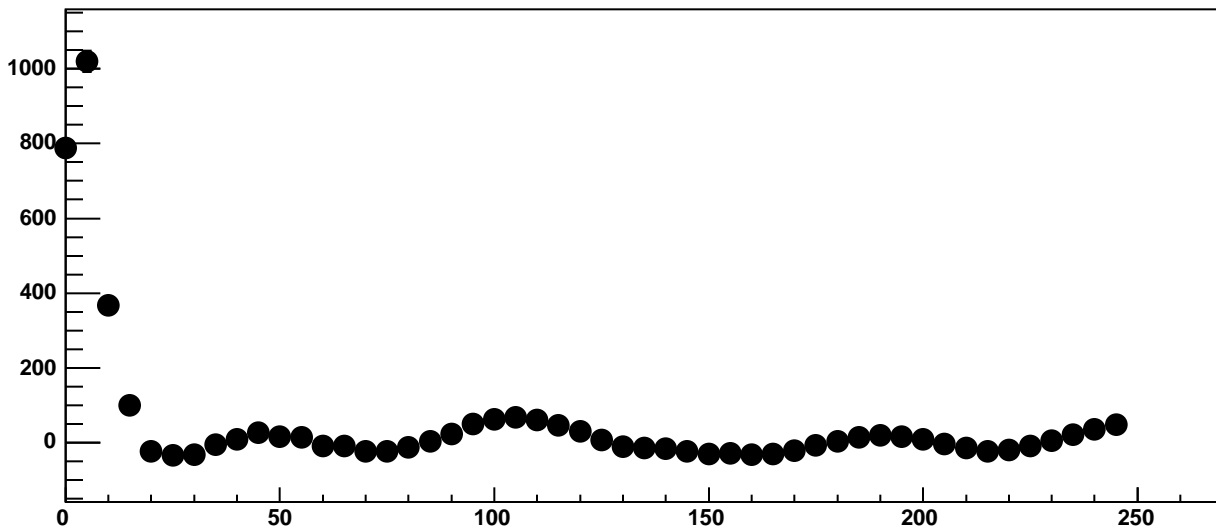


χ^2 / ndf	2524 / 42
p0	-805 ± 0.7375
p1	3.527 ± 0.03887
p2	9628 ± 4.242
p3	29.07 ± 0.01445

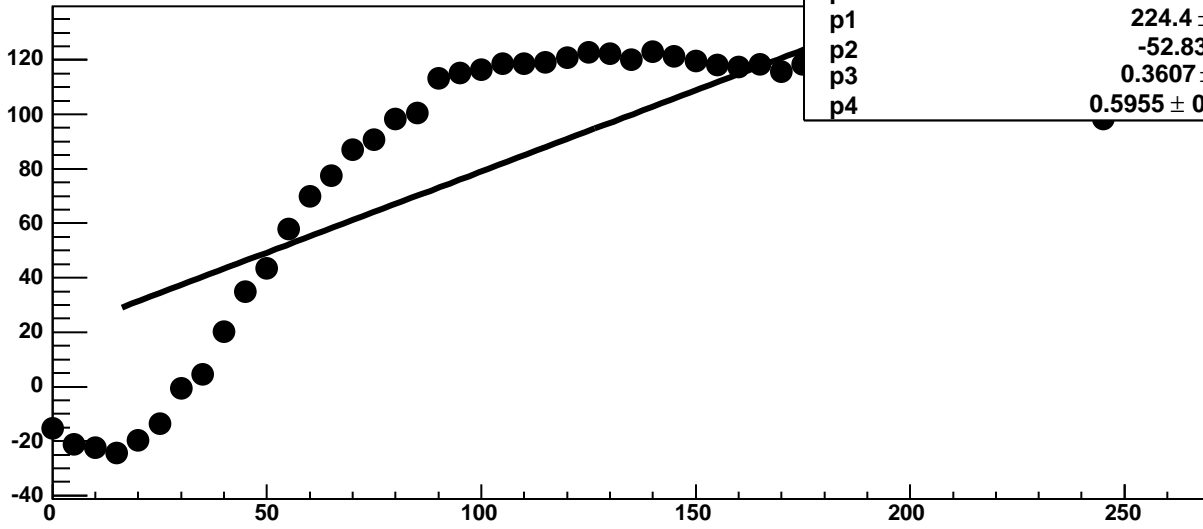
Chip 2, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold

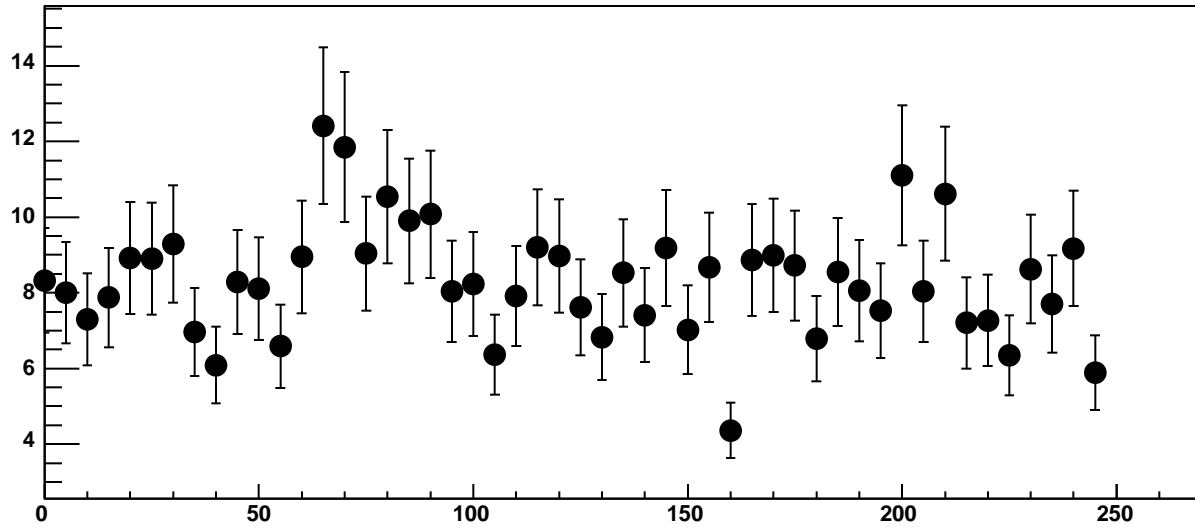


Chip 2, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

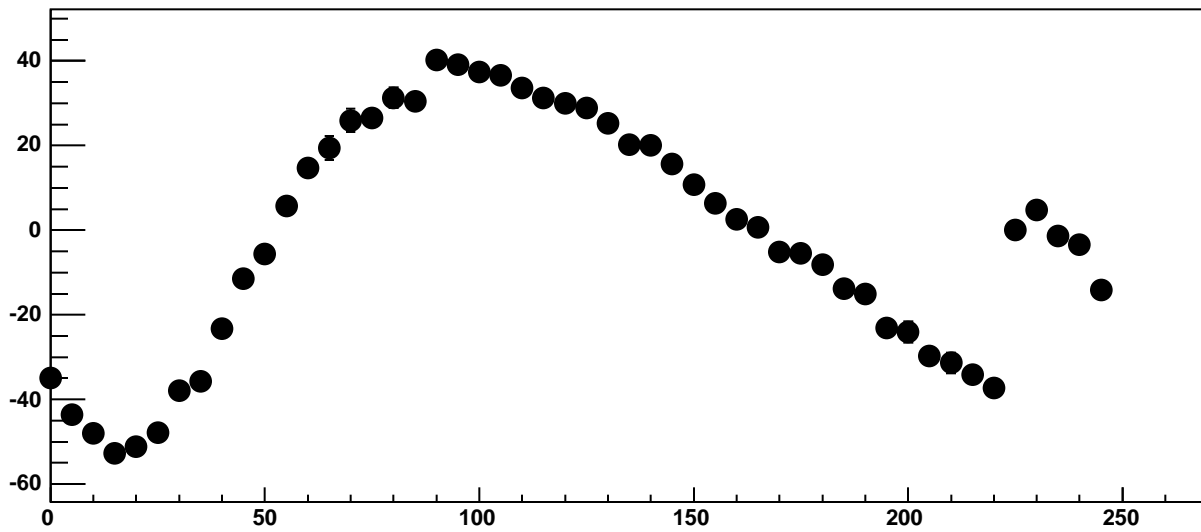


χ^2 / ndf	9114 / 41
p0	153.1 ± 0.6932
p1	224.4 ± 0.7676
p2	-52.83 ± 1.245
p3	0.3607 ± 0.4553
p4	0.5955 ± 0.004507

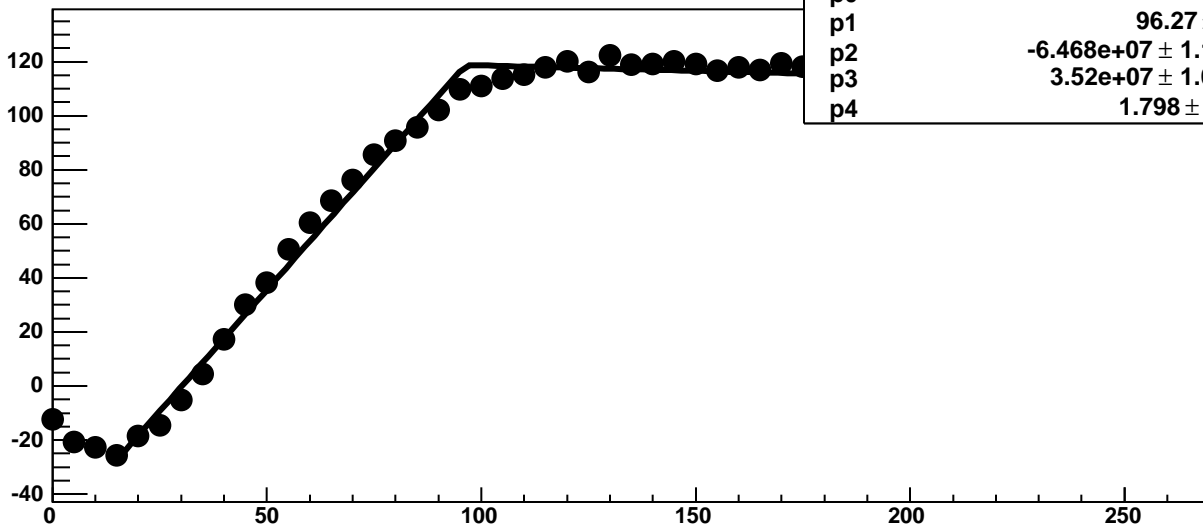
Chip 2, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

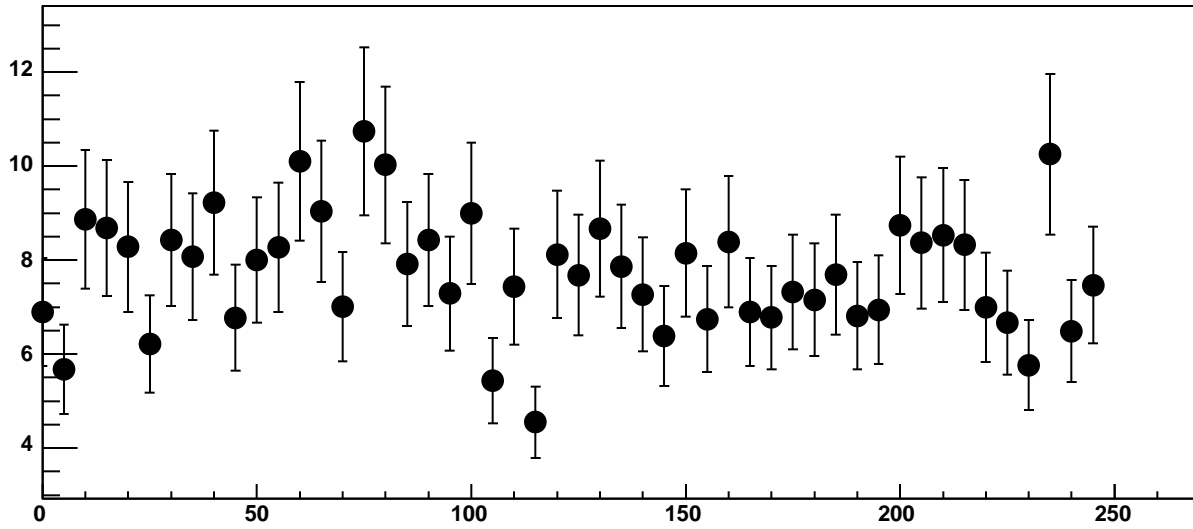


Chip 2, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

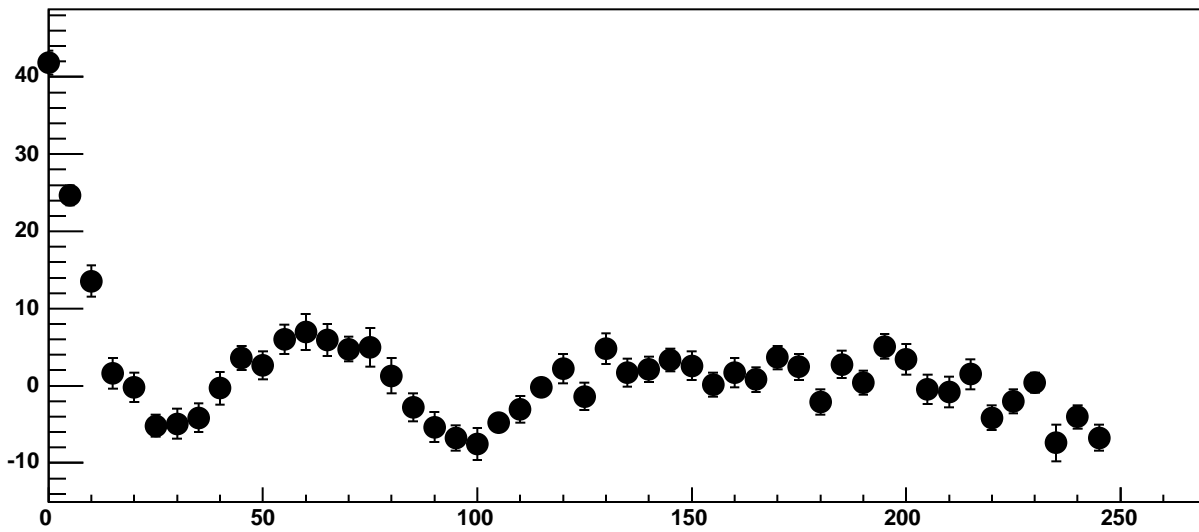


χ^2 / ndf	201.8 / 41
p0	118.8 ± 0.6182
p1	96.27 ± 0.4351
p2	$-6.468e+07 \pm 1.168e+06$
p3	$3.52e+07 \pm 1.004e+06$
p4	1.798 ± 0.01776

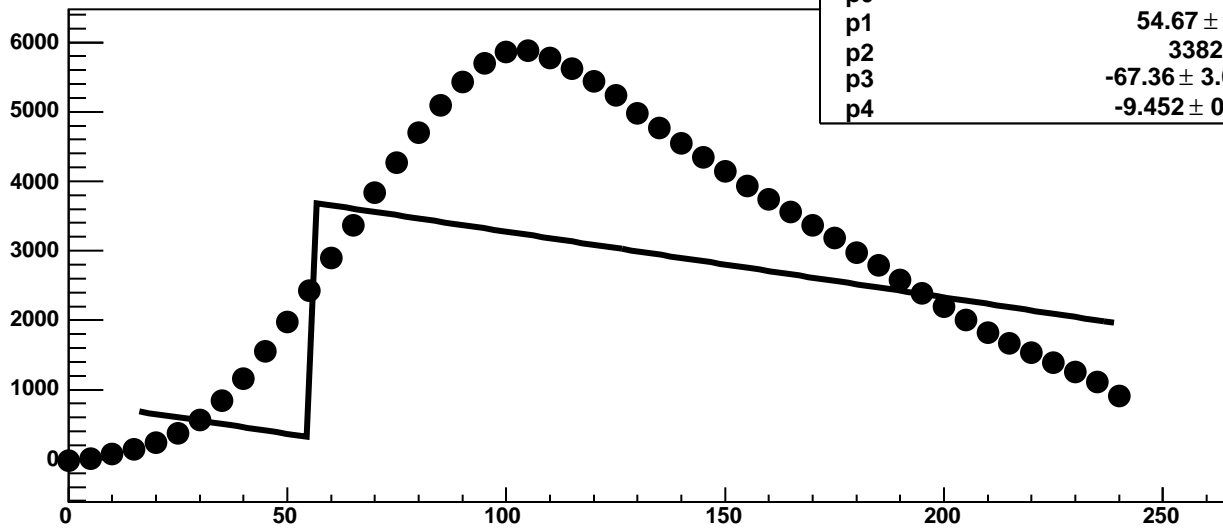
Chip 2, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

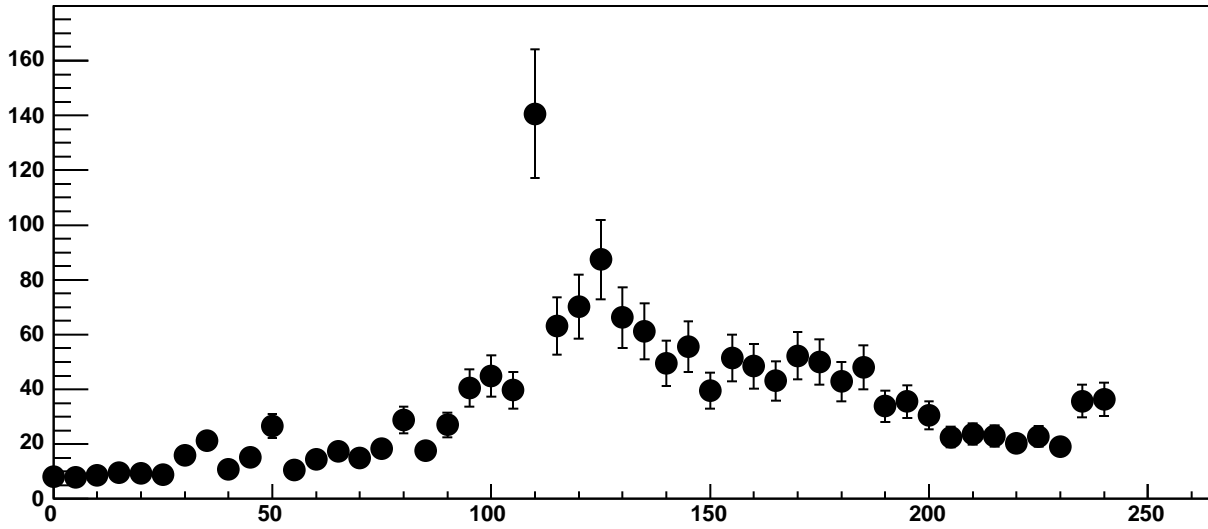


Chip 2, Channel 8, Enable 5, DAC=4000, ADC Mean vs Hold

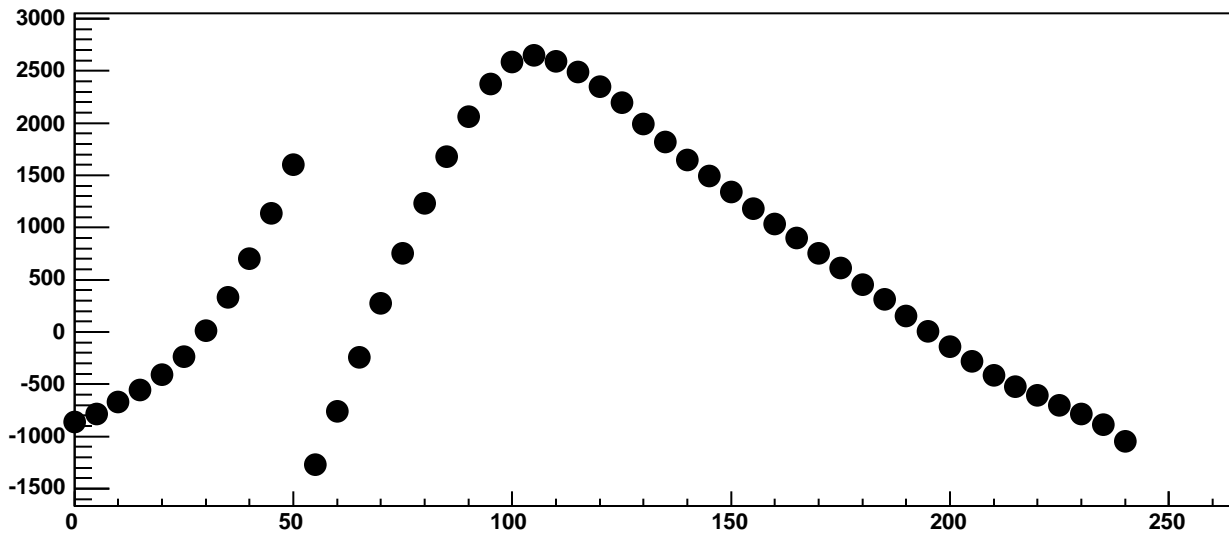


χ^2 / ndf	1.579e+06 / 41
p0	322 ± 0.7412
p1	54.67 ± 0.07841
p2	3382 ± 1.577
p3	-67.36 ± 3.003e-06
p4	-9.452 ± 0.008492

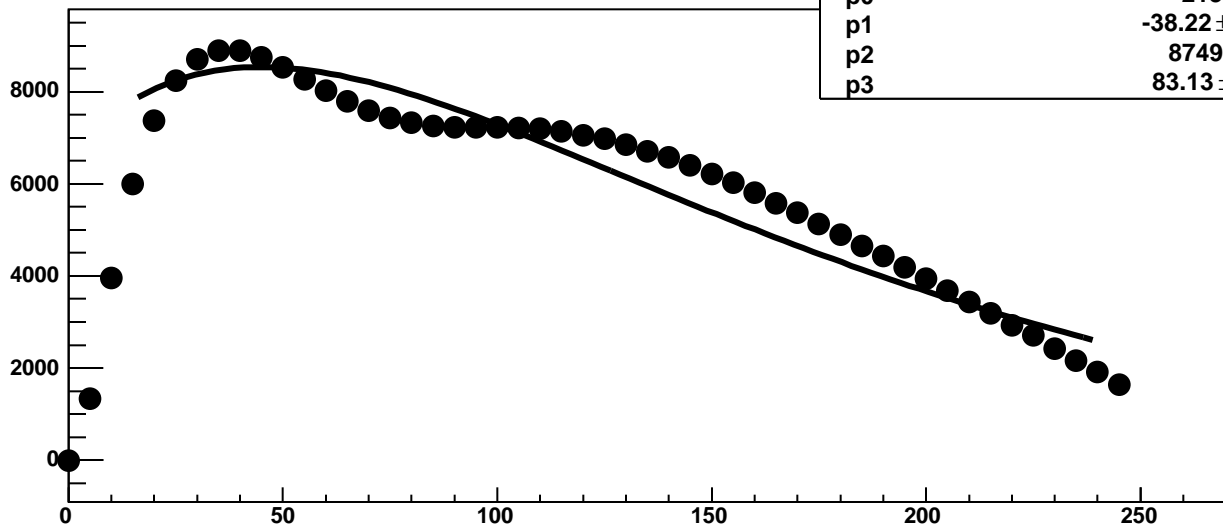
Chip 2, Channel 8, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 8, Enable 5, DAC=4000, ADC Residuals vs Hold

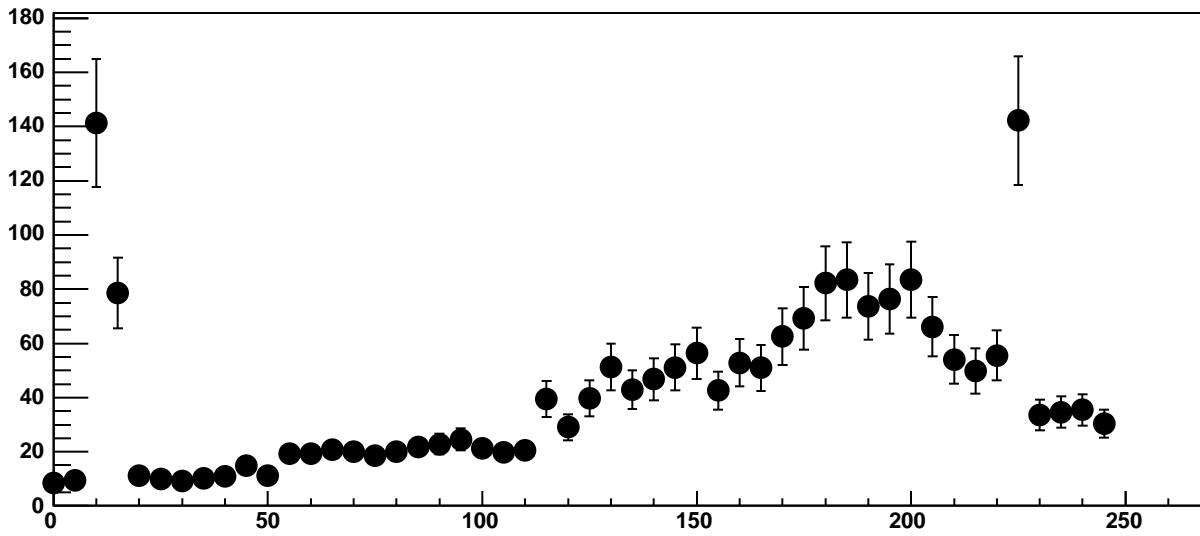


Chip 2, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold

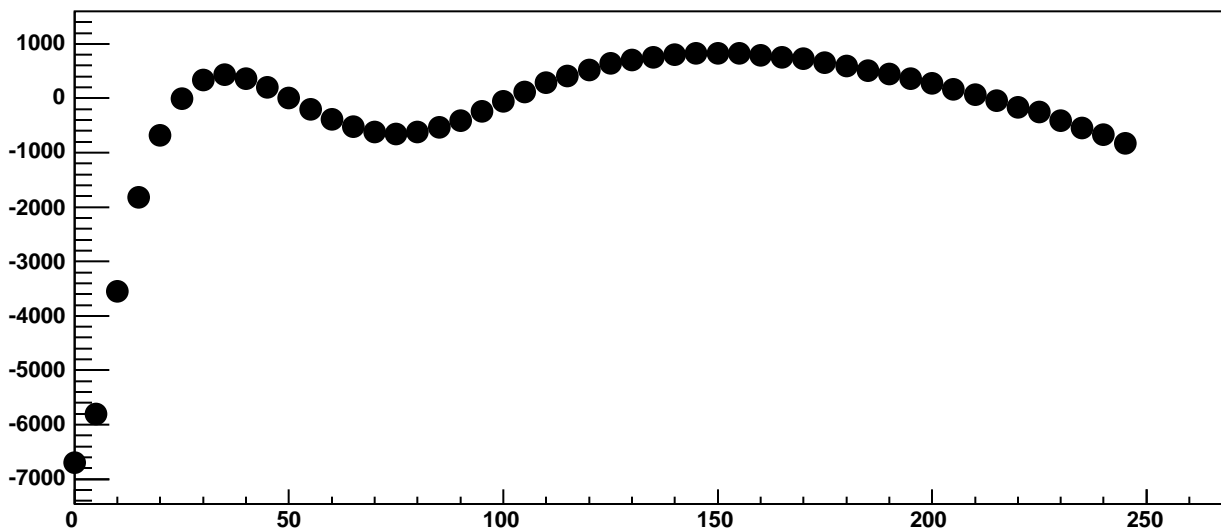


χ^2 / ndf	3.443e+05 / 42
p0	-213 ± 23.19
p1	-38.22 ± 0.2437
p2	8749 ± 22.39
p3	83.13 ± 0.2311

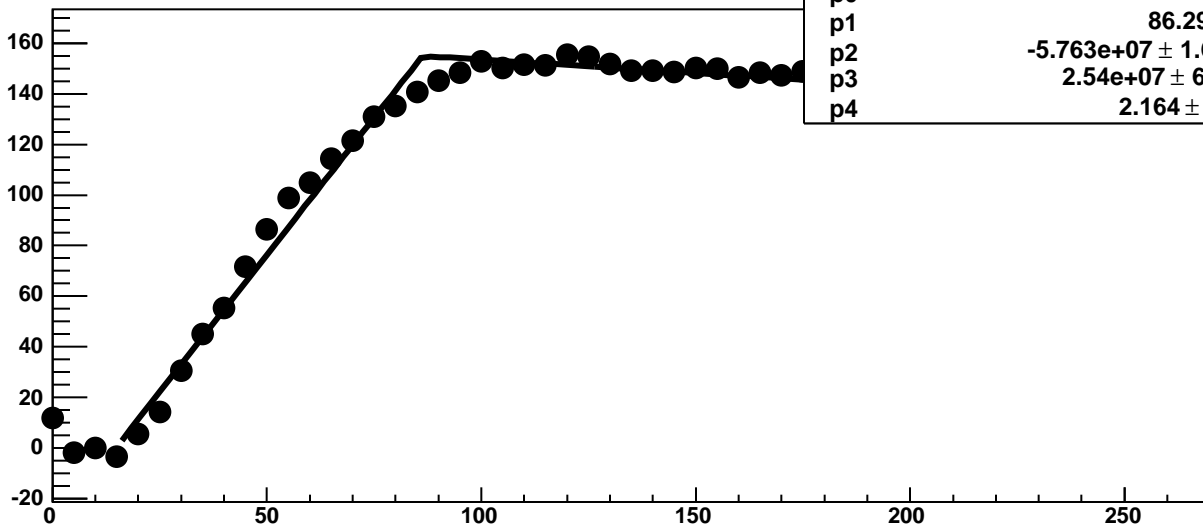
Chip 2, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold

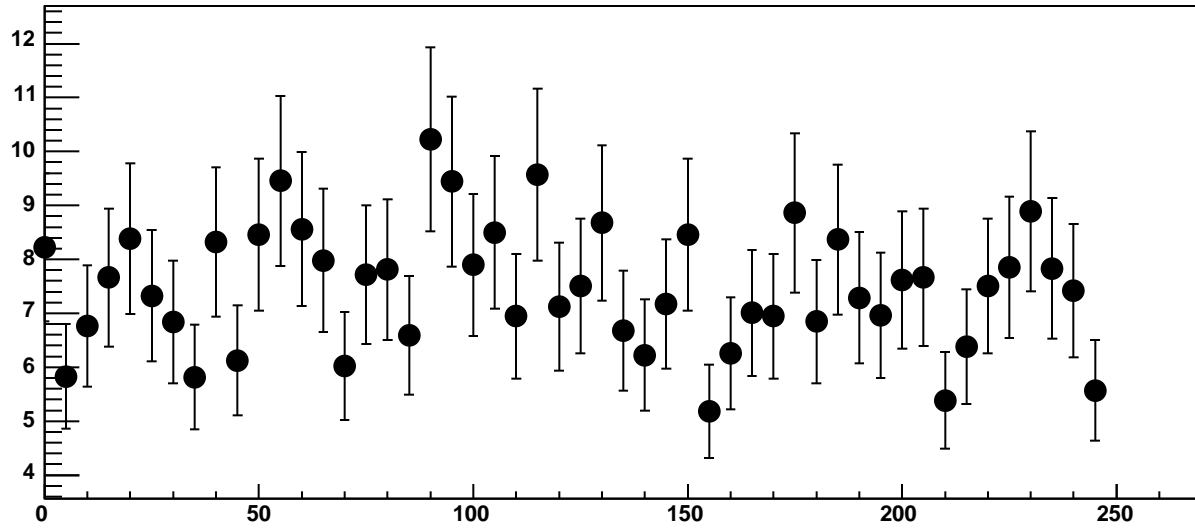


Chip 2, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

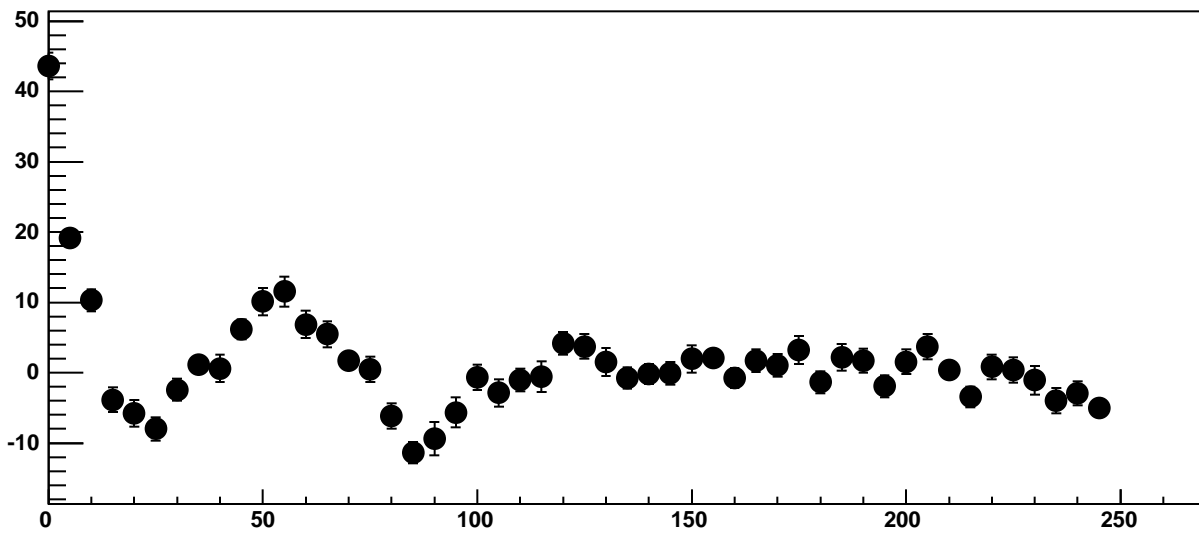


χ^2 / ndf	275.2 / 41
p0	154.9 ± 0.6307
p1	86.29 ± 0.471
p2	$-5.763\text{e}+07 \pm 1.613\text{e}+06$
p3	$2.54\text{e}+07 \pm 6.75\text{e}+05$
p4	2.164 ± 0.01995

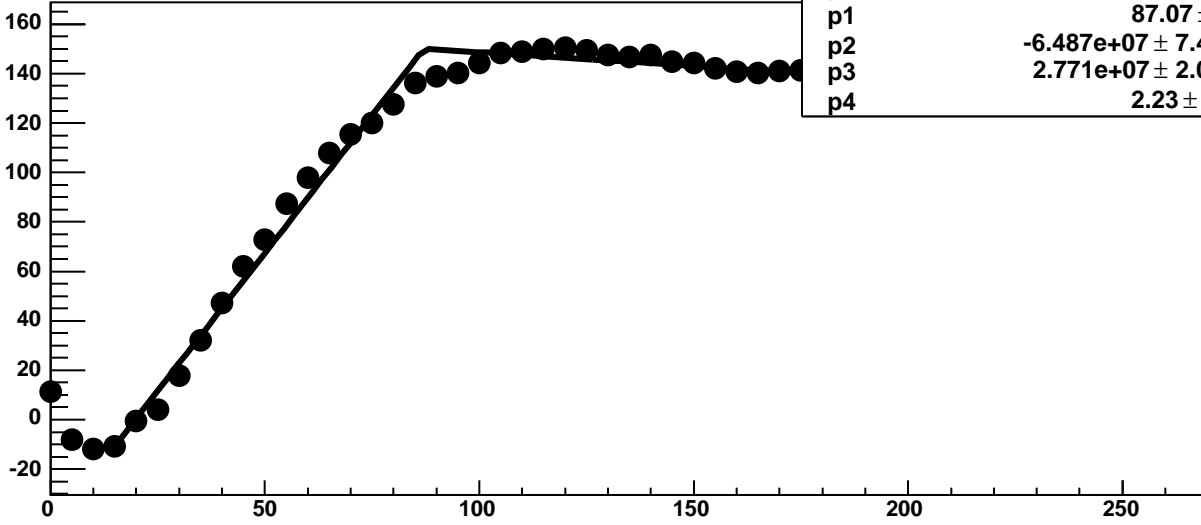
Chip 2, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

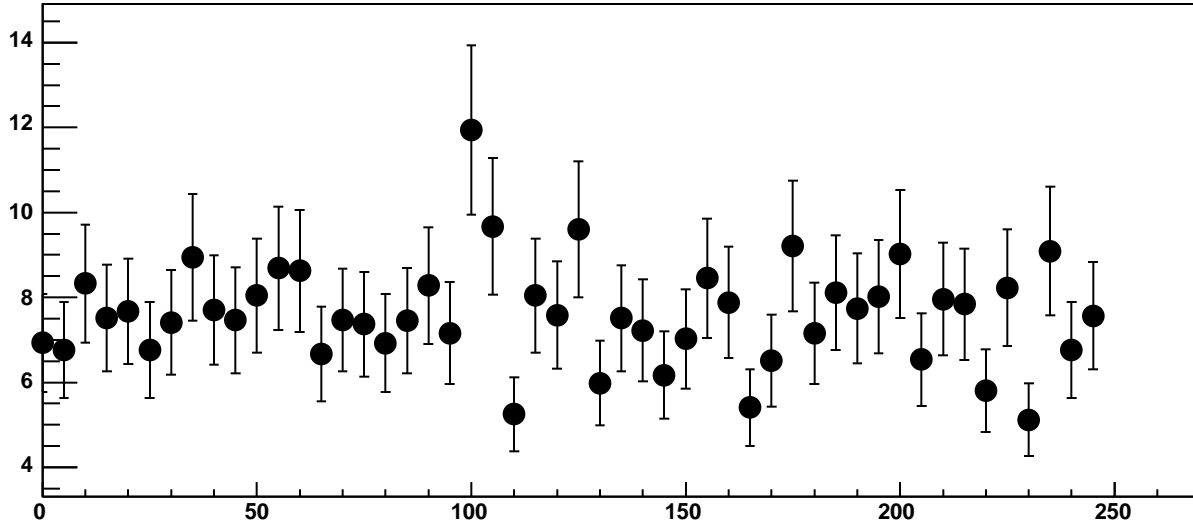


Chip 2, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

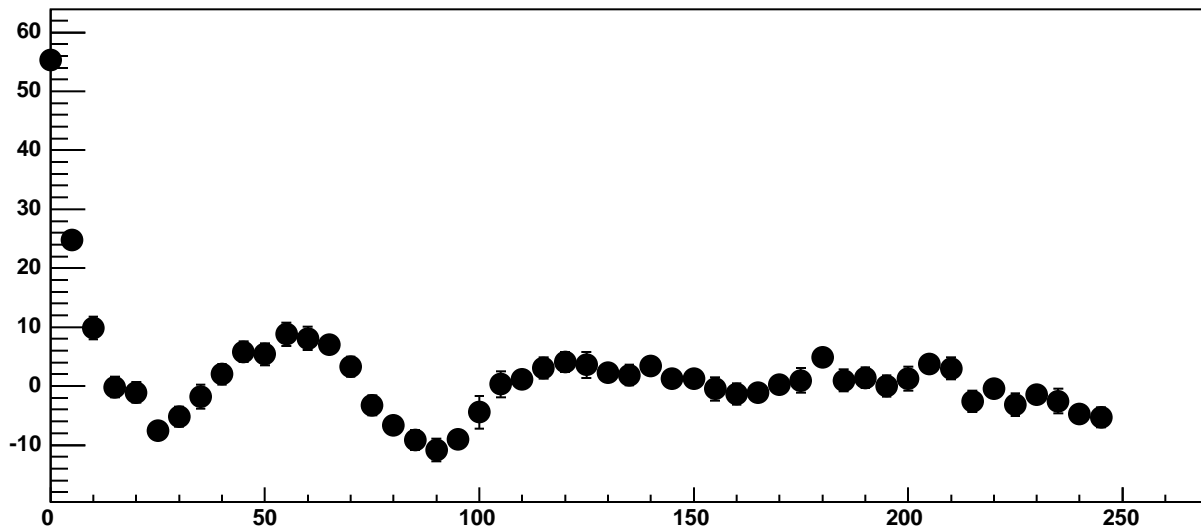


χ^2 / ndf	291.1 / 41
p0	150.1 ± 0.5875
p1	87.07 ± 0.4519
p2	$-6.487\text{e}+07 \pm 7.451\text{e}+05$
p3	$2.771\text{e}+07 \pm 2.019\text{e}+05$
p4	2.23 ± 0.02009

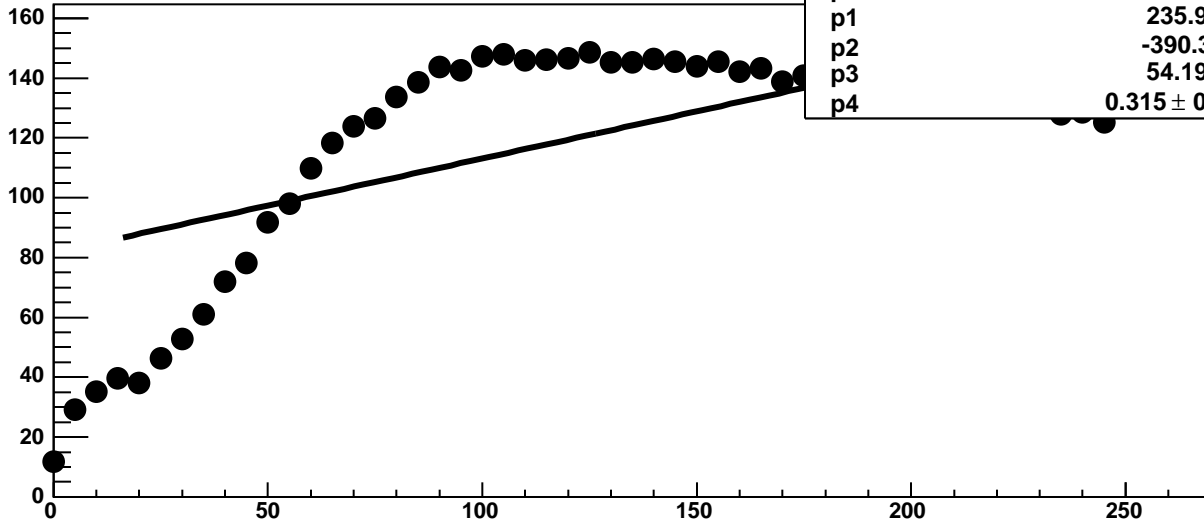
Chip 2, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold

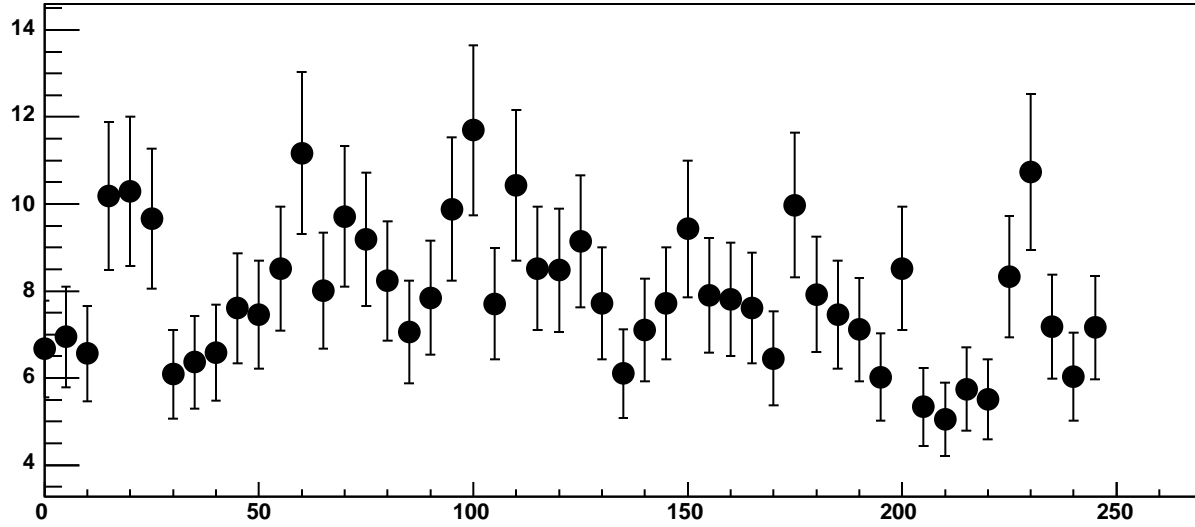


Chip 2, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold

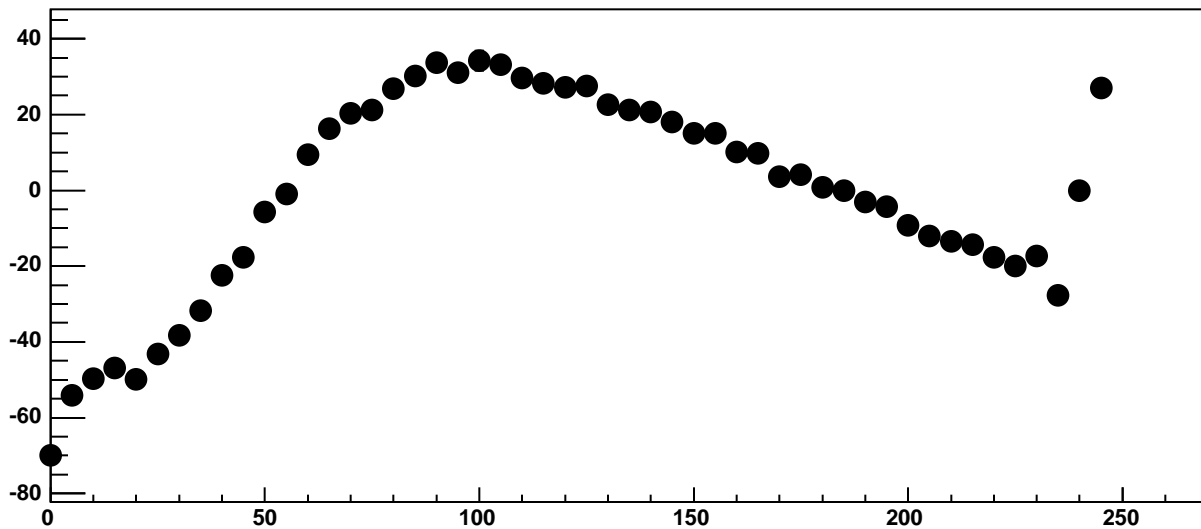


χ^2 / ndf	7266 / 41
p0	155.9 ± 0.9441
p1	235.9 ± 2.619
p2	-390.3 ± 231.1
p3	54.19 ± 27.47
p4	0.315 ± 0.003886

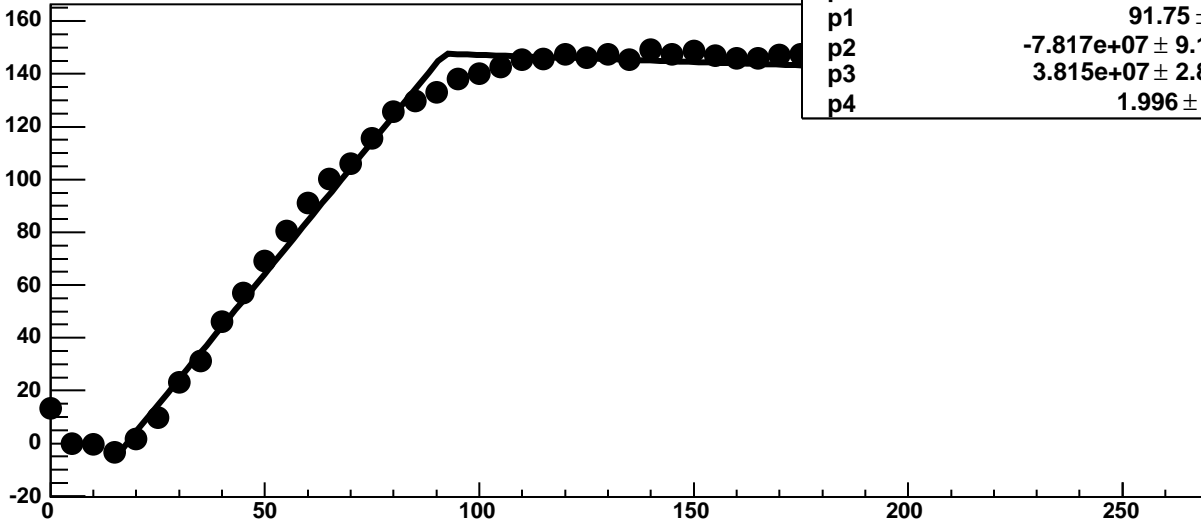
Chip 2, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold

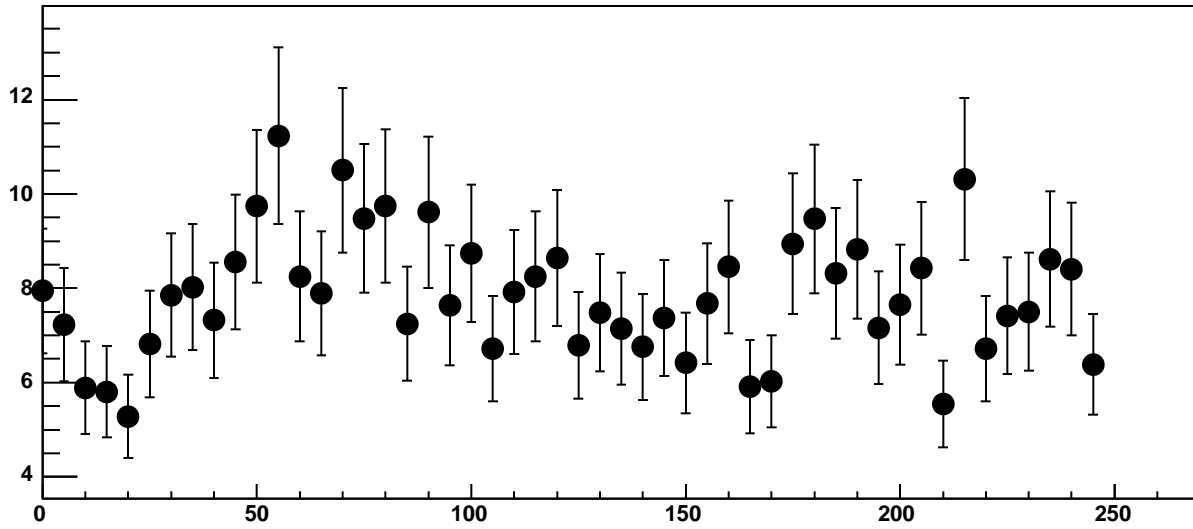


Chip 2, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

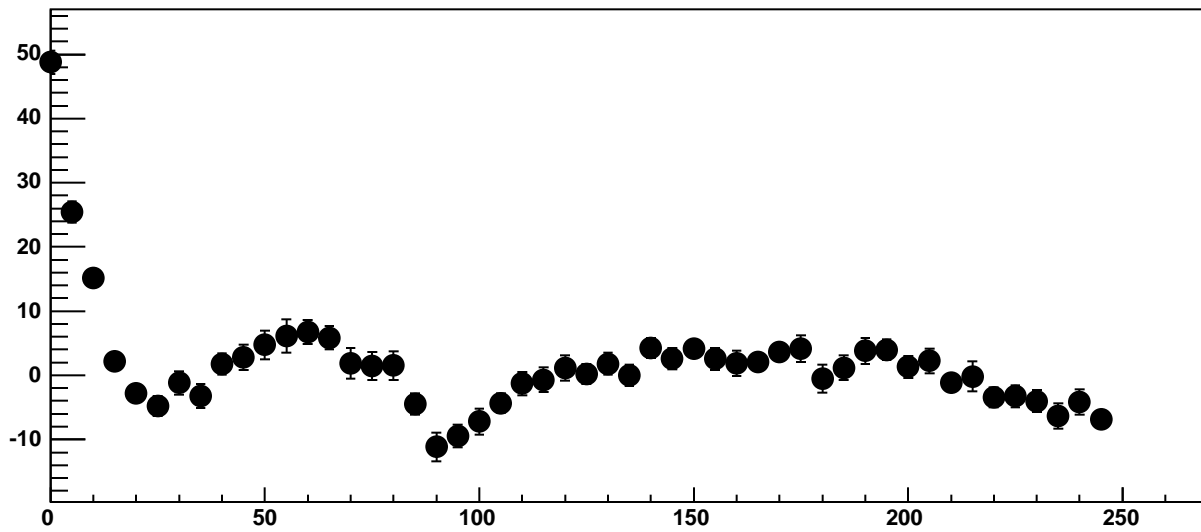


χ^2 / ndf	219.3 / 41
p0	147.7 ± 0.6177
p1	91.75 ± 0.5515
p2	$-7.817\text{e}+07 \pm 9.123\text{e}+05$
p3	$3.815\text{e}+07 \pm 2.805\text{e}+05$
p4	1.996 ± 0.01843

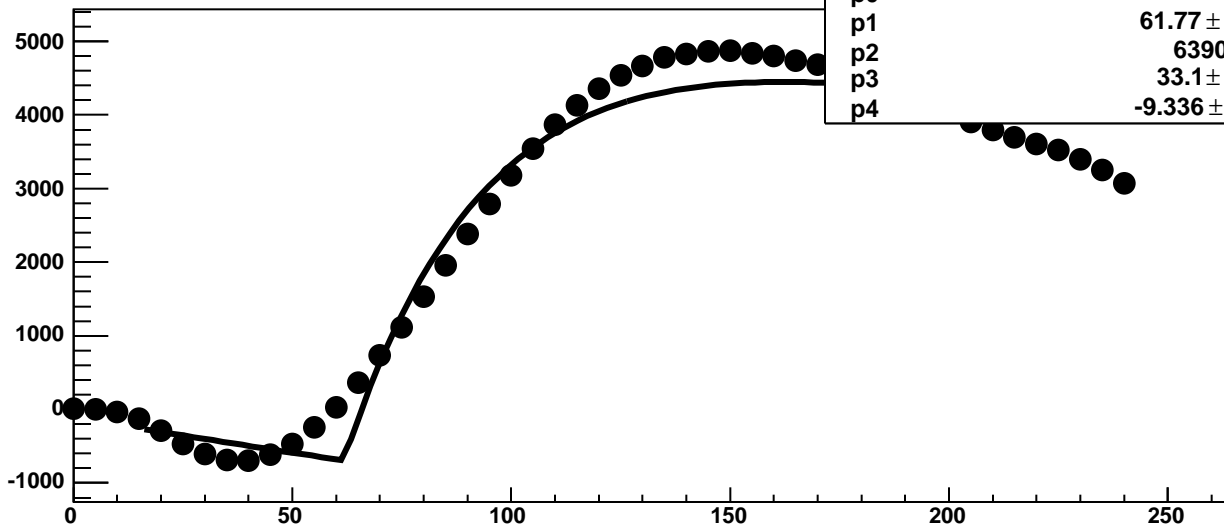
Chip 2, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

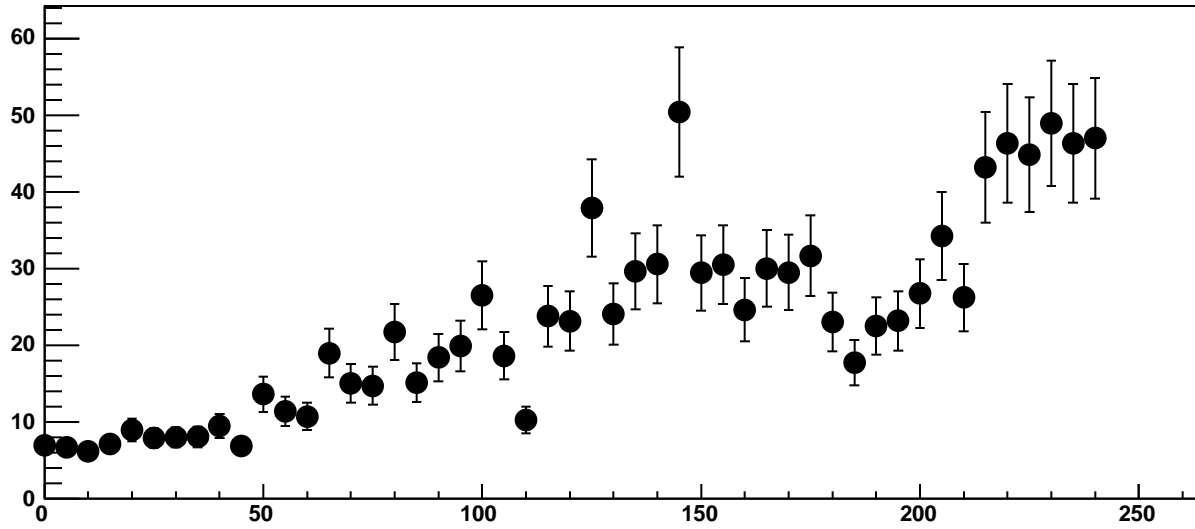


Chip 2, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold

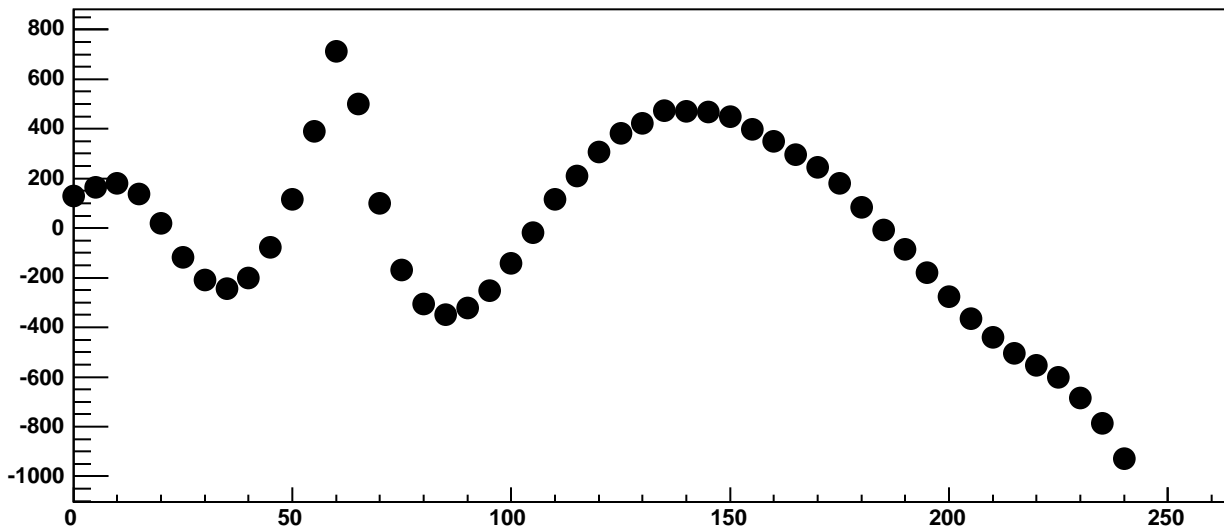


χ^2 / ndf	2.765e+05 / 41
p0	-697.9 ± 1.159
p1	61.77 ± 0.01918
p2	6390 ± 6.626
p3	33.1 ± 0.04847
p4	-9.336 ± 0.03791

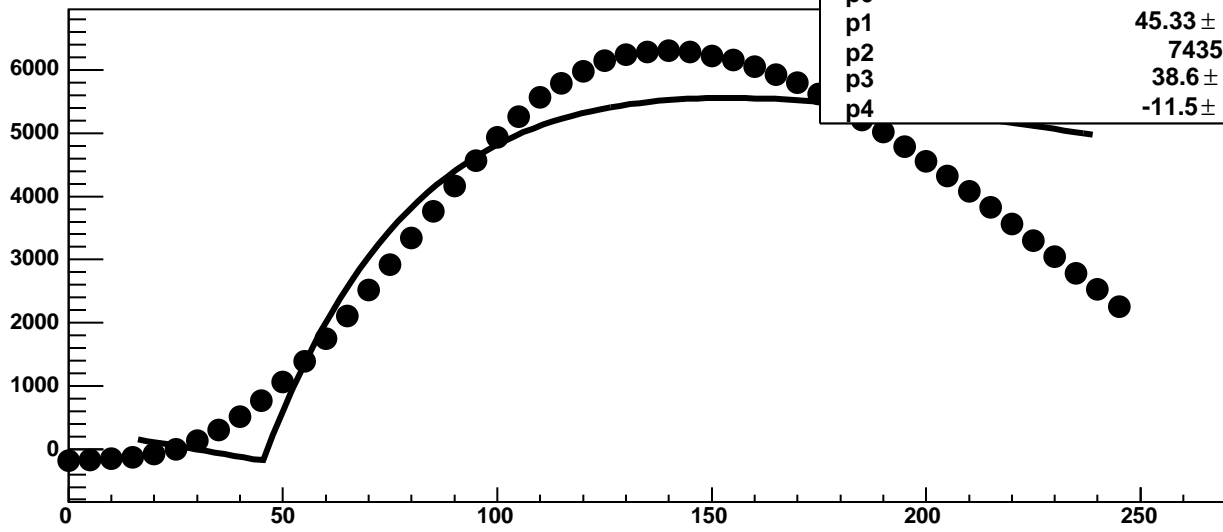
Chip 2, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



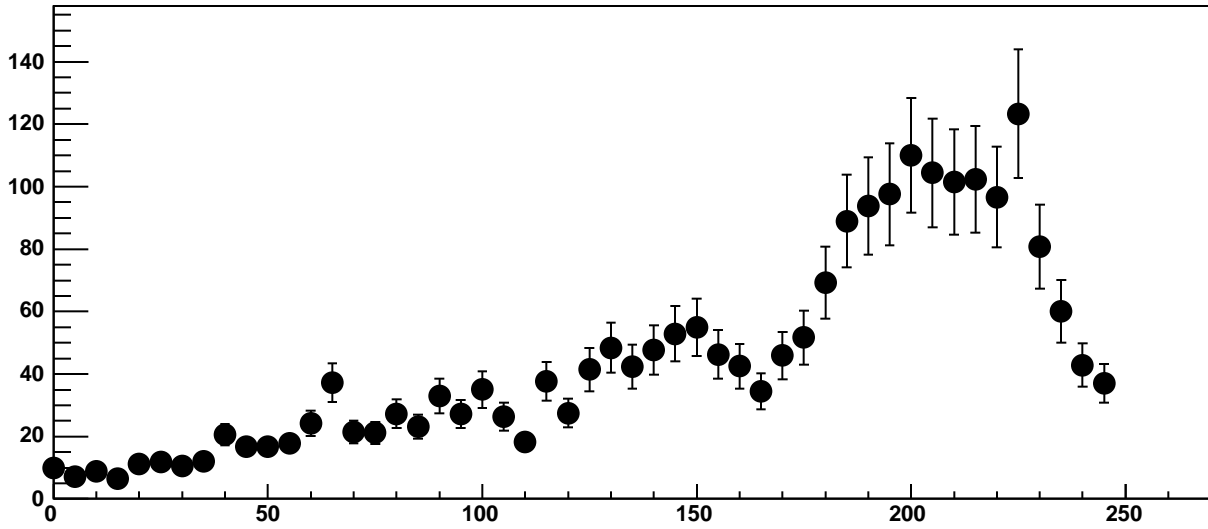
Chip 2, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold



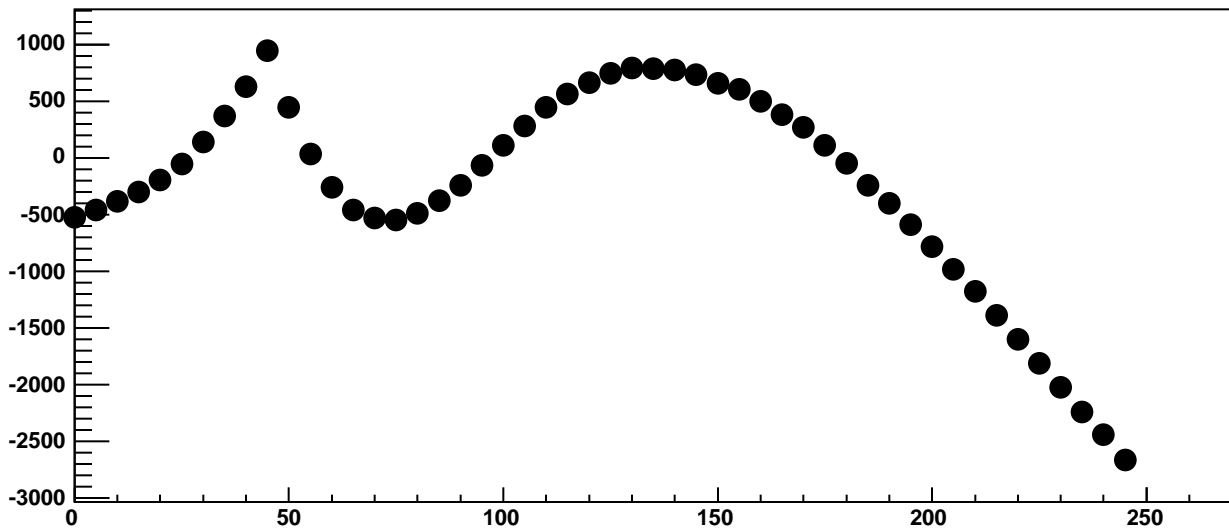
Chip 2, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold



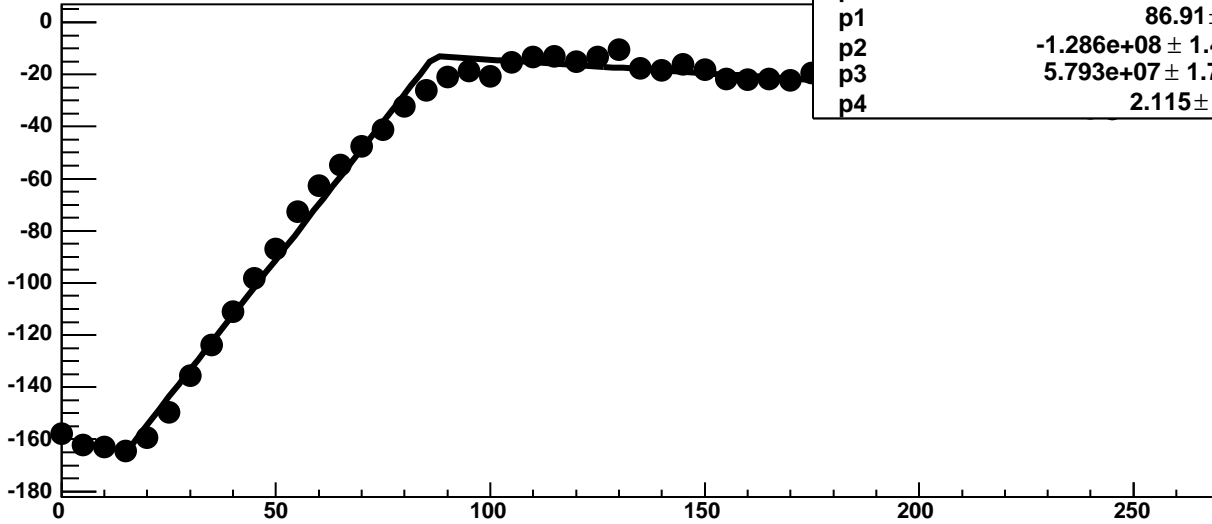
Chip 2, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold

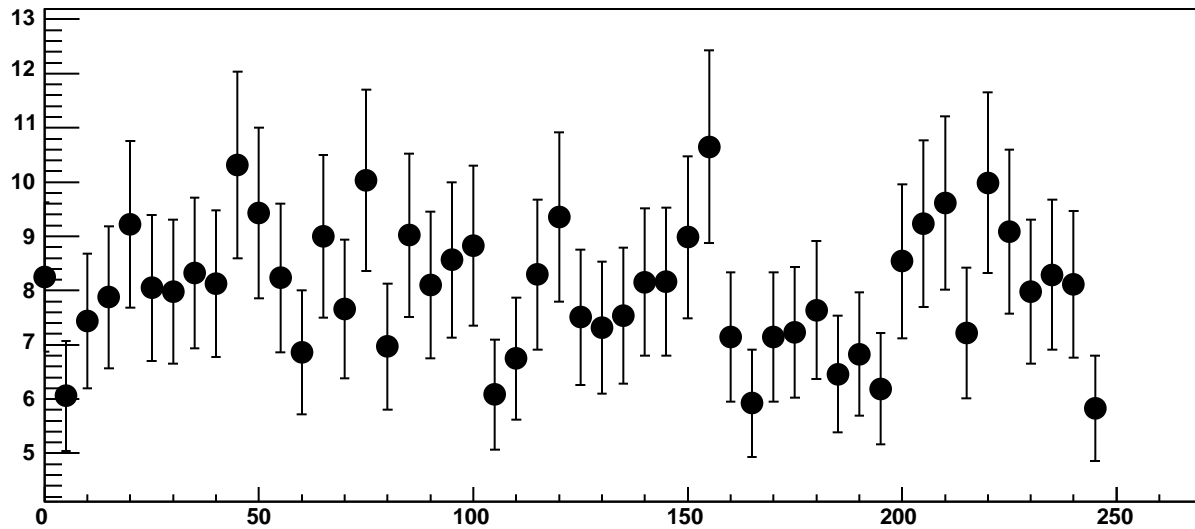


Chip 2, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

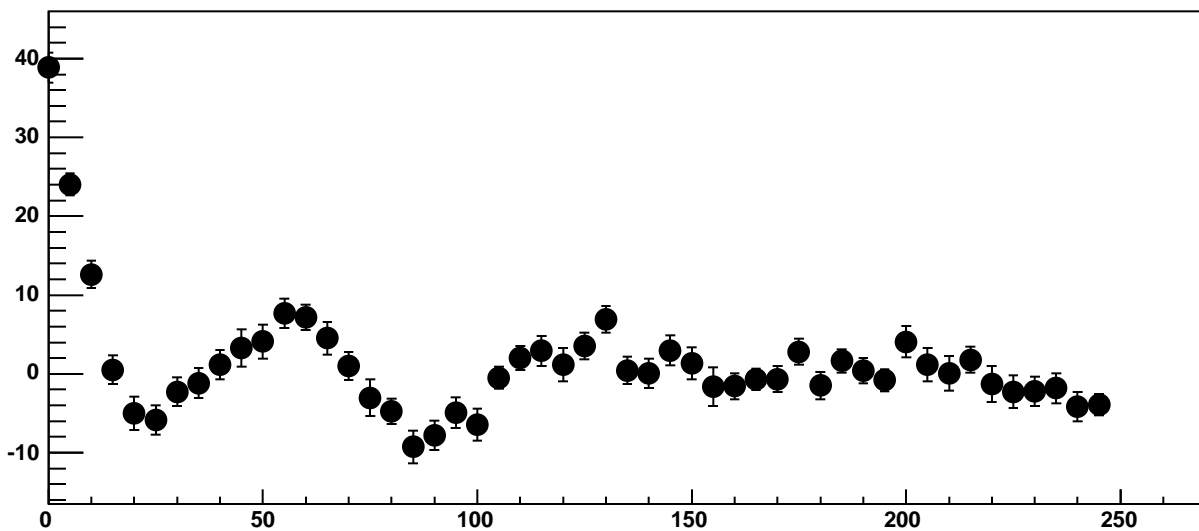


χ^2 / ndf	180.9 / 41
p0	-12.93 ± 0.6185
p1	86.91 ± 0.5198
p2	$-1.286\text{e}+08 \pm 1.428\text{e}+06$
p3	$5.793\text{e}+07 \pm 1.798\text{e}+05$
p4	2.115 ± 0.02254

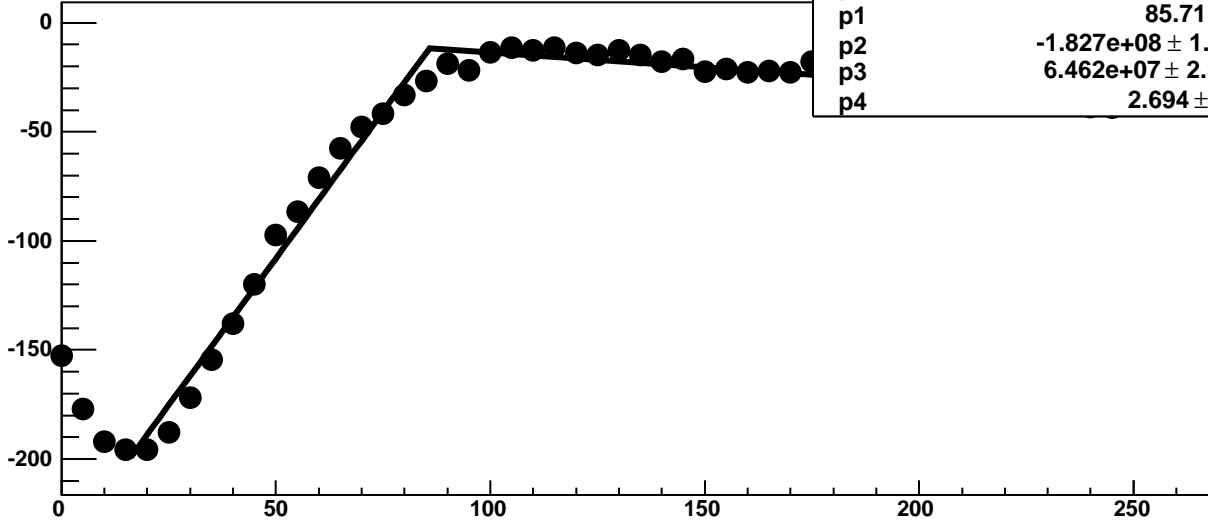
Chip 2, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

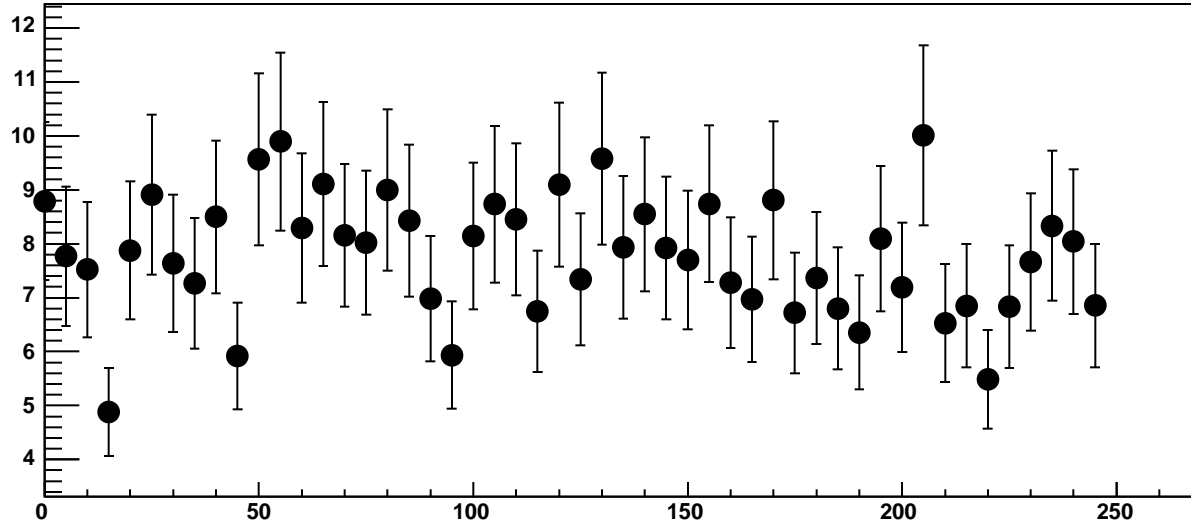


Chip 2, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

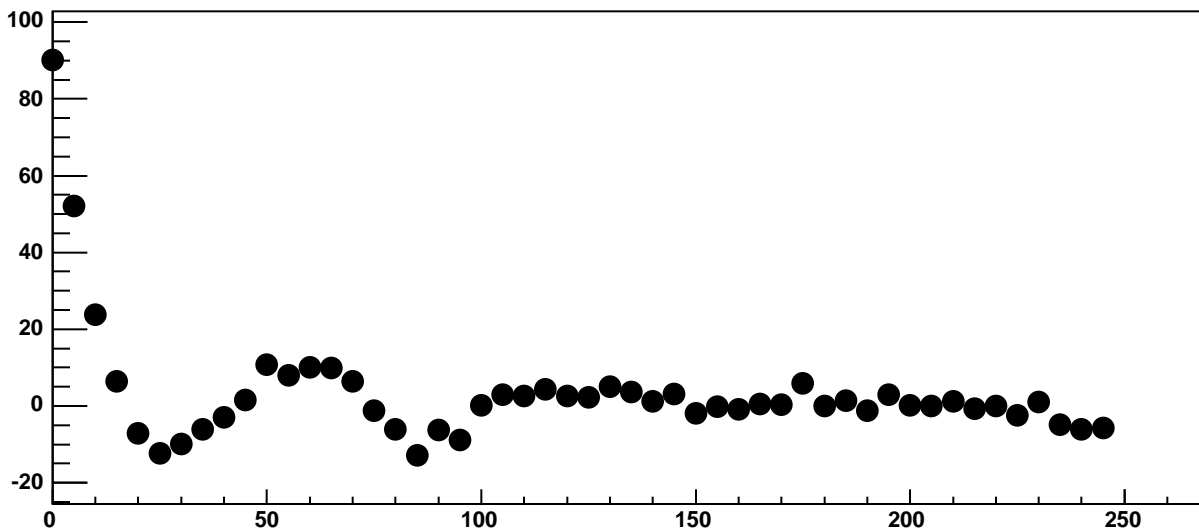


χ^2 / ndf	412.4 / 41
p0	-11.79 ± 0.5921
p1	85.71 ± 0.3979
p2	$-1.827\text{e}+08 \pm 1.474\text{e}+06$
p3	$6.462\text{e}+07 \pm 2.324\text{e}+05$
p4	2.694 ± 0.01998

Chip 2, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold

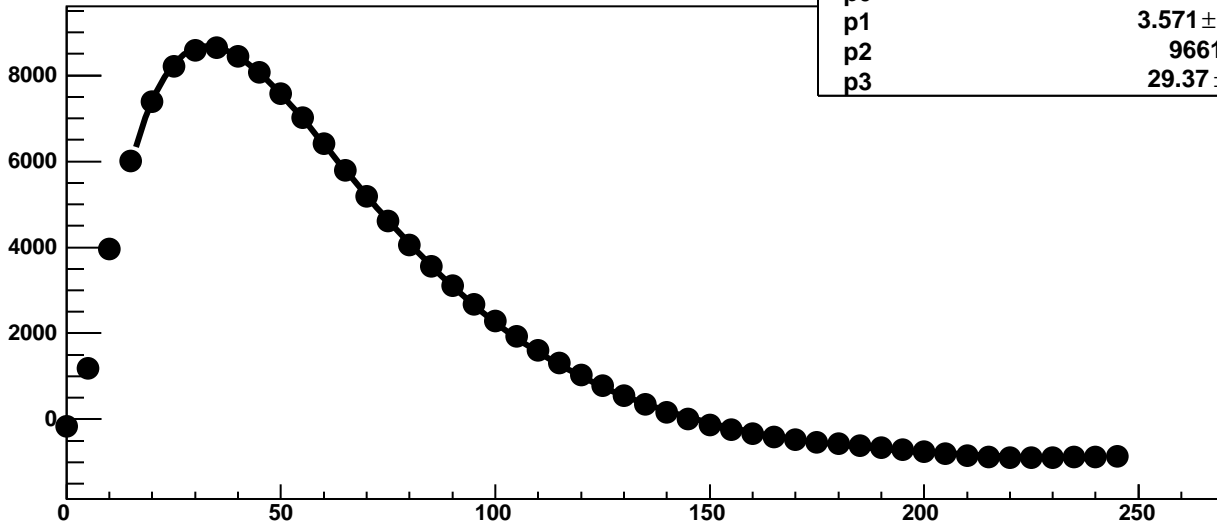


Chip 2, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

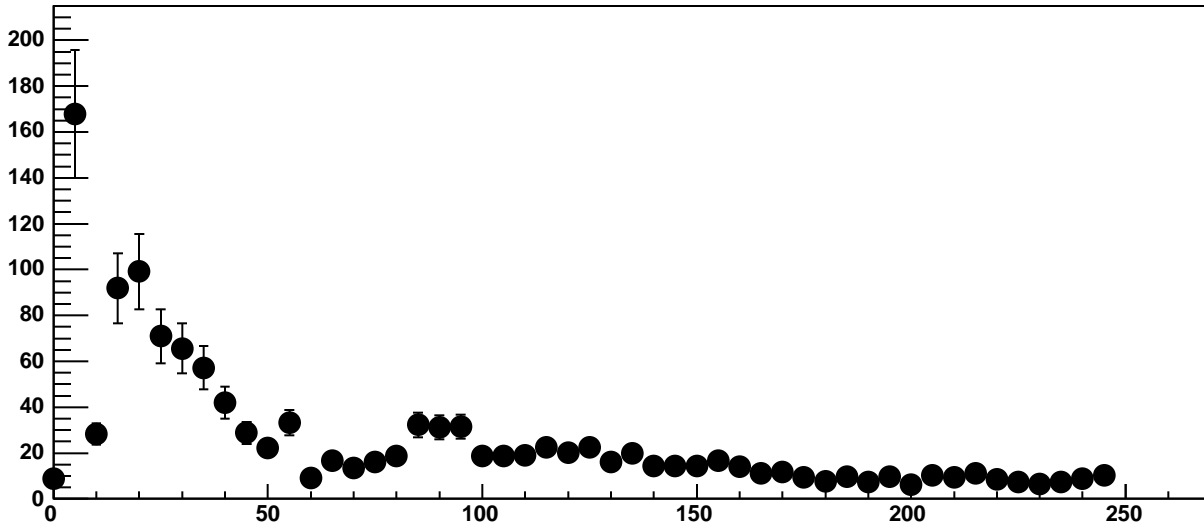


Chip 2, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold

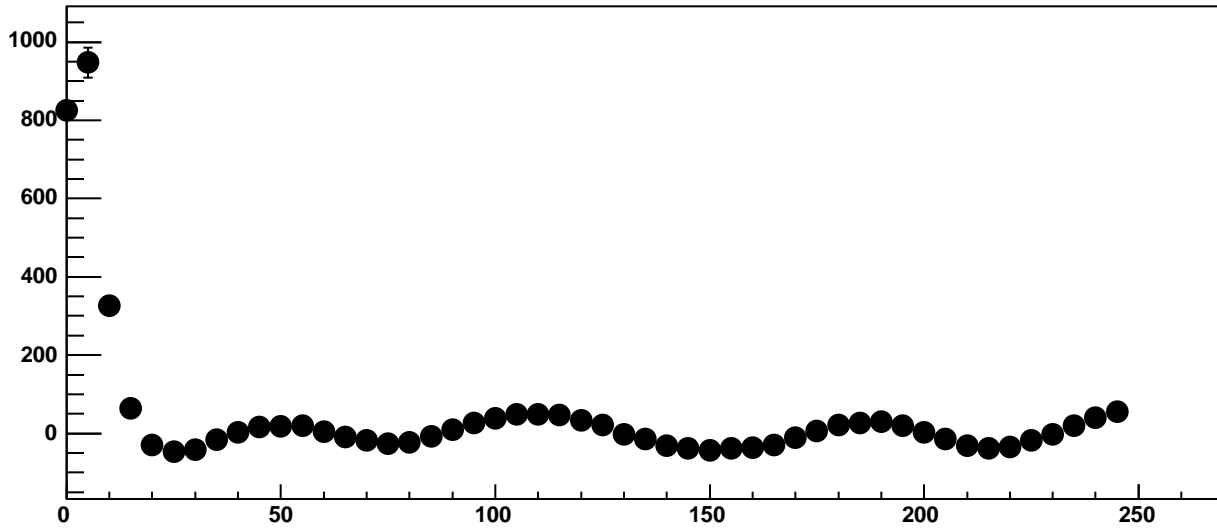
χ^2 / ndf	3589 / 42
p0	-980.9 ± 0.7059
p1	3.571 ± 0.03771
p2	9661 ± 3.956
p3	29.37 ± 0.0139



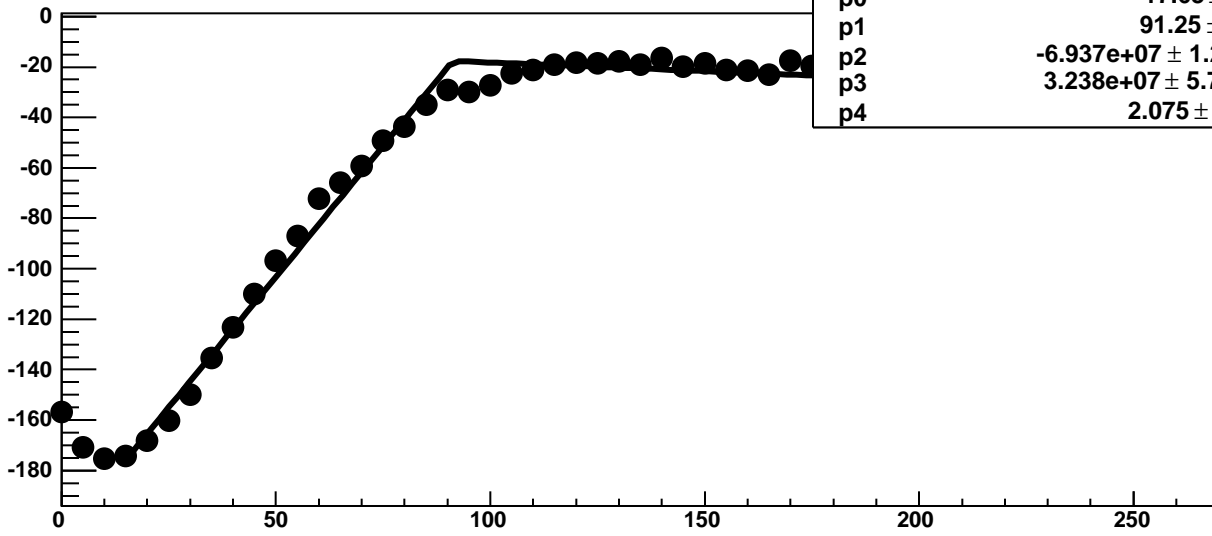
Chip 2, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold

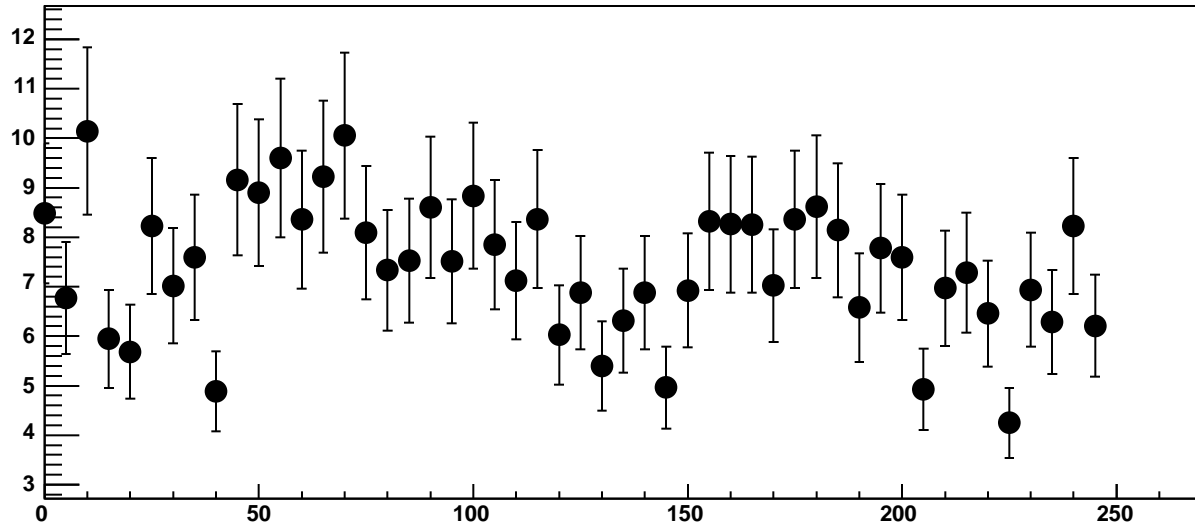


Chip 2, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

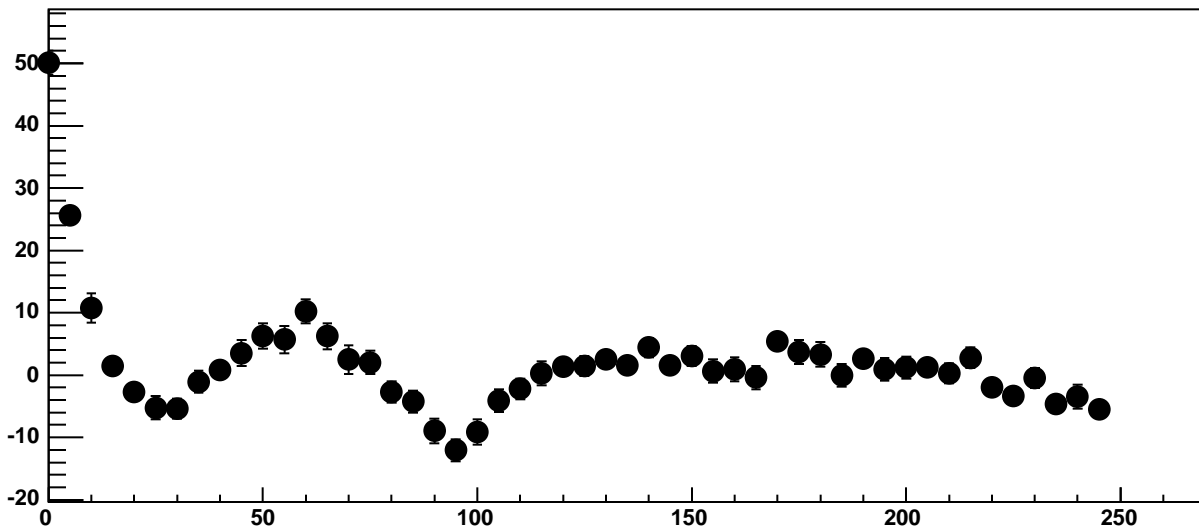


χ^2 / ndf	263 / 41
p0	-17.63 ± 0.5717
p1	91.25 ± 0.4995
p2	-6.937e+07 ± 1.248e+06
p3	3.238e+07 ± 5.777e+05
p4	2.075 ± 0.01784

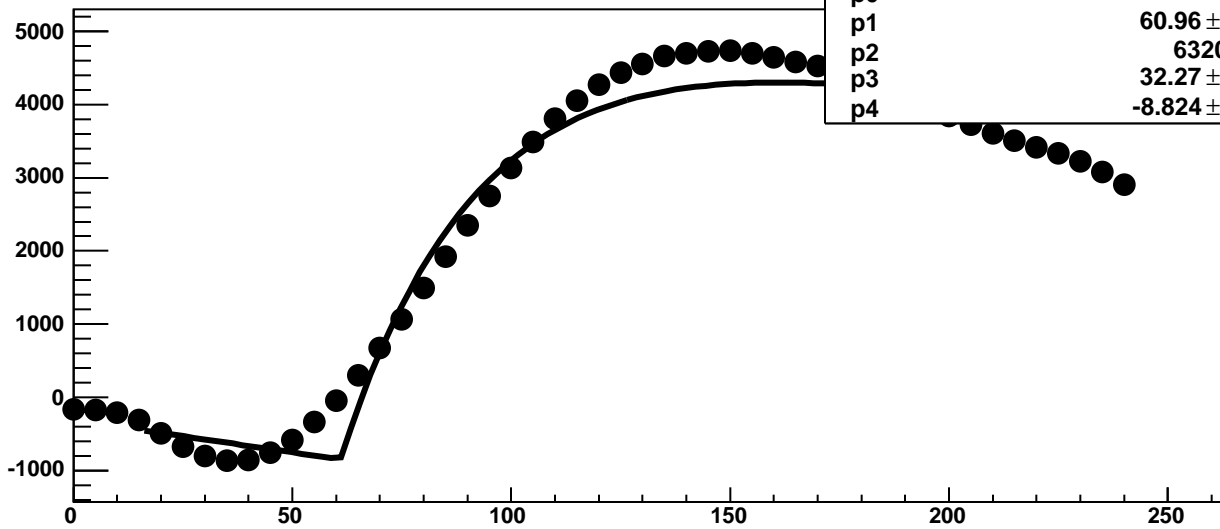
Chip 2, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

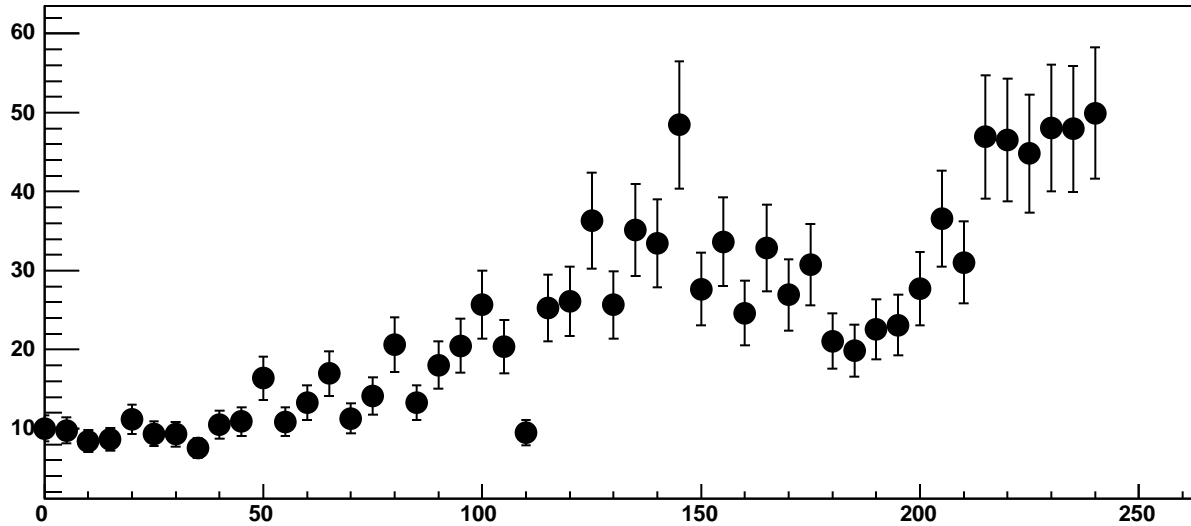


Chip 2, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold

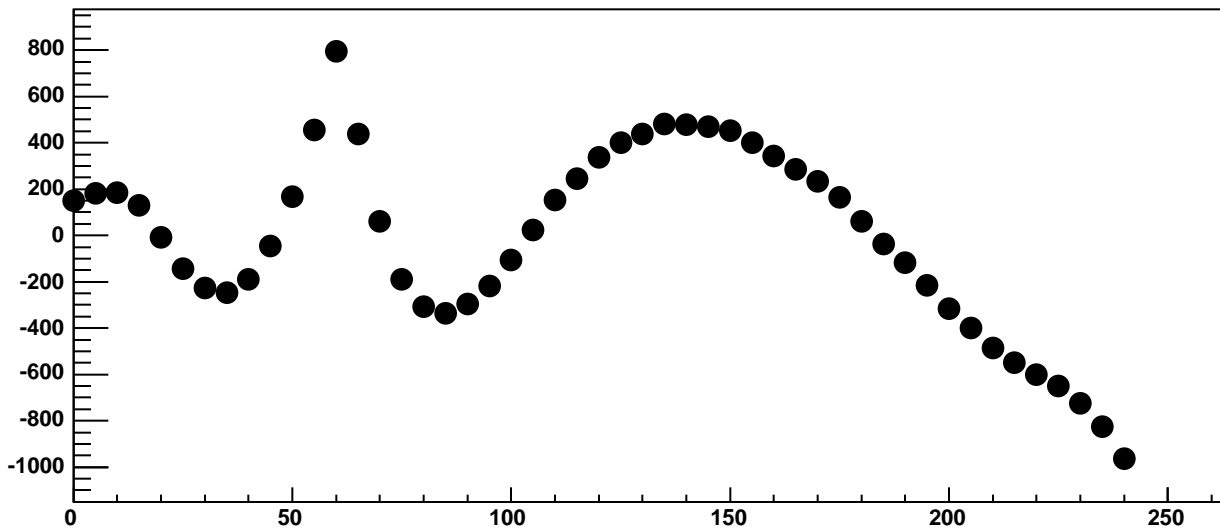


χ^2 / ndf	2.711e+05 / 41
p0	-849.8 ± 1.275
p1	60.96 ± 0.01849
p2	6320 ± 7.158
p3	32.27 ± 0.04954
p4	-8.824 ± 0.04195

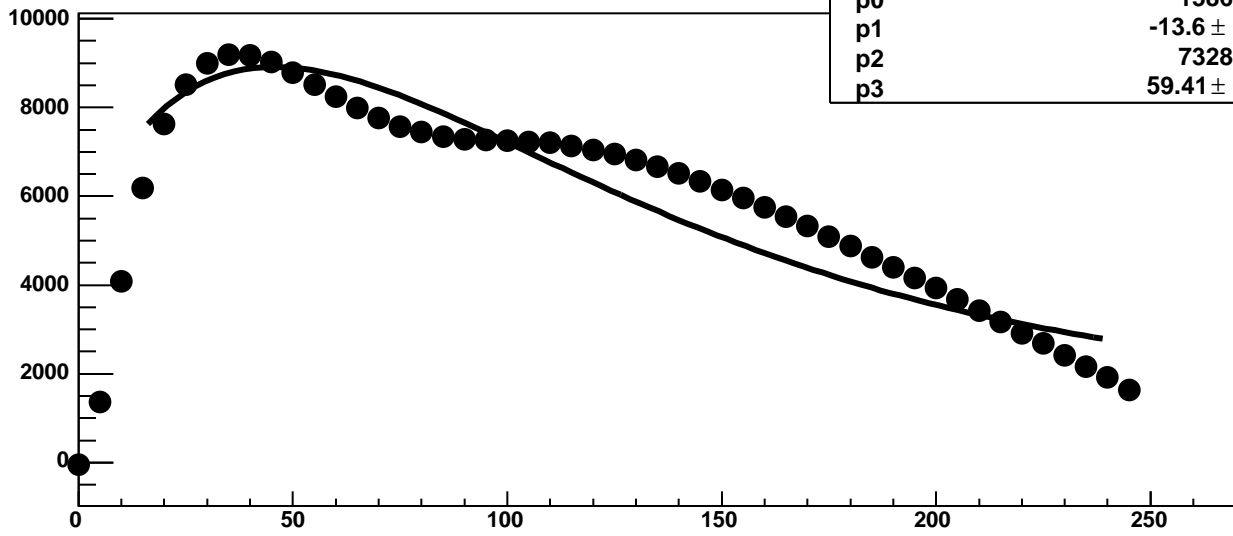
Chip 2, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

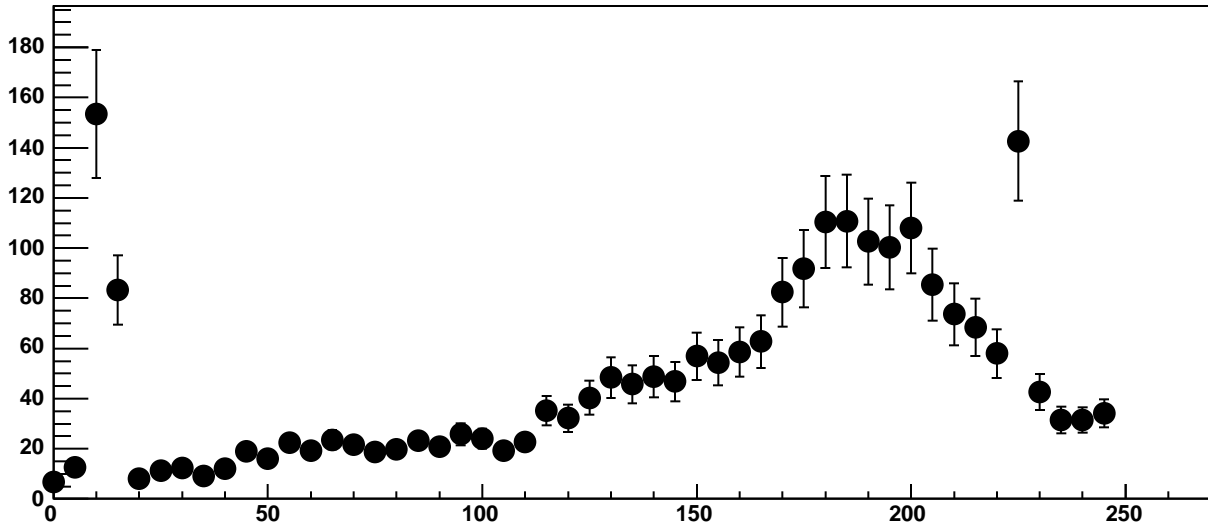


Chip 2, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold

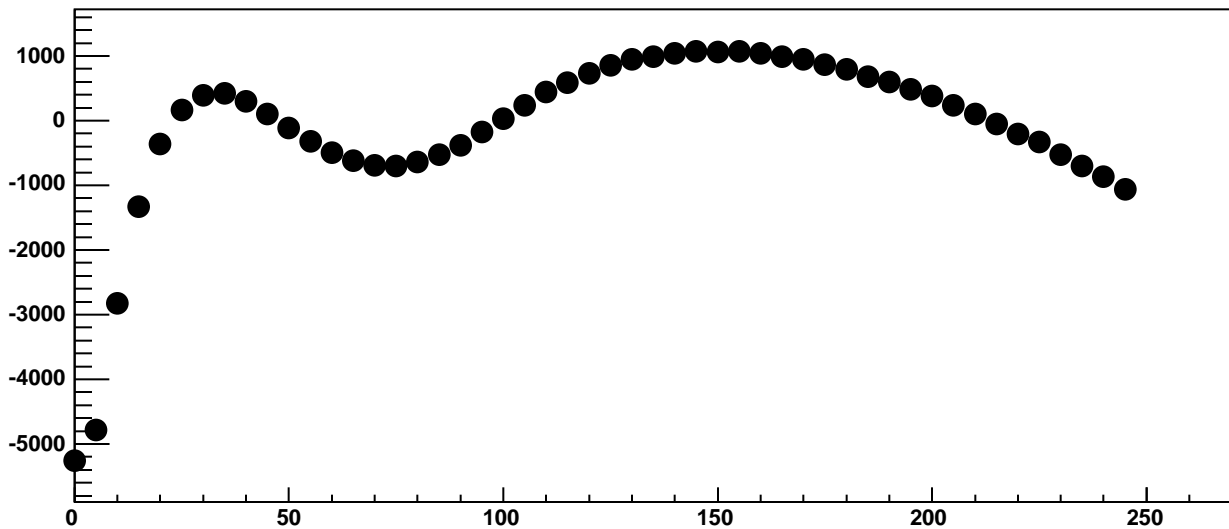


χ^2 / ndf	3.605e+05 / 42
p0	1586 ± 10.13
p1	-13.6 ± 0.09103
p2	7328 ± 9.509
p3	59.41 ± 0.09496

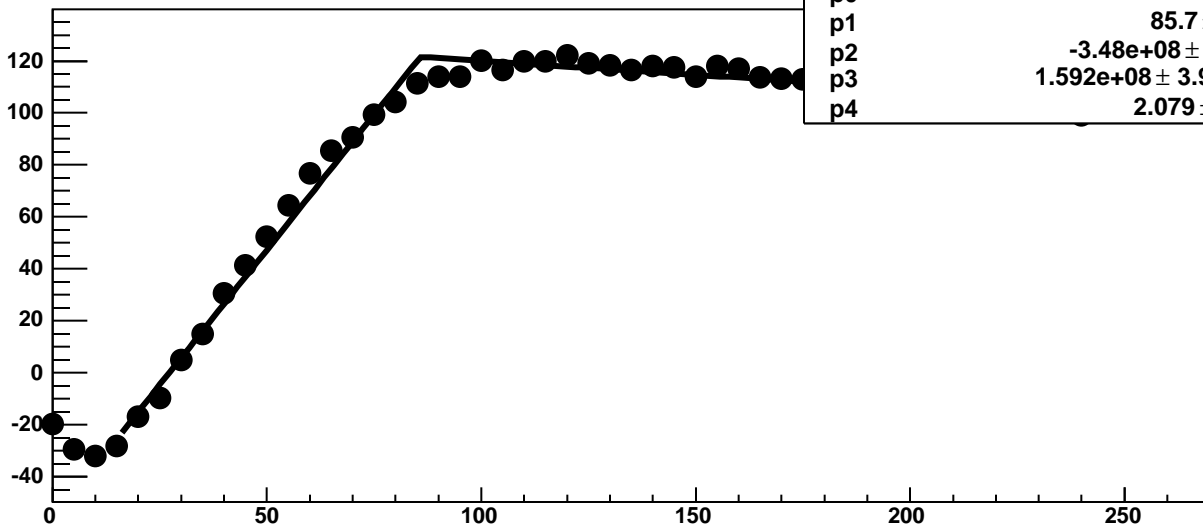
Chip 2, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold

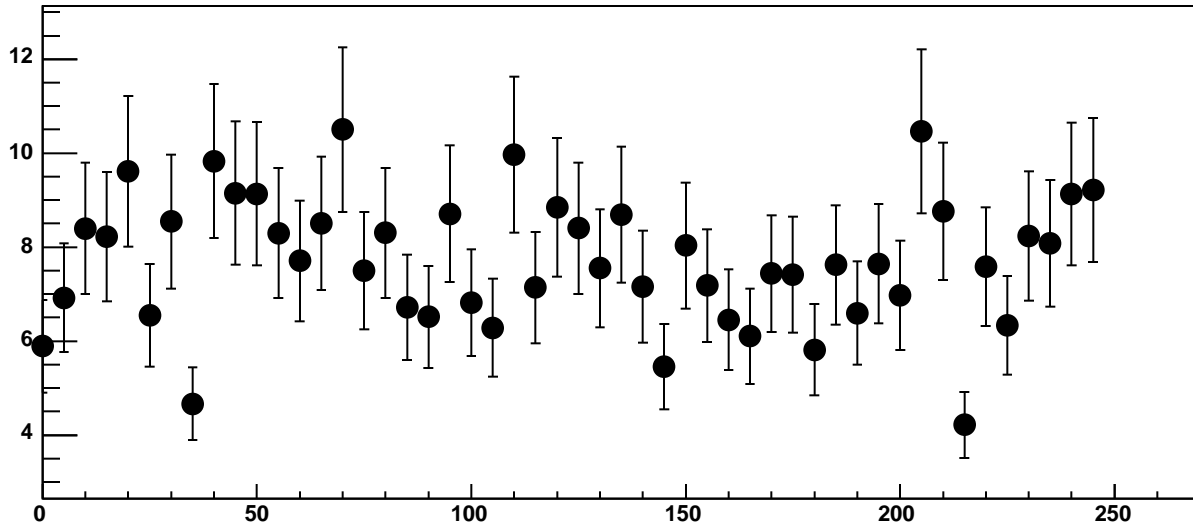


Chip 2, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

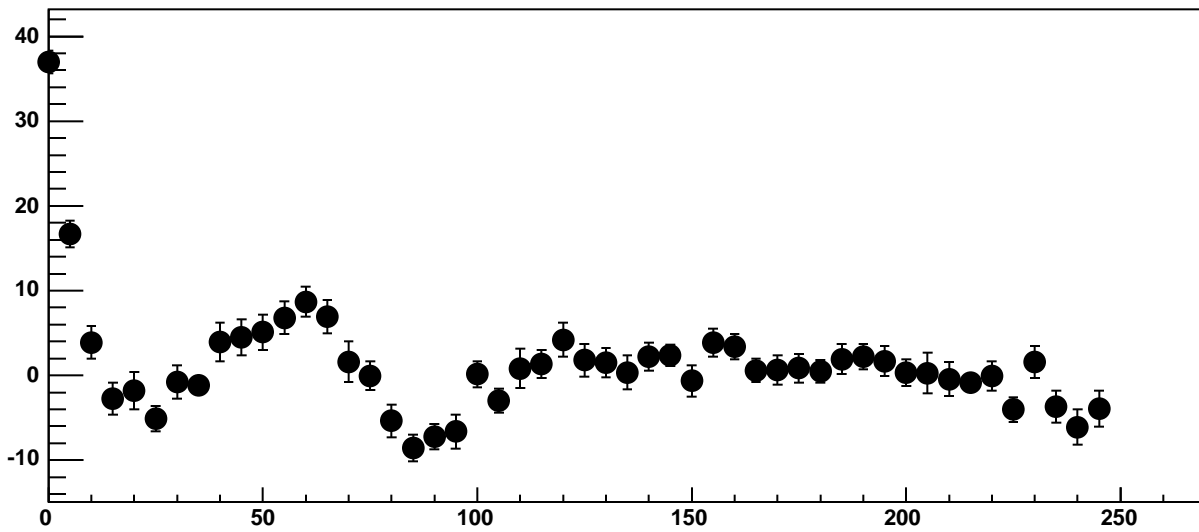


χ^2 / ndf	205.7 / 41
p0	121.5 ± 0.5908
p1	85.7 ± 0.5351
p2	$-3.48\text{e}+08 \pm 3.4\text{e}+06$
p3	$1.592\text{e}+08 \pm 3.991\text{e}+05$
p4	2.079 ± 0.0219

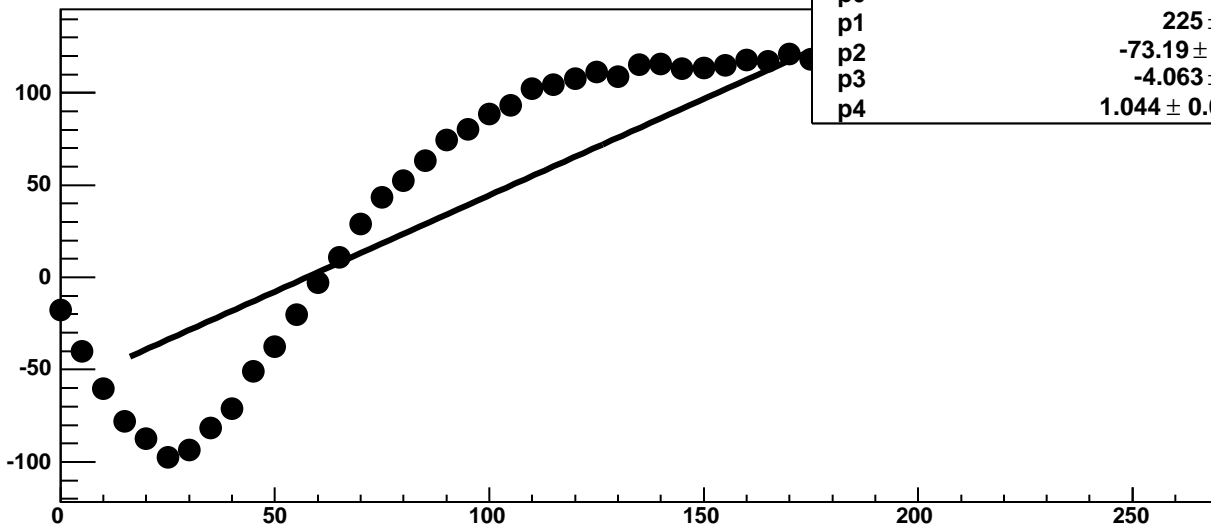
Chip 2, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



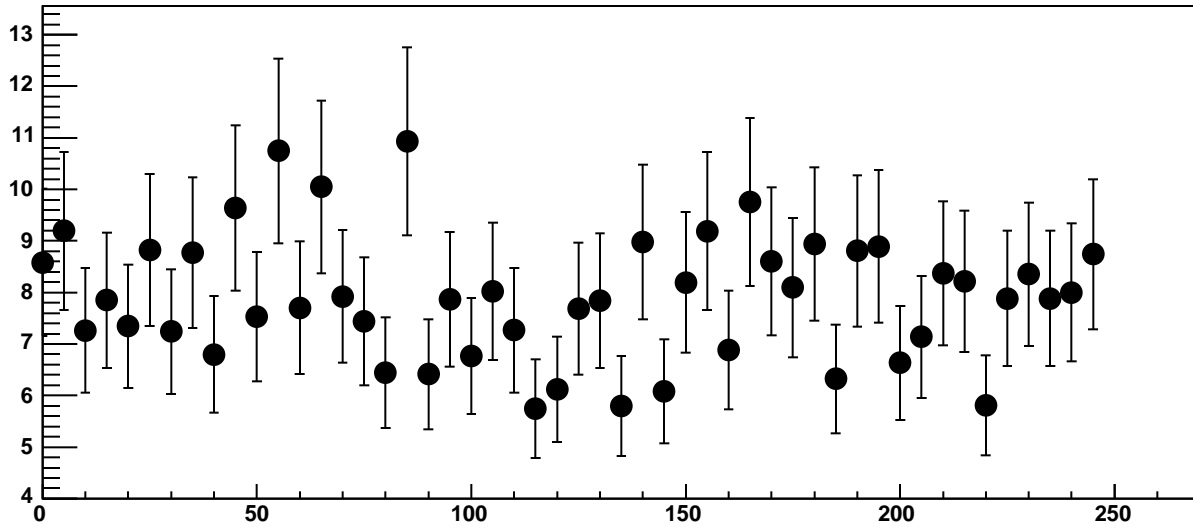
Chip 2, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold



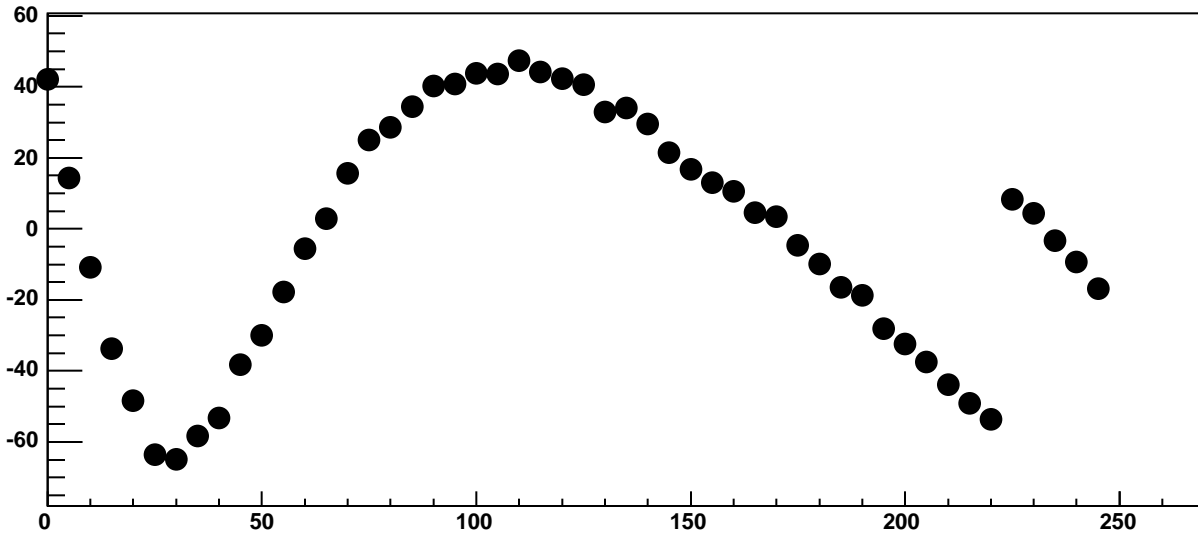
Chip 2, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold



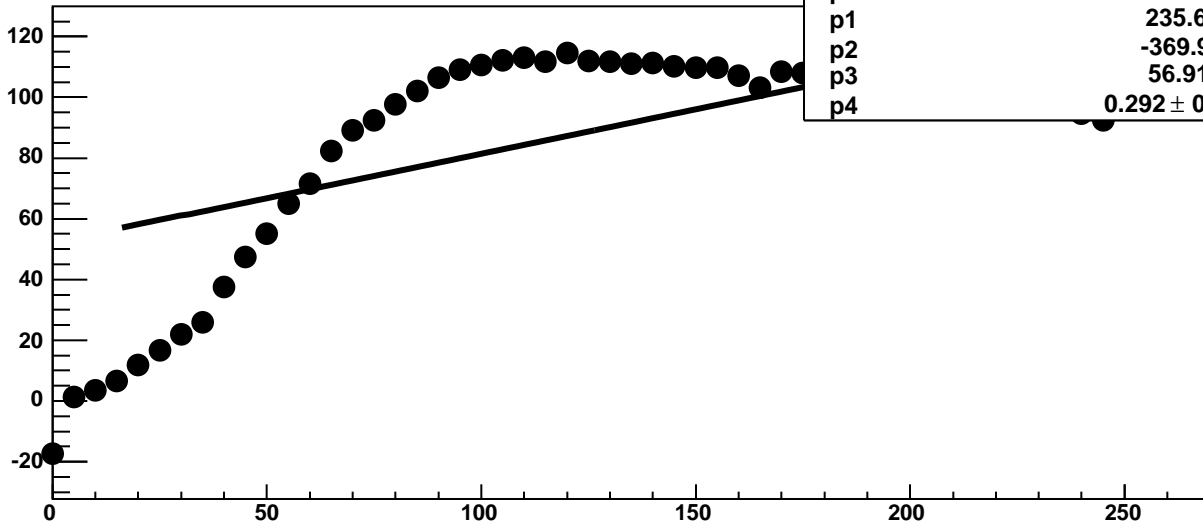
Chip 2, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

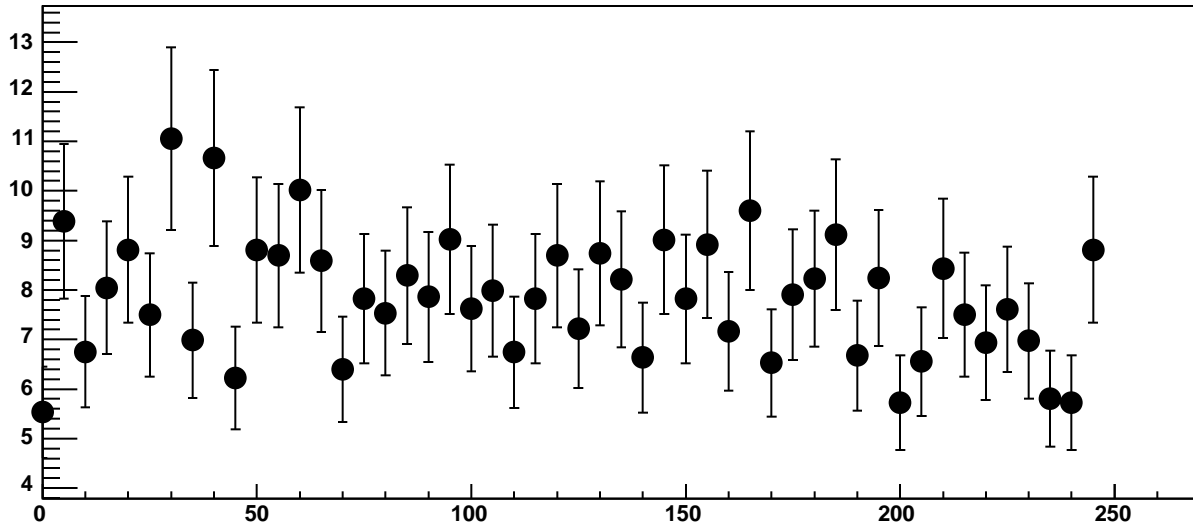


Chip 2, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold

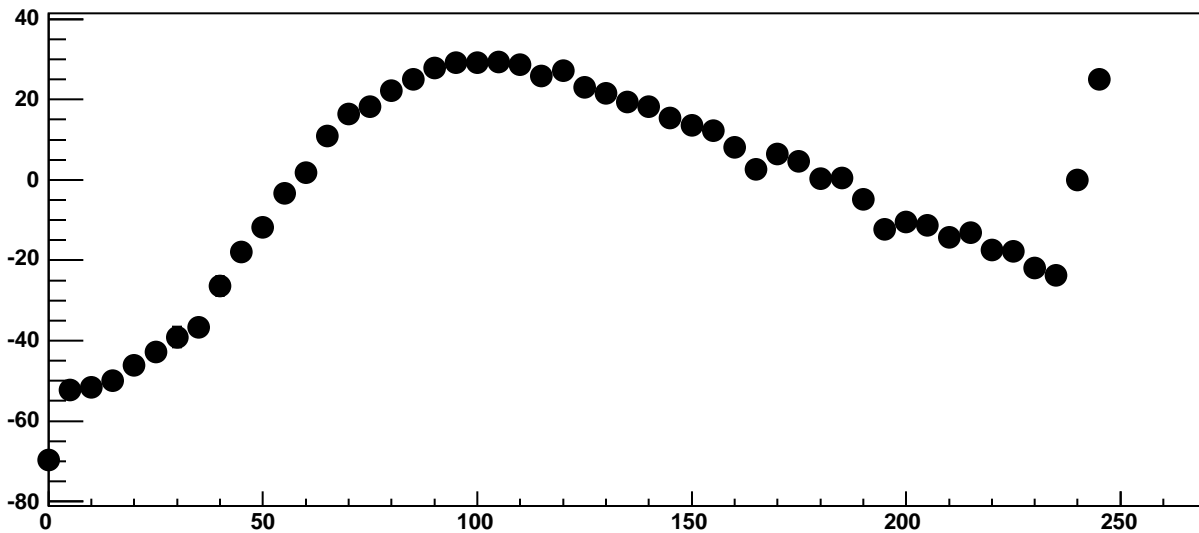


χ^2 / ndf	7030 / 41
p0	121.1 ± 0.9478
p1	235.6 ± 2.803
p2	-369.9 ± 219.1
p3	56.91 ± 28.89
p4	0.292 ± 0.003979

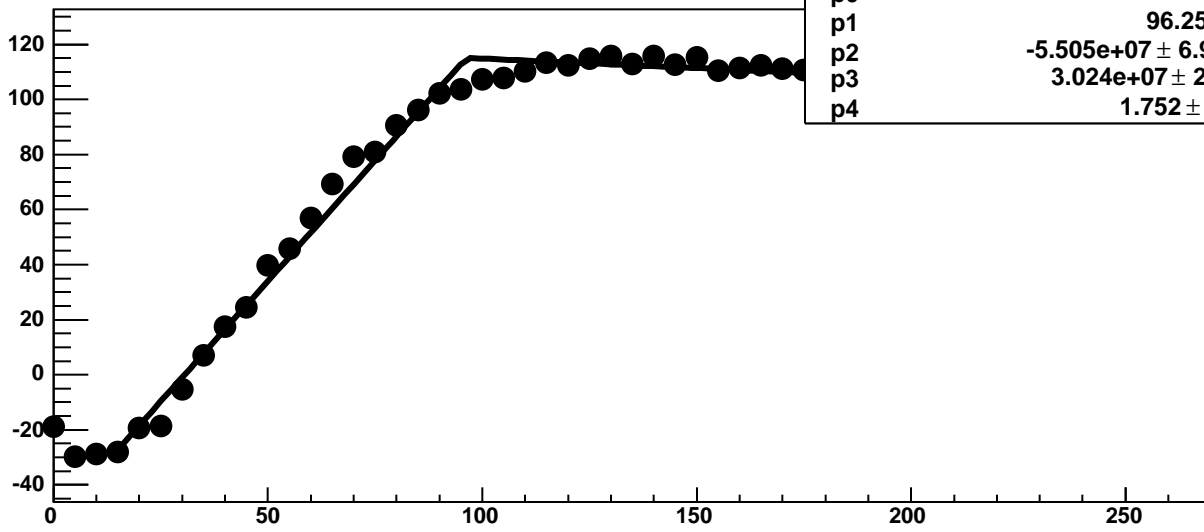
Chip 2, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold

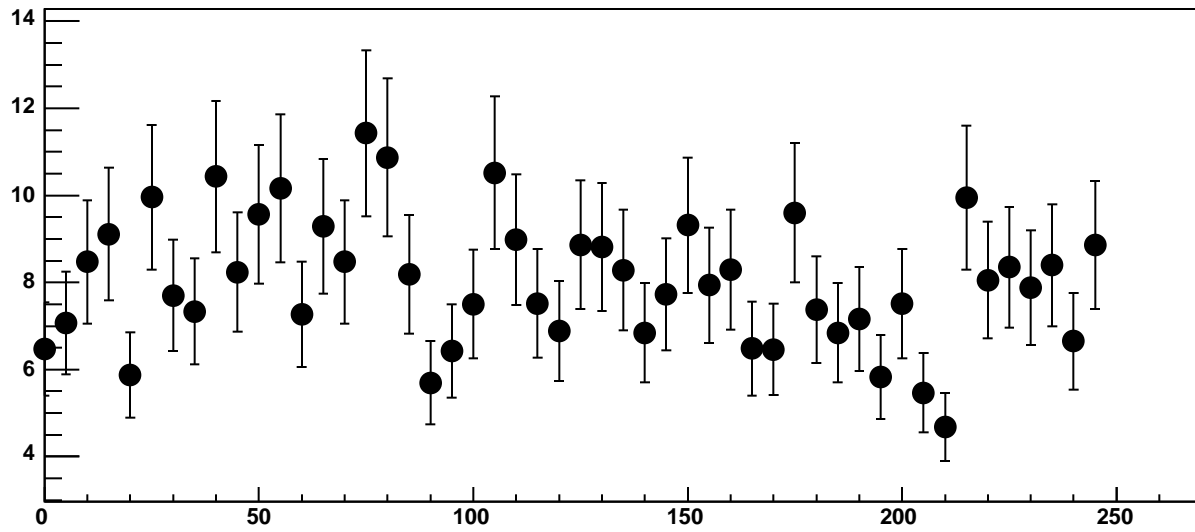


Chip 2, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

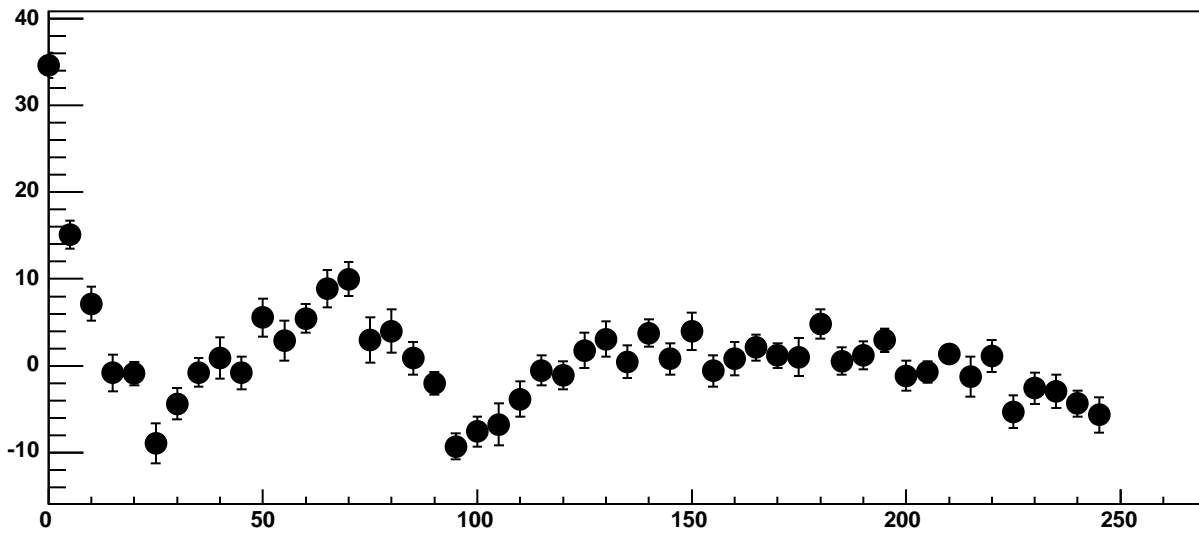


χ^2 / ndf	214.8 / 41
p0	115.1 ± 0.6656
p1	96.25 ± 0.582
p2	-5.505e+07 ± 6.943e+05
p3	3.024e+07 ± 2.27e+05
p4	1.752 ± 0.01672

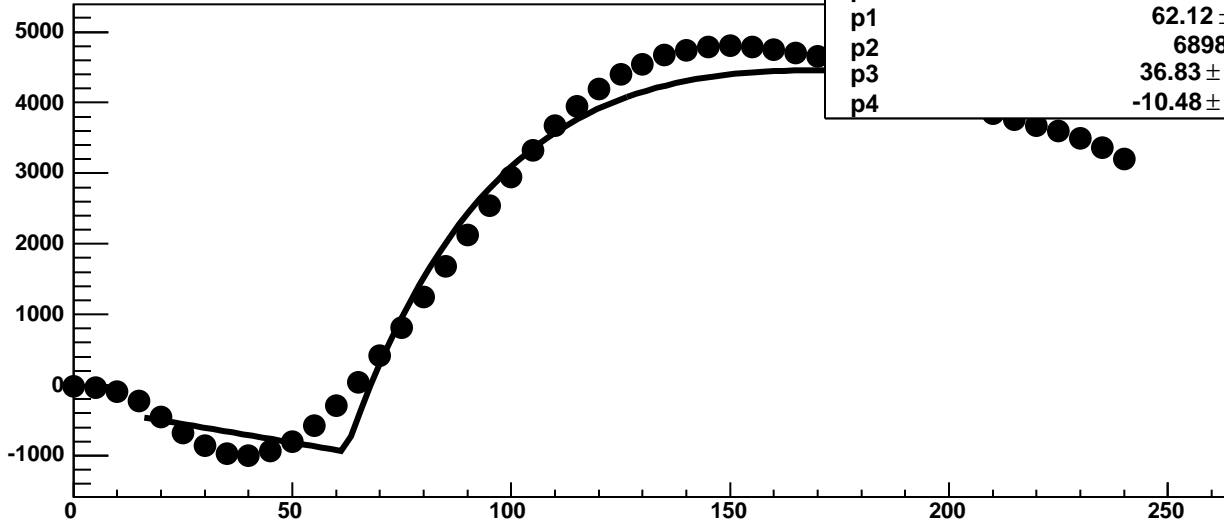
Chip 2, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

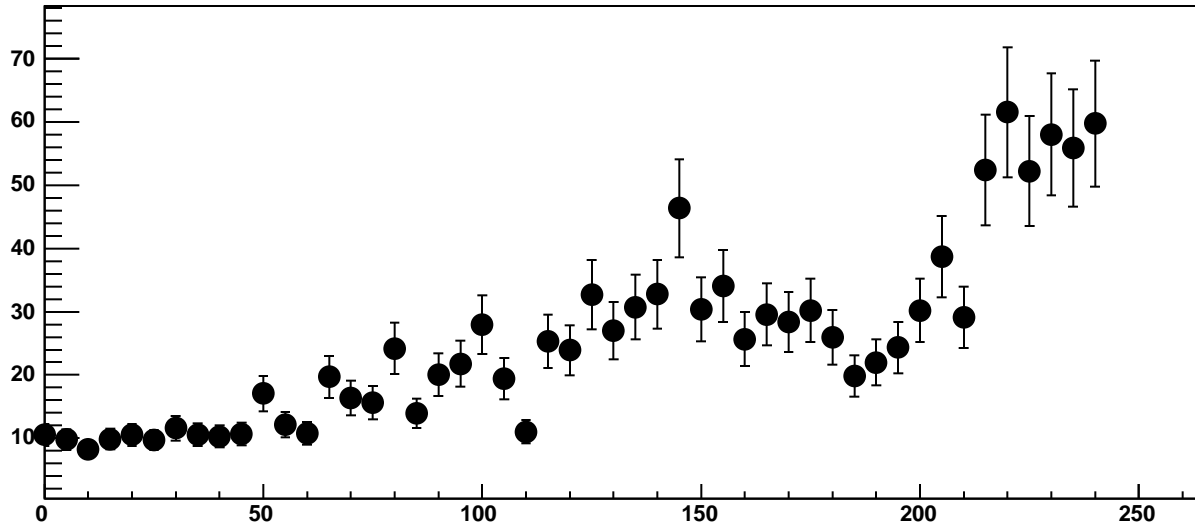


Chip 2, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold

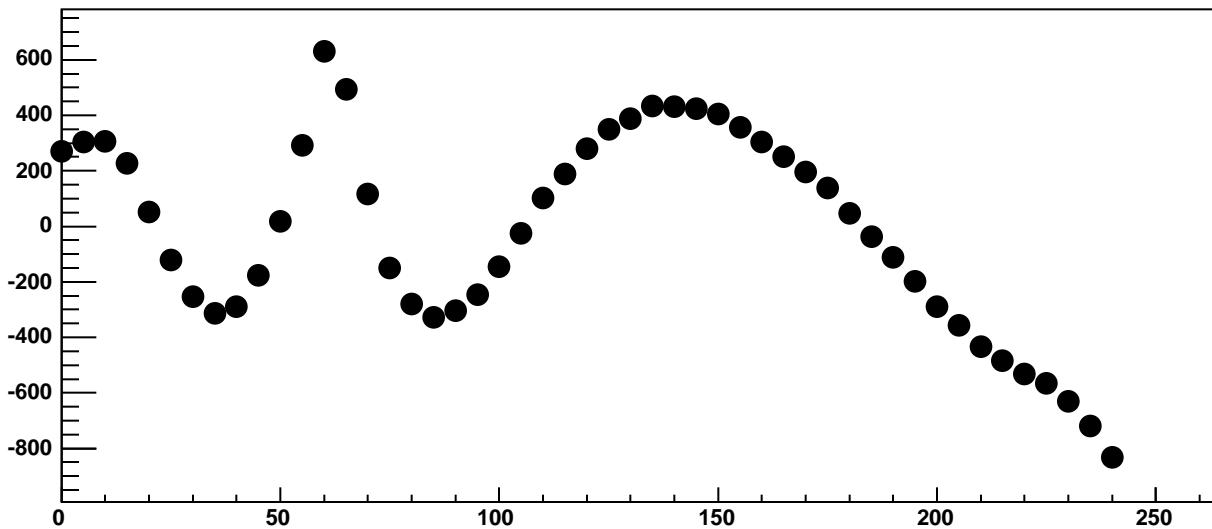


χ^2 / ndf	2.252e+05 / 41
p0	-942.5 ± 1.374
p1	62.12 ± 0.0204
p2	6898 ± 8.234
p3	36.83 ± 0.05689
p4	-10.48 ± 0.04609

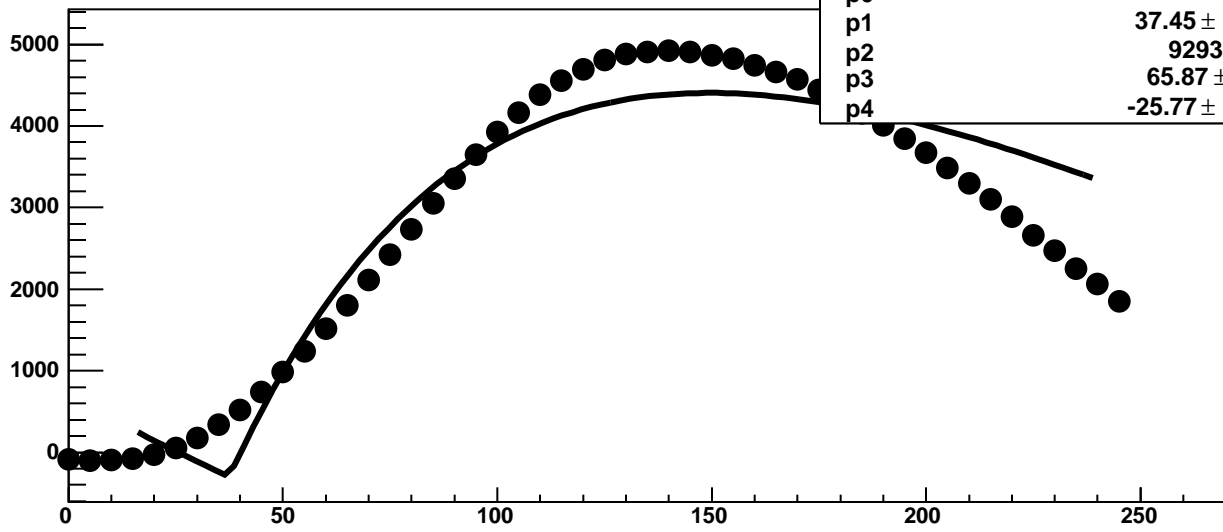
Chip 2, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

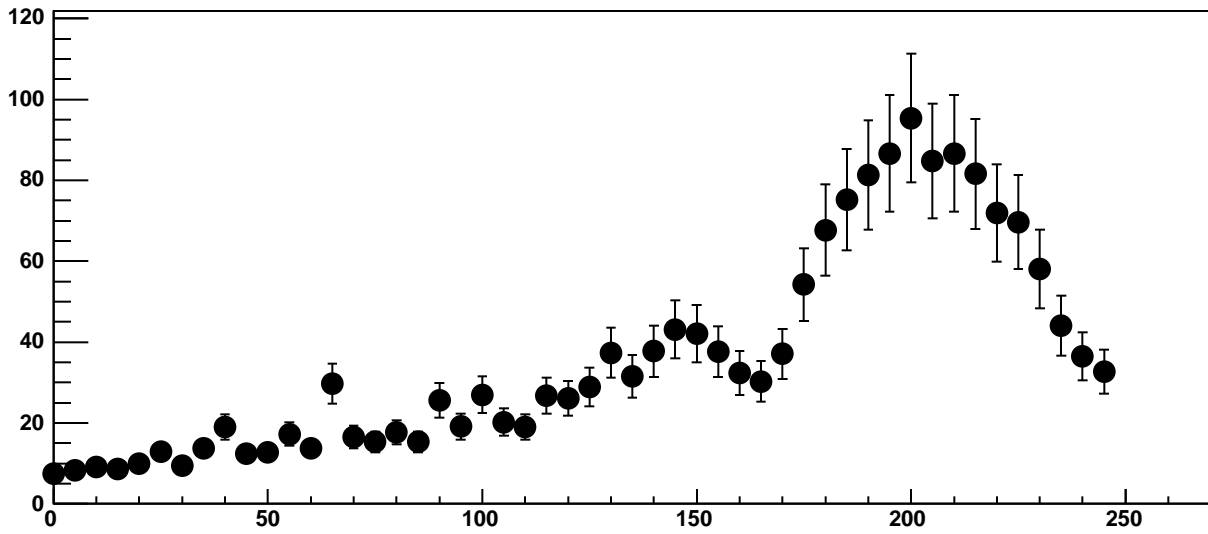


Chip 2, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

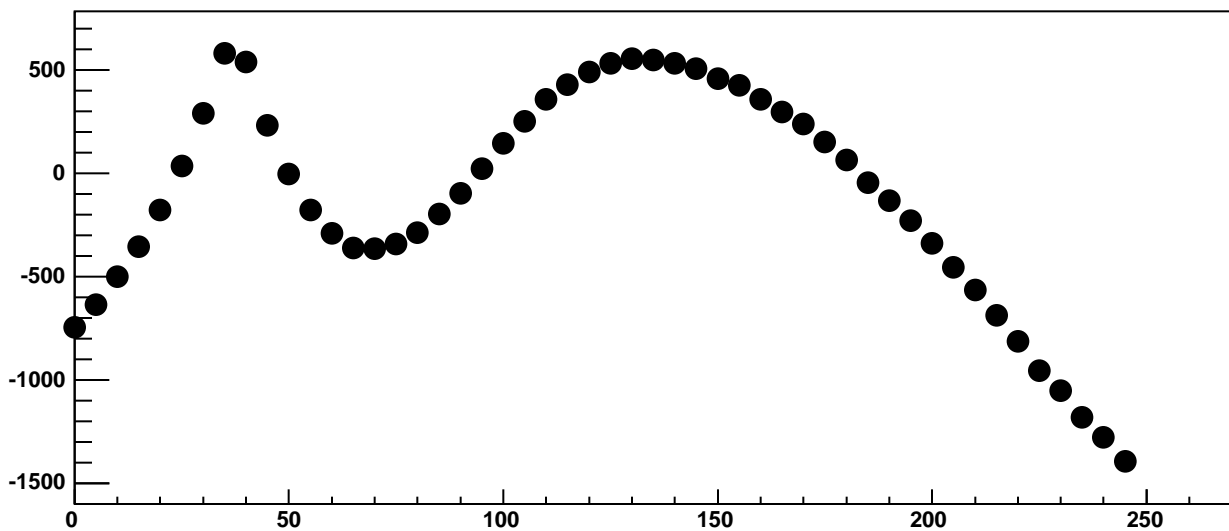


χ^2 / ndf	2.603e+05 / 41
p0	-303.9 ± 1.385
p1	37.45 ± 0.02098
p2	9293 ± 19.89
p3	65.87 ± 0.1276
p4	-25.77 ± 0.08765

Chip 2, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold

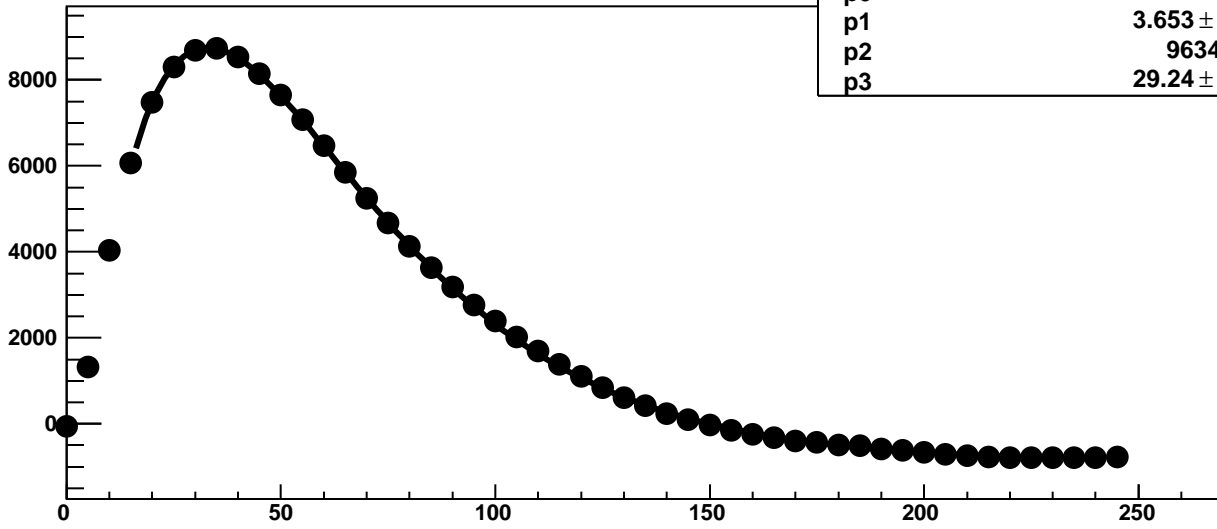


Chip 2, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

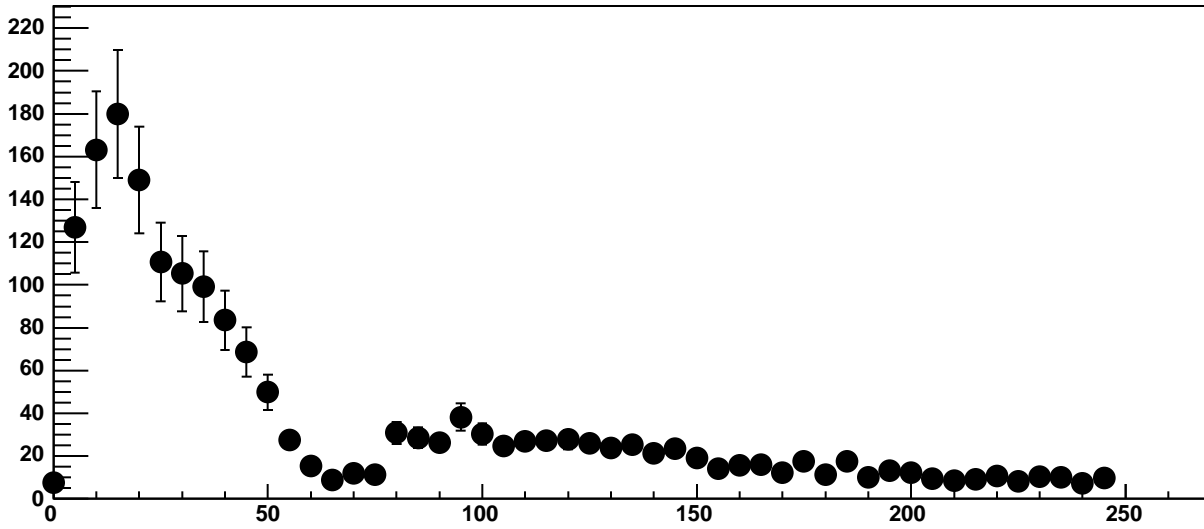


Chip 2, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold

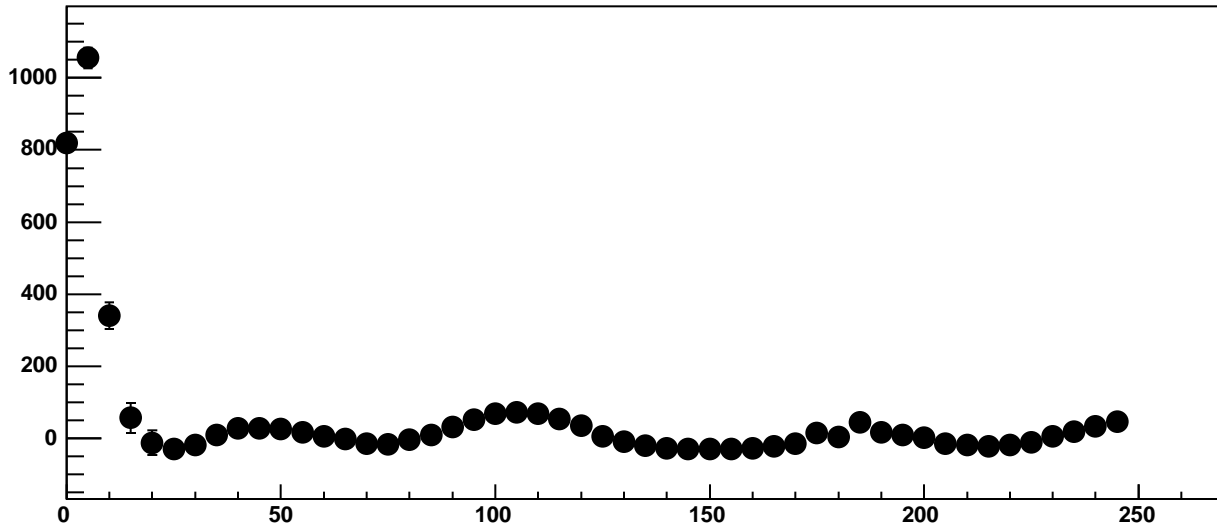
χ^2 / ndf	1954 / 42
p0	-881.4 ± 0.8611
p1	3.653 ± 0.05883
p2	9634 ± 6.692
p3	29.24 ± 0.01897



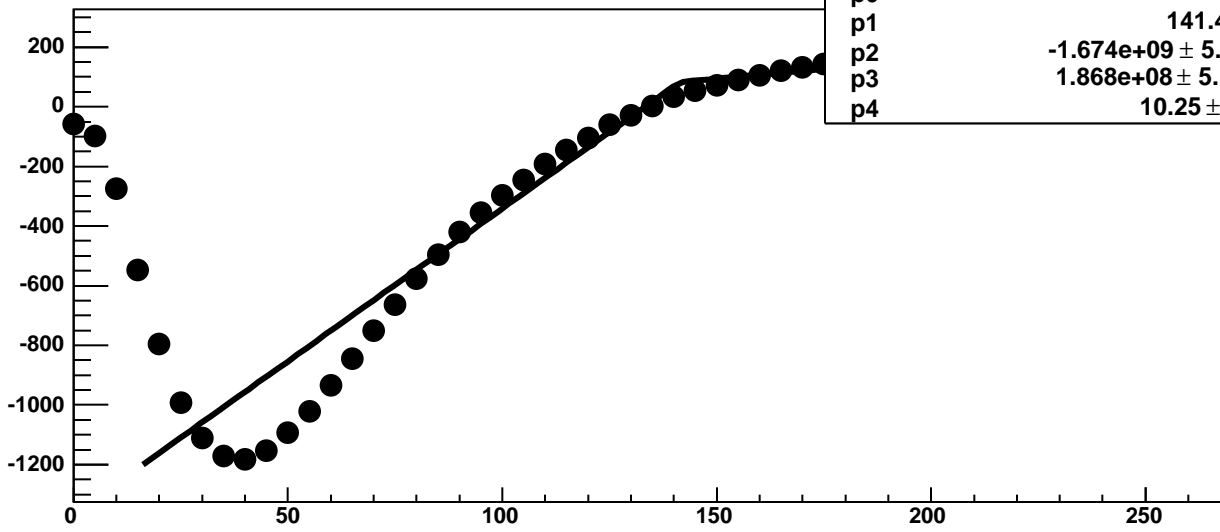
Chip 2, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold

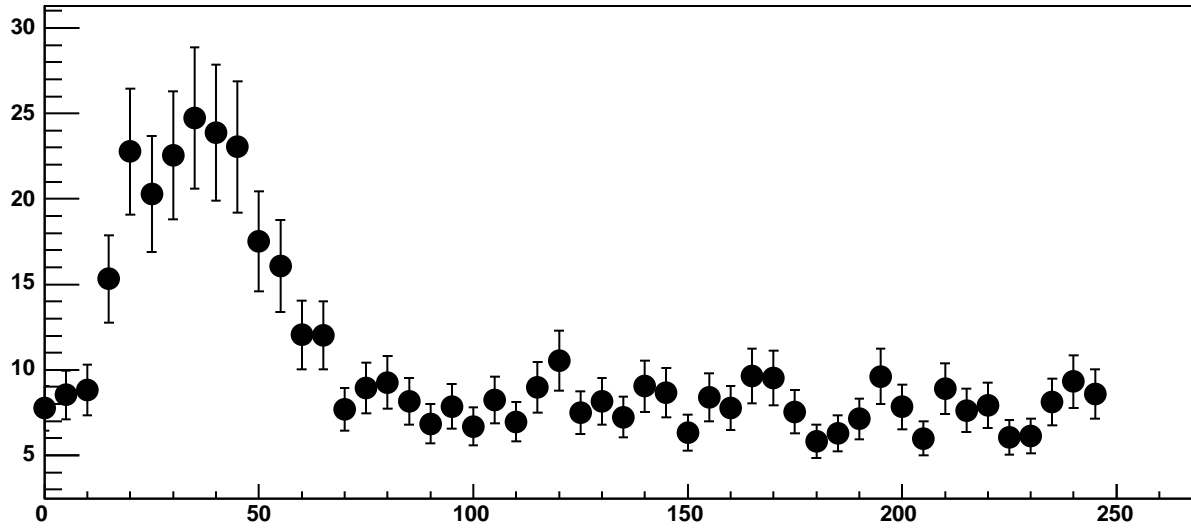


Chip 2, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

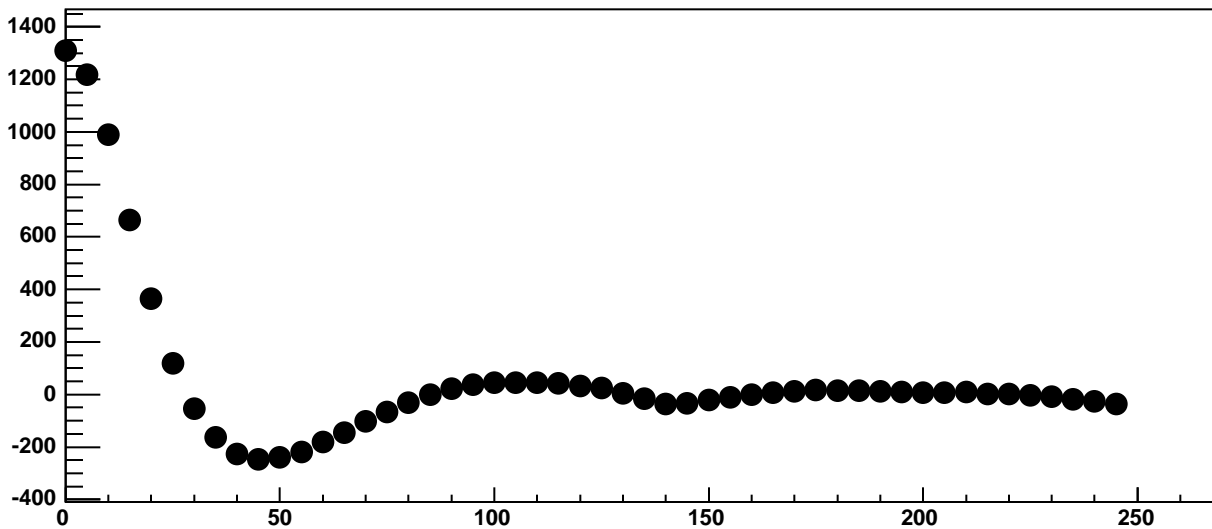


χ^2 / ndf	7.075e+04 / 41
p0	83.33 ± 0.925
p1	141.4 ± 0.127
p2	-1.674e+09 ± 5.989e+06
p3	1.868e+08 ± 5.168e+05
p4	10.25 ± 0.01516

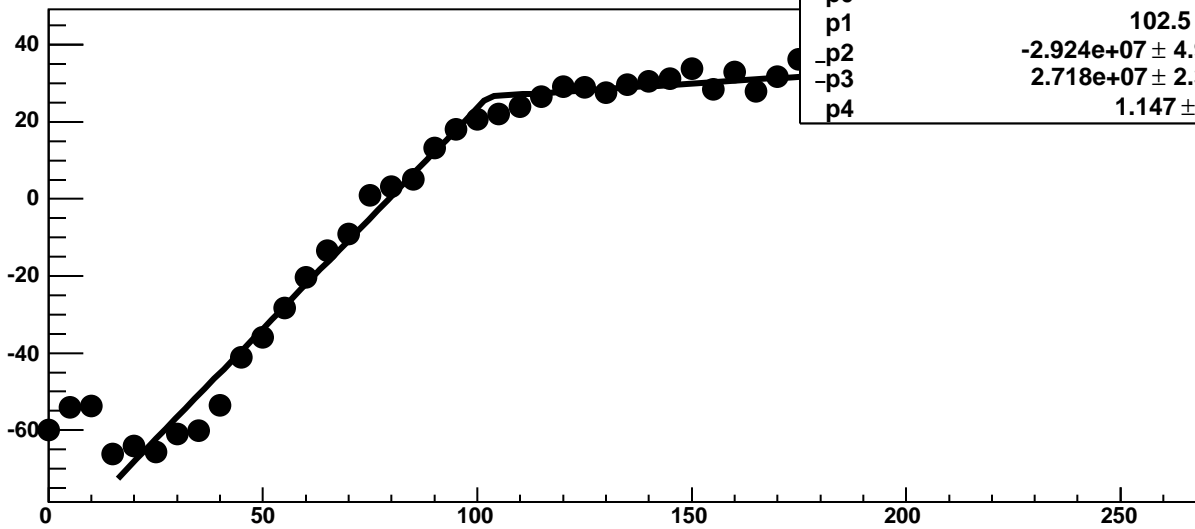
Chip 2, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

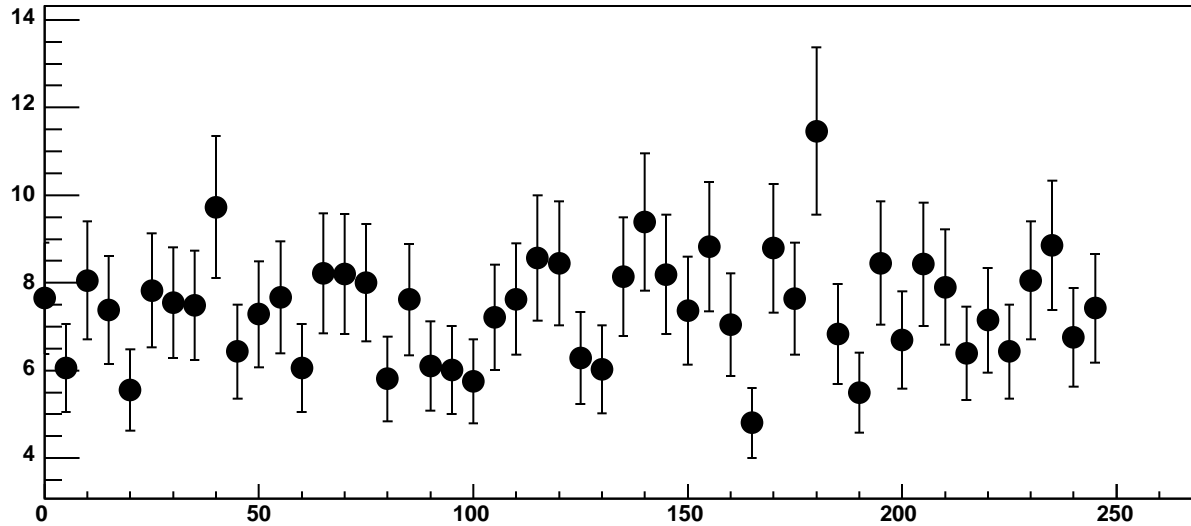


Chip 2, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

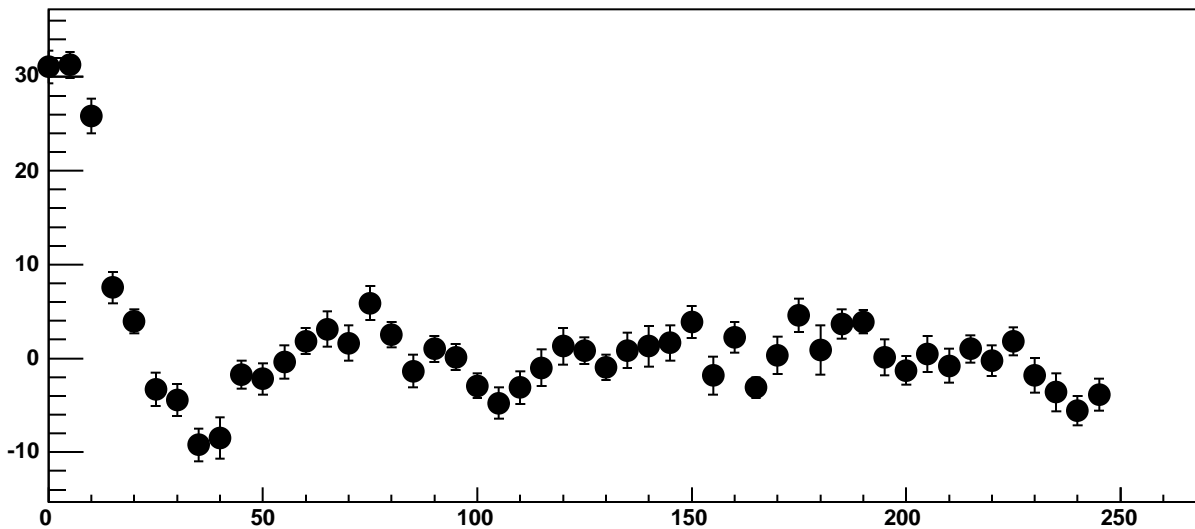


χ^2 / ndf	183.7 / 41
p0	26.58 ± 0.6874
p1	102.5 ± 0.8783
-p2	$-2.924\text{e}+07 \pm 4.956\text{e}+05$
-p3	$2.718\text{e}+07 \pm 2.327\text{e}+05$
p4	1.147 ± 0.01365

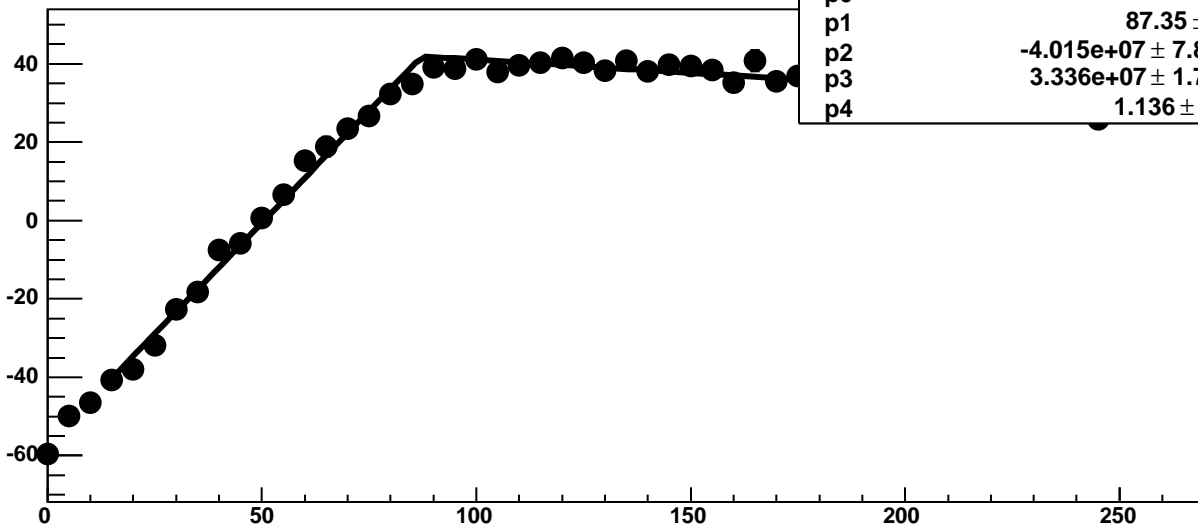
Chip 2, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

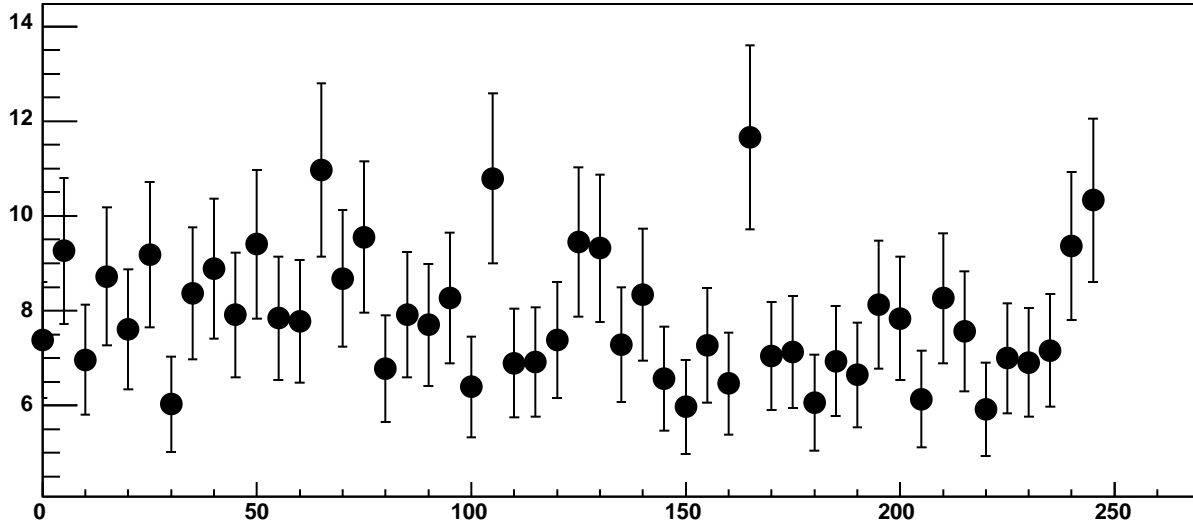


Chip 2, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold

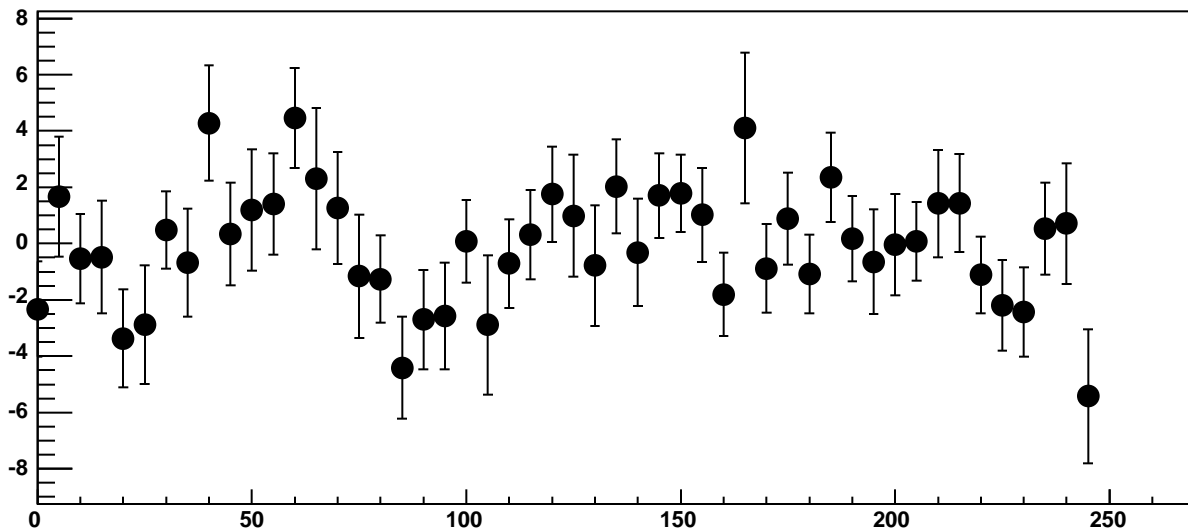


χ^2 / ndf	51.43 / 41
p0	41.93 ± 0.5922
p1	87.35 ± 0.9505
p2	$-4.015\text{e}+07 \pm 7.802\text{e}+05$
p3	$3.336\text{e}+07 \pm 1.714\text{e}+05$
p4	1.136 ± 0.02185

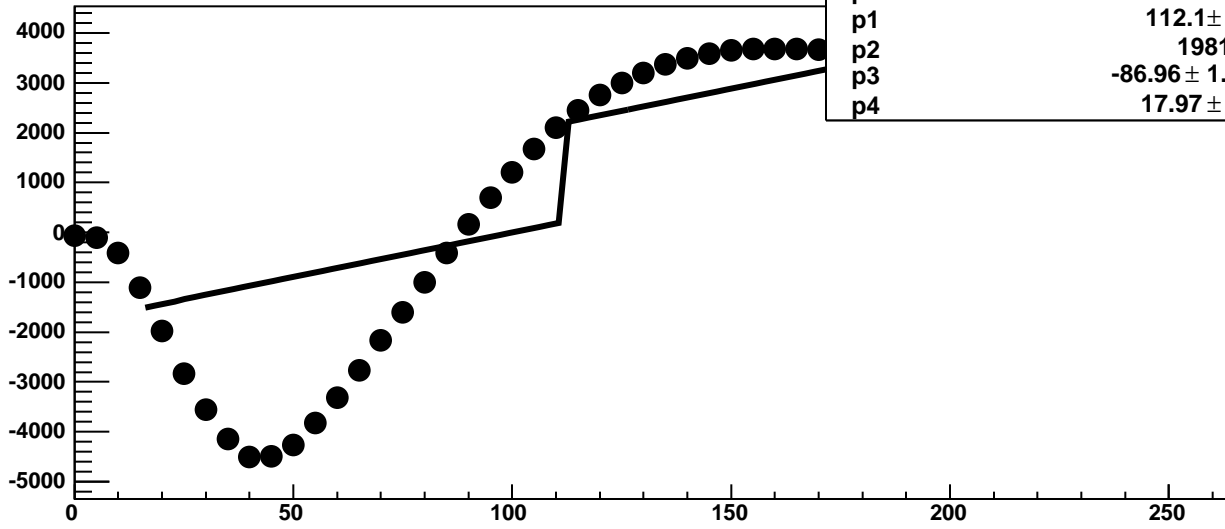
Chip 2, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold

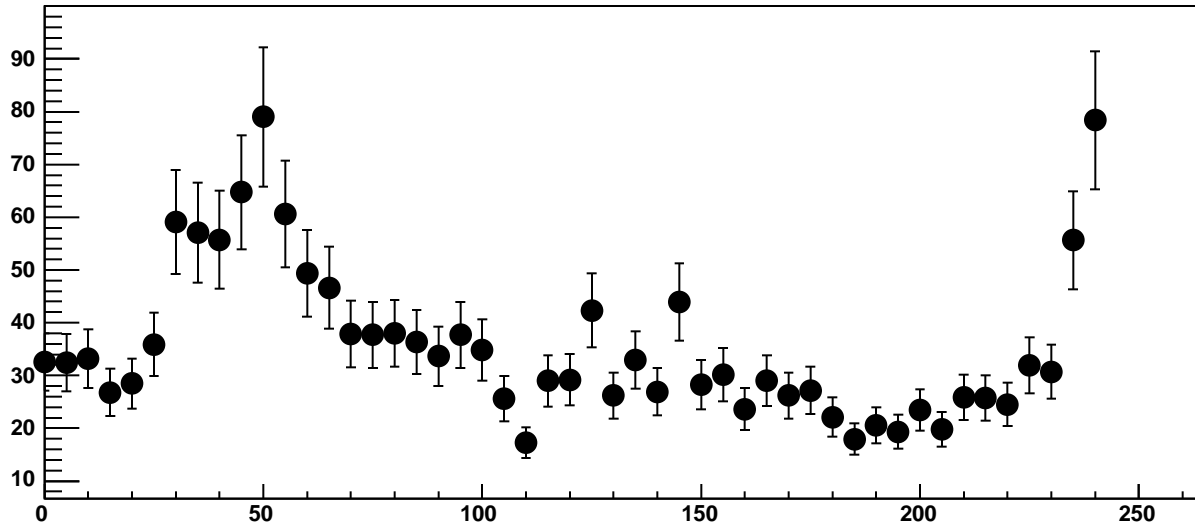


Chip 2, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

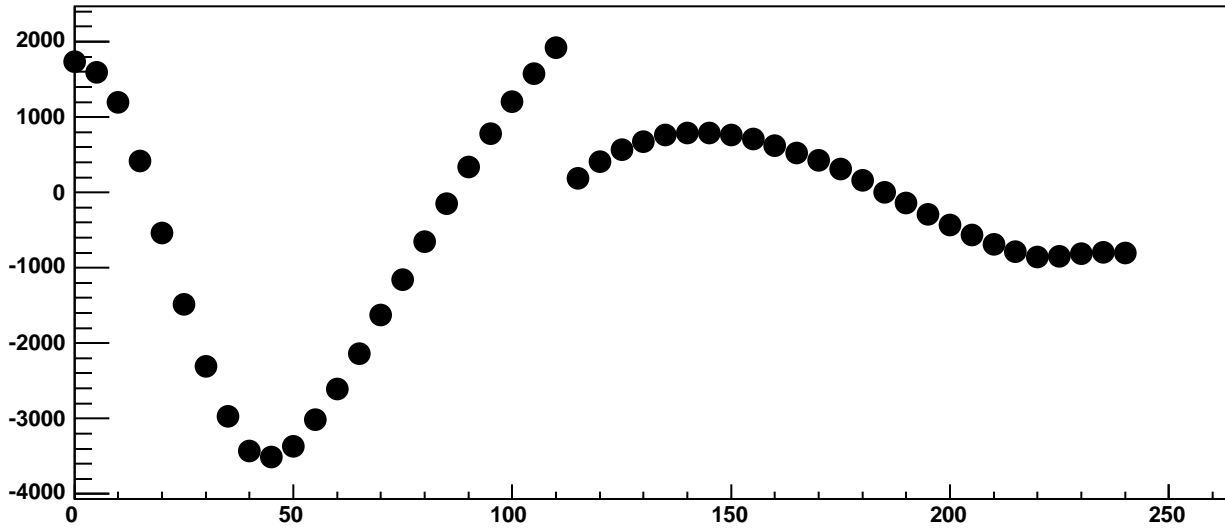


χ^2 / ndf	1.044e+06 / 41
p0	220.5 ± 2.129
p1	112.1 ± 0.00312
p2	1981 ± 3.818
p3	-86.96 ± 1.904e-07
p4	17.97 ± 0.03003

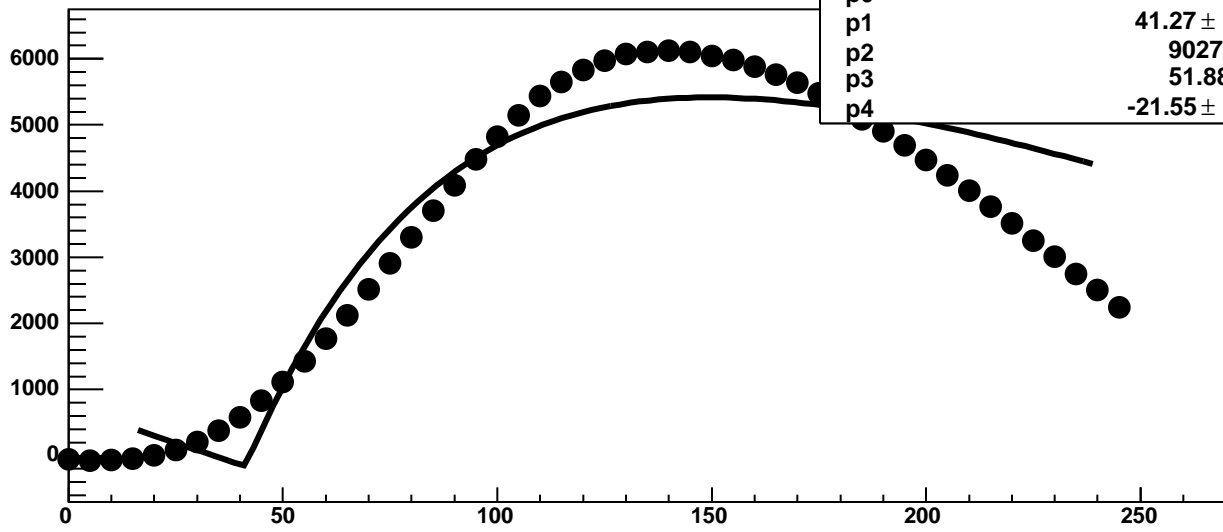
Chip 2, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold

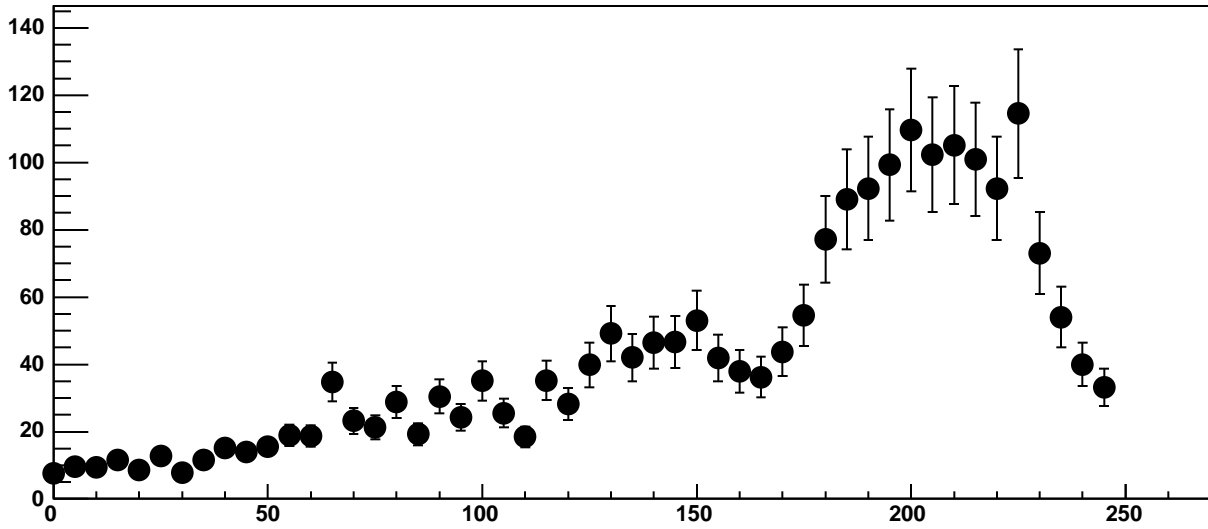


Chip 2, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold

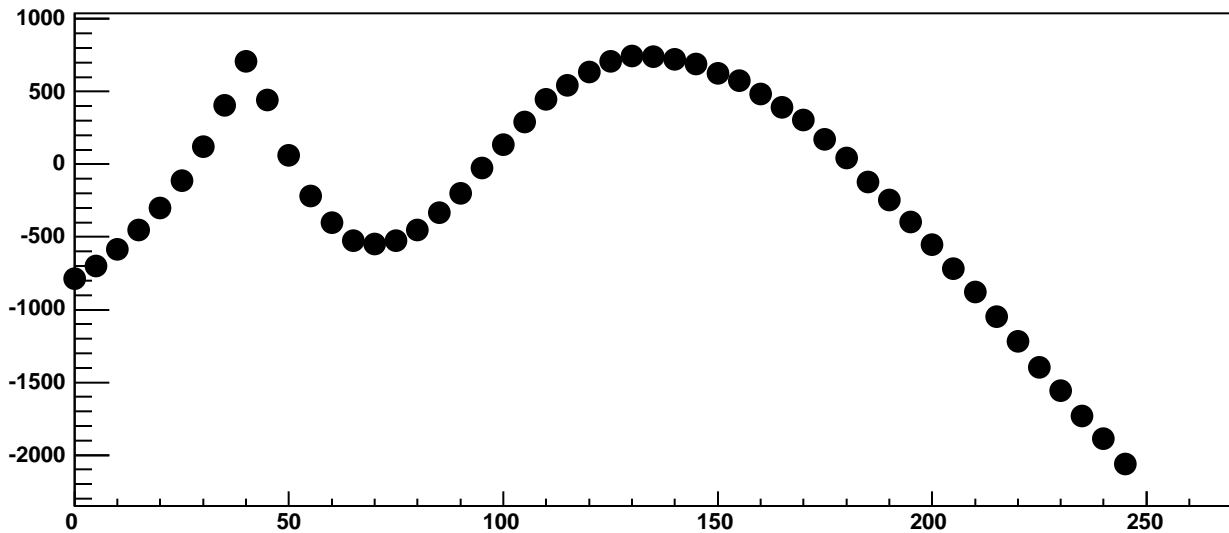


χ^2 / ndf	3.394e+05 / 41
p0	-157.2 ± 1.364
p1	41.27 ± 0.01812
p2	9027 ± 15.39
p3	51.88 ± 0.09
p4	-21.55 ± 0.07825

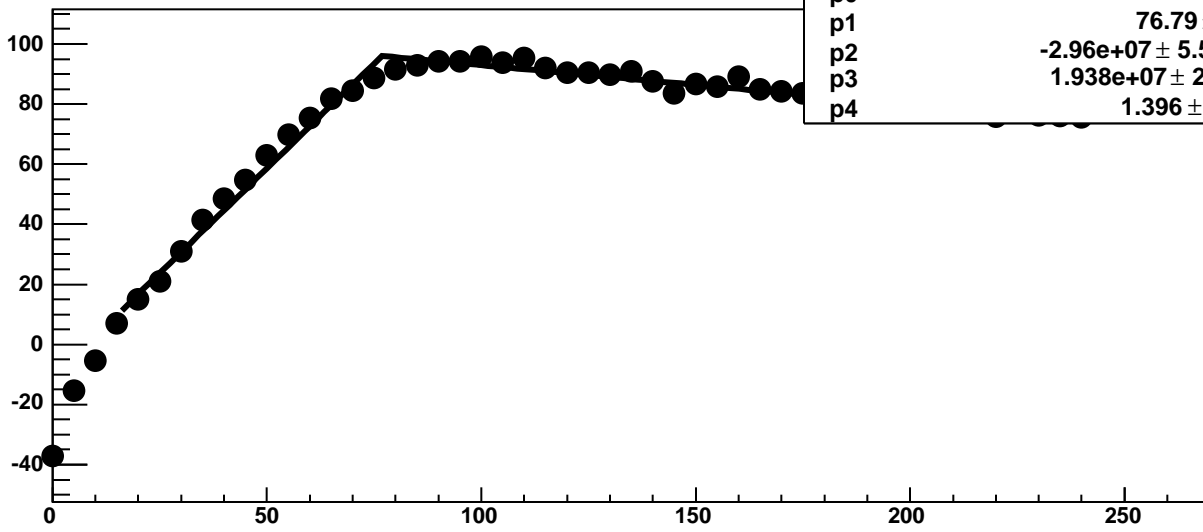
Chip 2, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold

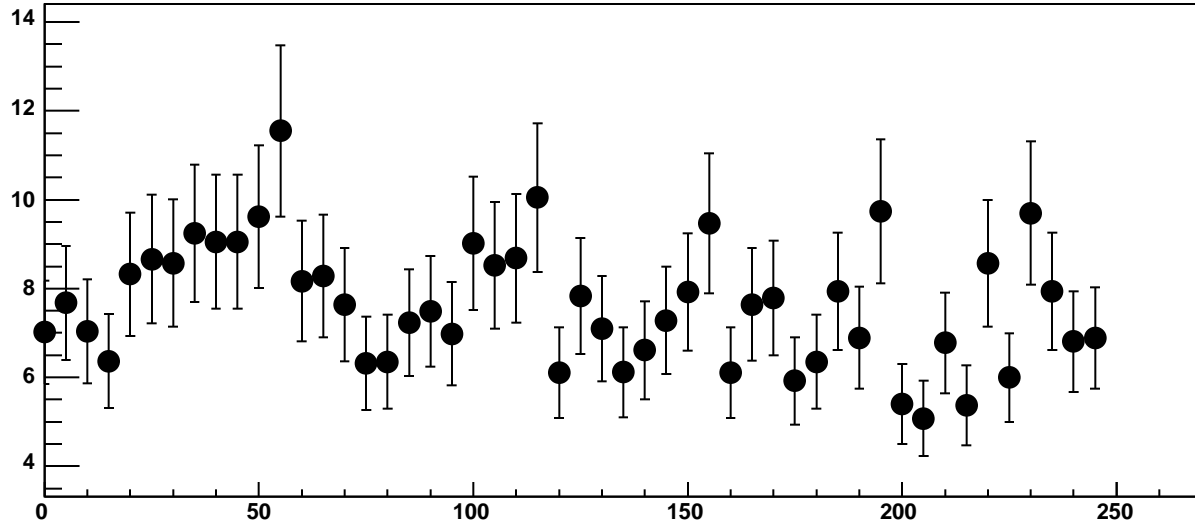


Chip 2, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold

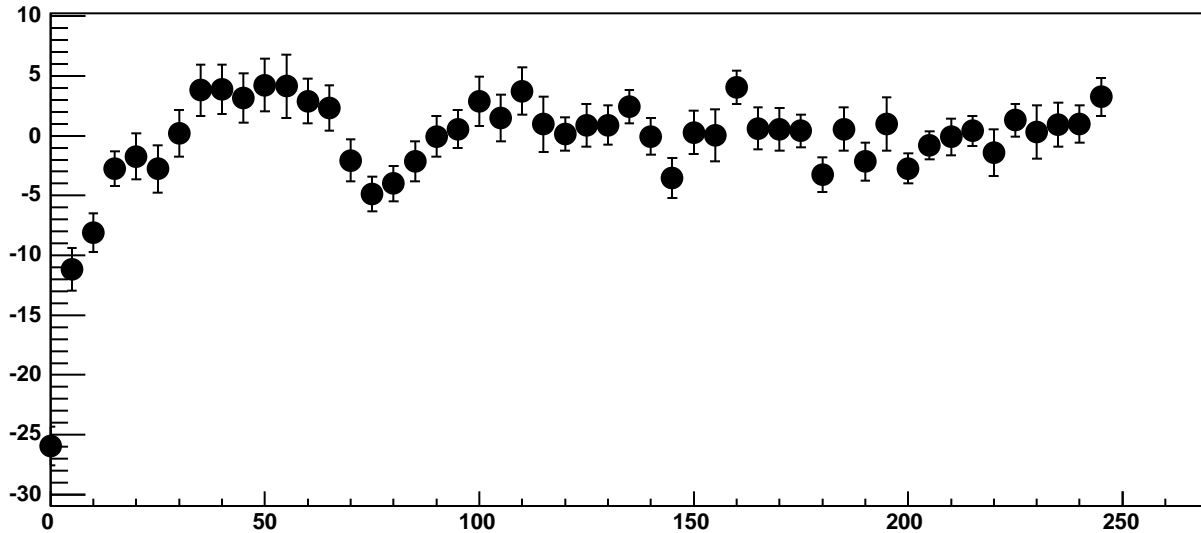


χ^2 / ndf	85.13 / 41
p0	96.01 ± 0.5463
p1	76.79 ± 0.7311
p2	$-2.96\text{e}+07 \pm 5.593\text{e}+05$
p3	$1.938\text{e}+07 \pm 2.03\text{e}+05$
p4	1.396 ± 0.02491

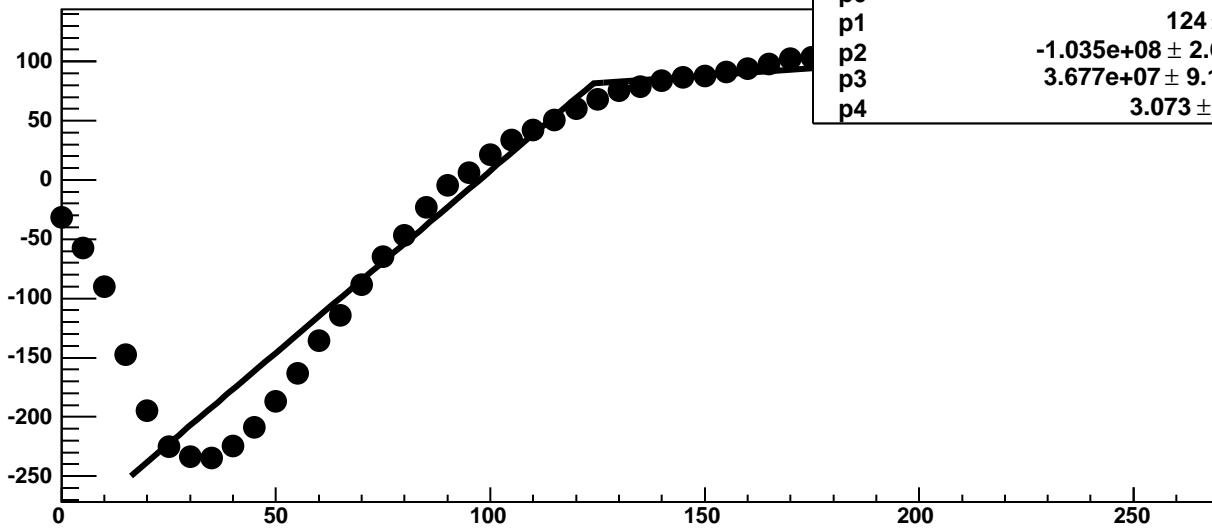
Chip 2, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold

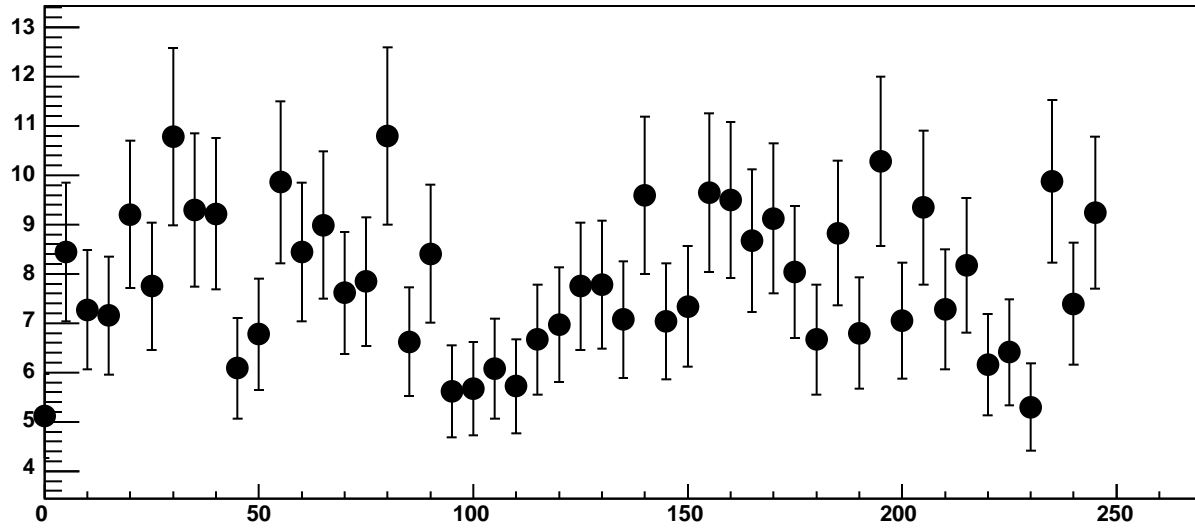


Chip 2, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

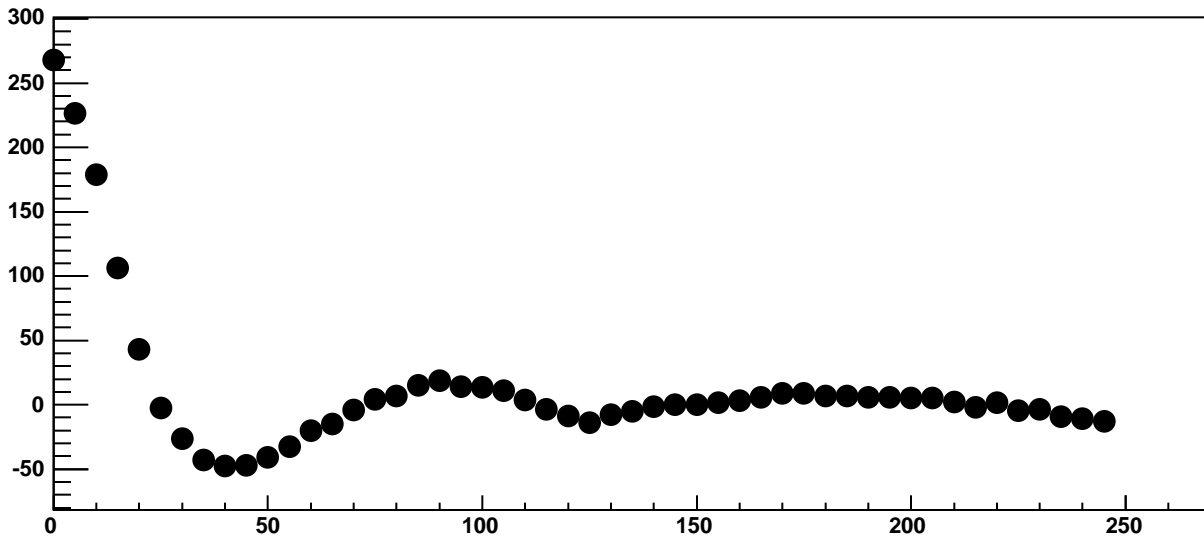


χ^2 / ndf	8684 / 41
p0	81.42 ± 0.7875
p1	124 ± 0.3471
p2	$-1.035\text{e}+08 \pm 2.642\text{e}+06$
p3	$3.677\text{e}+07 \pm 9.178\text{e}+05$
p4	3.073 ± 0.01115

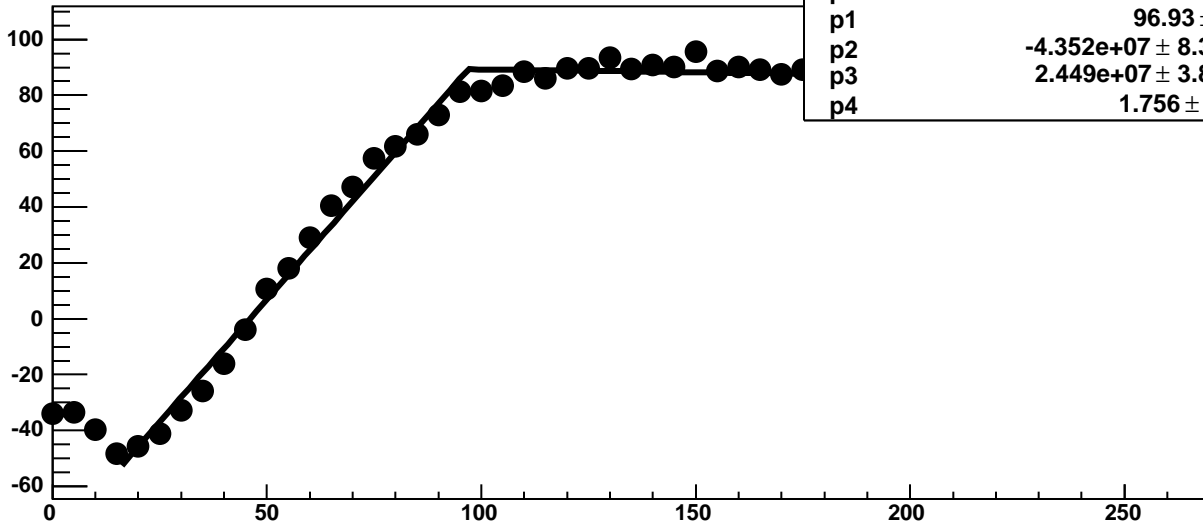
Chip 2, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

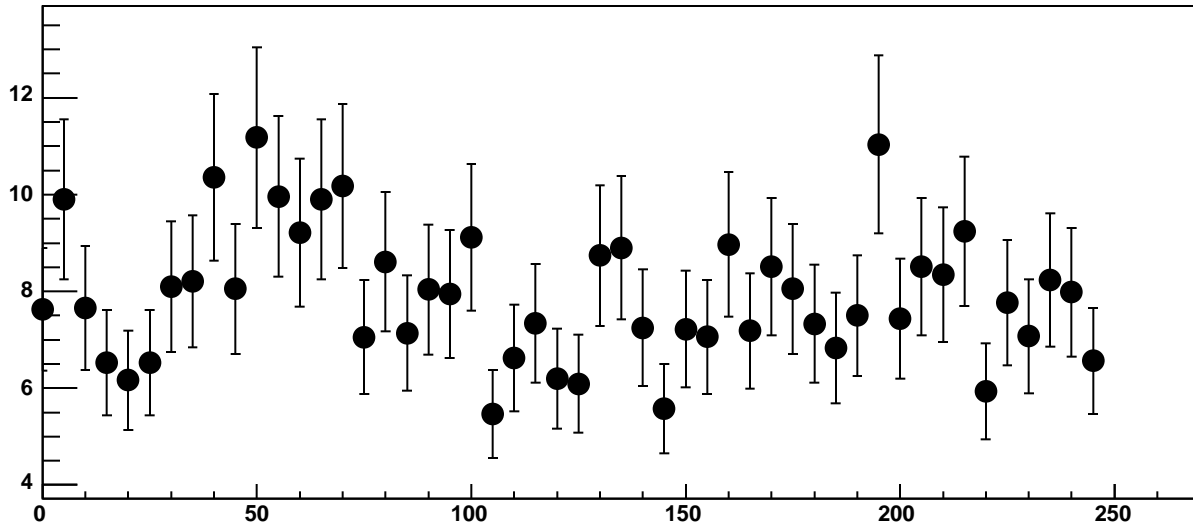


Chip 2, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

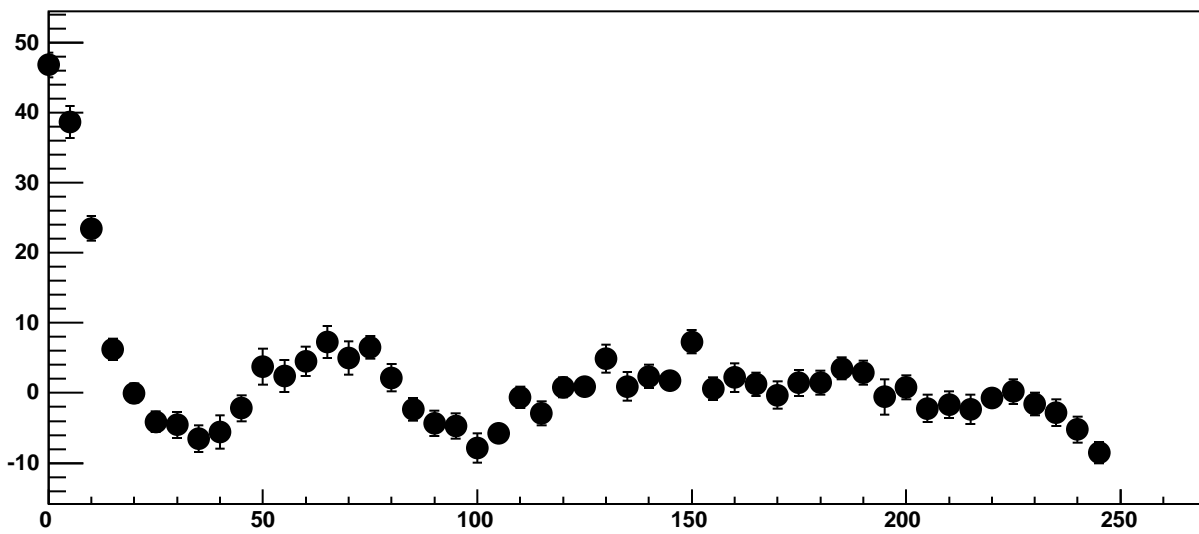


χ^2 / ndf	198.9 / 41
p0	89.33 ± 0.5905
p1	96.93 ± 0.5938
p2	$-4.352e+07 \pm 8.336e+05$
p3	$2.449e+07 \pm 3.849e+05$
p4	1.756 ± 0.01667

Chip 2, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold

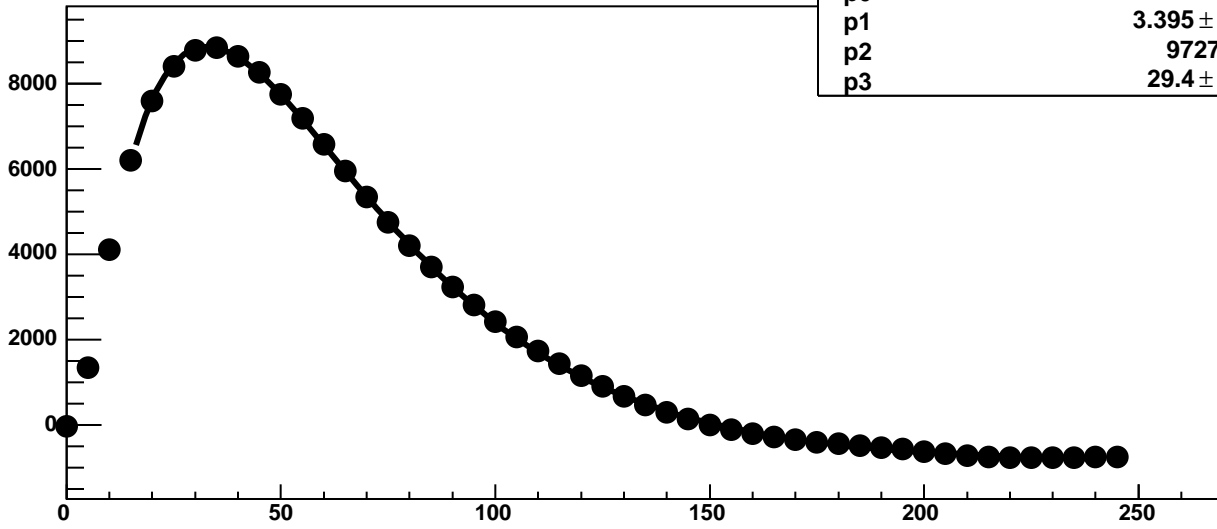


Chip 2, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

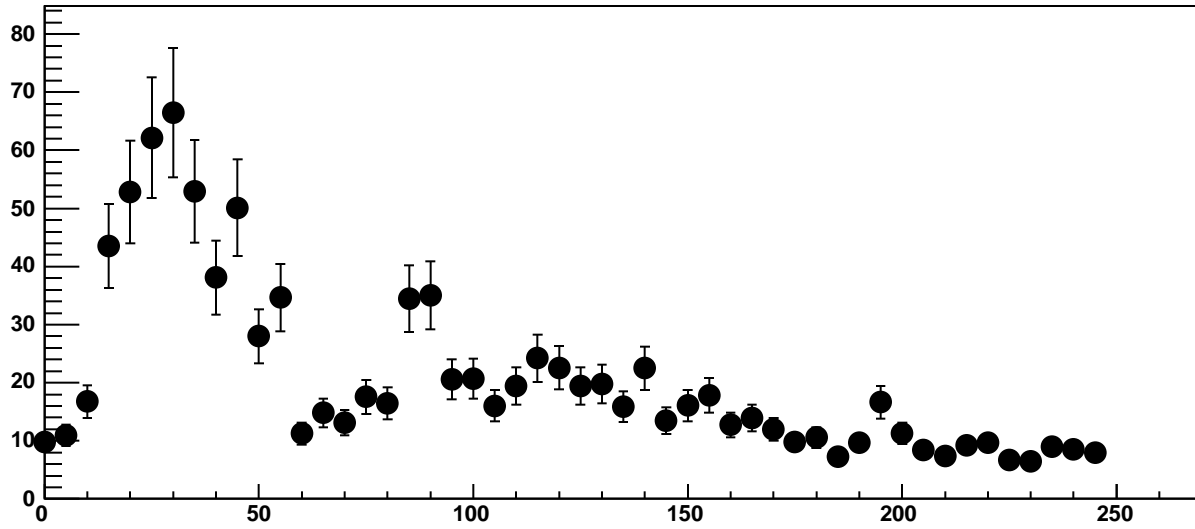


Chip 2, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold

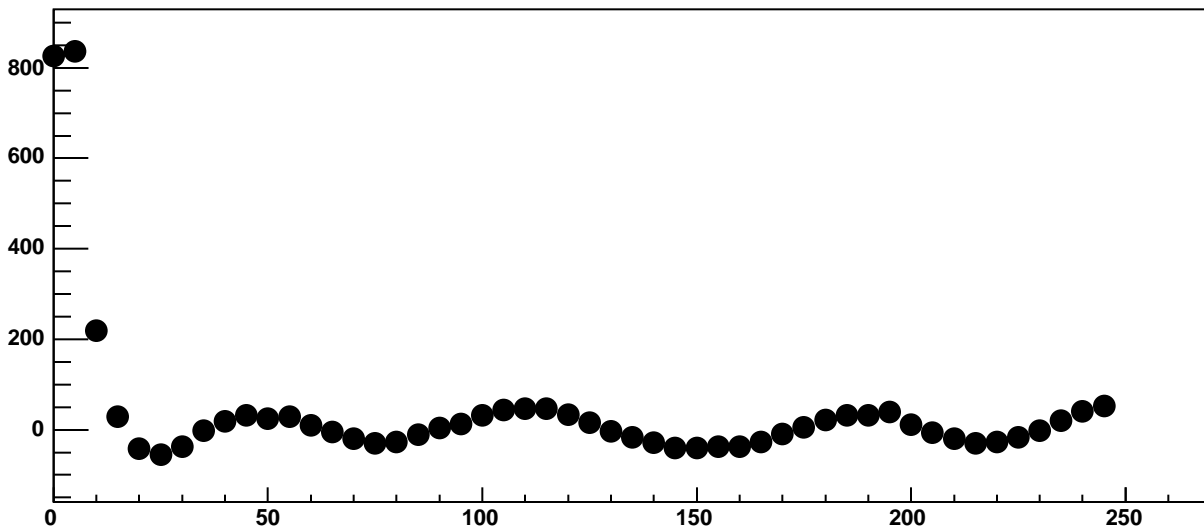
χ^2 / ndf	3268 / 42
p0	-862.6 ± 0.7092
p1	3.395 ± 0.02374
p2	9727 ± 3.105
p3	29.4 ± 0.01268



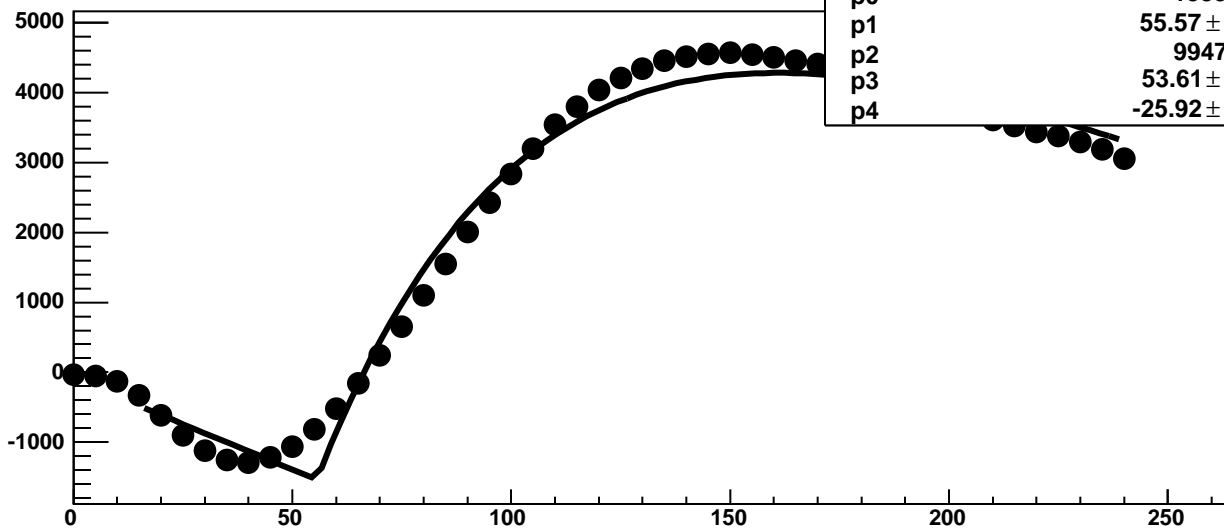
Chip 2, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold

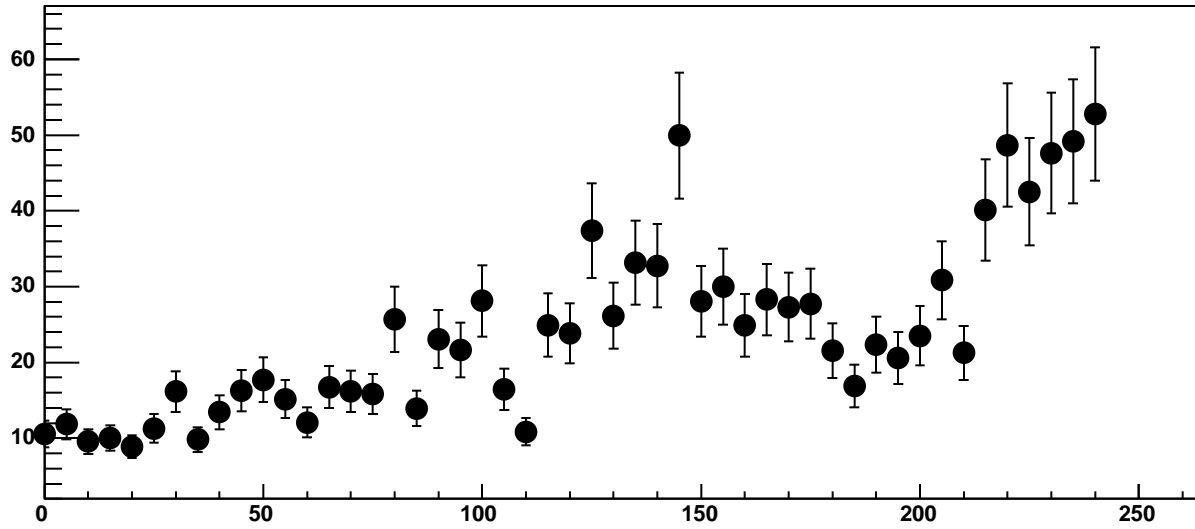


Chip 2, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

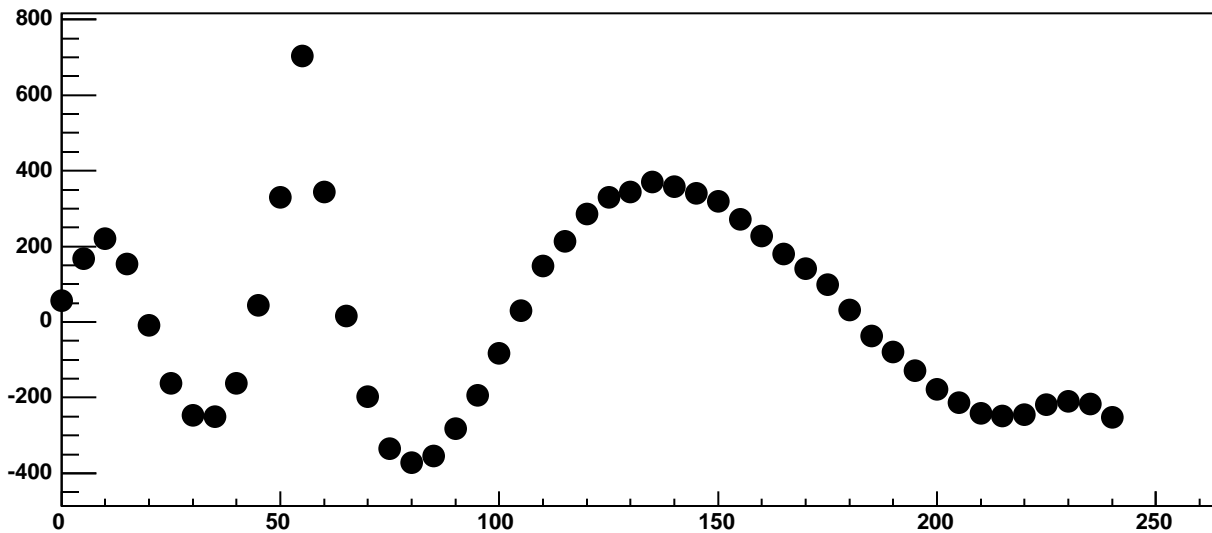


χ^2 / ndf	1.566e+05 / 41
p0	-1535 \pm 1.598
p1	55.57 \pm 0.01827
p2	9947 \pm 13.65
p3	53.61 \pm 0.07515
p4	-25.92 \pm 0.06356

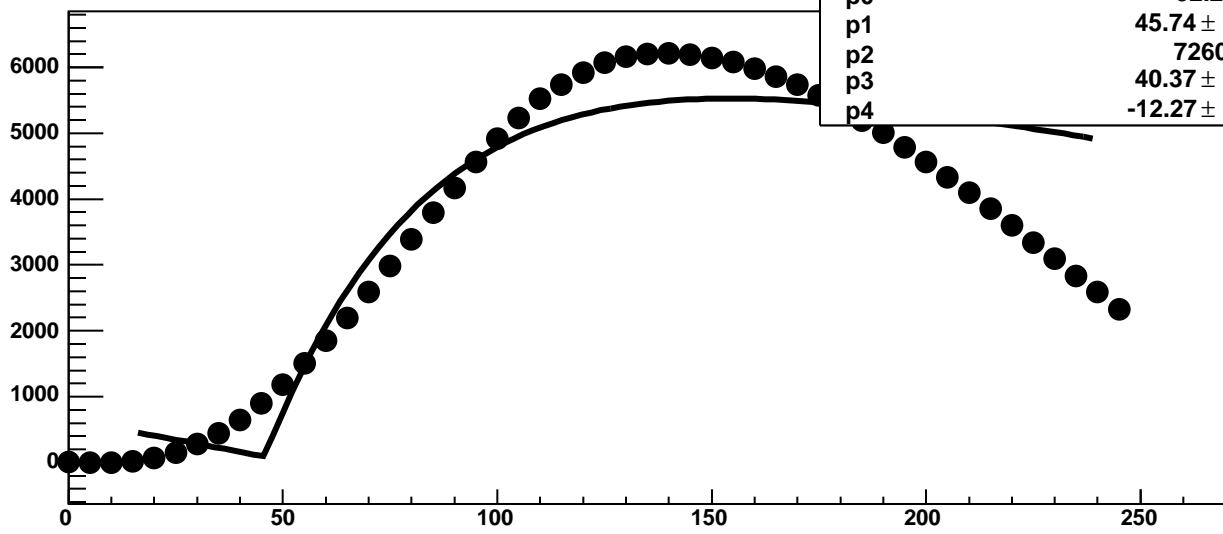
Chip 2, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



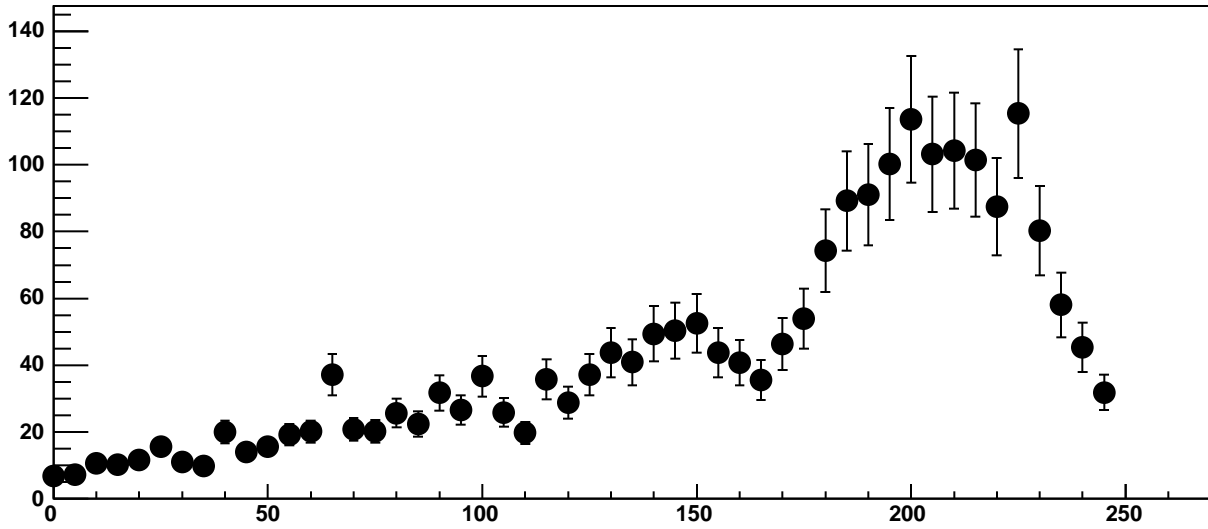
Chip 2, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold



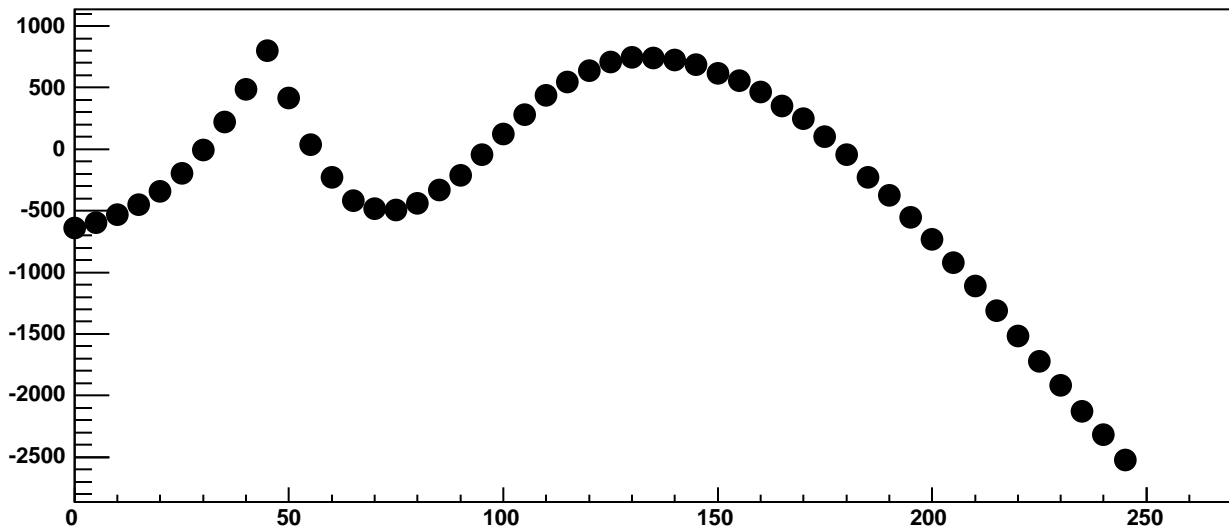
Chip 2, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold



Chip 2, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold

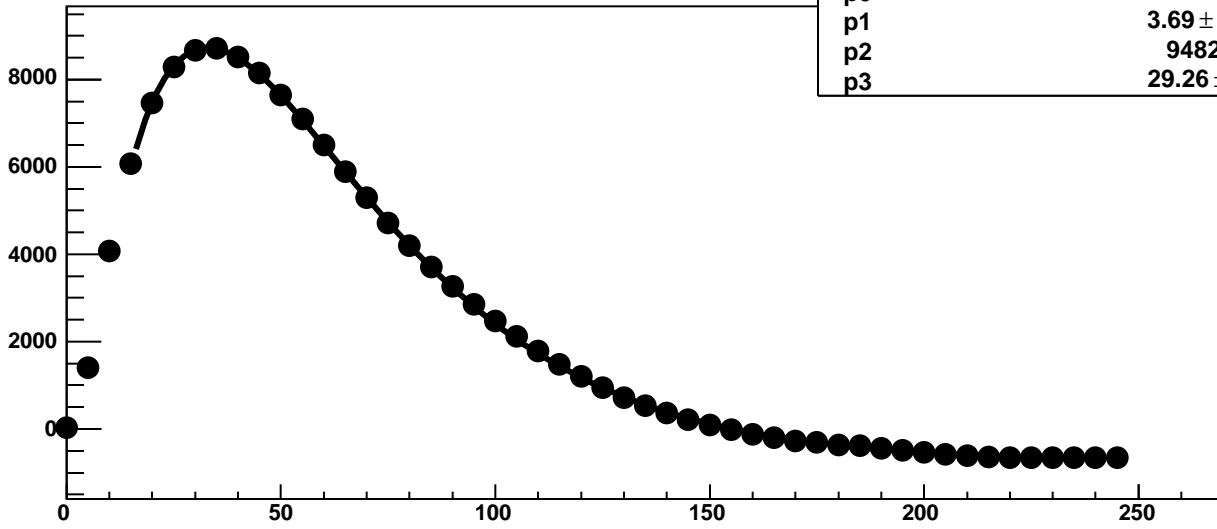


Chip 2, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

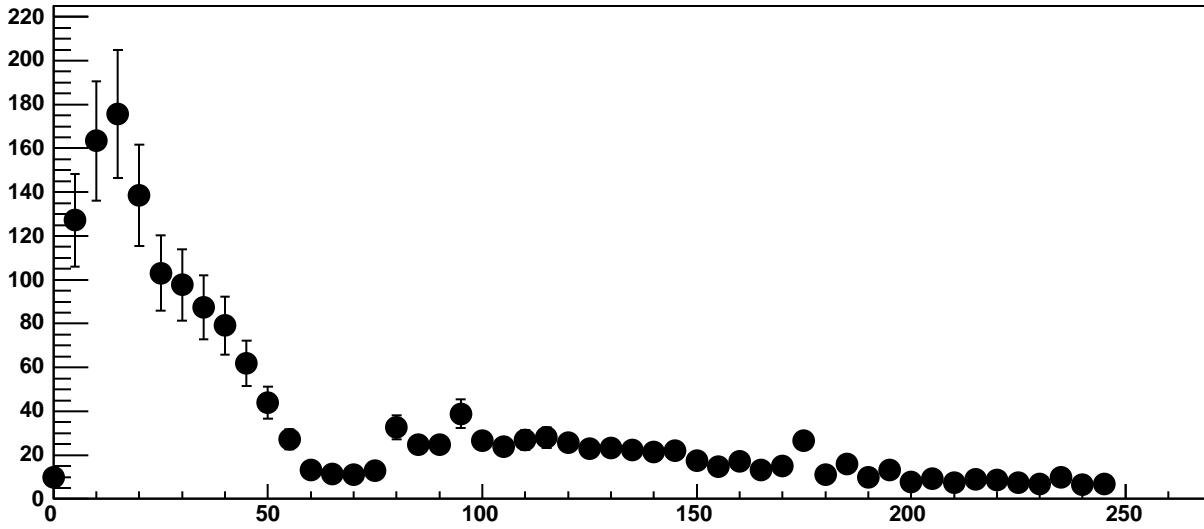


Chip 2, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold

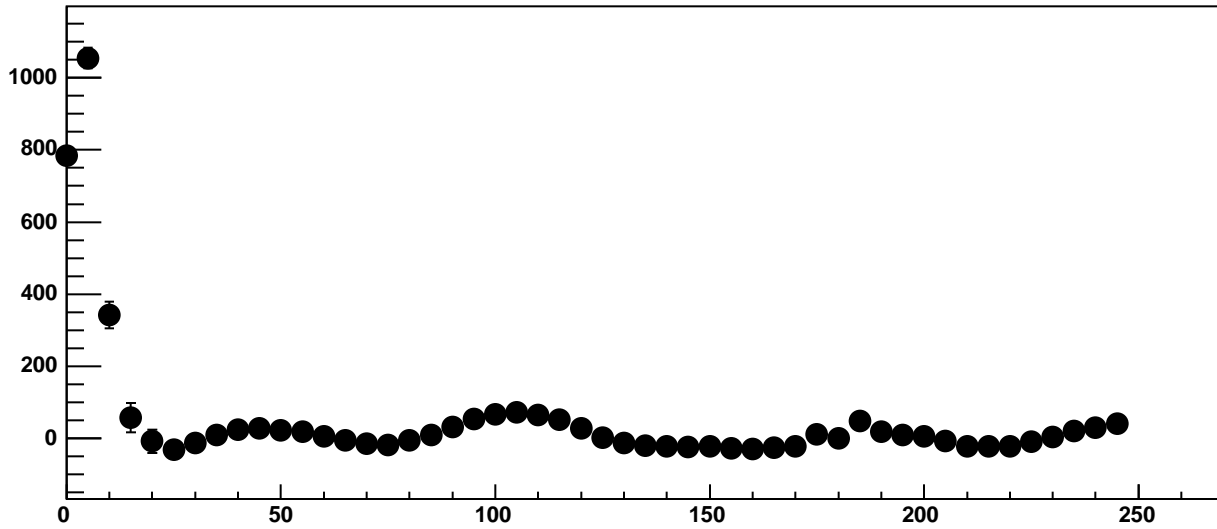
χ^2 / ndf	2190 / 42
p0	-749 ± 0.78
p1	3.69 ± 0.05874
p2	9482 ± 6.339
p3	29.26 ± 0.0188



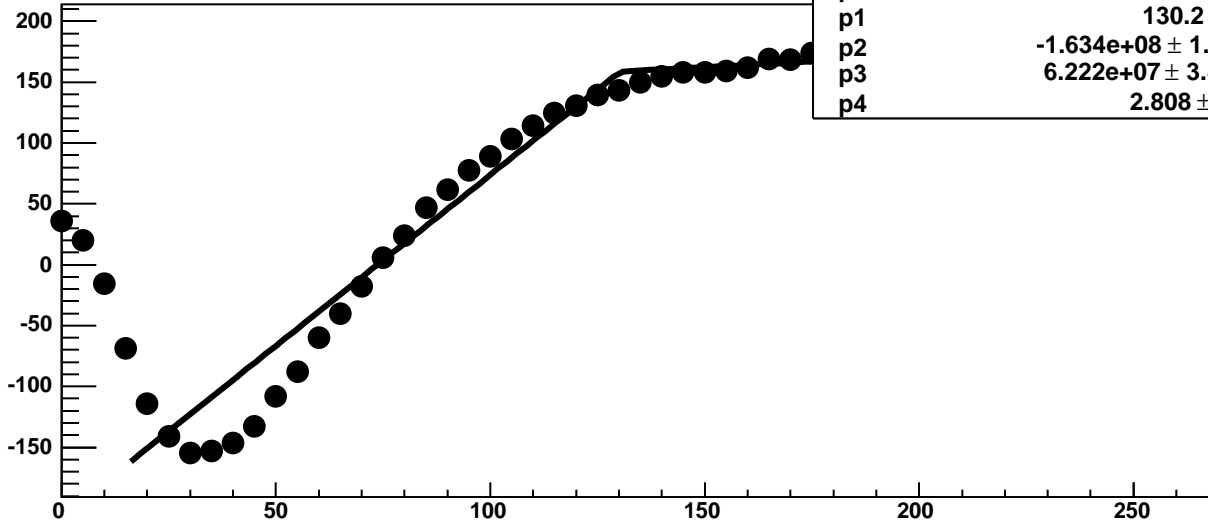
Chip 2, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold

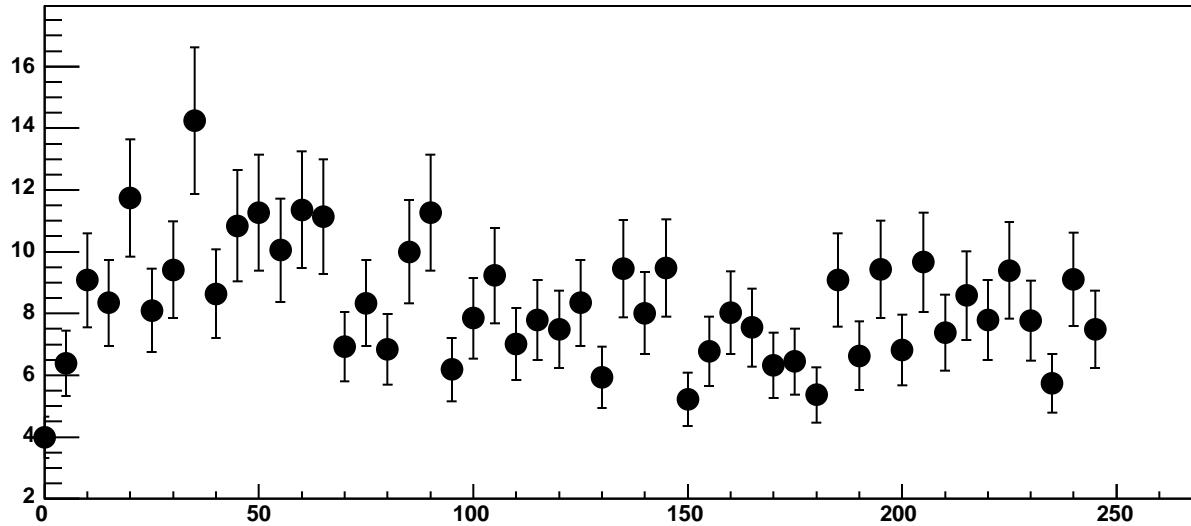


Chip 2, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

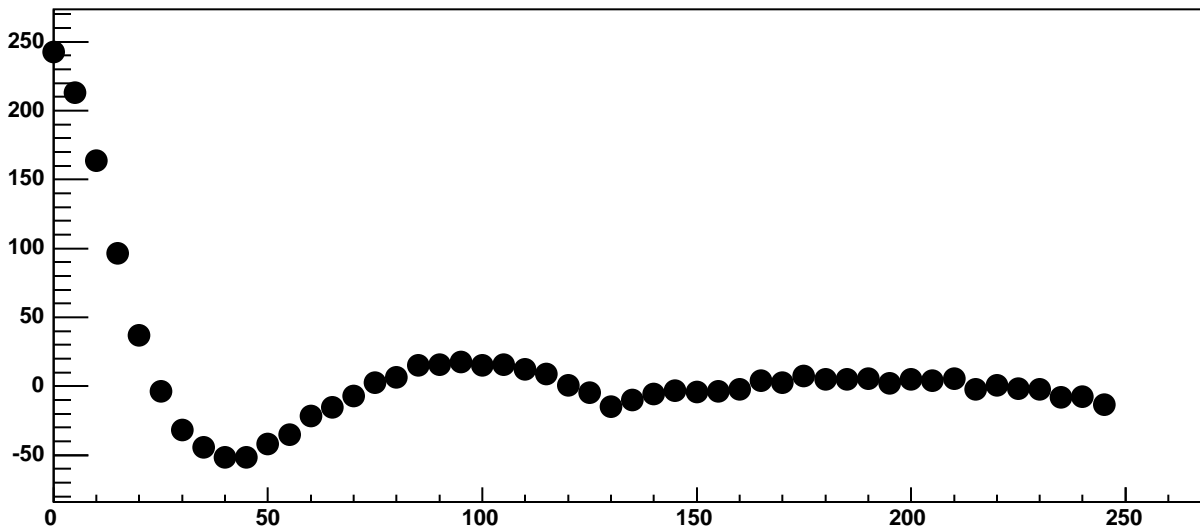


χ^2 / ndf	5639 / 41
p0	158.6 ± 0.8016
p1	130.2 ± 0.3867
p2	$-1.634\text{e}+08 \pm 1.448\text{e}+06$
p3	$6.222\text{e}+07 \pm 3.866\text{e}+05$
p4	2.808 ± 0.01127

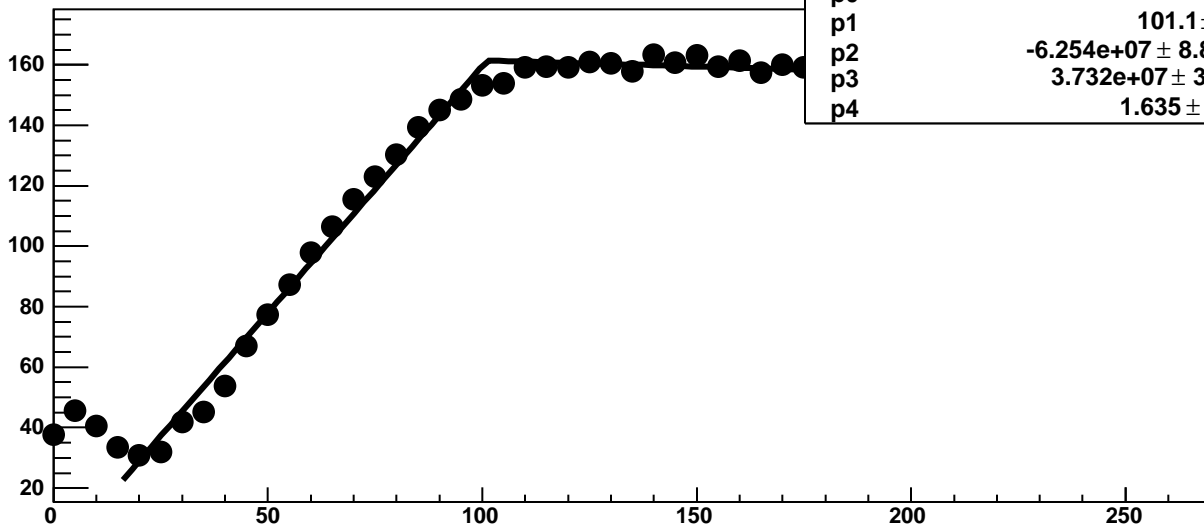
Chip 2, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

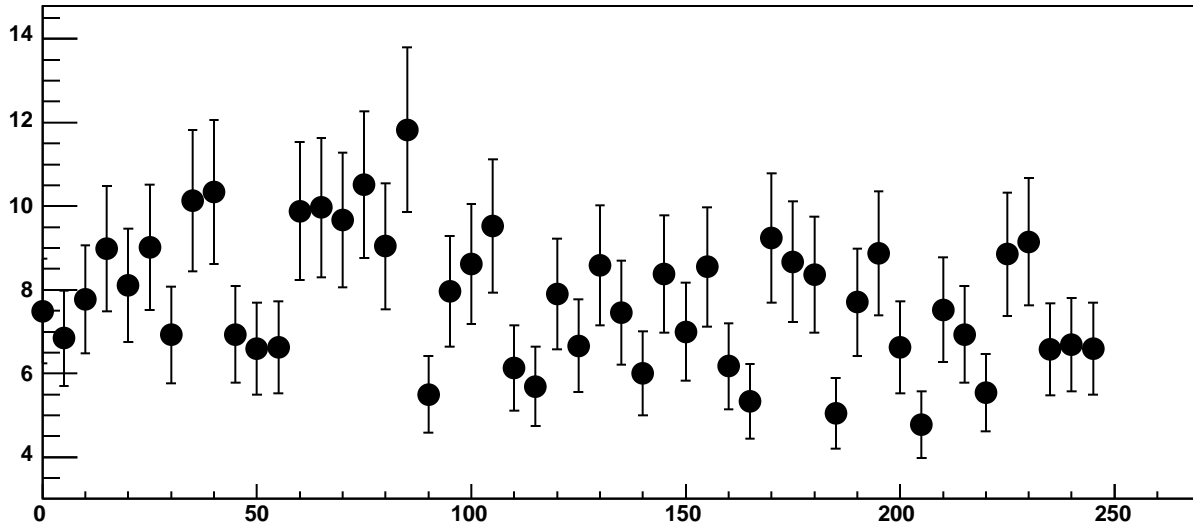


Chip 2, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

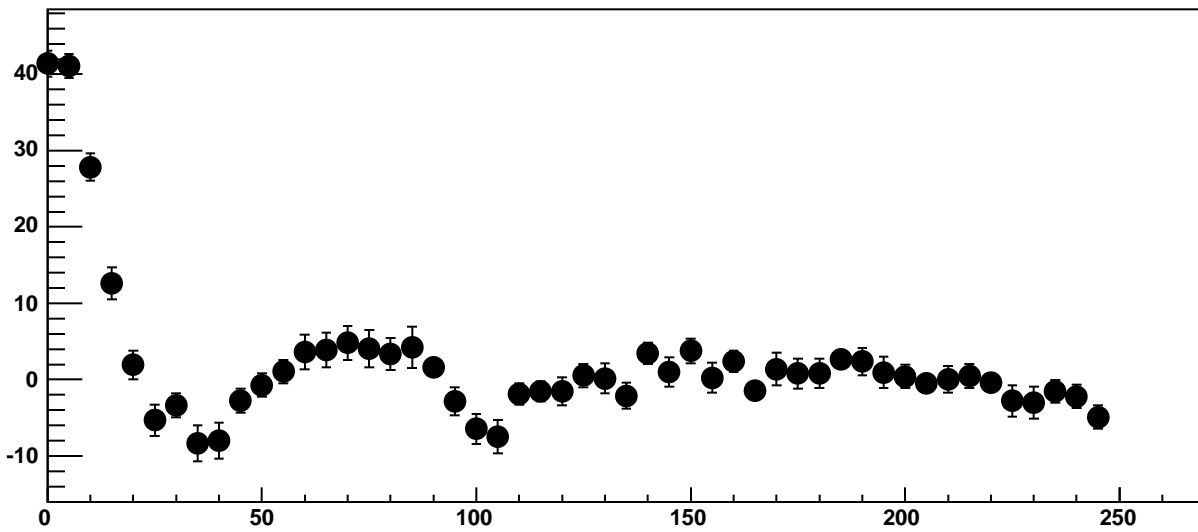


χ^2 / ndf	159 / 41
p0	161.5 ± 0.6057
p1	101.1 ± 0.6329
p2	$-6.254e+07 \pm 8.885e+05$
p3	$3.732e+07 \pm 3.51e+05$
p4	1.635 ± 0.01734

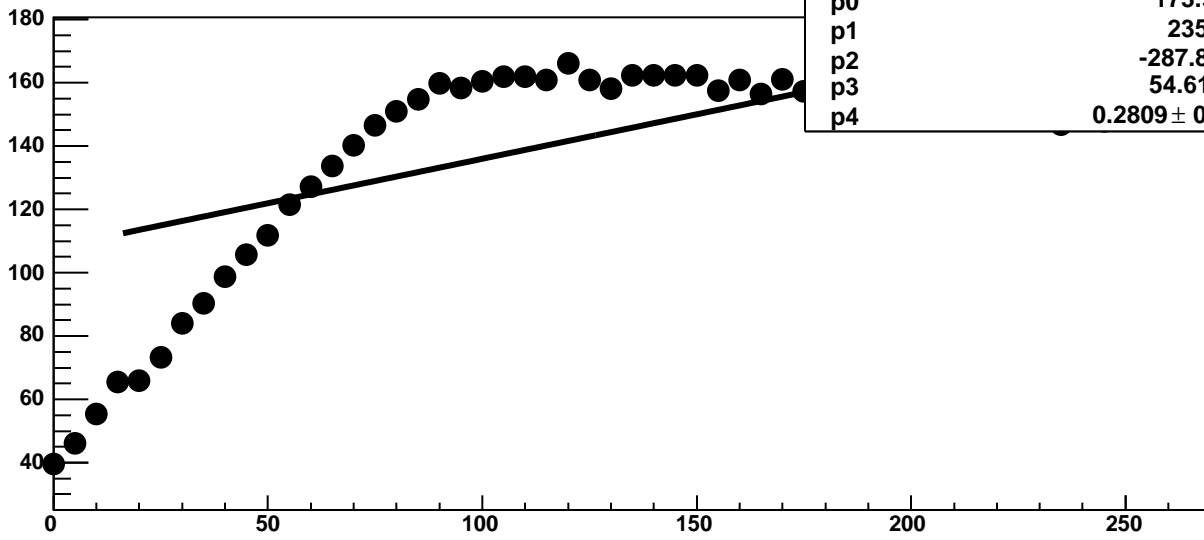
Chip 2, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

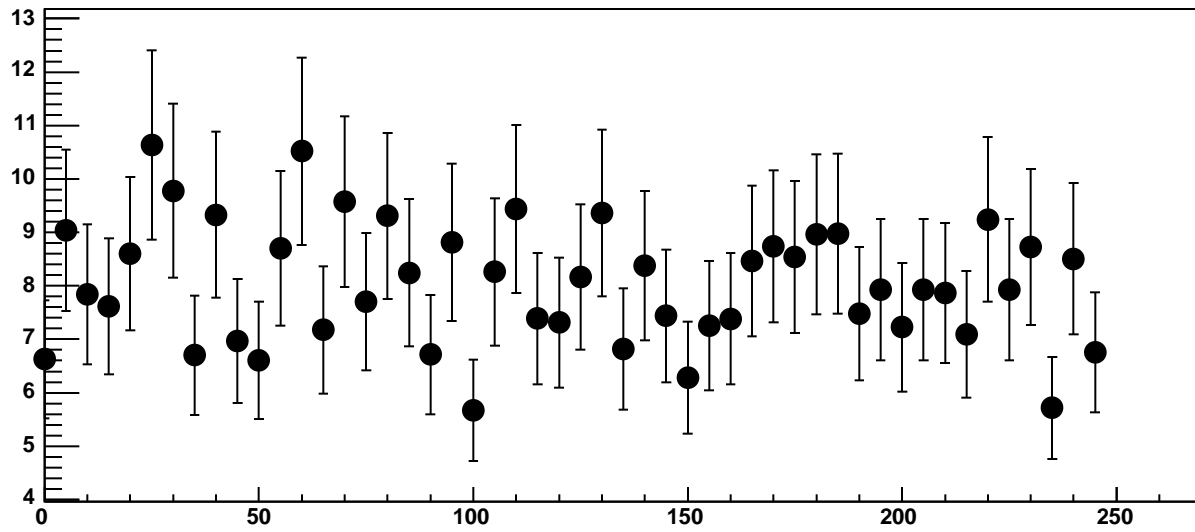


Chip 2, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold

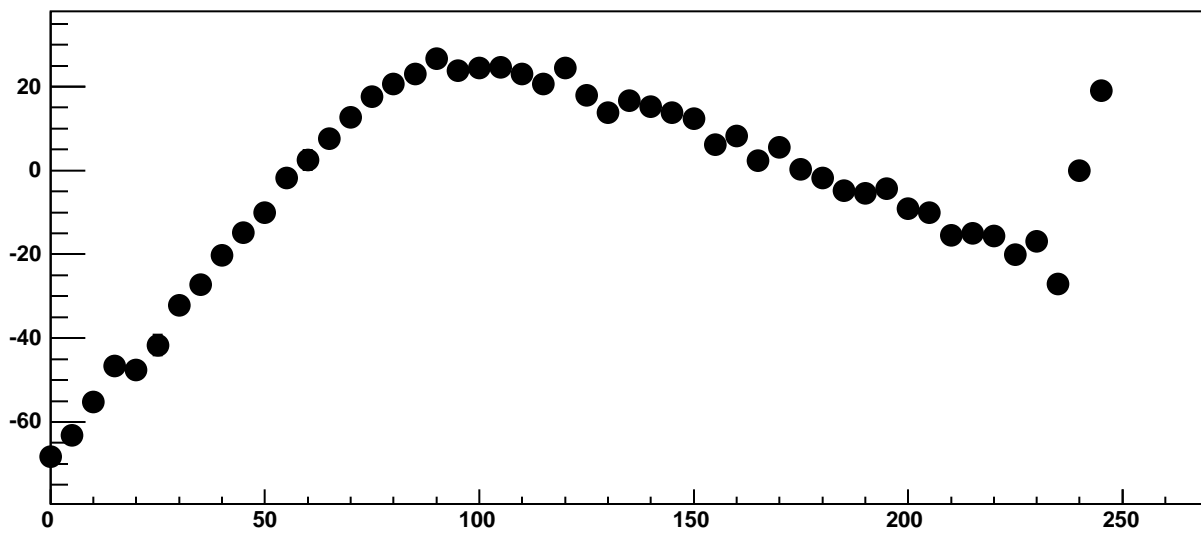


χ^2 / ndf	5563 / 41
p0	173.9 ± 1.33
p1	235 ± 4.385
p2	-287.8 ± 234.2
p3	54.61 ± 38.23
p4	0.2809 ± 0.004136

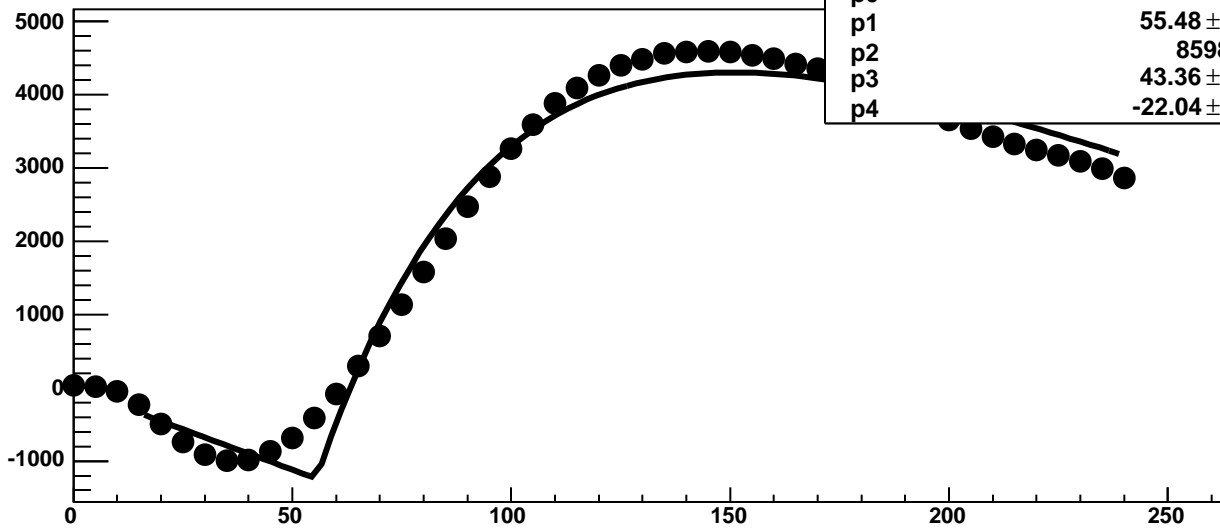
Chip 2, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold



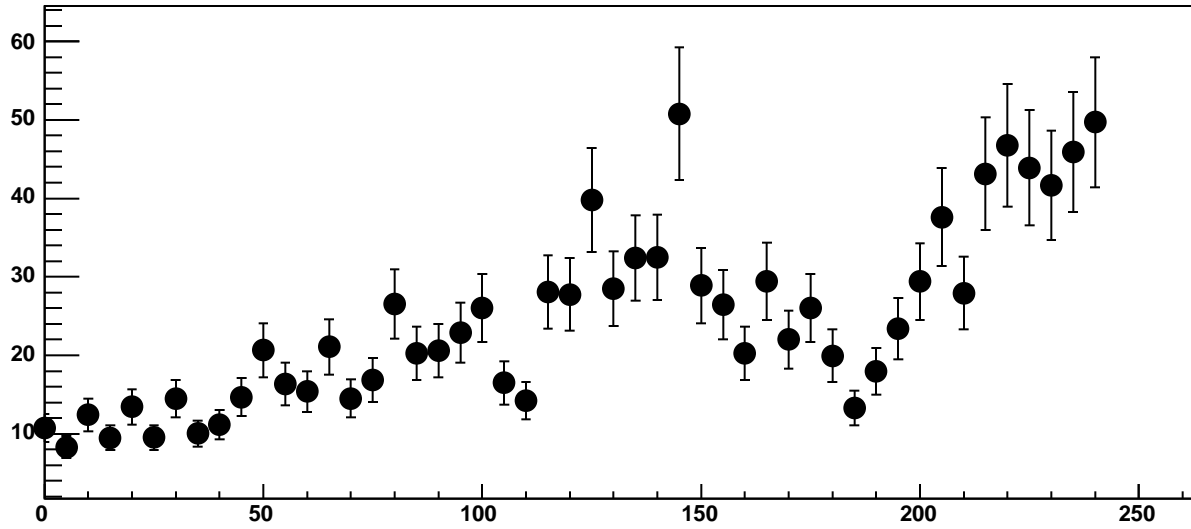
Chip 2, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold



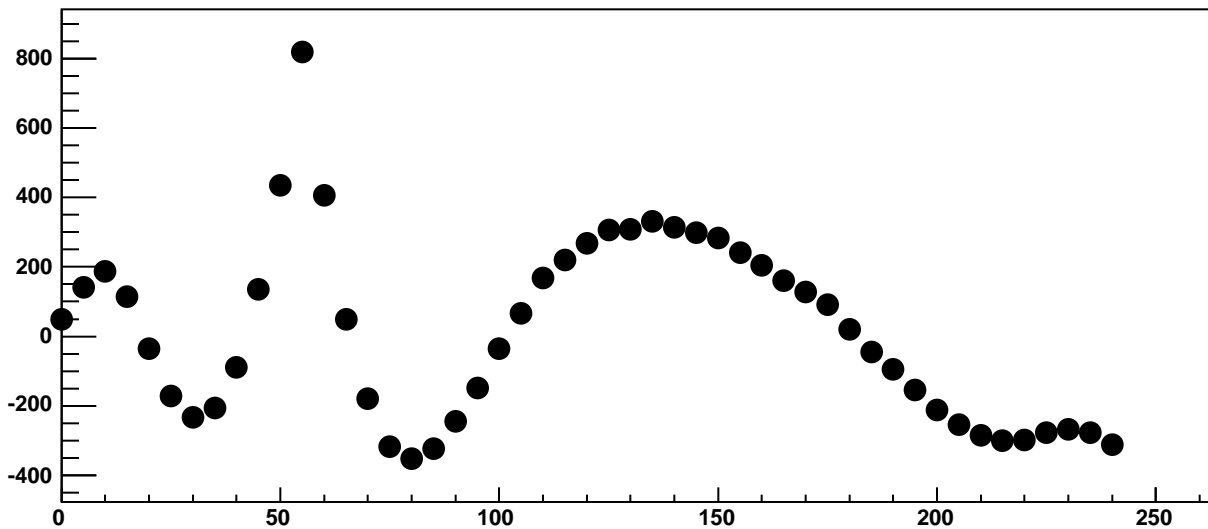
Chip 2, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold



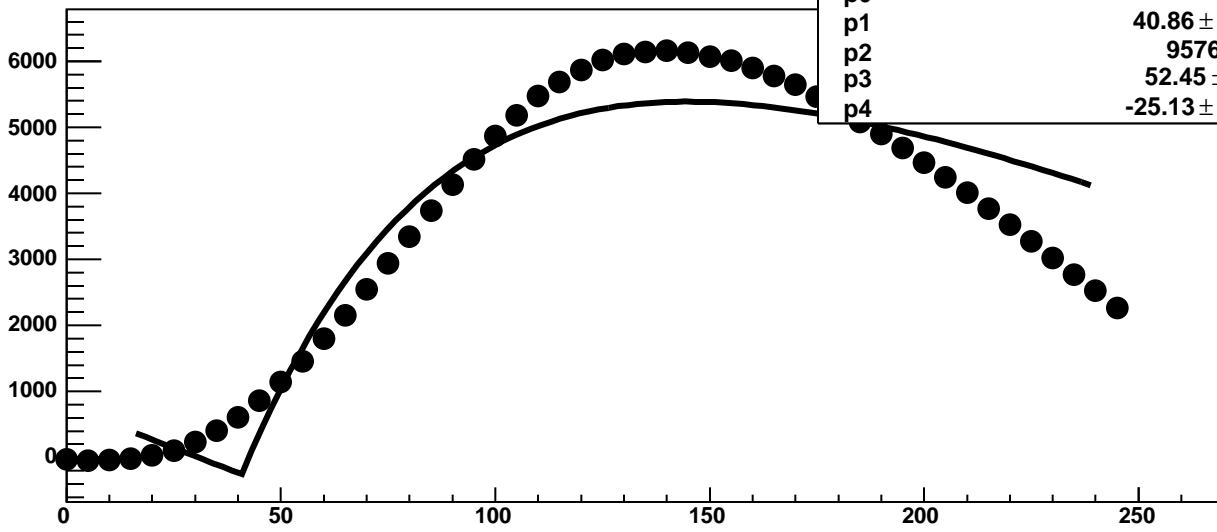
Chip 2, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

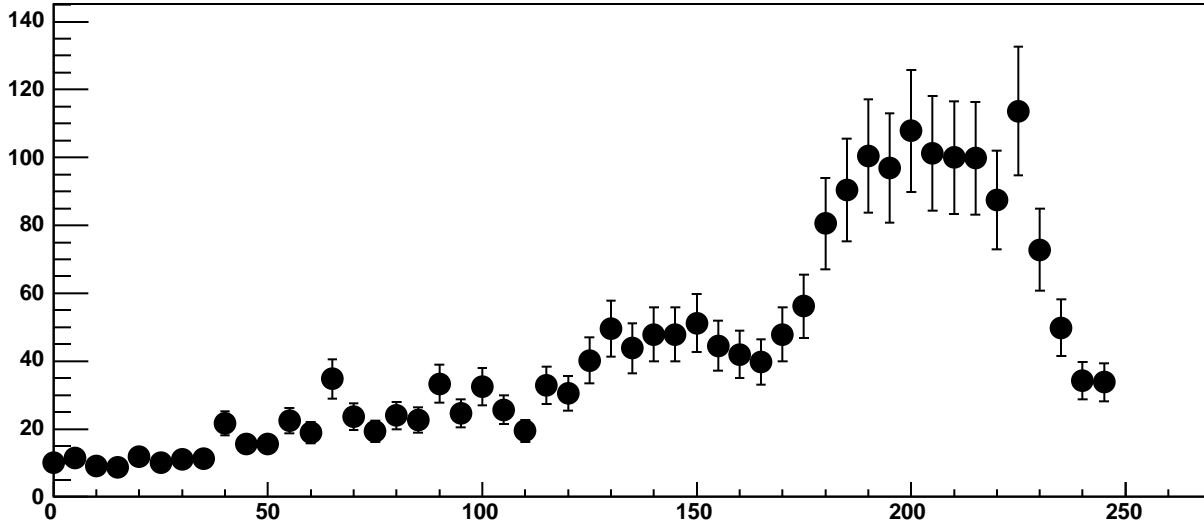


Chip 2, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

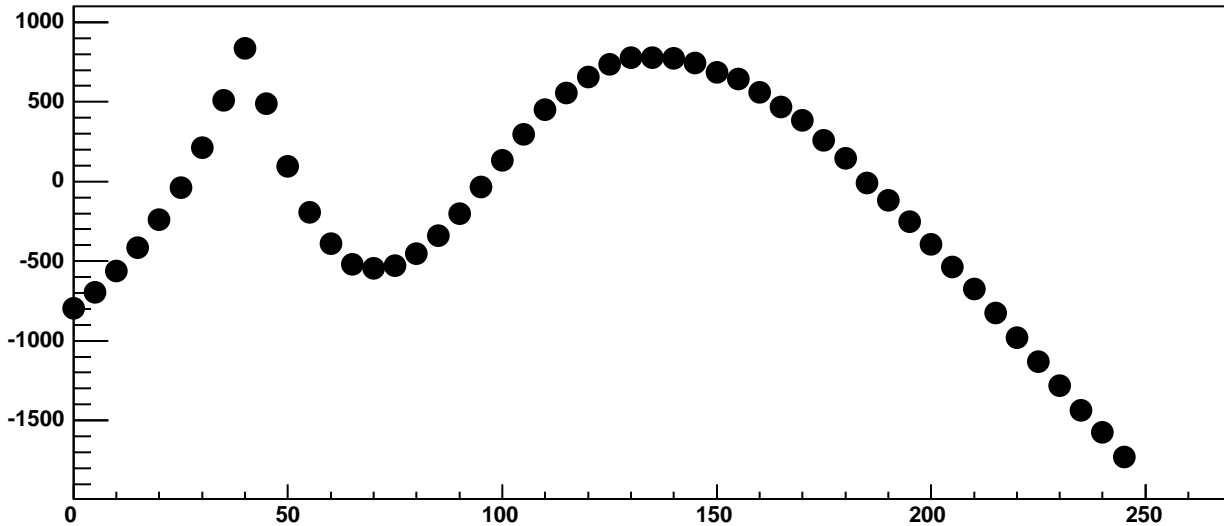


χ^2 / ndf	3.345e+05 / 41
p0	-255 ± 1.453
p1	40.86 ± 0.01925
p2	9576 ± 15.84
p3	52.45 ± 0.0896
p4	-25.13 ± 0.07798

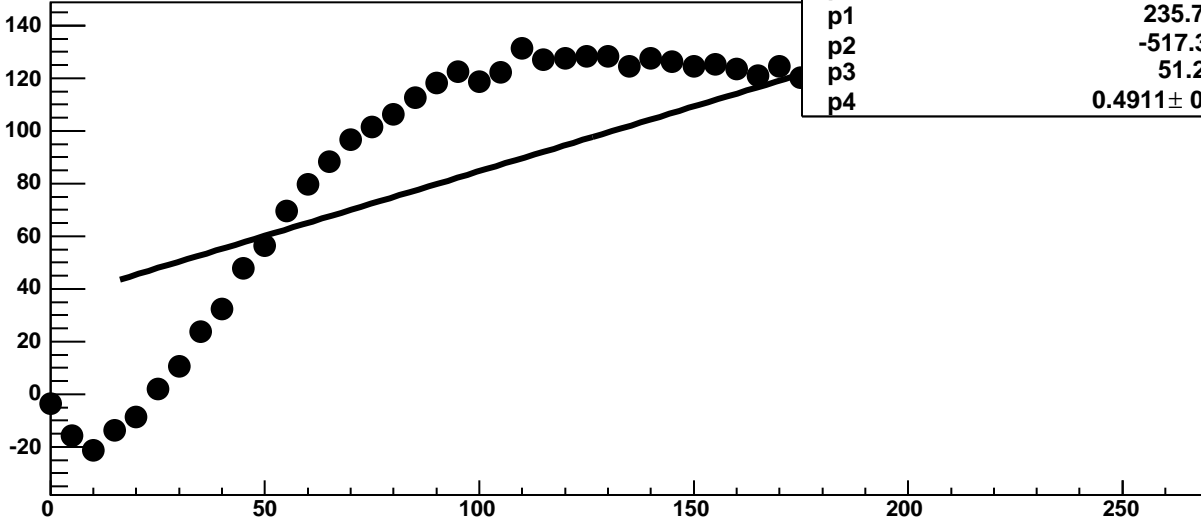
Chip 2, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold

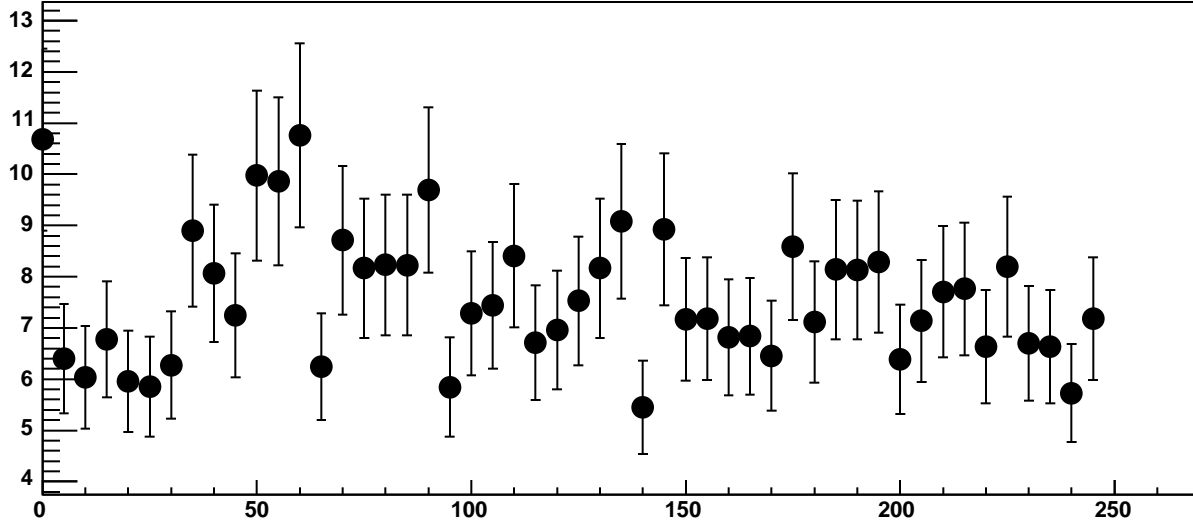


Chip 2, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

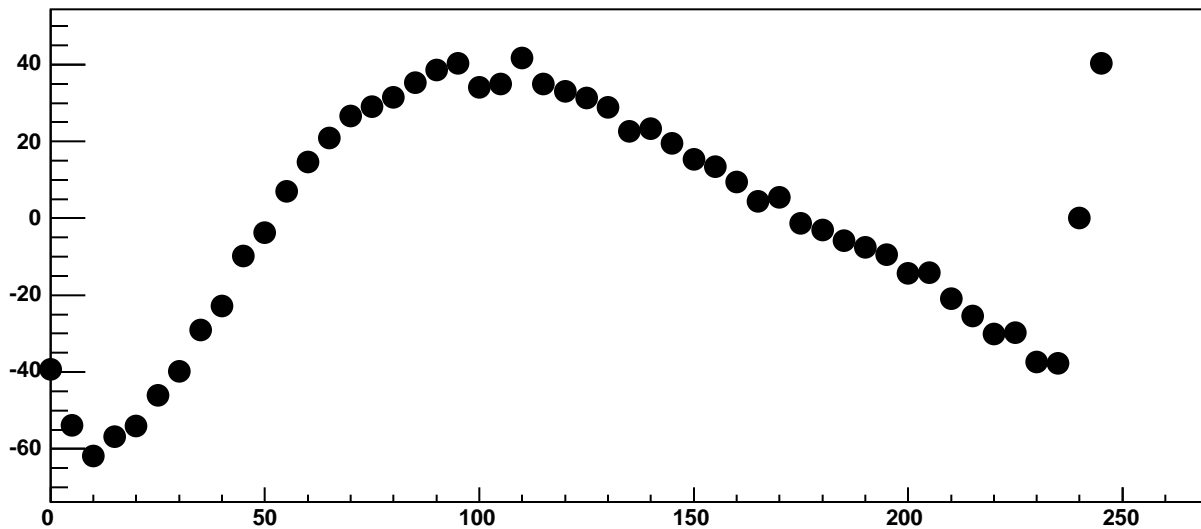


χ^2 / ndf	1.374e+04 / 41
p0	151.4 ± 1.045
p1	235.7 ± 1.896
p2	-517.3 ± 211.2
p3	51.26 ± 18
p4	0.4911 ± 0.003777

Chip 2, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold

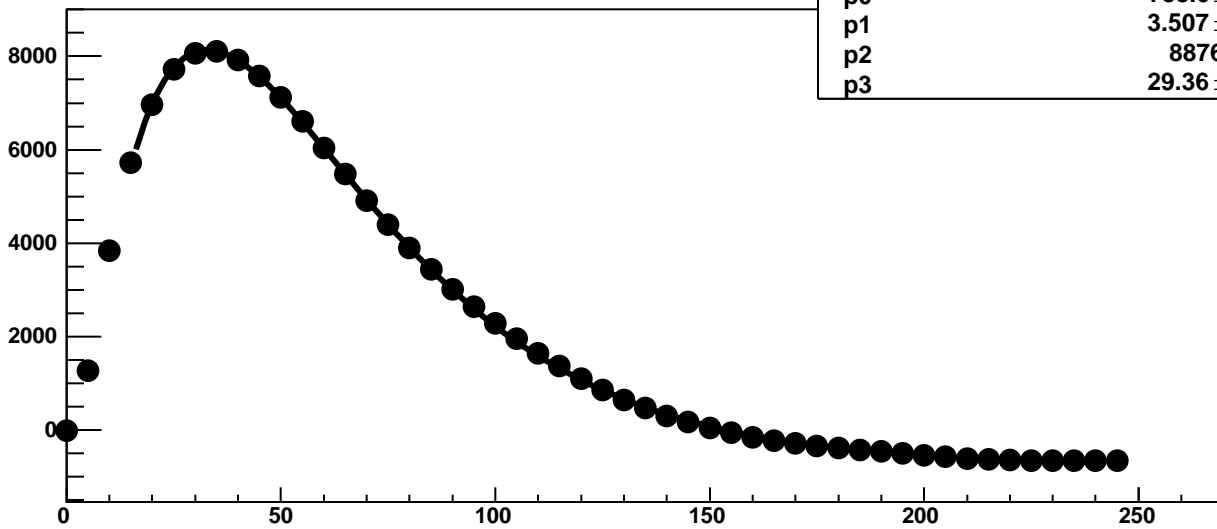


Chip 2, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

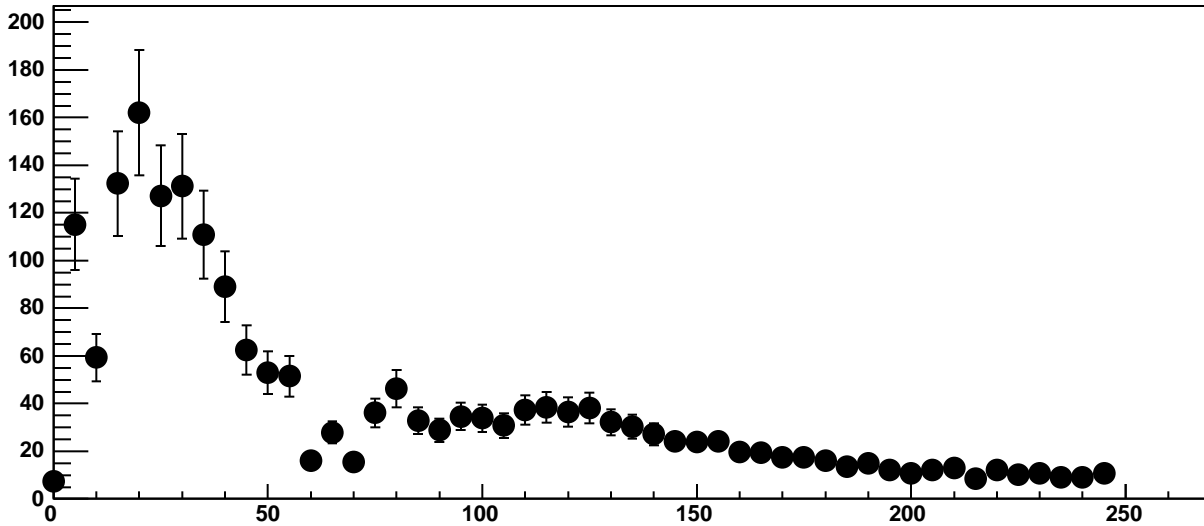


Chip 2, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold

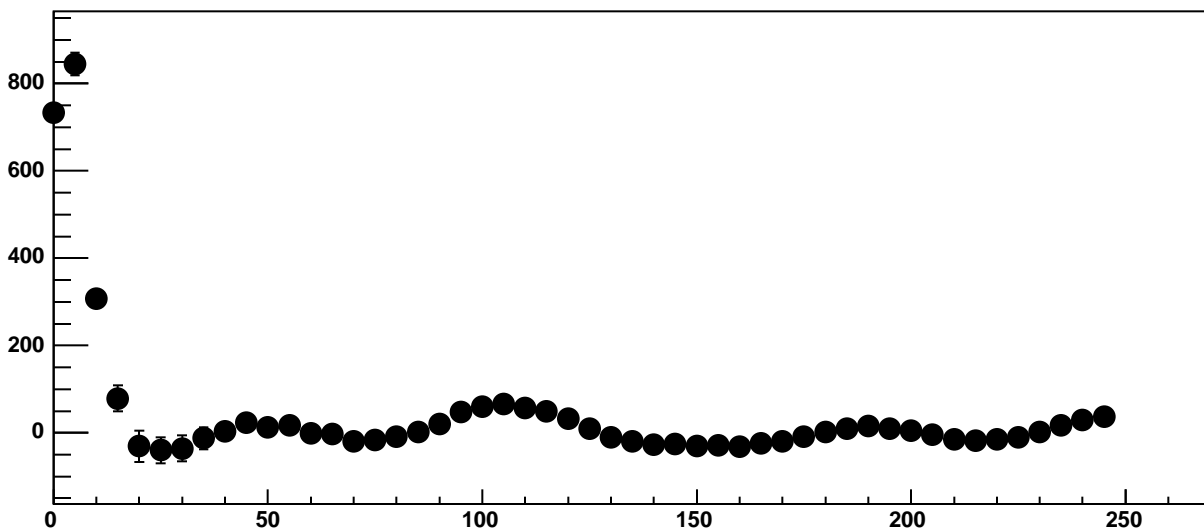
χ^2 / ndf	1035 / 42
p0	-738.6 ± 0.9828
p1	3.507 ± 0.0632
p2	8876 ± 6.771
p3	29.36 ± 0.0239



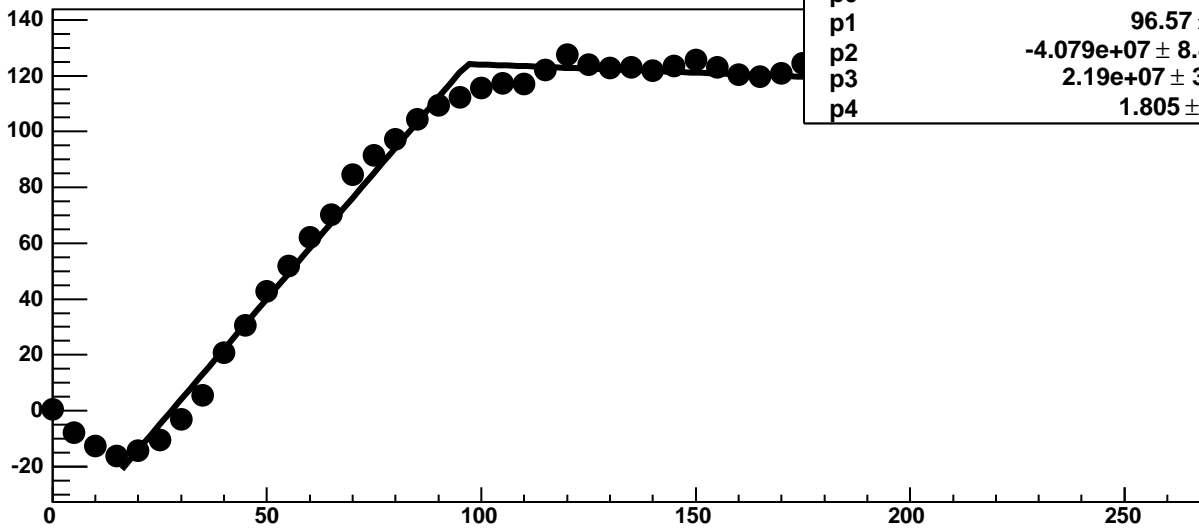
Chip 2, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold

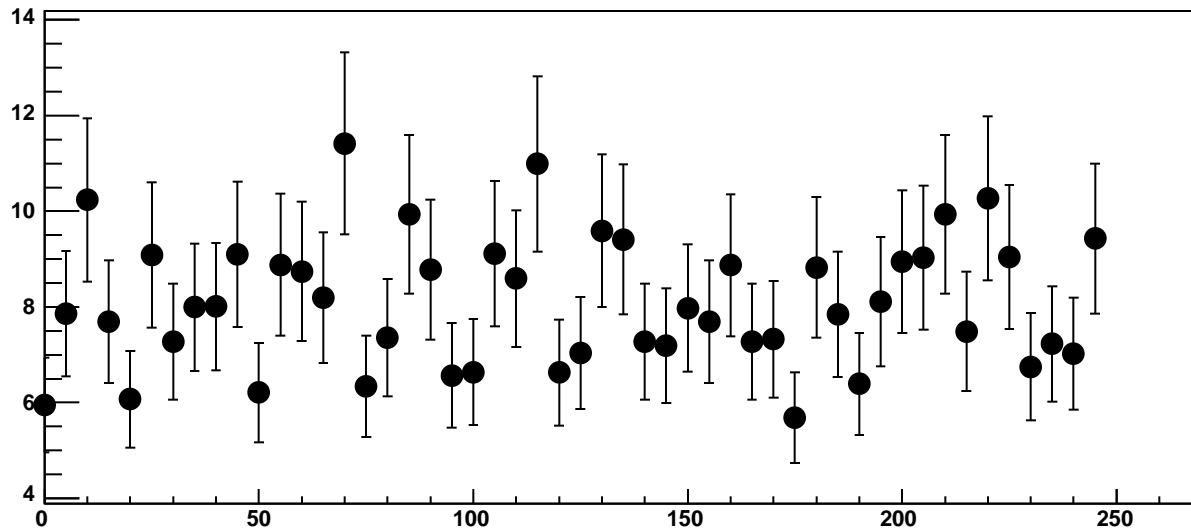


Chip 2, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

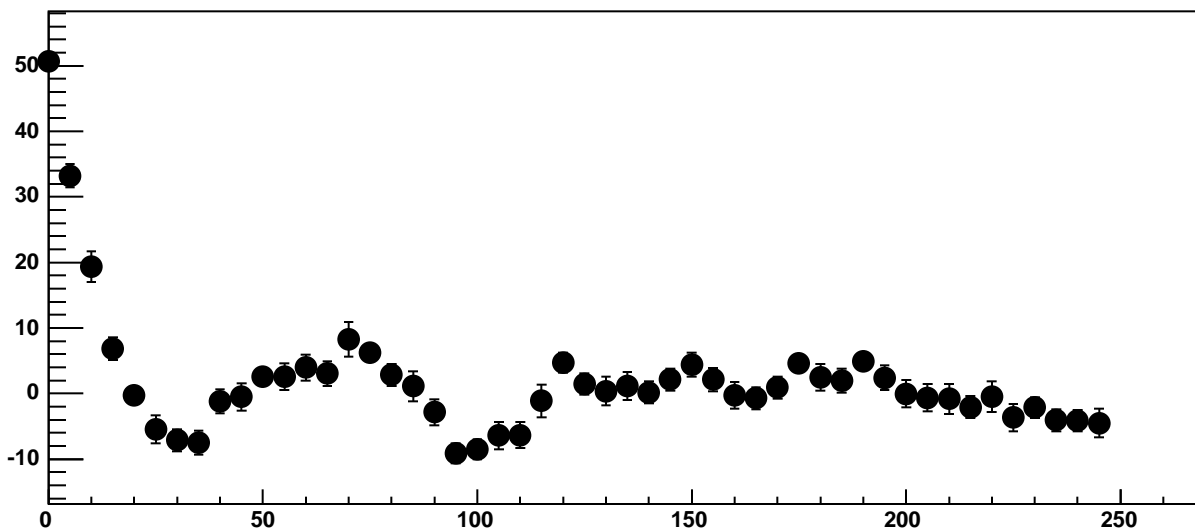


χ^2 / ndf	257.4 / 41
p0	124.2 ± 0.6547
p1	96.57 ± 0.5805
p2	$-4.079\text{e}+07 \pm 8.449\text{e}+05$
p3	$2.19\text{e}+07 \pm 3.98\text{e}+05$
p4	1.805 ± 0.01703

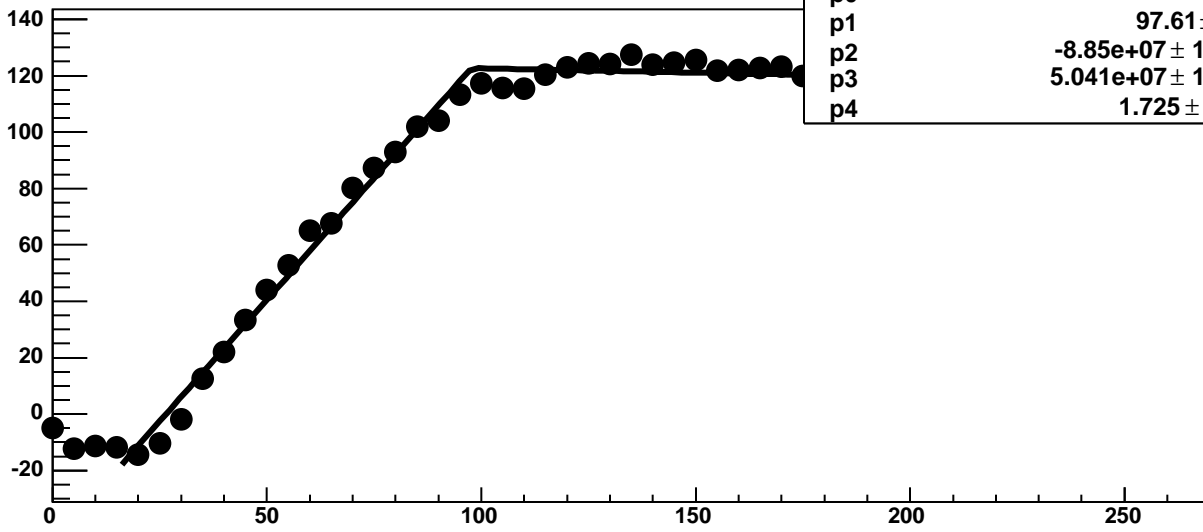
Chip 2, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

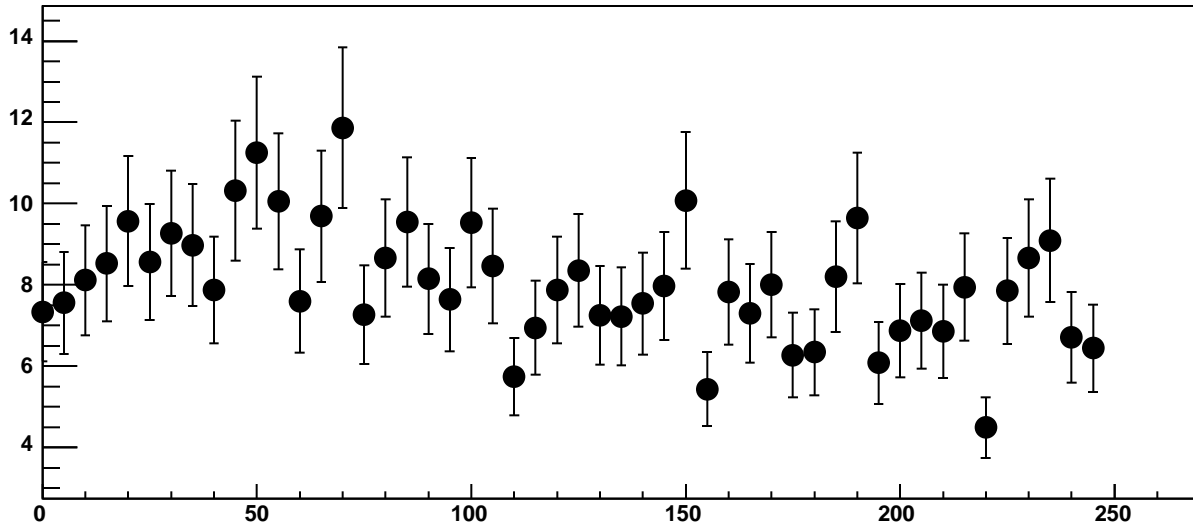


Chip 2, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

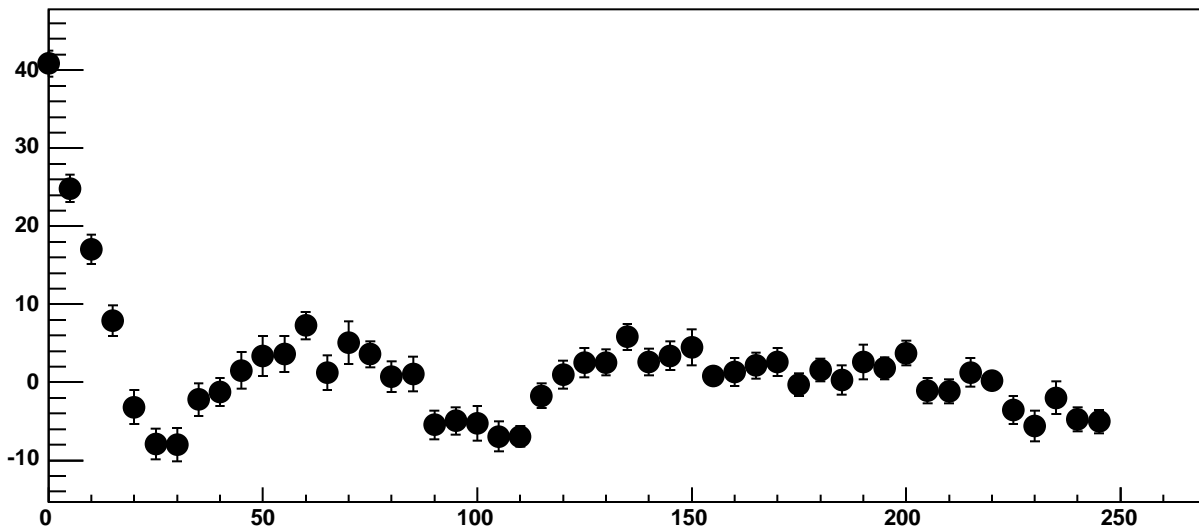


χ^2 / ndf	210 / 41
p0	122.7 ± 0.6246
p1	97.61 ± 0.6509
p2	$-8.85e+07 \pm 1.08e+06$
p3	$5.041e+07 \pm 1.97e+05$
p4	1.725 ± 0.01947

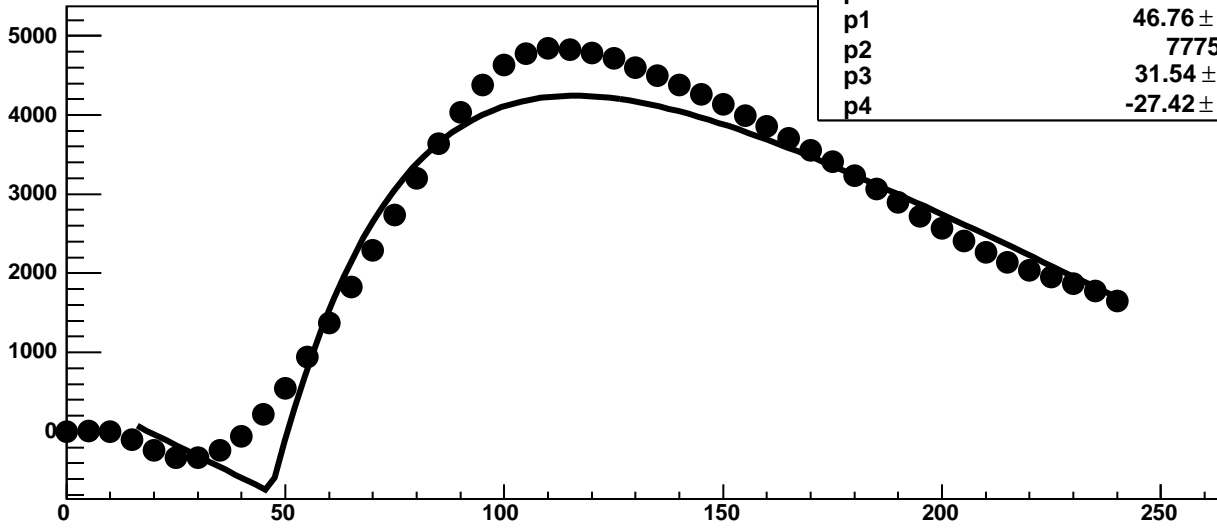
Chip 2, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

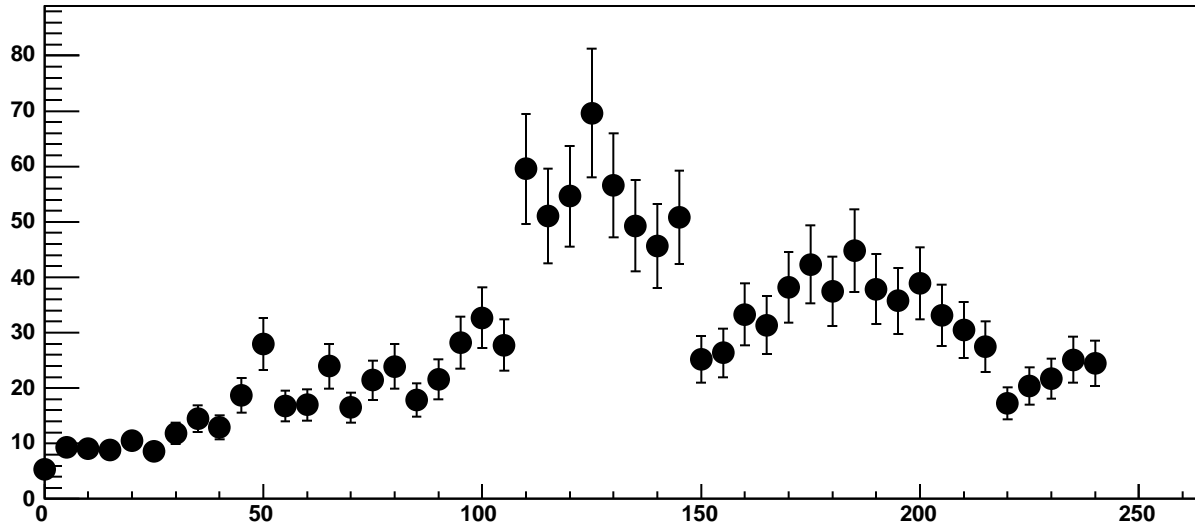


Chip 2, Channel 15, Enable 5, DAC=4000, ADC Mean vs Hold

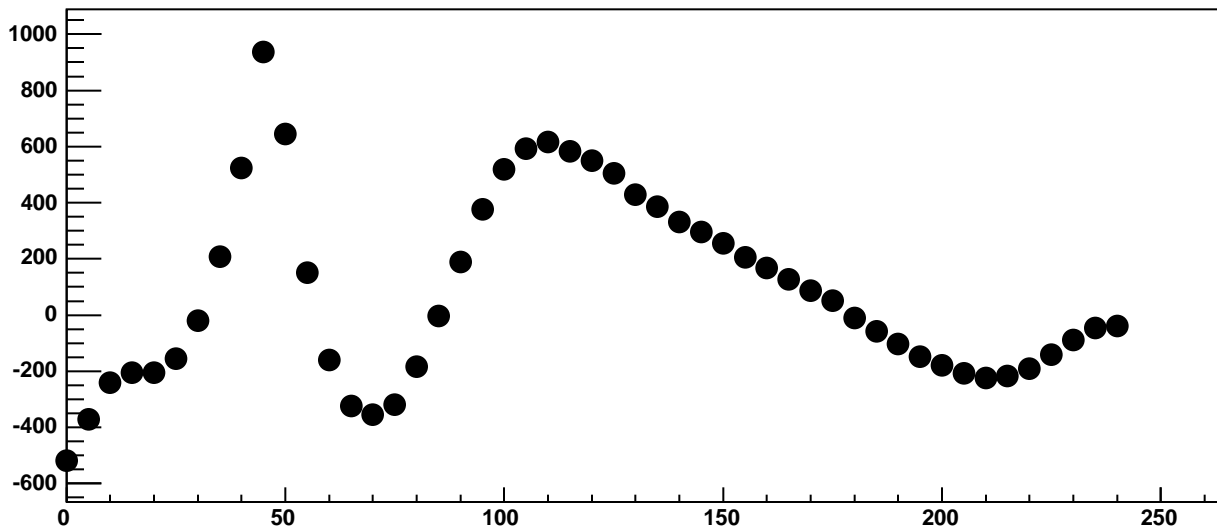


χ^2 / ndf	1.783e+05 / 41
p0	-768 ± 1.246
p1	46.76 ± 0.01994
p2	7775 ± 8.749
p3	31.54 ± 0.05341
p4	-27.42 ± 0.04623

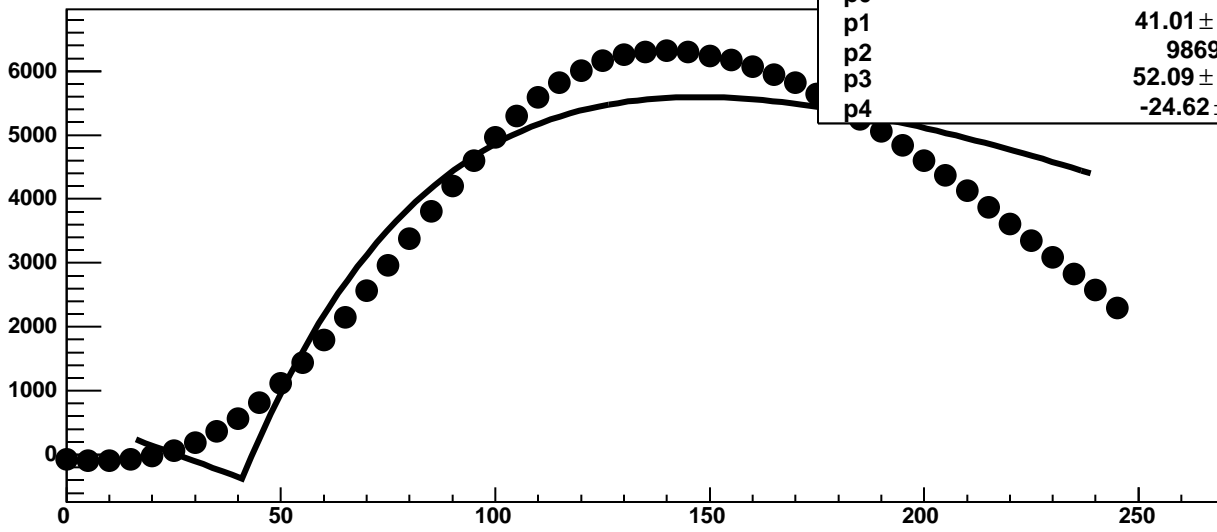
Chip 2, Channel 15, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 15, Enable 5, DAC=4000, ADC Residuals vs Hold

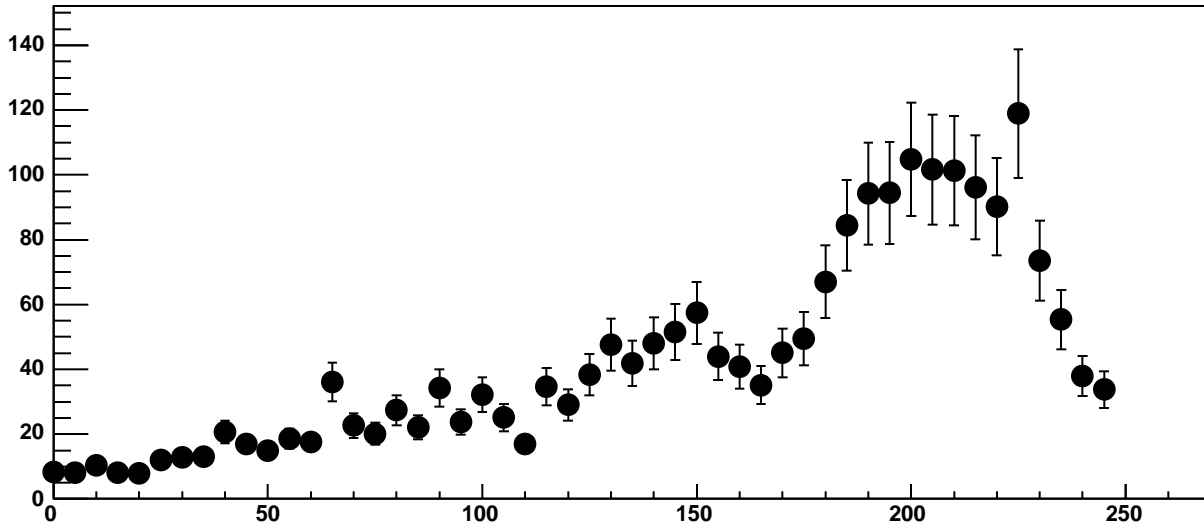


Chip 2, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

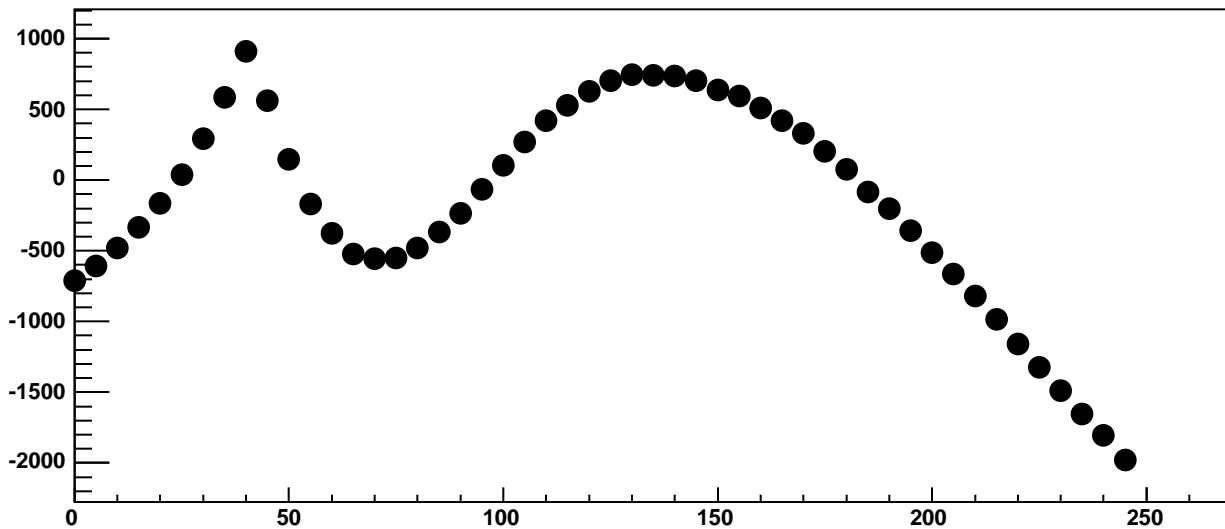


χ^2 / ndf	3.458e+05 / 41
p0	-374.7 ± 1.56
p1	41.01 ± 0.01905
p2	9869 ± 16.15
p3	52.09 ± 0.08754
p4	-24.62 ± 0.0798

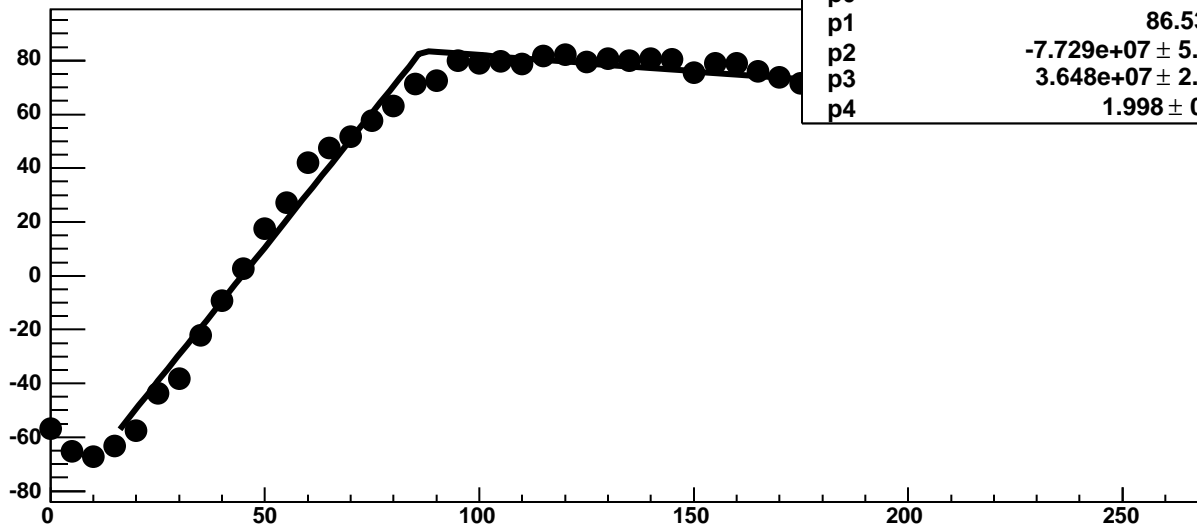
Chip 2, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

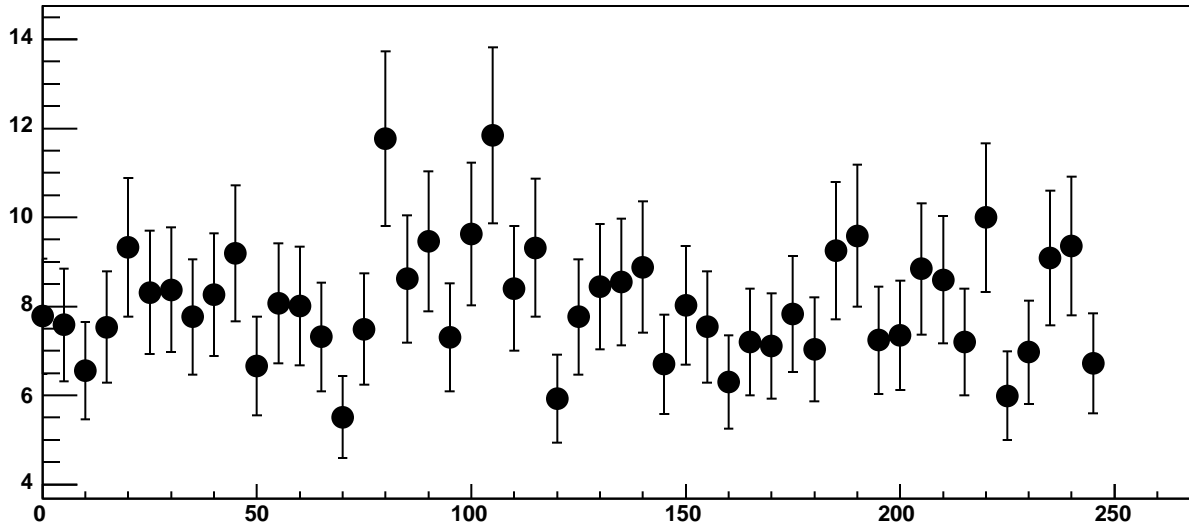


Chip 2, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold

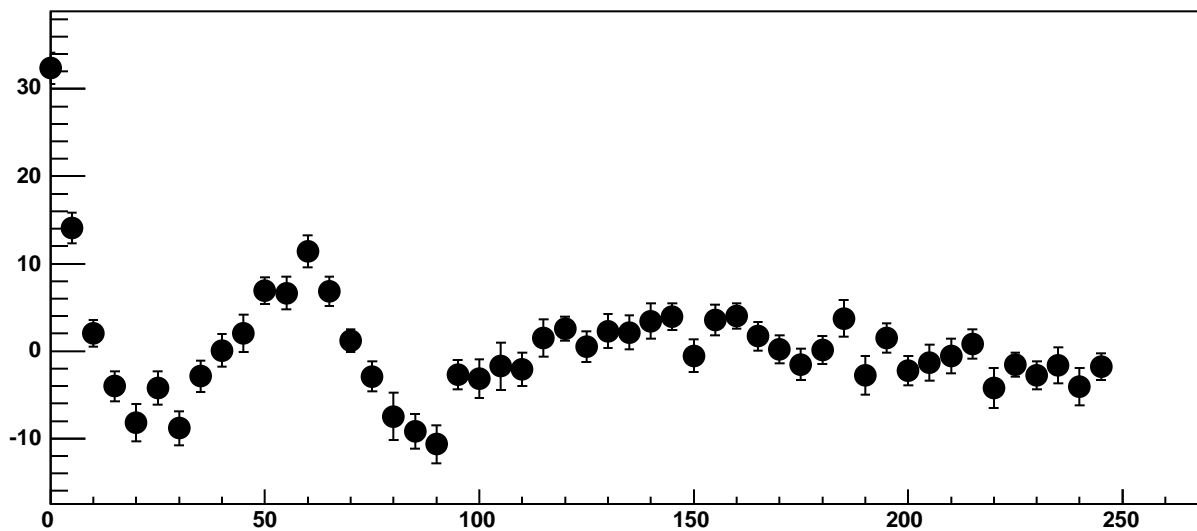


χ^2 / ndf	251.6 / 41
p0	83.63 ± 0.5669
p1	86.53 ± 0.441
p2	$-7.729\text{e}+07 \pm 5.689\text{e}+08$
p3	$3.648\text{e}+07 \pm 2.685\text{e}+08$
p4	1.998 ± 0.006713

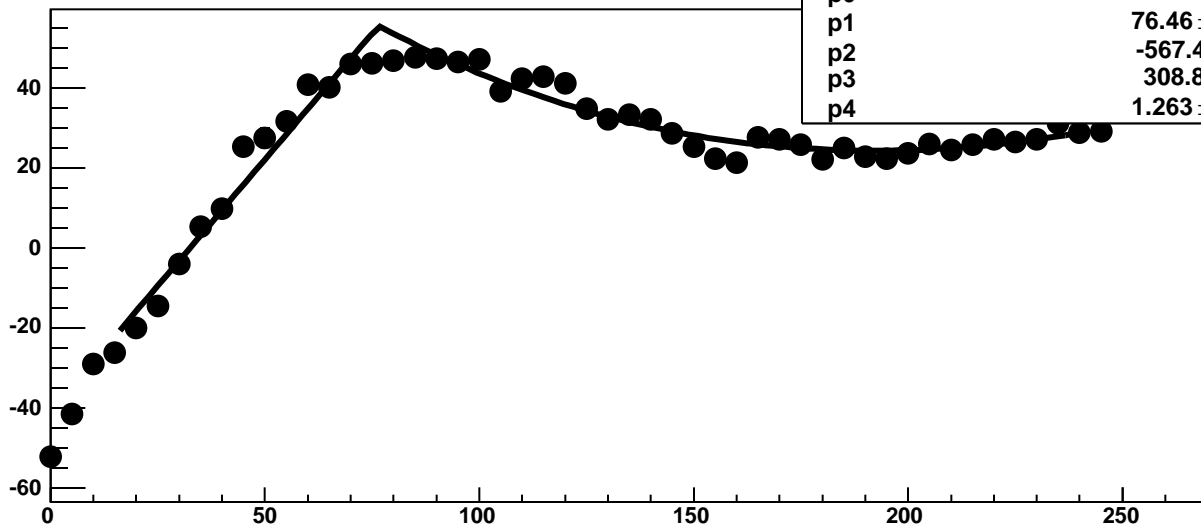
Chip 2, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



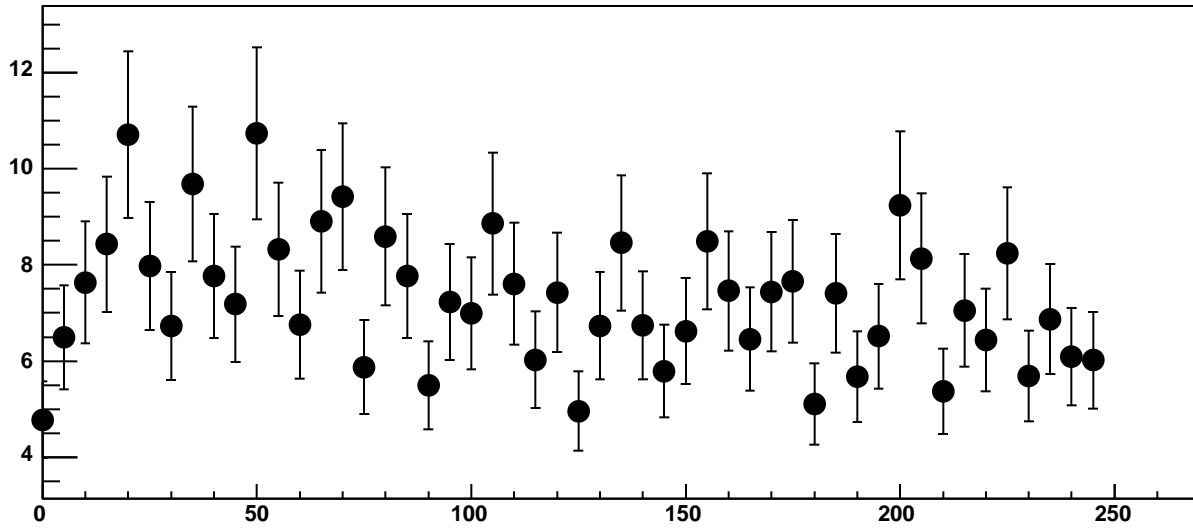
Chip 2, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold



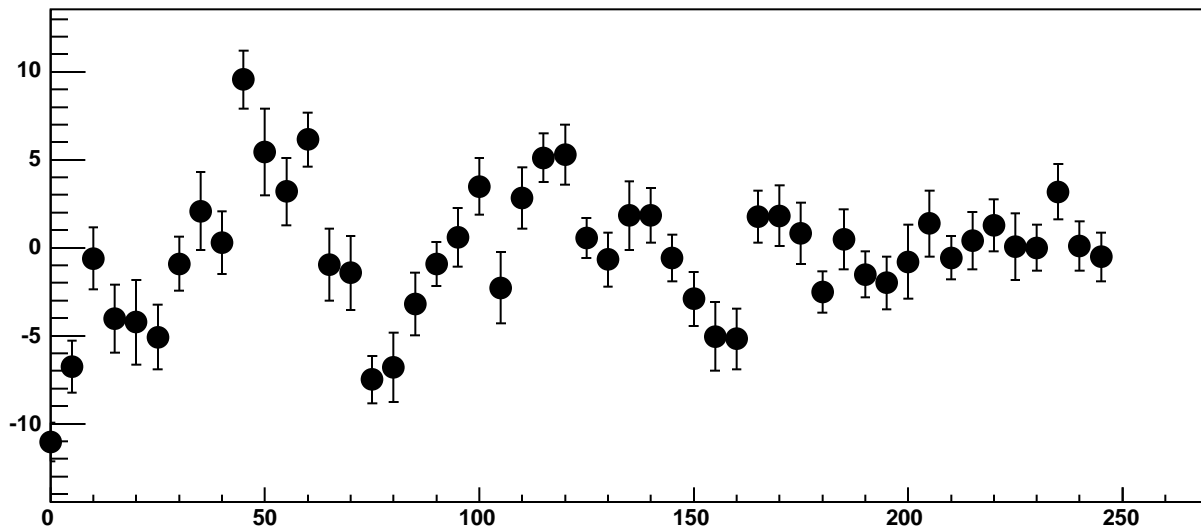
Chip 2, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold



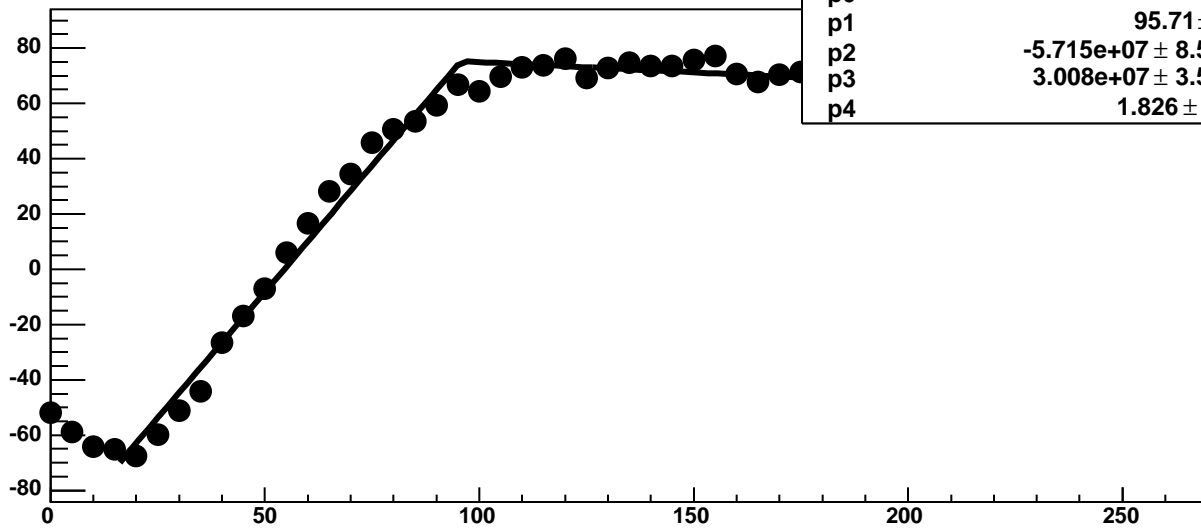
Chip 2, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold

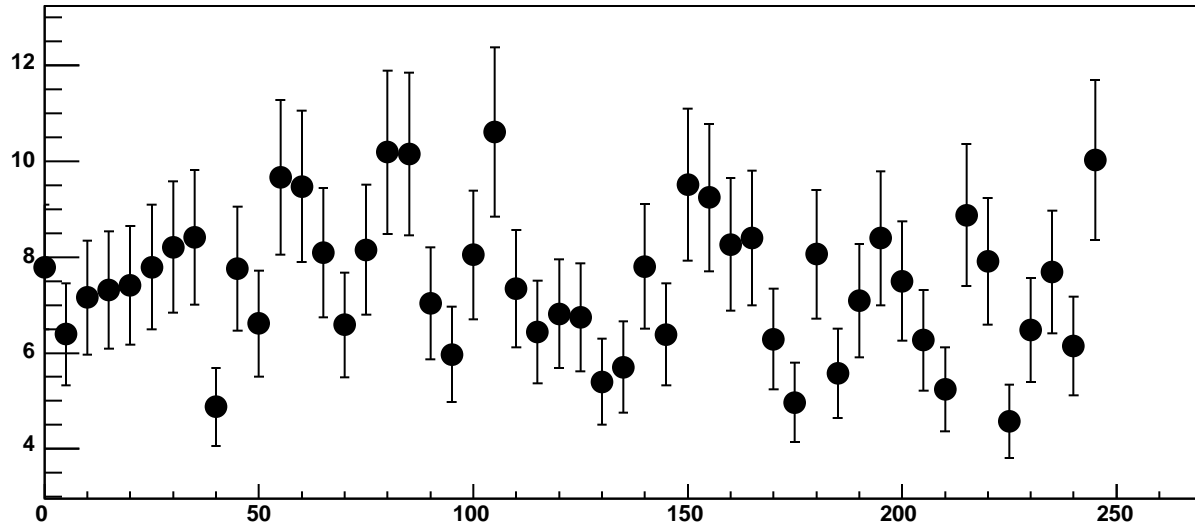


Chip 2, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

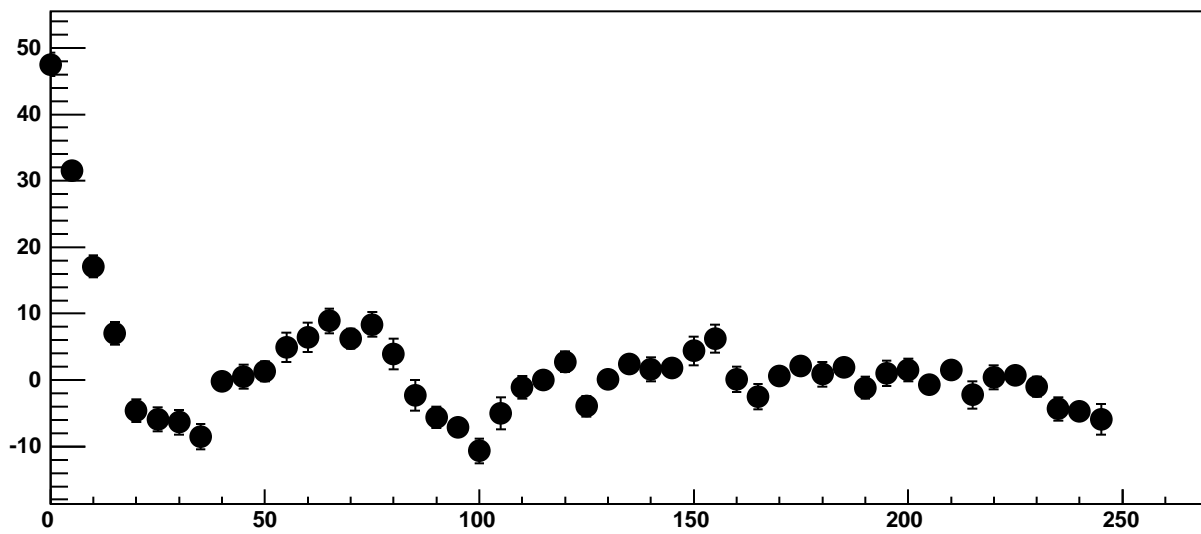


χ^2 / ndf	280.4 / 41
p0	75.28 ± 0.5894
p1	95.71 ± 0.5322
p2	$-5.715\text{e}+07 \pm 8.546\text{e}+05$
p3	$3.008\text{e}+07 \pm 3.547\text{e}+05$
p4	1.826 ± 0.01662

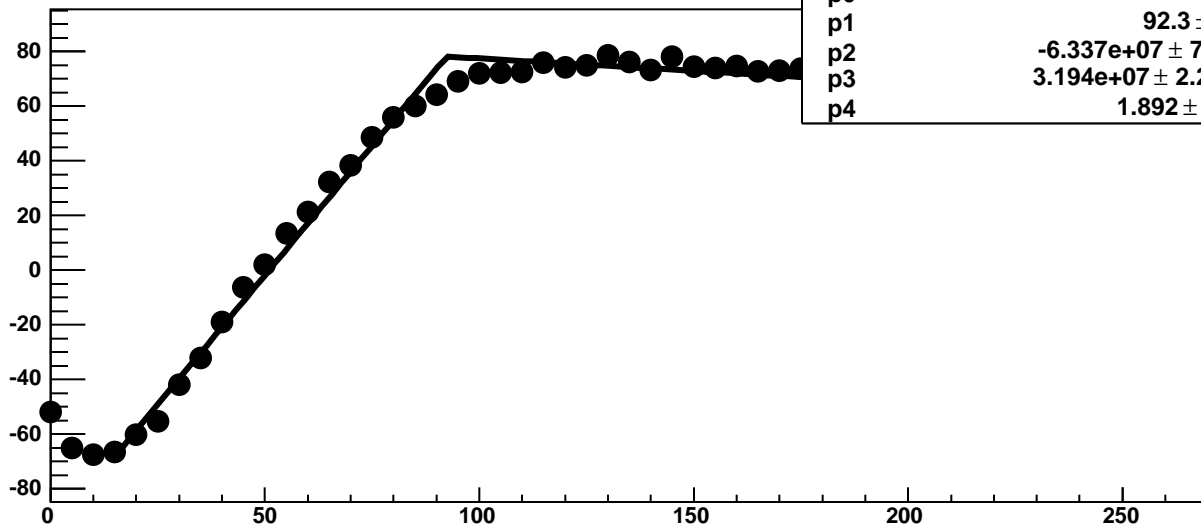
Chip 2, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

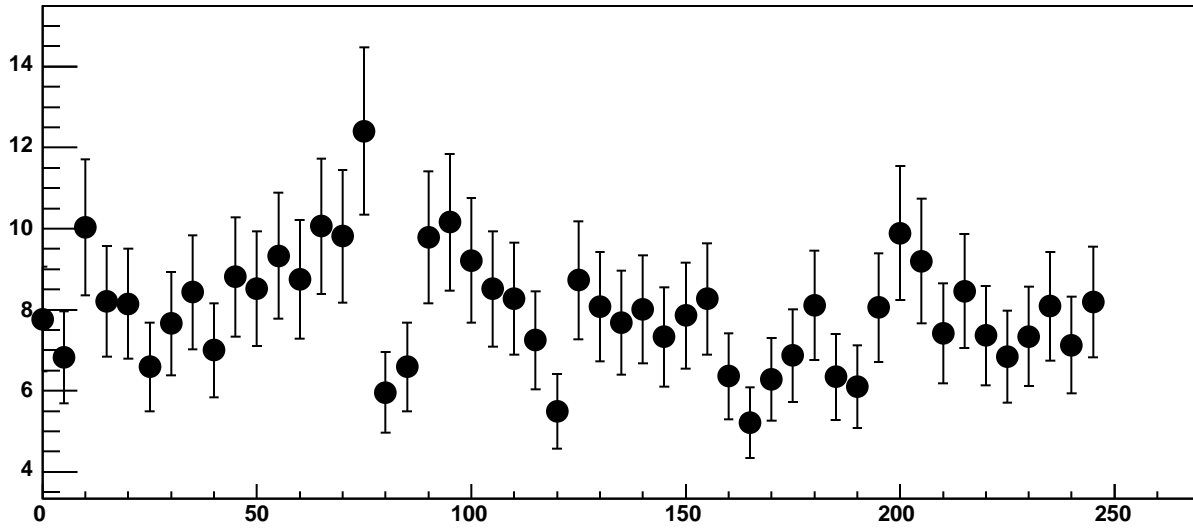


Chip 2, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

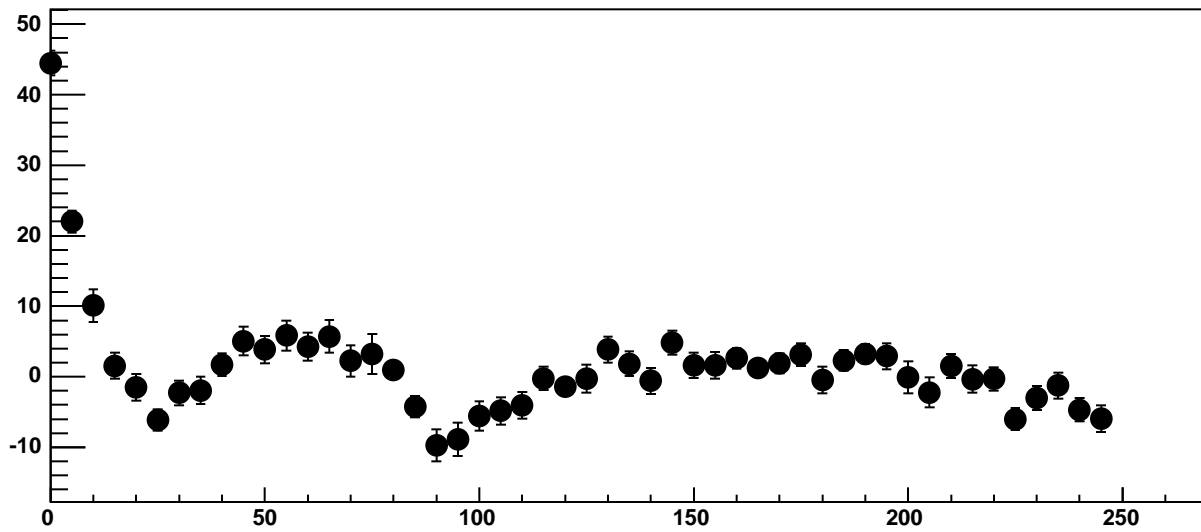


χ^2 / ndf	177 / 41
p0	78.16 ± 0.6283
p1	92.3 ± 0.5674
p2	$-6.337\text{e}+07 \pm 7.96\text{e}+05$
p3	$3.194\text{e}+07 \pm 2.245\text{e}+05$
p4	1.892 ± 0.01938

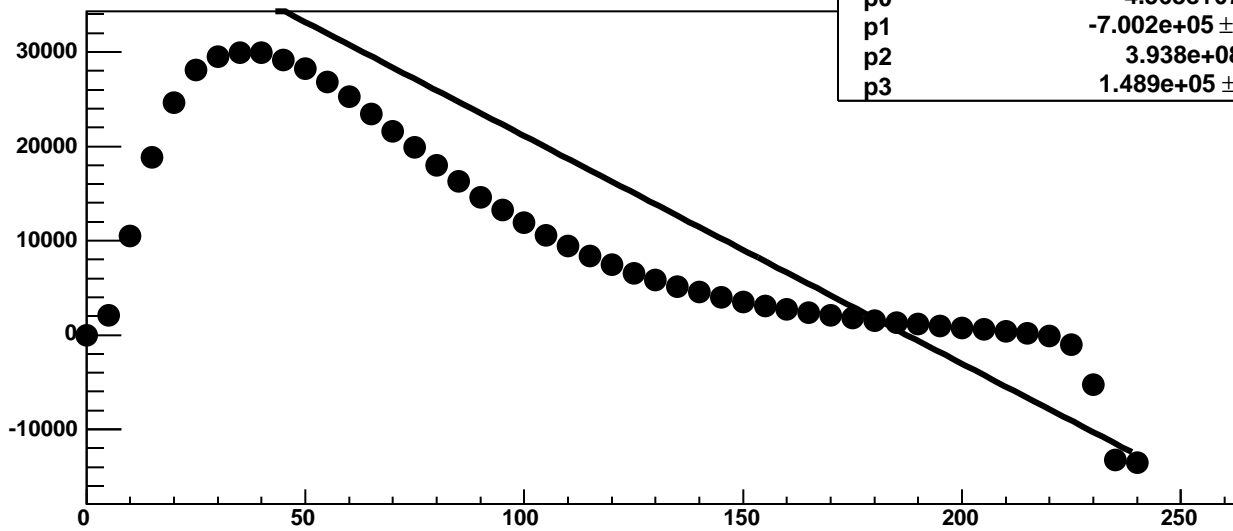
Chip 2, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

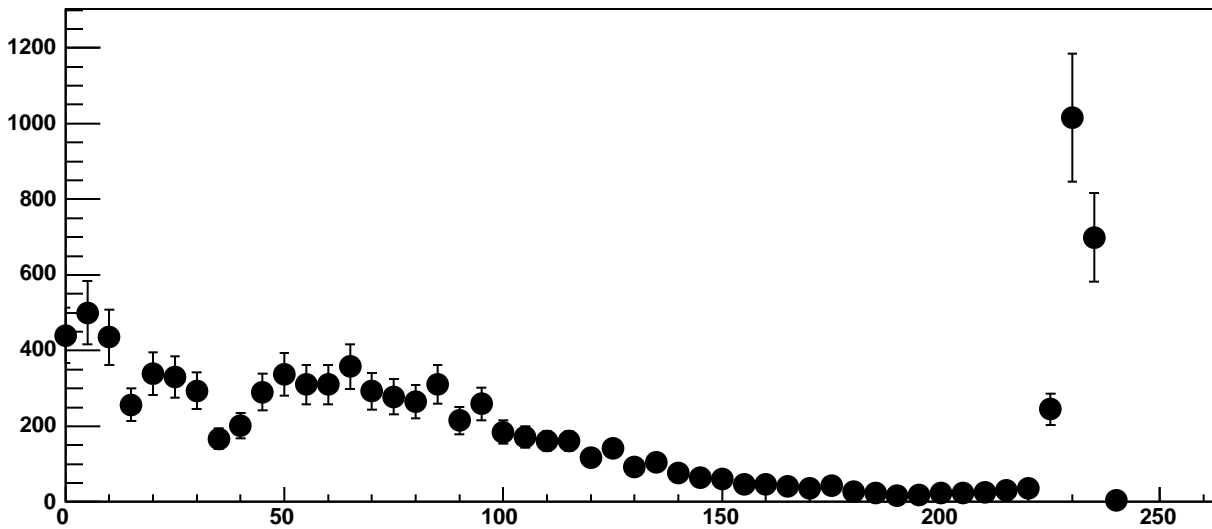


Chip 2, Channel 16, Enable 5!, DAC=4000, ADC Mean vs Hold

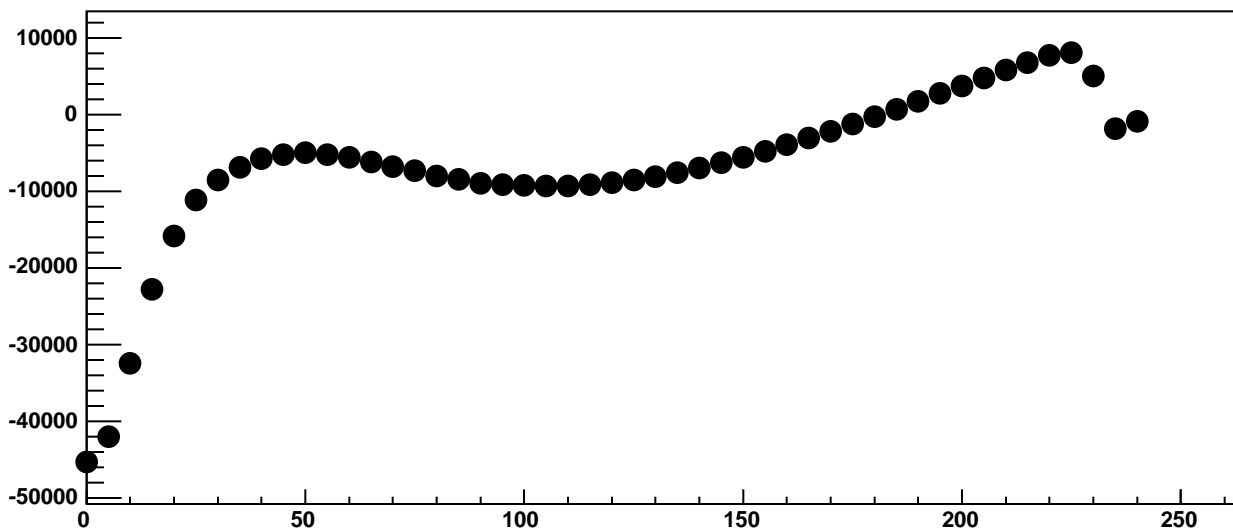


χ^2 / ndf	7.431e+06 / 42
p0	-4.568e+07 ± 12.56
p1	-7.002e+05 ± 0.05204
p2	3.938e+08 ± 108.3
p3	1.489e+05 ± 0.01107

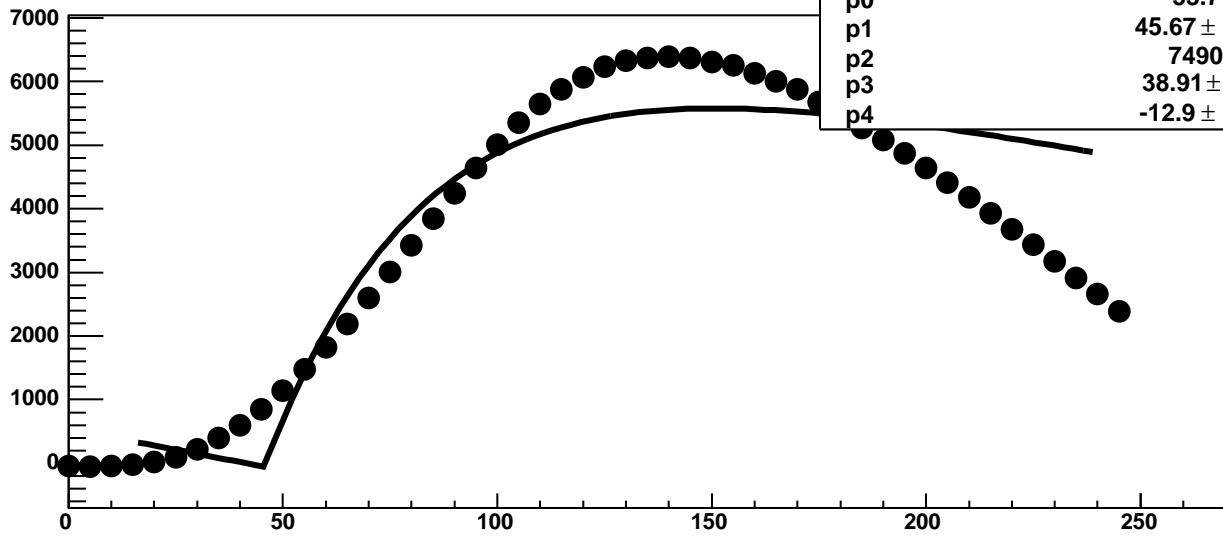
Chip 2, Channel 16, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 16, Enable 5!, DAC=4000, ADC Residuals vs Hold

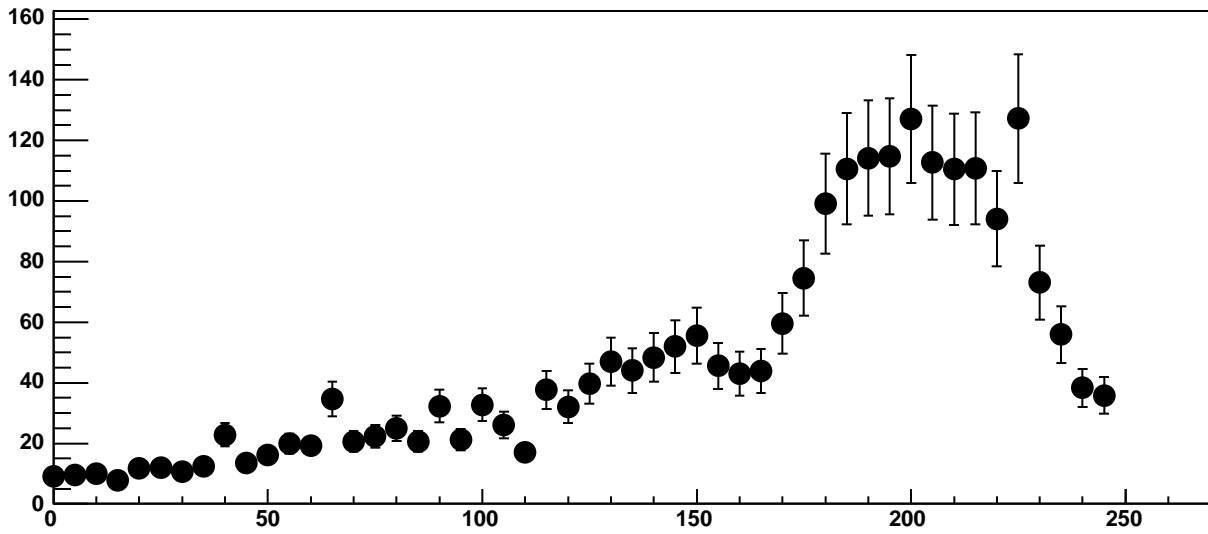


Chip 2, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

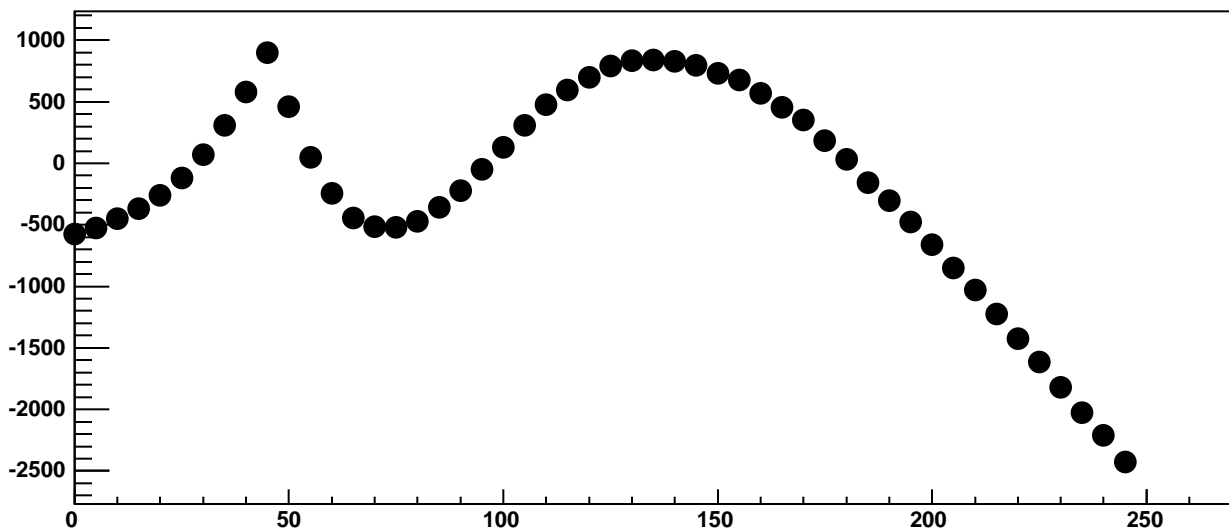


χ^2 / ndf	4.117e+05 / 41
p0	-53.71 ± 1.48
p1	45.67 ± 0.02054
p2	7490 ± 10.73
p3	38.91 ± 0.06711
p4	-12.9 ± 0.06144

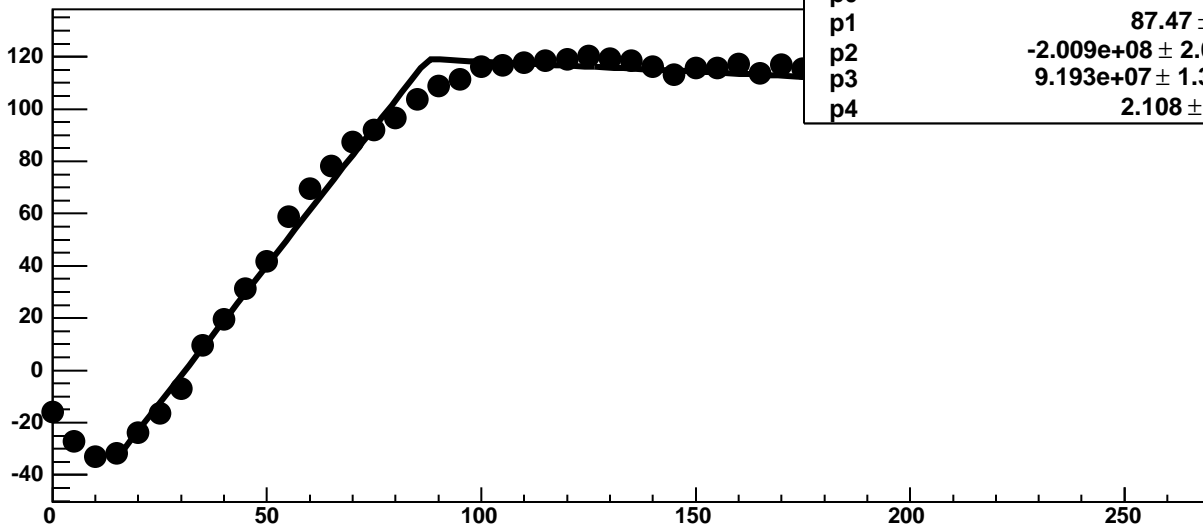
Chip 2, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

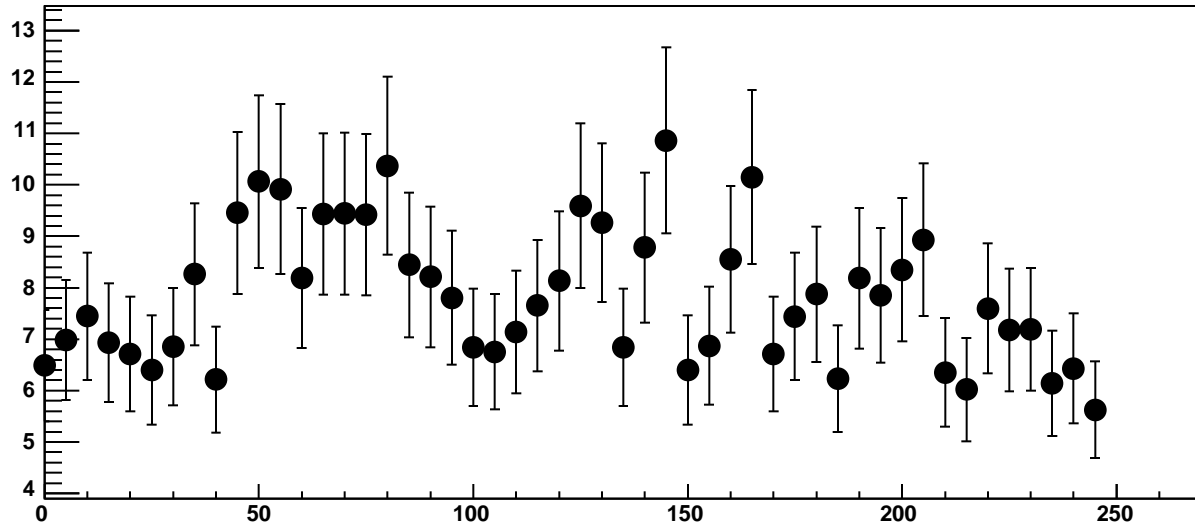


Chip 2, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

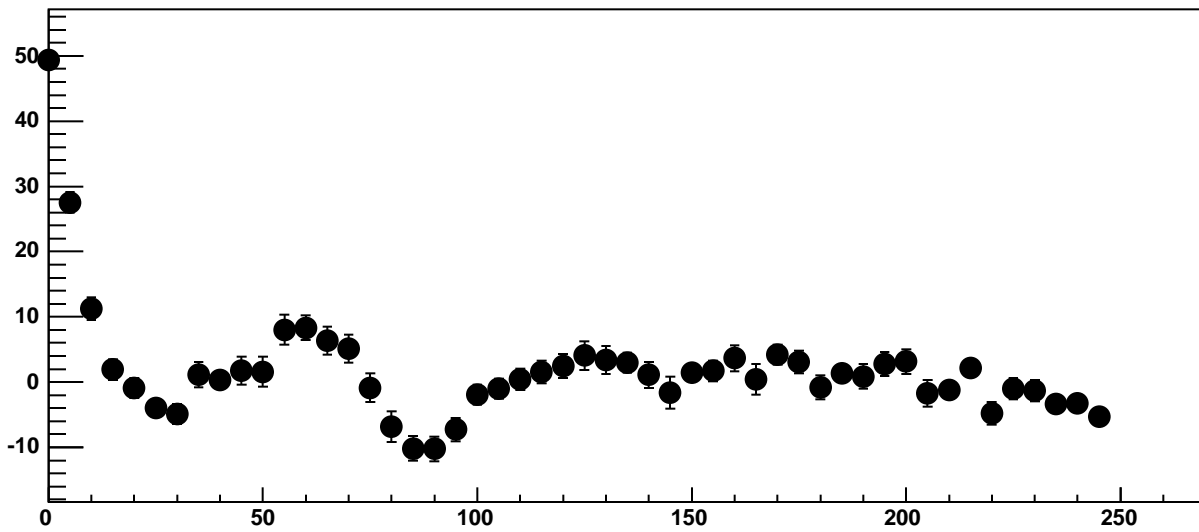


χ^2 / ndf	209.2 / 41
p0	119.1 ± 0.5984
p1	87.47 ± 0.5535
p2	-2.009e+08 ± 2.088e+06
p3	9.193e+07 ± 1.338e+05
p4	2.108 ± 0.02171

Chip 2, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold

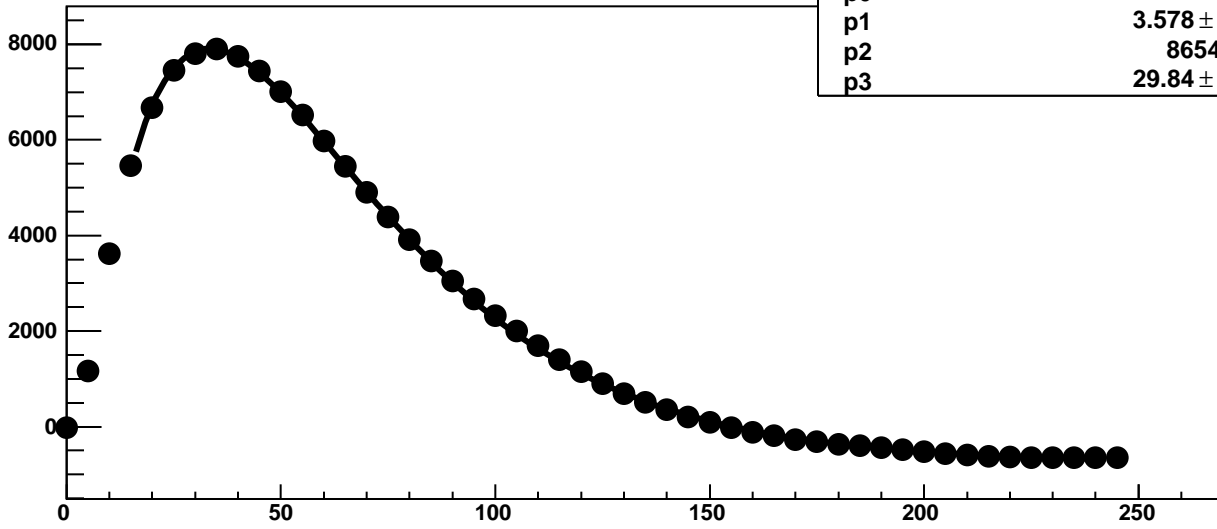


Chip 2, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

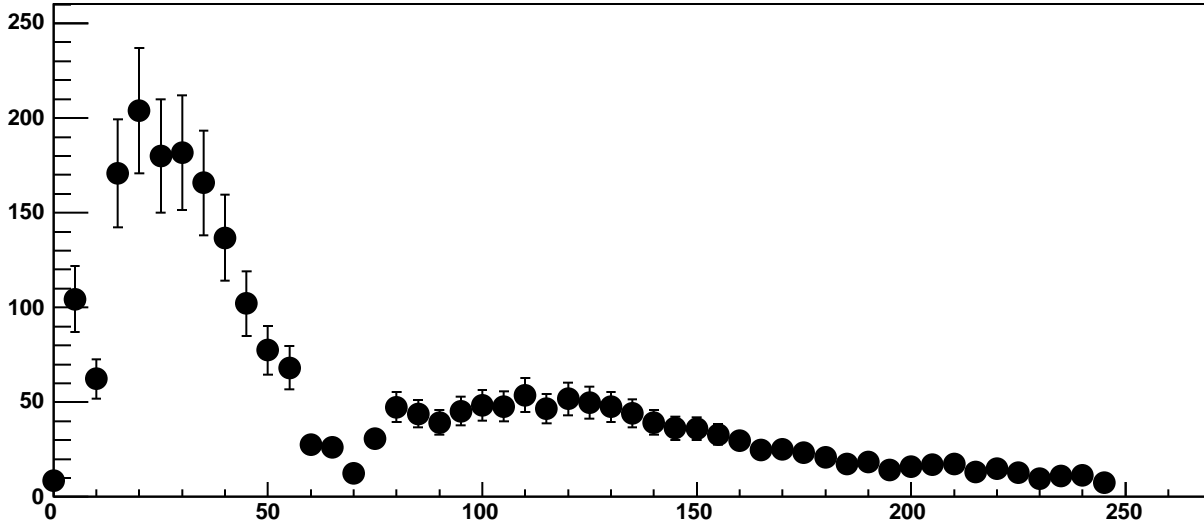


Chip 2, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold

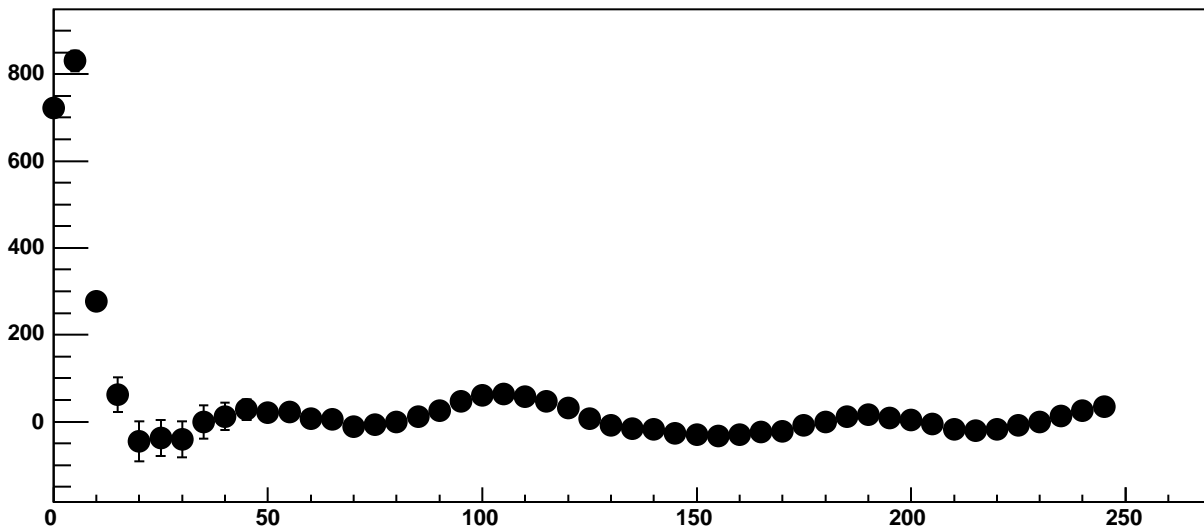
χ^2 / ndf	531.9 / 42
p0	-739.5 ± 1.27
p1	3.578 ± 0.08366
p2	8654 ± 9.005
p3	29.84 ± 0.03344



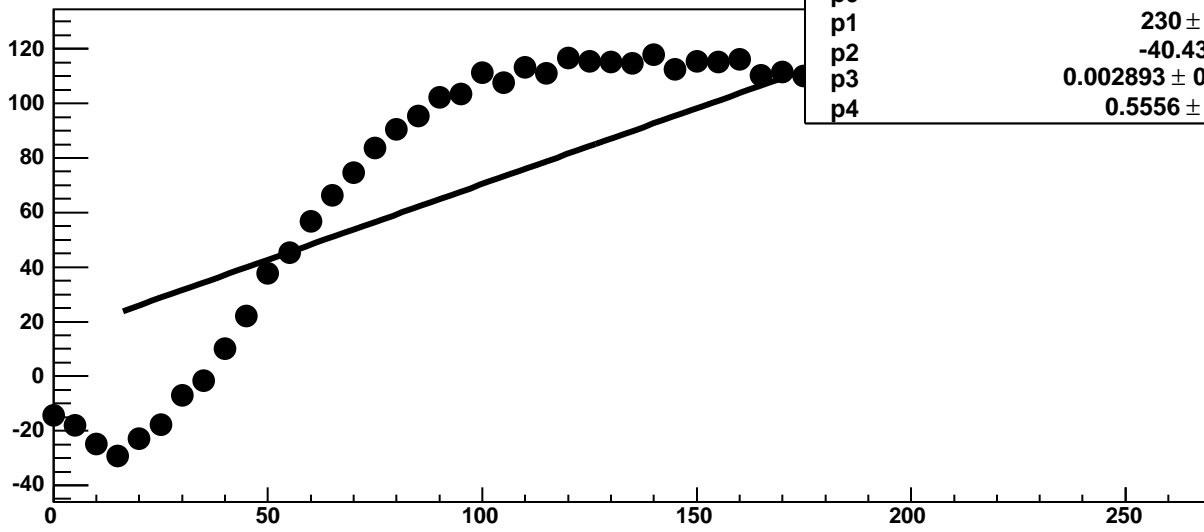
Chip 2, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold

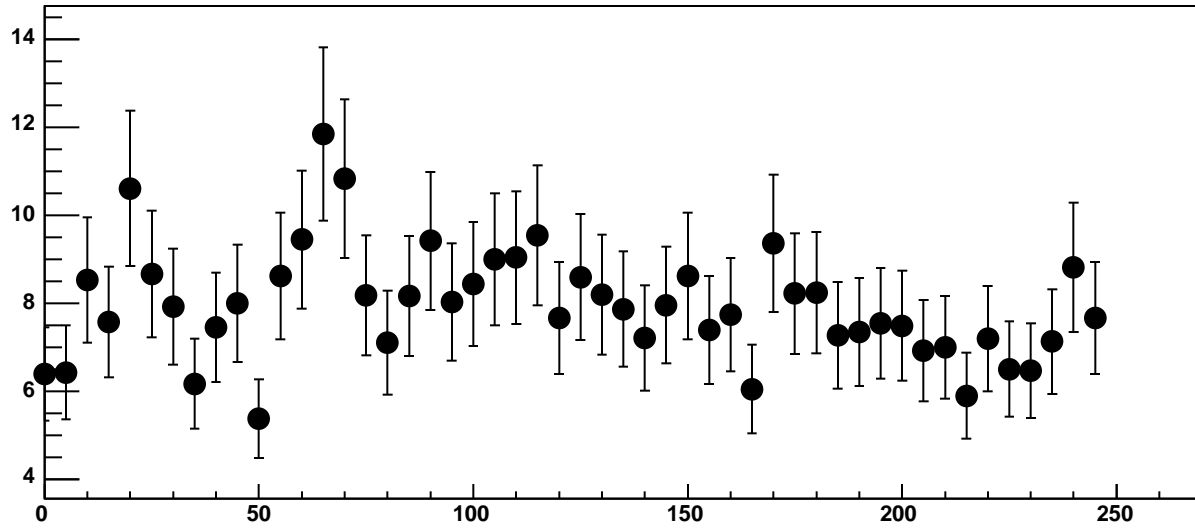


Chip 2, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold

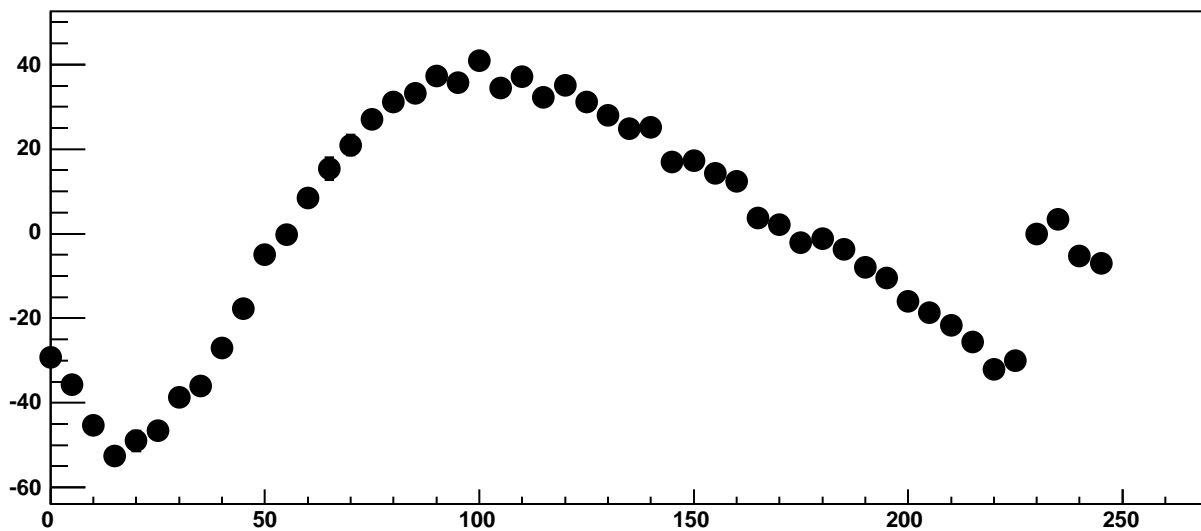


χ^2 / ndf	9478 / 41
p0	142.7 ± 0.4439
p1	230 ± 0.01866
p2	-40.43 ± 1.296
p3	0.002893 ± 0.007278
p4	0.5556 ± 0.00363

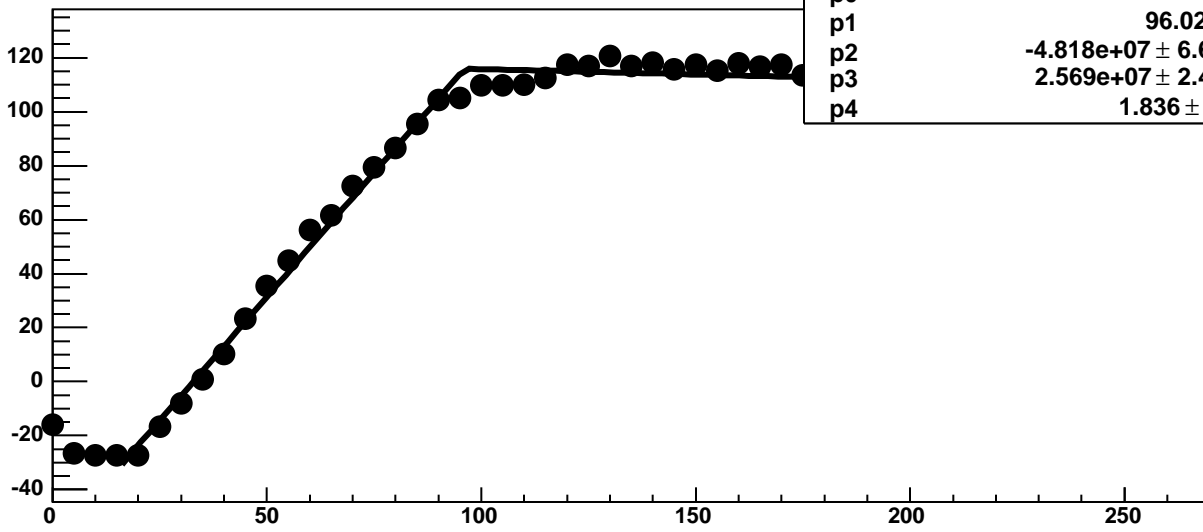
Chip 2, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold

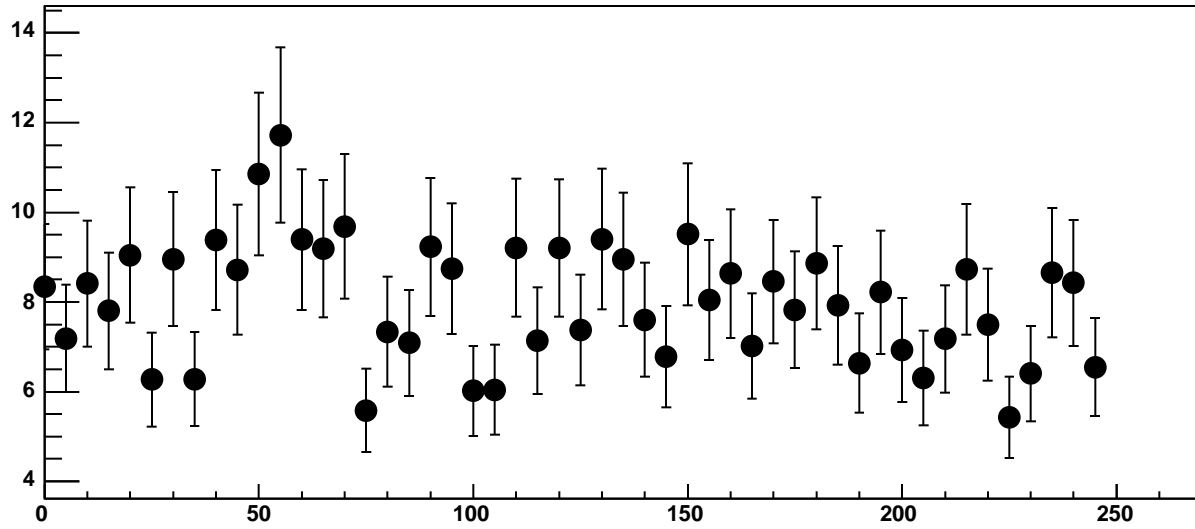


Chip 2, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold

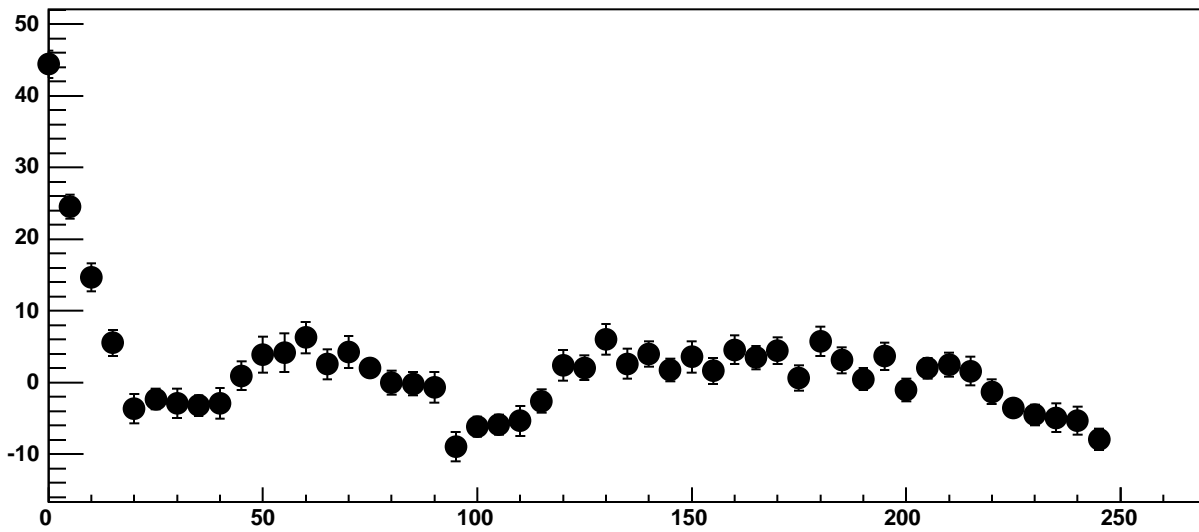


χ^2 / ndf	201.3 / 41
p0	116 ± 0.6242
p1	96.02 ± 0.569
p2	$-4.818e+07 \pm 6.682e+05$
p3	$2.569e+07 \pm 2.441e+05$
p4	1.836 ± 0.01774

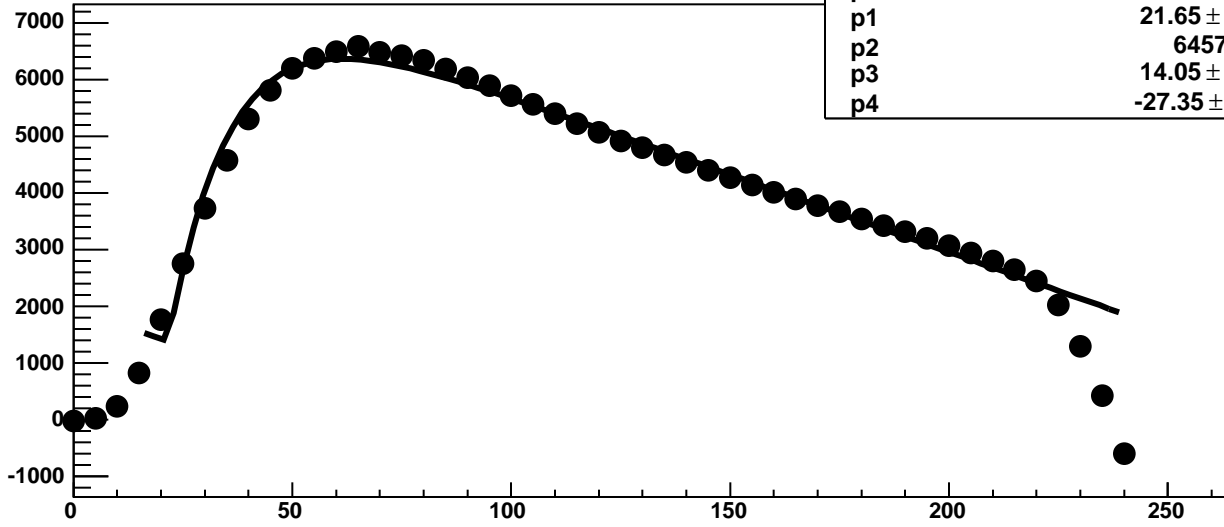
Chip 2, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

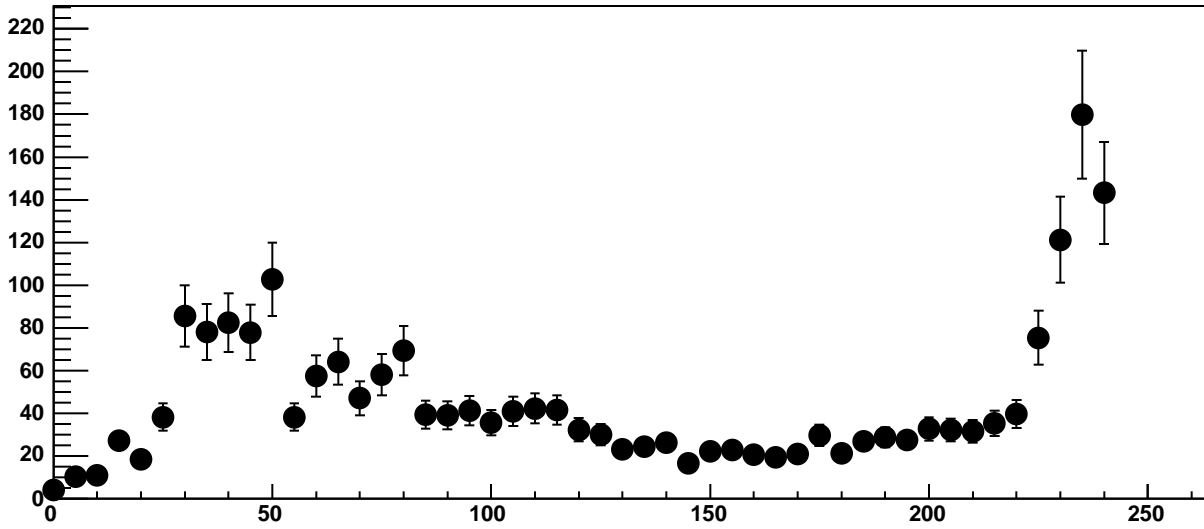


Chip 2, Channel 17, Enable 5, DAC=4000, ADC Mean vs Hold

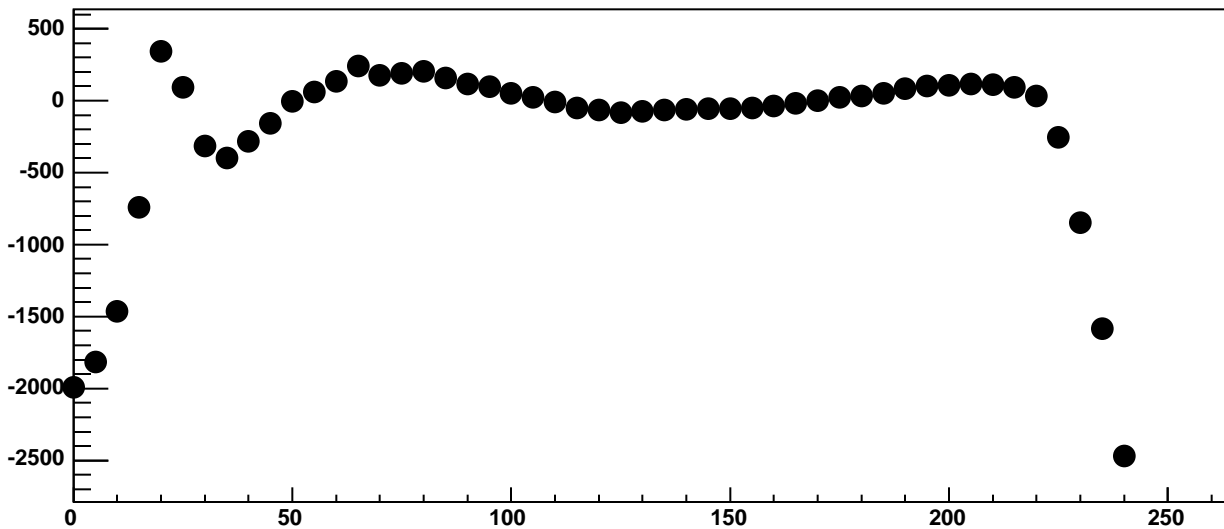


χ^2 / ndf	3.408e+04 / 41
p0	1380 ± 3.453
p1	21.65 ± 0.03146
p2	6457 ± 6.314
p3	14.05 ± 0.06075
p4	-27.35 ± 0.03621

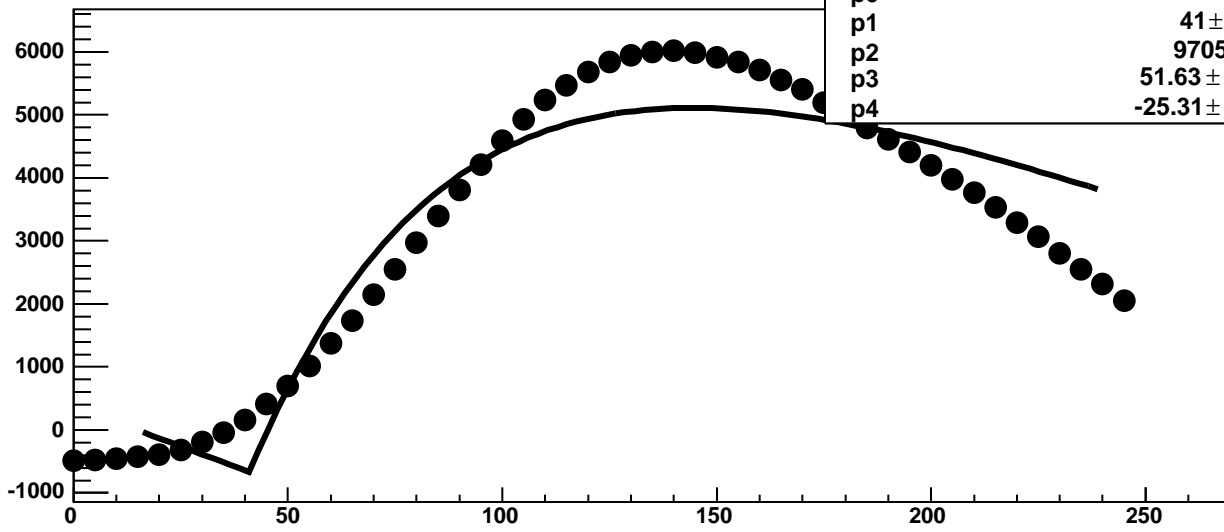
Chip 2, Channel 17, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 2, Channel 17, Enable 5, DAC=4000, ADC Residuals vs Hold

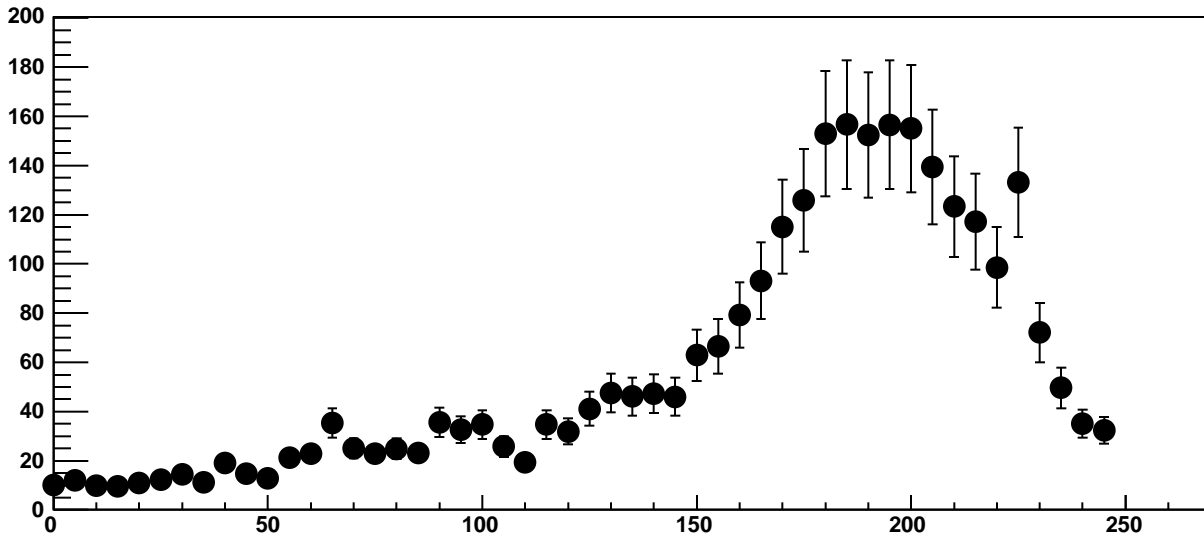


Chip 3, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold

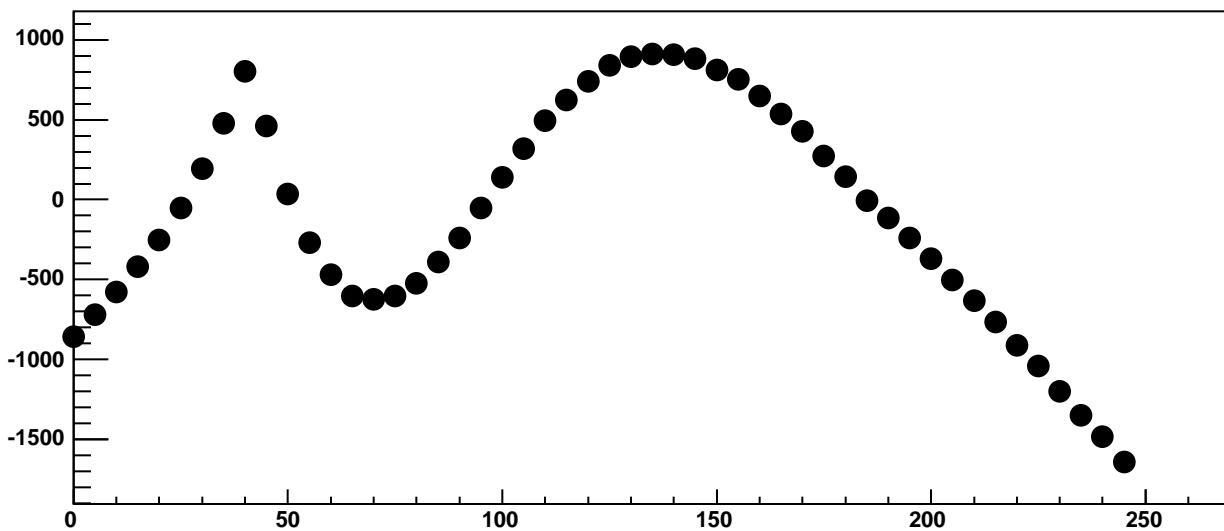


χ^2 / ndf	3.281e+05 / 41
p0	-665.9 ± 1.512
p1	41 ± 0.01801
p2	9705 ± 16.36
p3	51.63 ± 0.09135
p4	-25.31 ± 0.08003

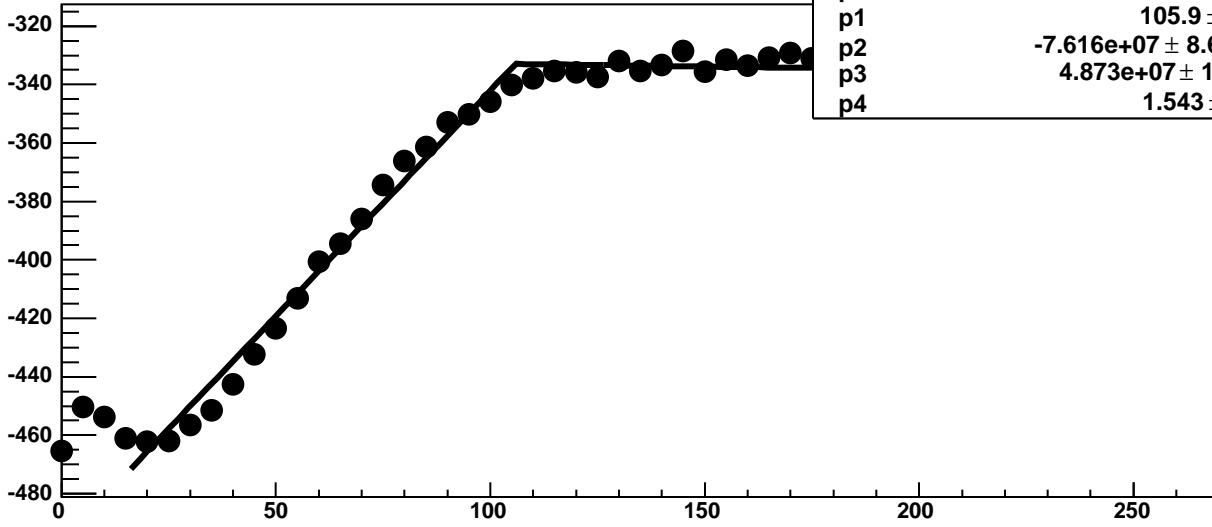
Chip 3, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold

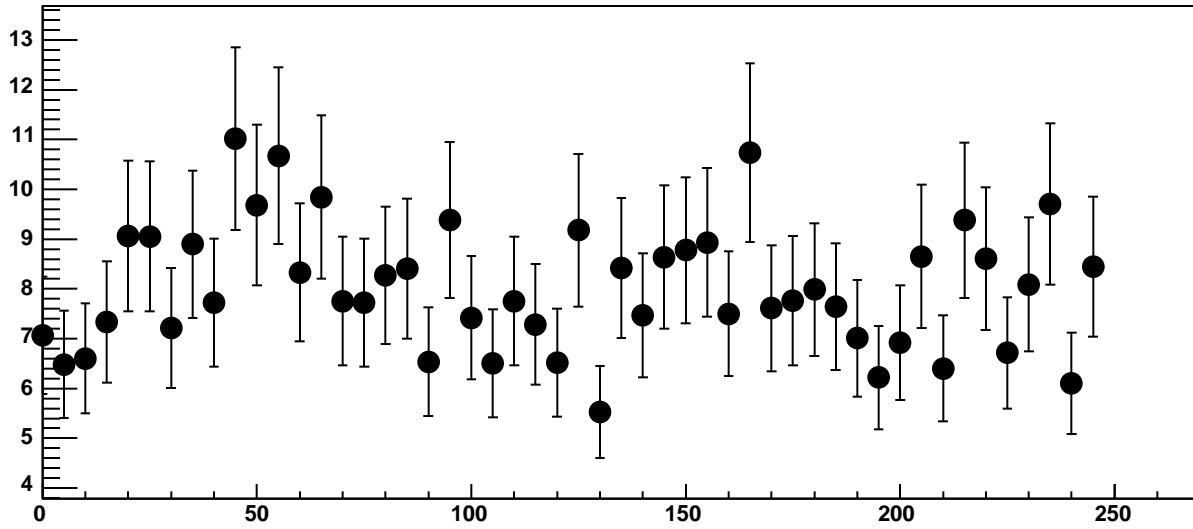


Chip 3, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

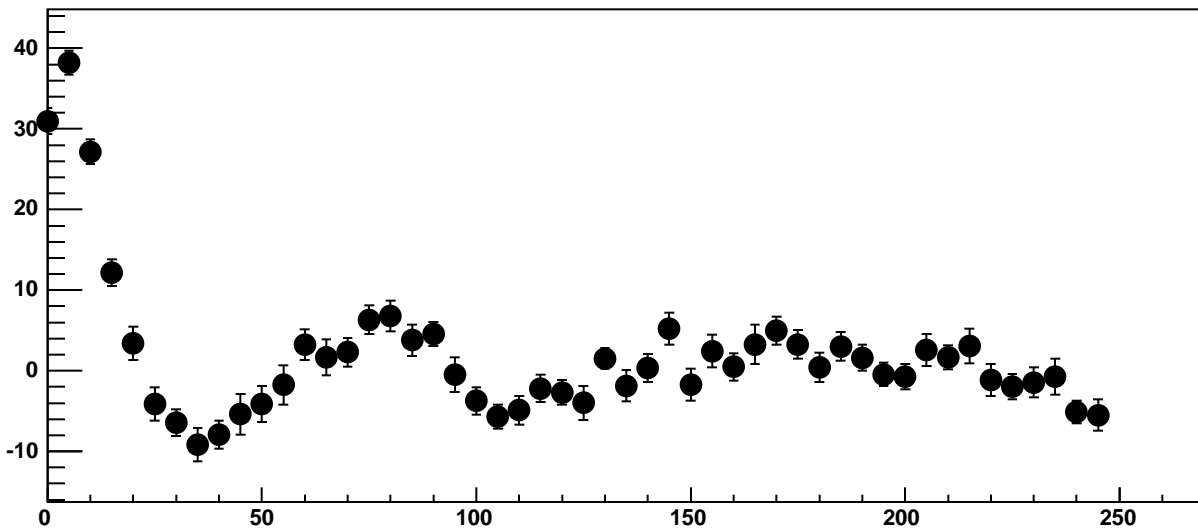


χ^2 / ndf	252 / 41
p0	-332.9 ± 0.6611
p1	105.9 ± 0.6555
p2	$-7.616\text{e}+07 \pm 8.698\text{e}+05$
p3	$4.873\text{e}+07 \pm 1.68\text{e}+05$
p4	1.543 ± 0.0149

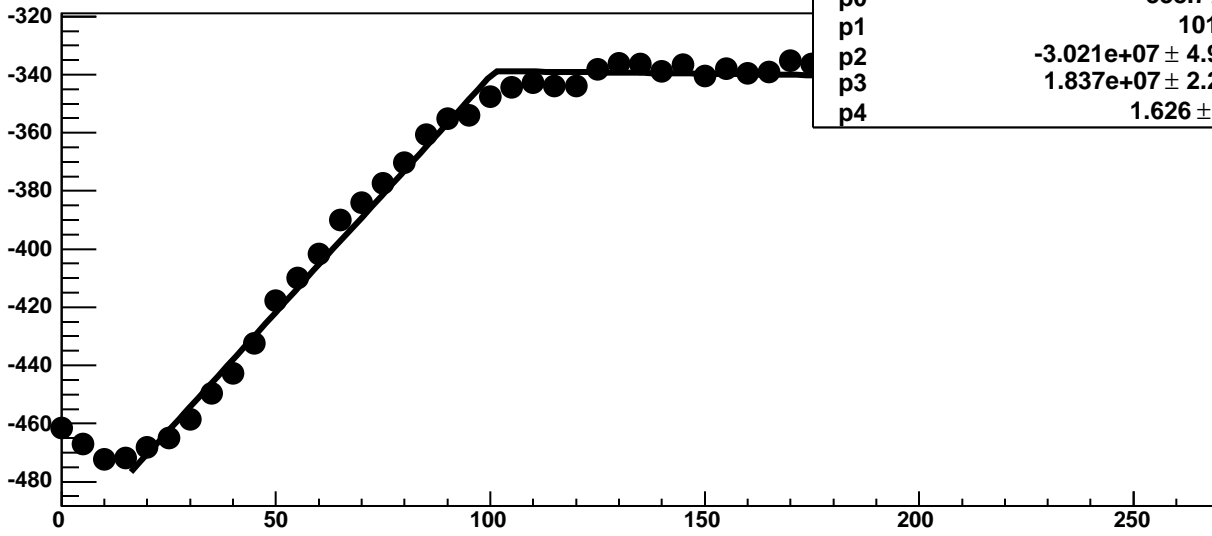
Chip 3, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold

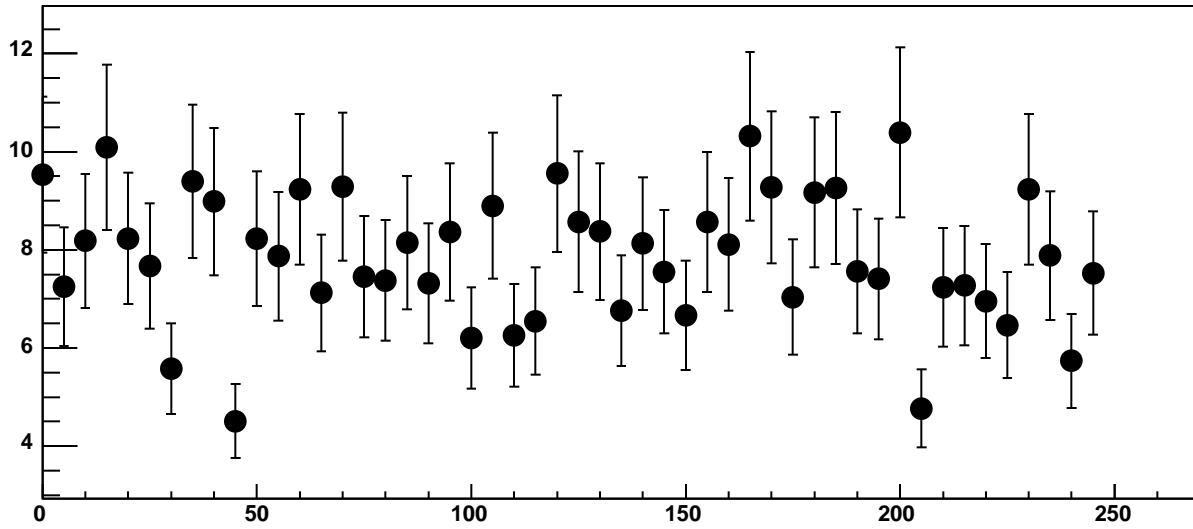


Chip 3, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

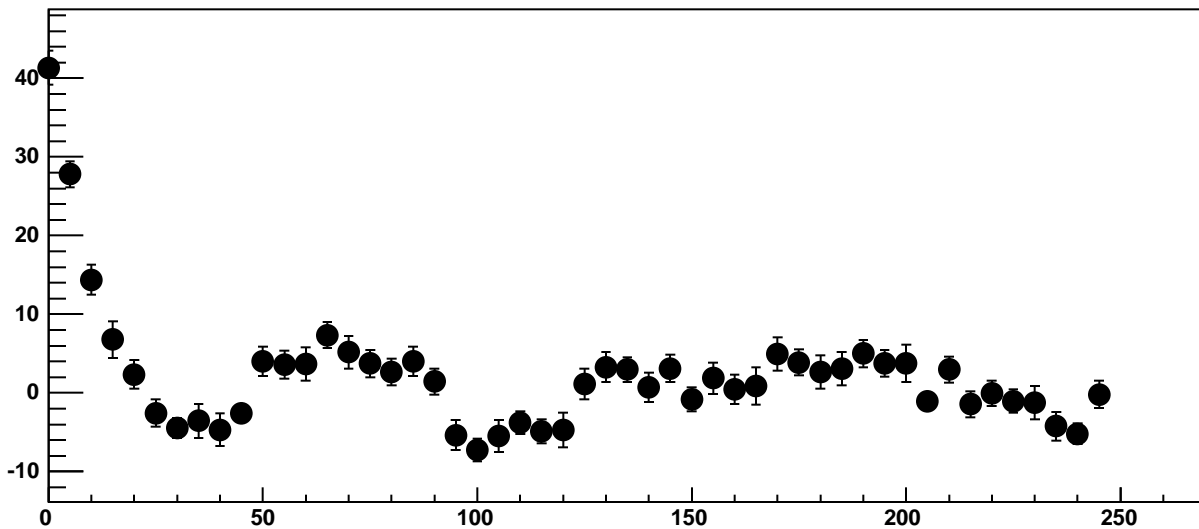


χ^2 / ndf	221.3 / 41
p0	-338.7 ± 0.6571
p1	101 ± 0.625
p2	$-3.021\text{e}+07 \pm 4.903\text{e}+05$
p3	$1.837\text{e}+07 \pm 2.286\text{e}+05$
p4	1.626 ± 0.01561

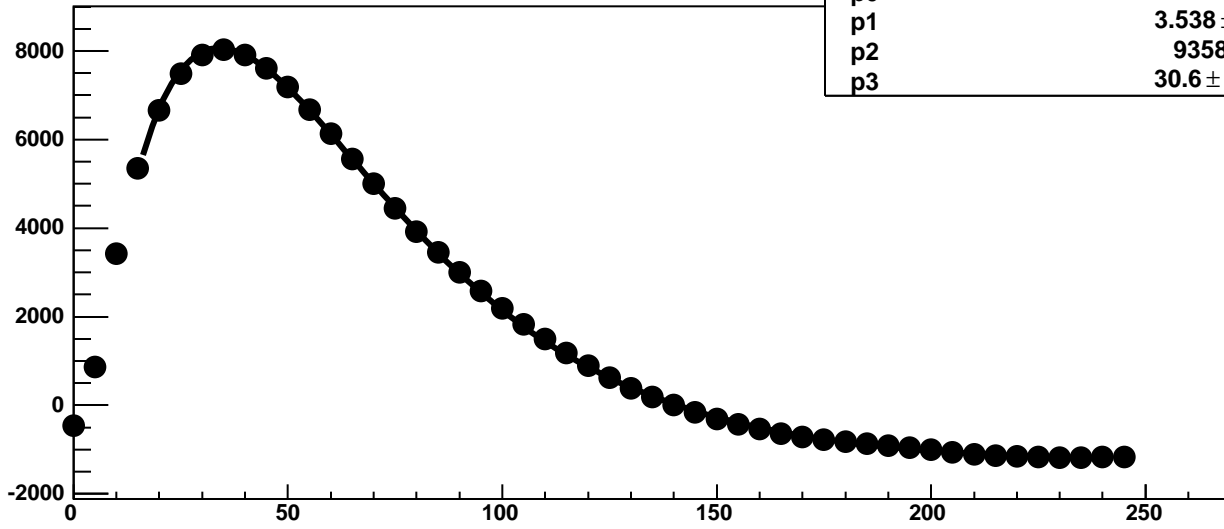
Chip 3, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

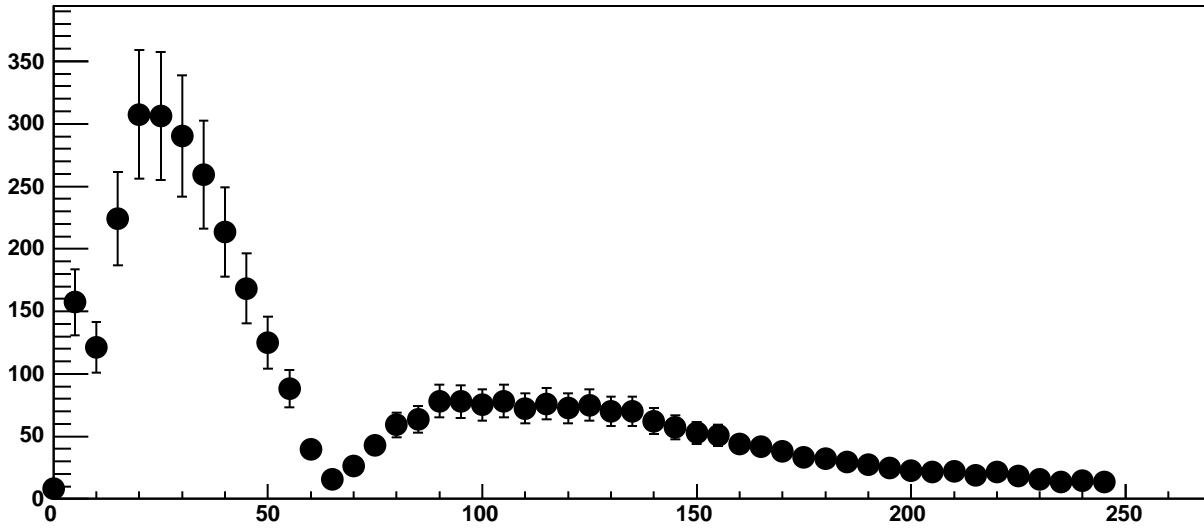


Chip 3, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold

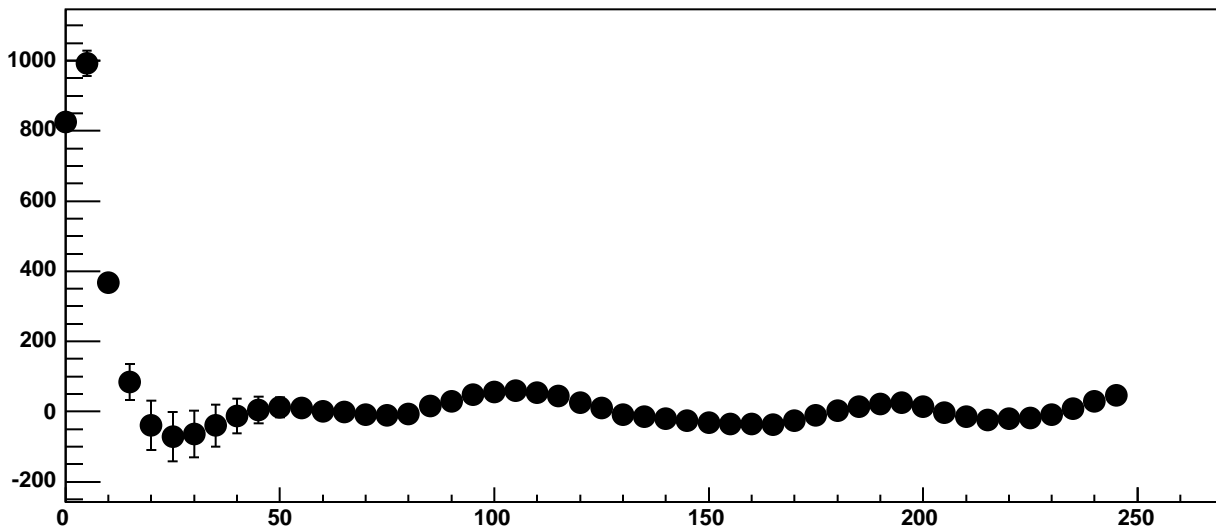


χ^2 / ndf	333 / 42
p0	-1290 ± 1.847
p1	3.538 ± 0.1079
p2	9358 ± 12.18
p3	30.6 ± 0.04403

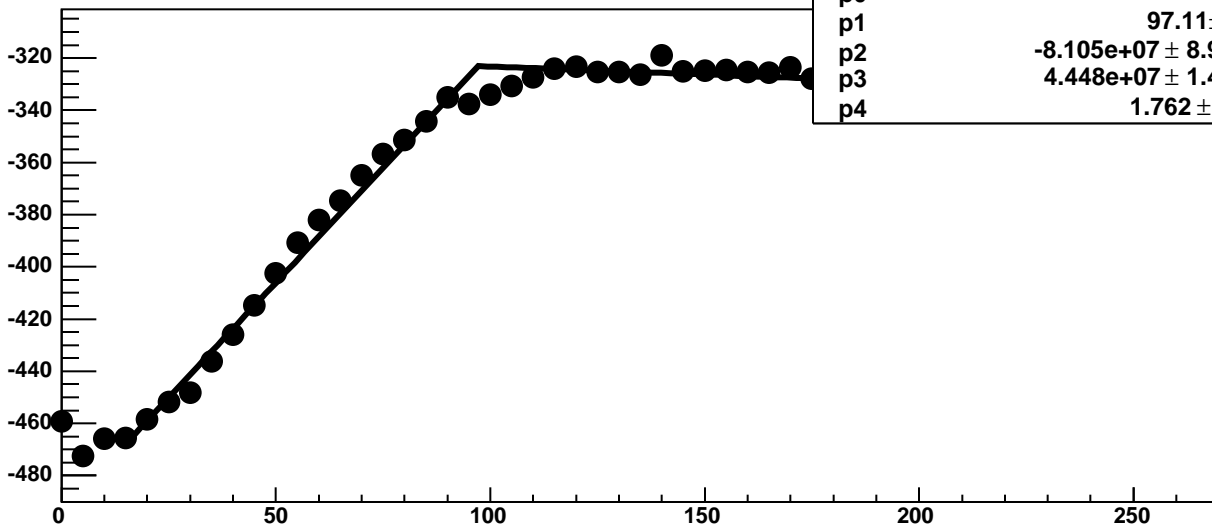
Chip 3, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold

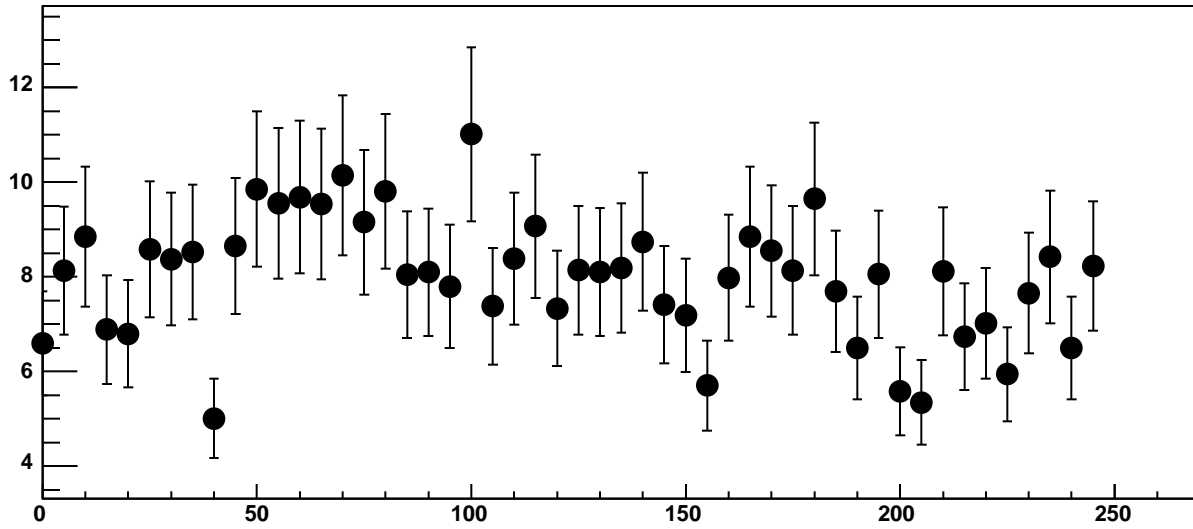


Chip 3, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

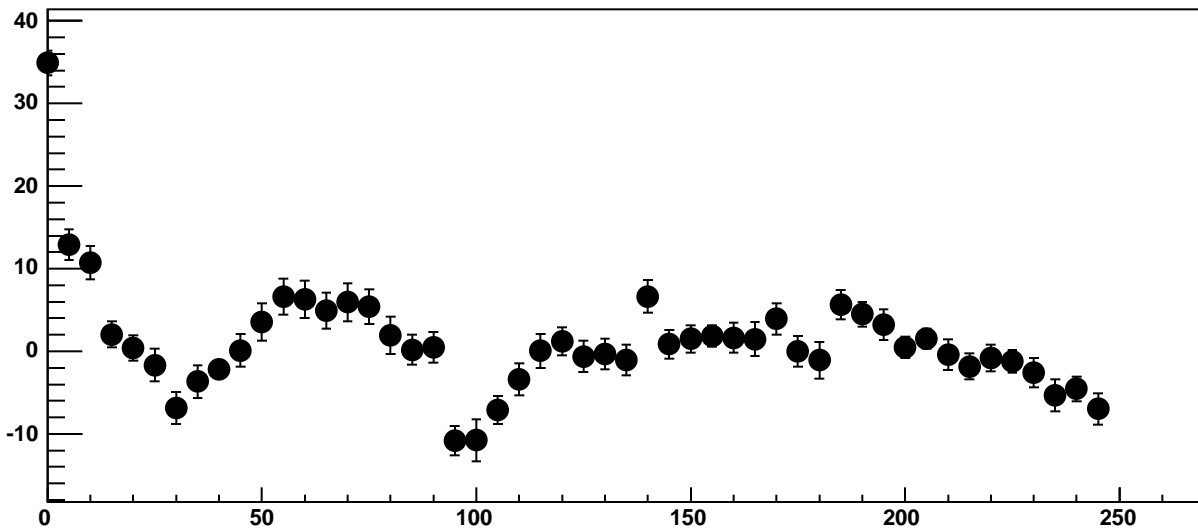


χ^2 / ndf	202.6 / 41
p0	-323.1 ± 0.6648
p1	97.11 ± 0.6329
p2	$-8.105e+07 \pm 8.915e+05$
p3	$4.448e+07 \pm 1.475e+05$
p4	1.762 ± 0.01771

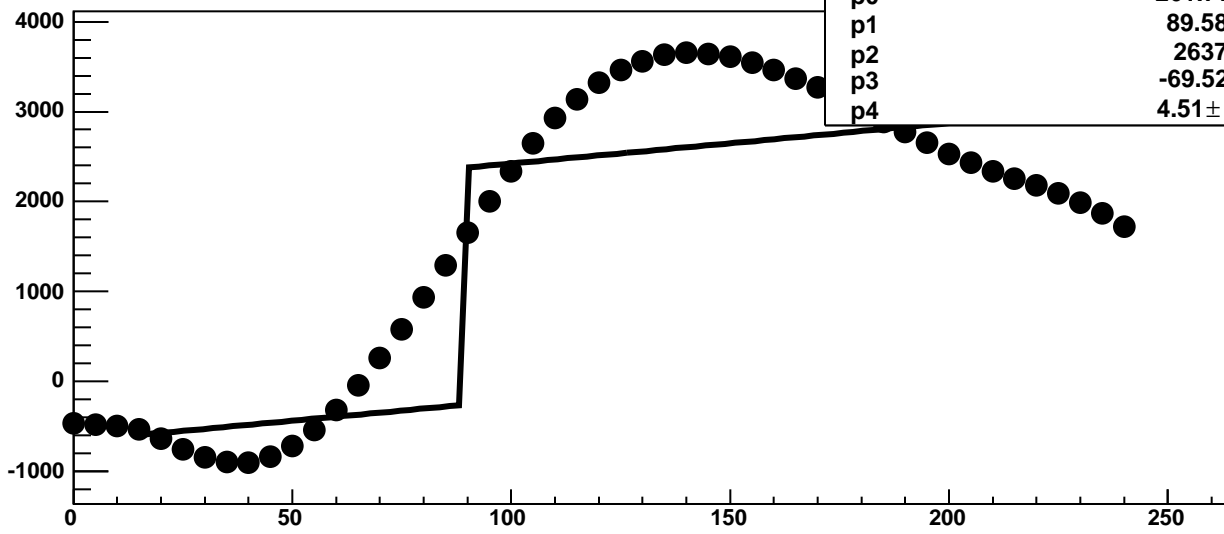
Chip 3, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

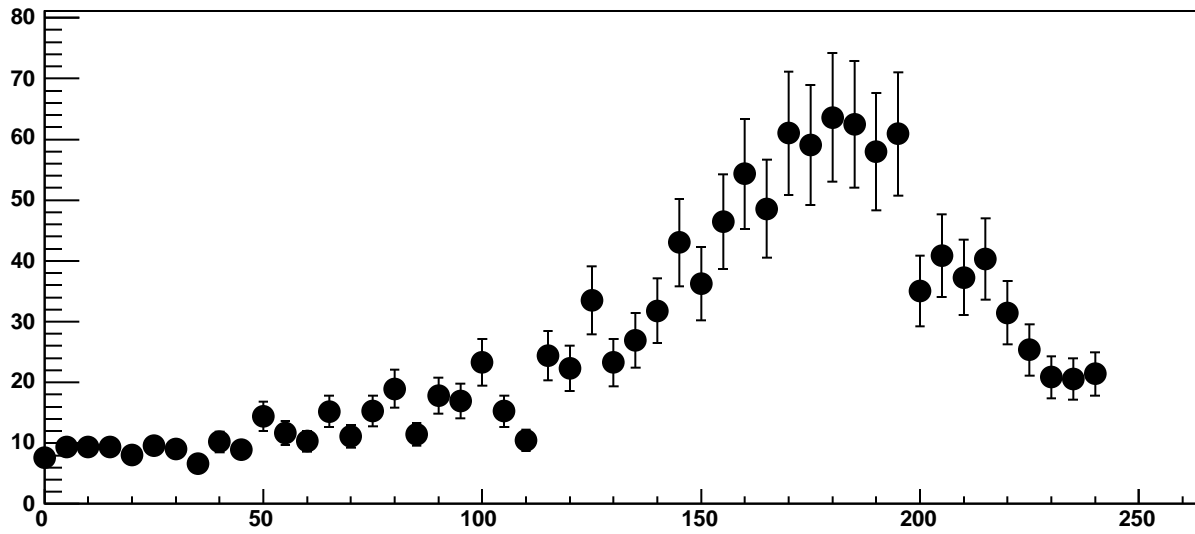


Chip 3, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold

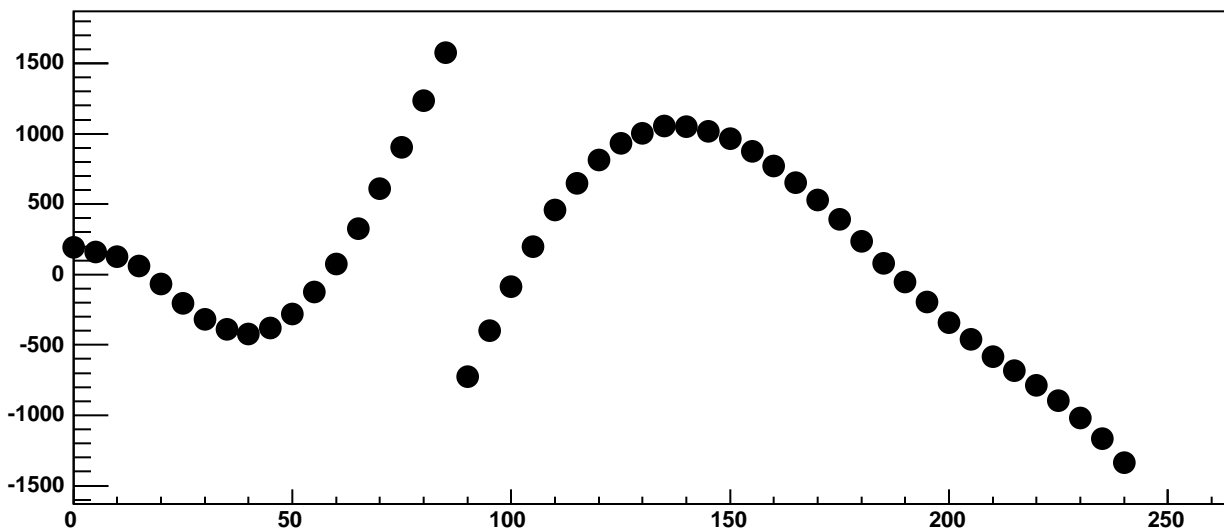


χ^2 / ndf	1.238e+06 / 41
p0	-261.7 \pm 0.5167
p1	89.58 \pm 1.282
p2	2637 \pm 4.215
p3	-69.52 \pm 3.318
p4	4.51 \pm 0.05903

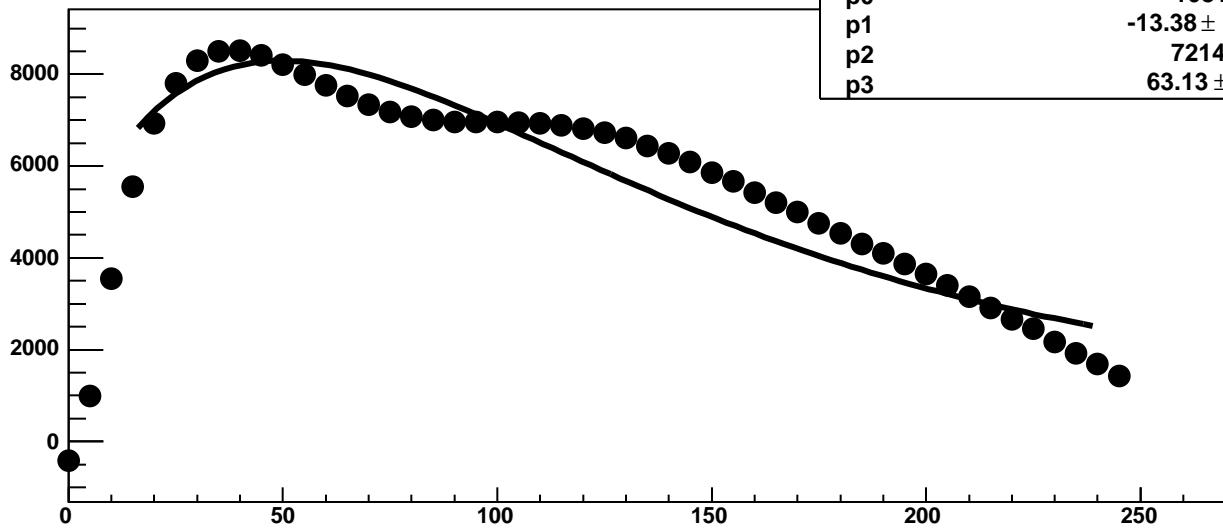
Chip 3, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold

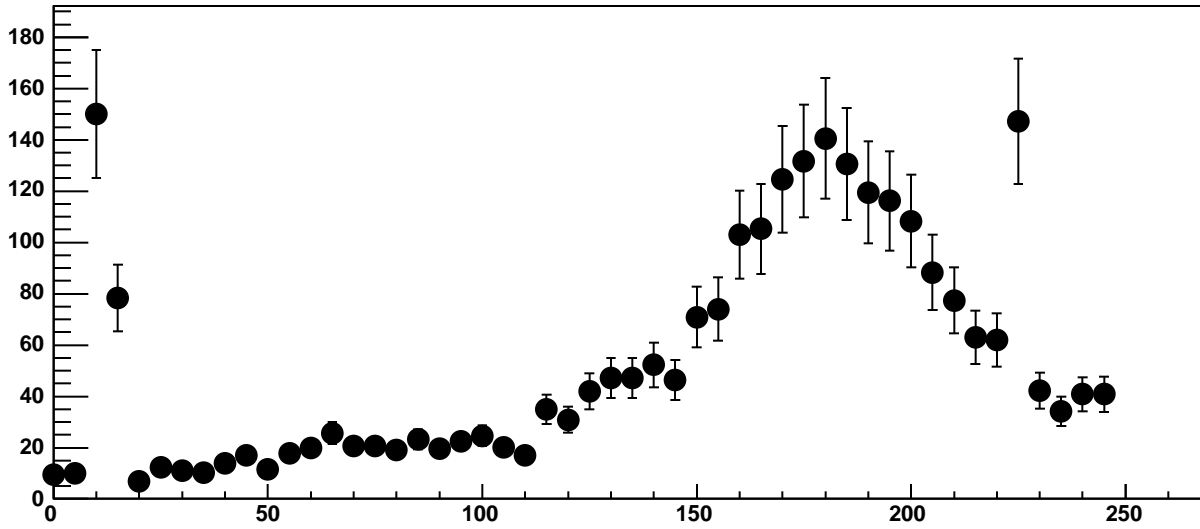


Chip 3, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold

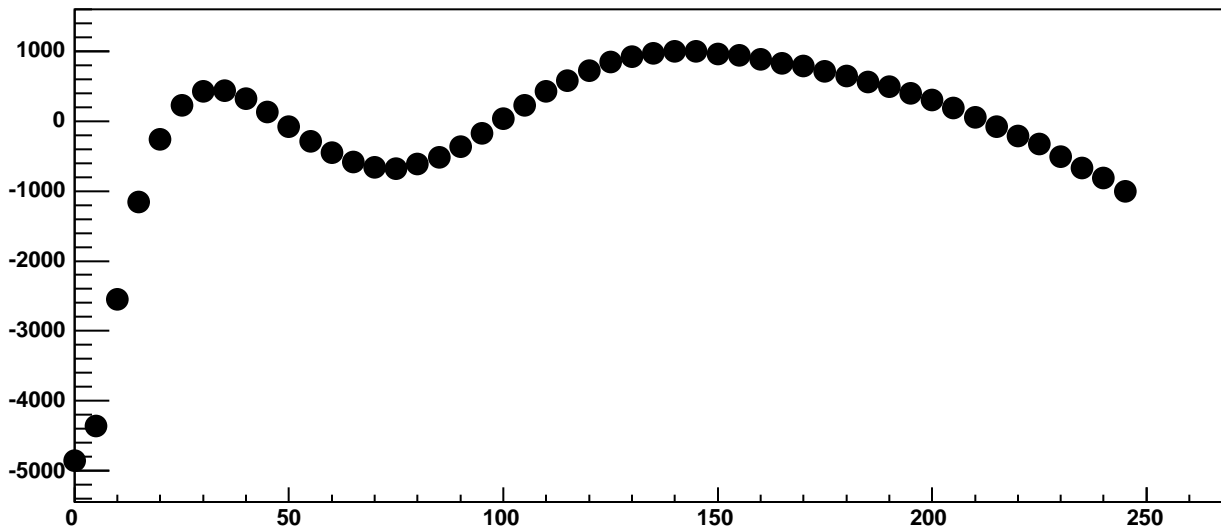


χ^2 / ndf	3.156e+05 / 42
p0	1081 ± 11.73
p1	-13.38 ± 0.08987
p2	7214 ± 11.25
p3	63.13 ± 0.1016

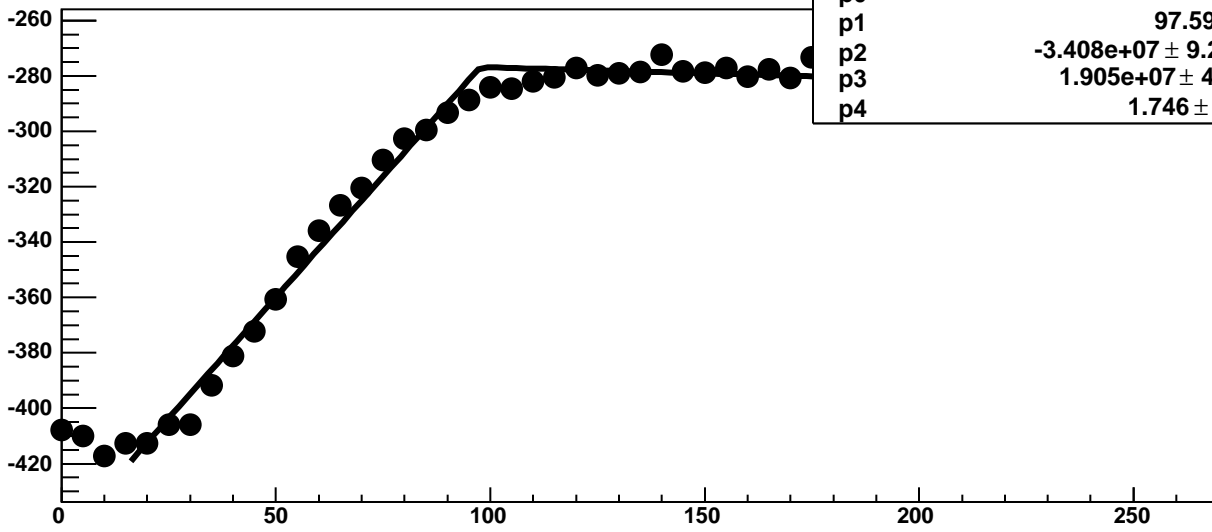
Chip 3, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold

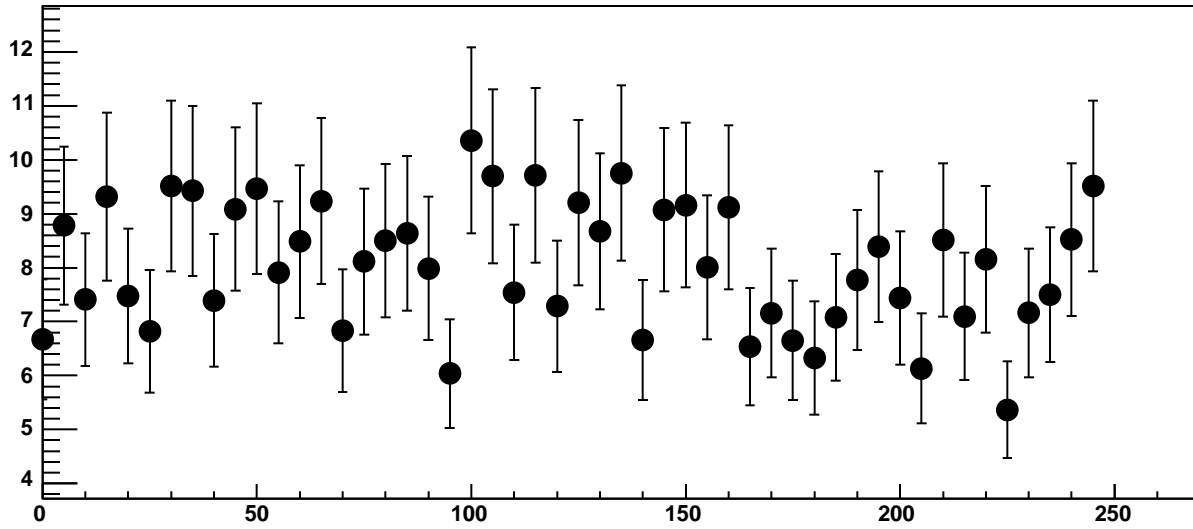


Chip 3, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

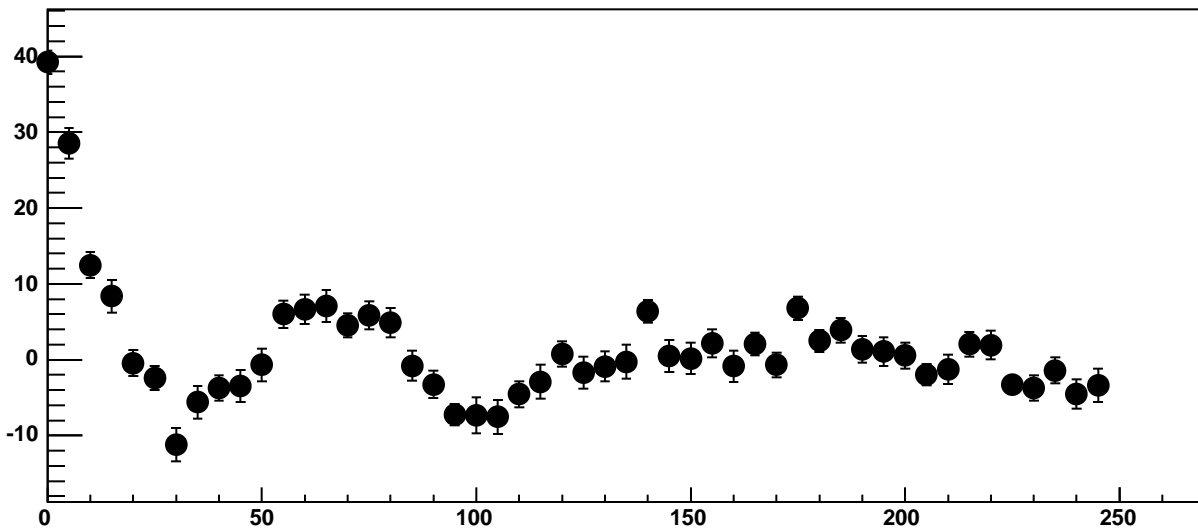


χ^2 / ndf	252.2 / 41
p0	-276.8 ± 0.7005
p1	97.59 ± 0.617
p2	$-3.408\text{e}+07 \pm 9.234\text{e}+05$
p3	$1.905\text{e}+07 \pm 4.73\text{e}+05$
p4	1.746 ± 0.01749

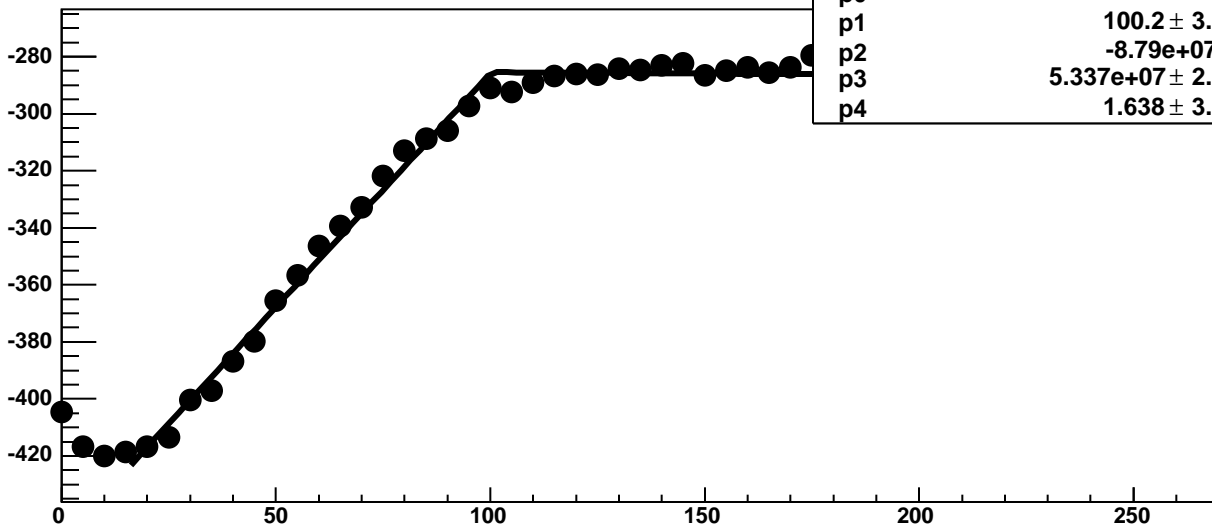
Chip 3, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

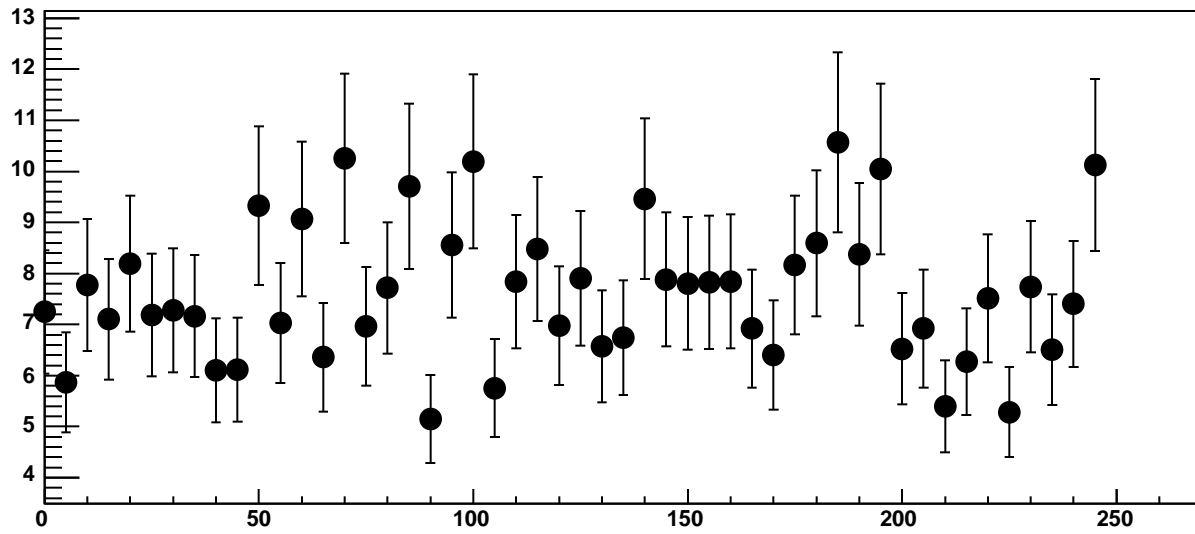


Chip 3, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

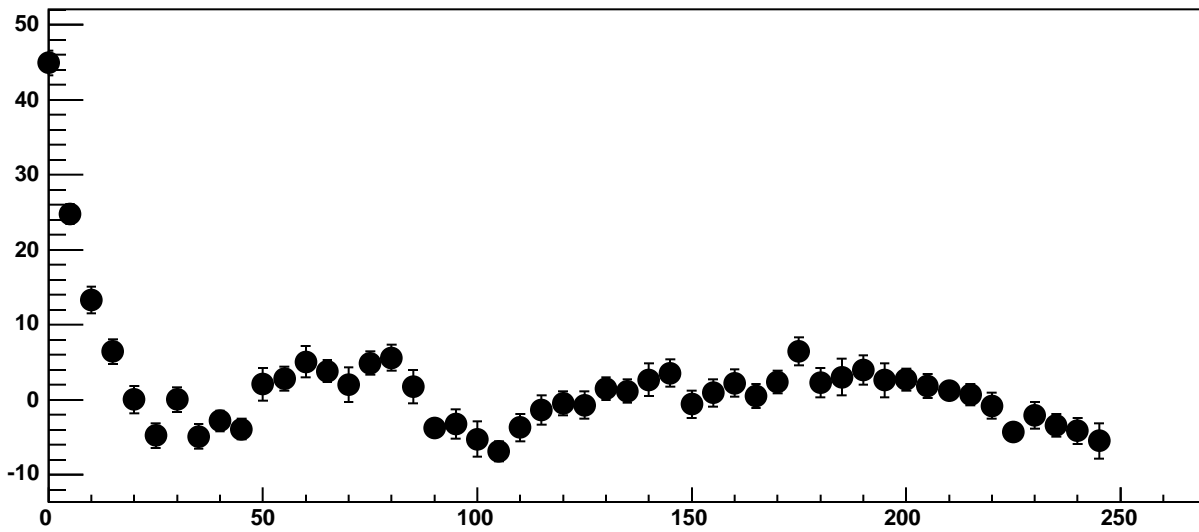


χ^2 / ndf	194.9 / 41
p0	$-285.4 \pm 3.605e+11$
p1	$100.2 \pm 3.605e+11$
p2	$-8.79e+07 \pm 6509$
p3	$5.337e+07 \pm 2.864e+11$
p4	$1.638 \pm 3.605e+11$

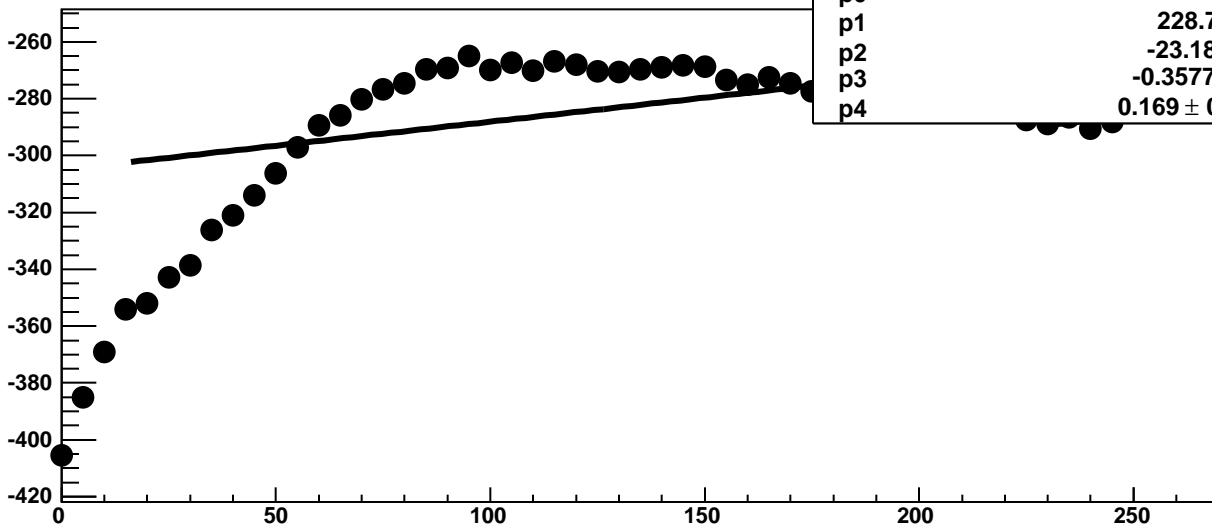
Chip 3, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

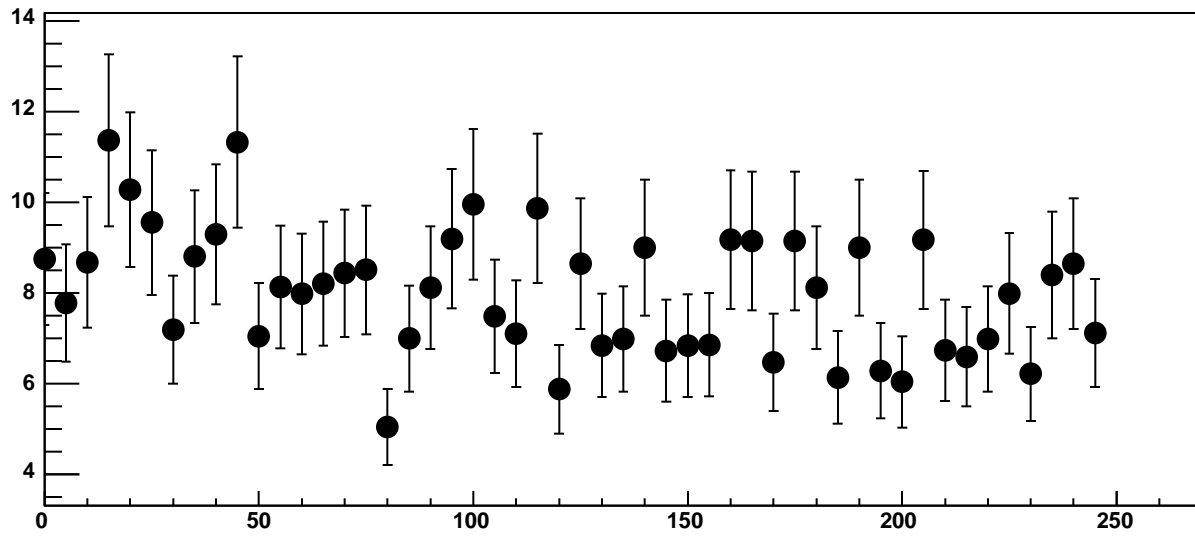


Chip 3, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold

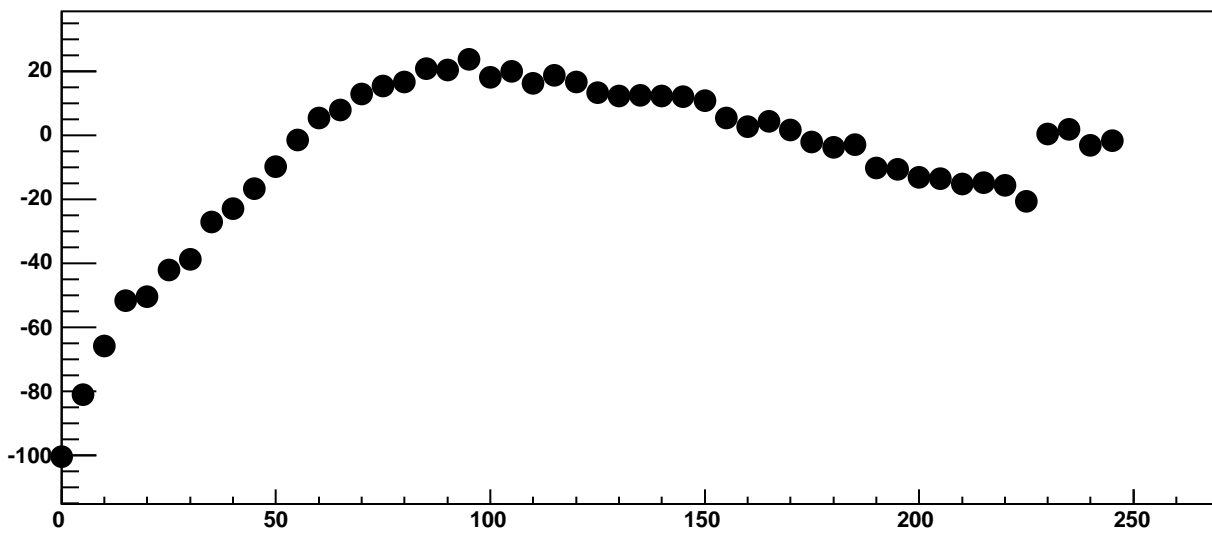


χ^2 / ndf	4397 / 41
p0	-266.3 ± 0.2884
p1	228.7 ± 11.19
p2	-23.18 ± 1.156
p3	-0.3577 ± 5.486
p4	0.169 ± 0.001641

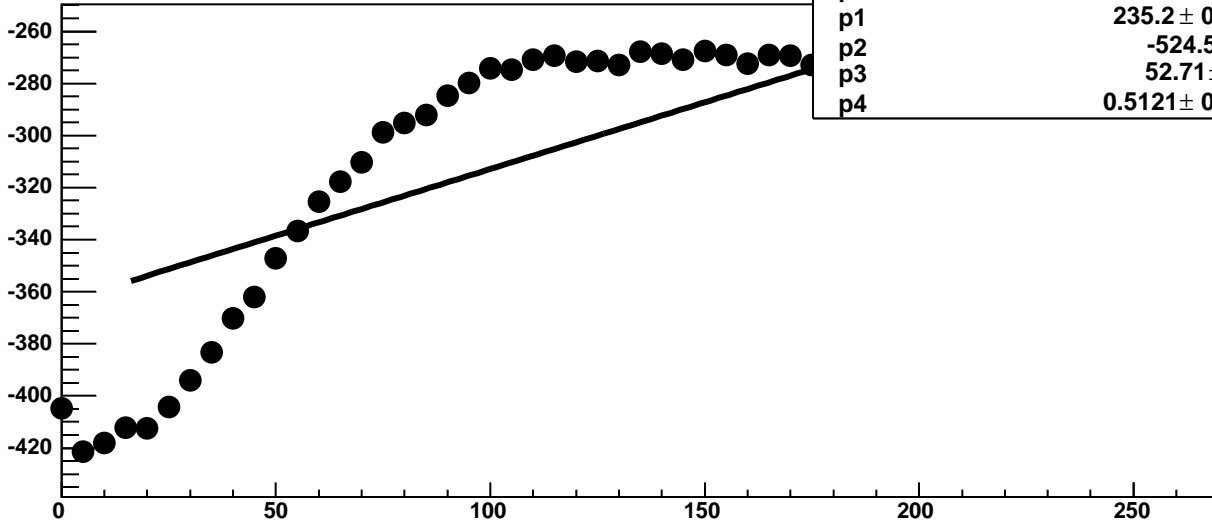
Chip 3, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold

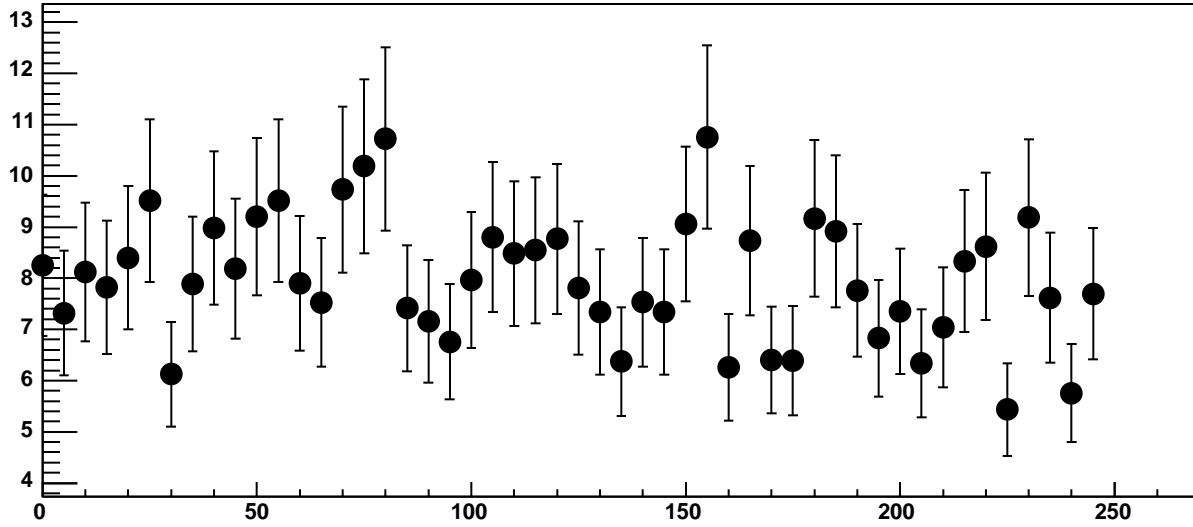


Chip 3, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

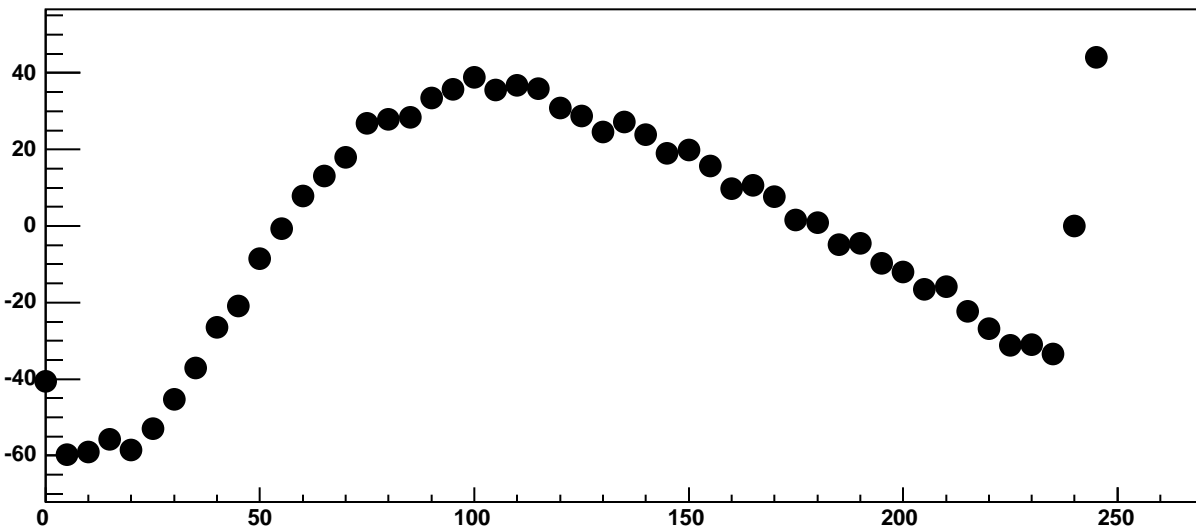


χ^2 / ndf	1.078e+04 / 41
p0	-243.7 ± 0.4983
p1	235.2 ± 0.003569
p2	-524.5 ± 16.43
p3	52.71 ± 0.1163
p4	0.5121 ± 0.004059

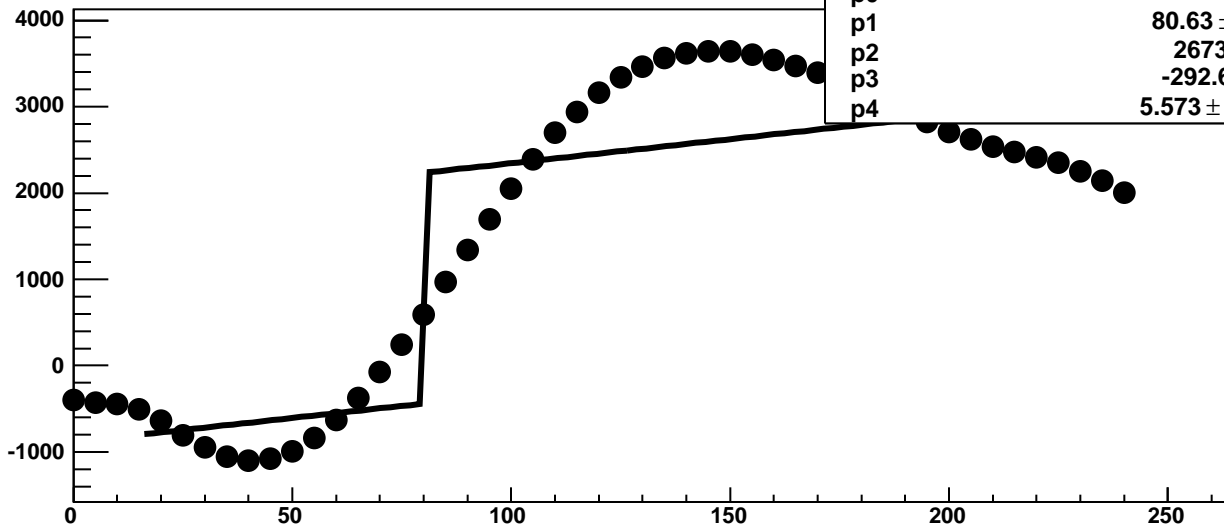
Chip 3, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

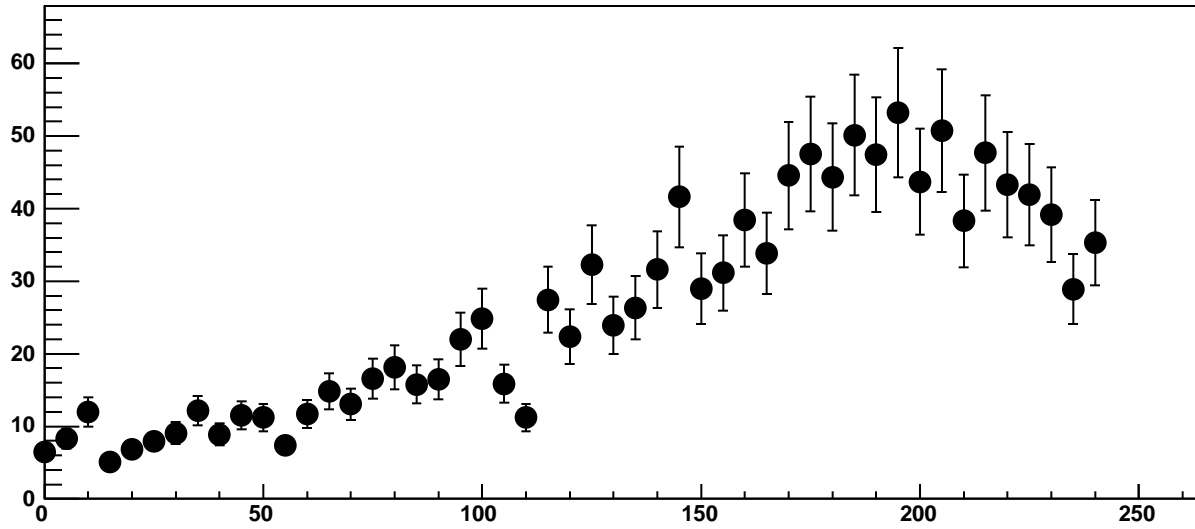


Chip 3, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

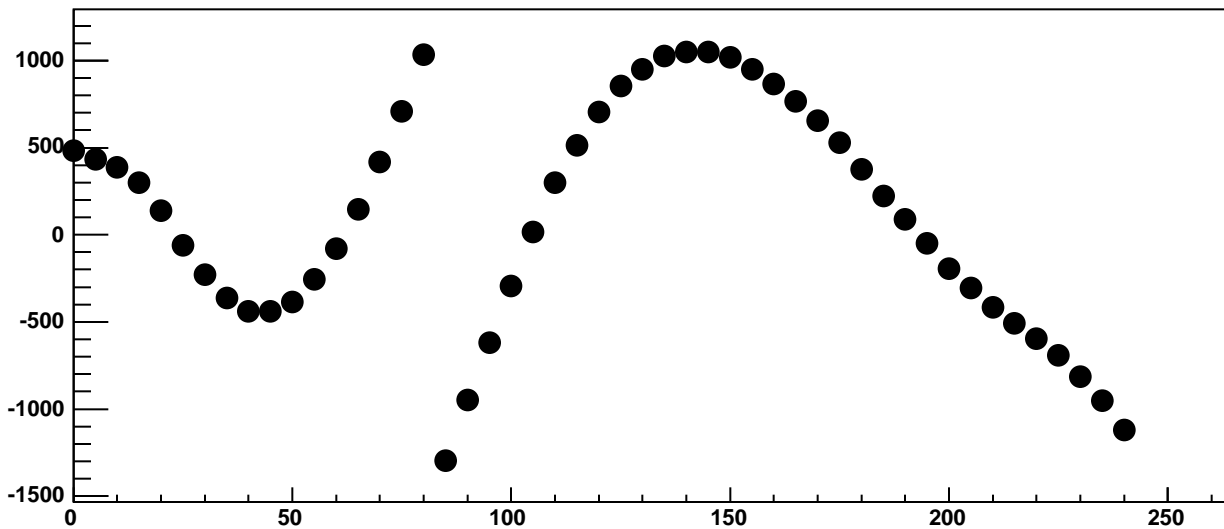


χ^2 / ndf	8.237e+05 / 41
p0	-435.9 ± 0.5014
p1	80.63 ± 0.9636
p2	2673 ± 1.032
p3	-292.6 ± 3.491
p4	5.573 ± 0.03223

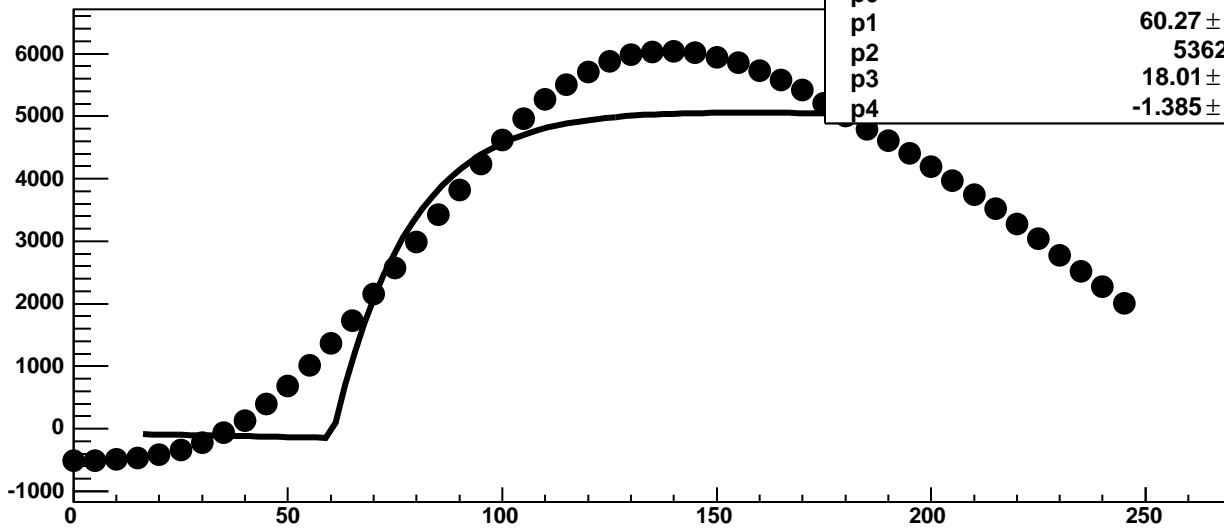
Chip 3, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



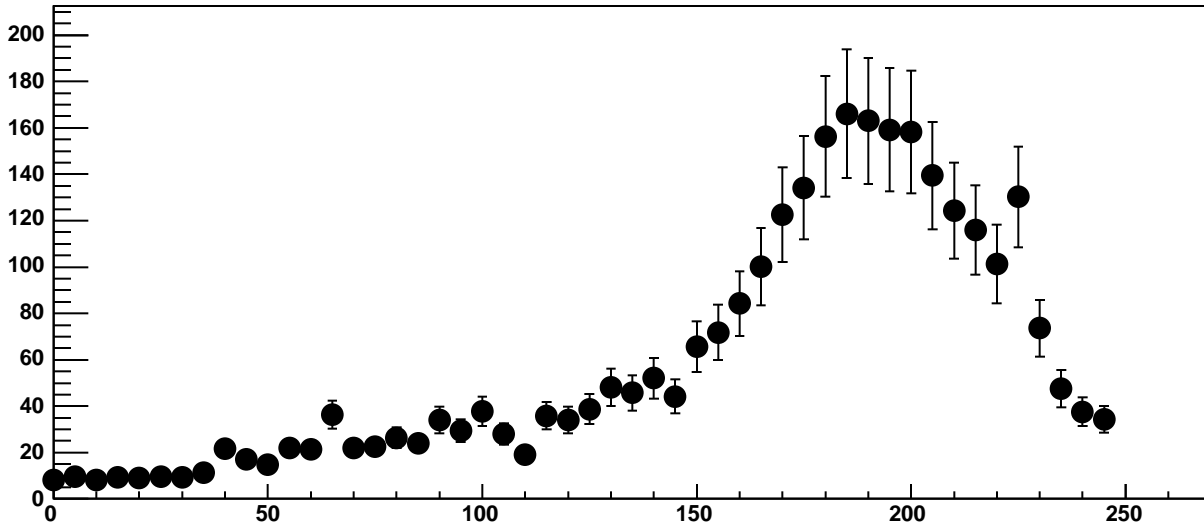
Chip 3, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold



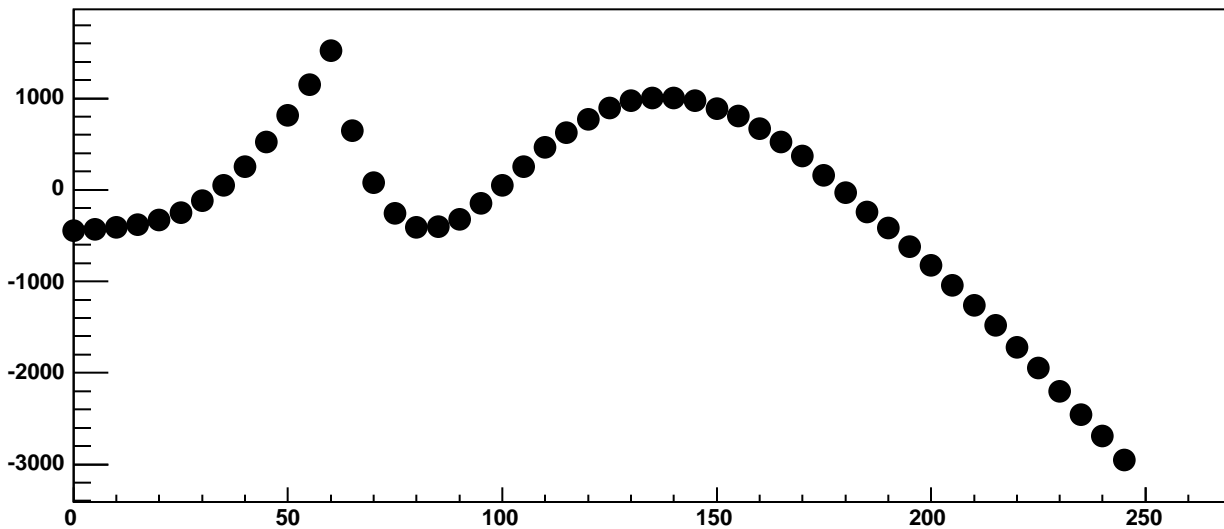
Chip 3, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold



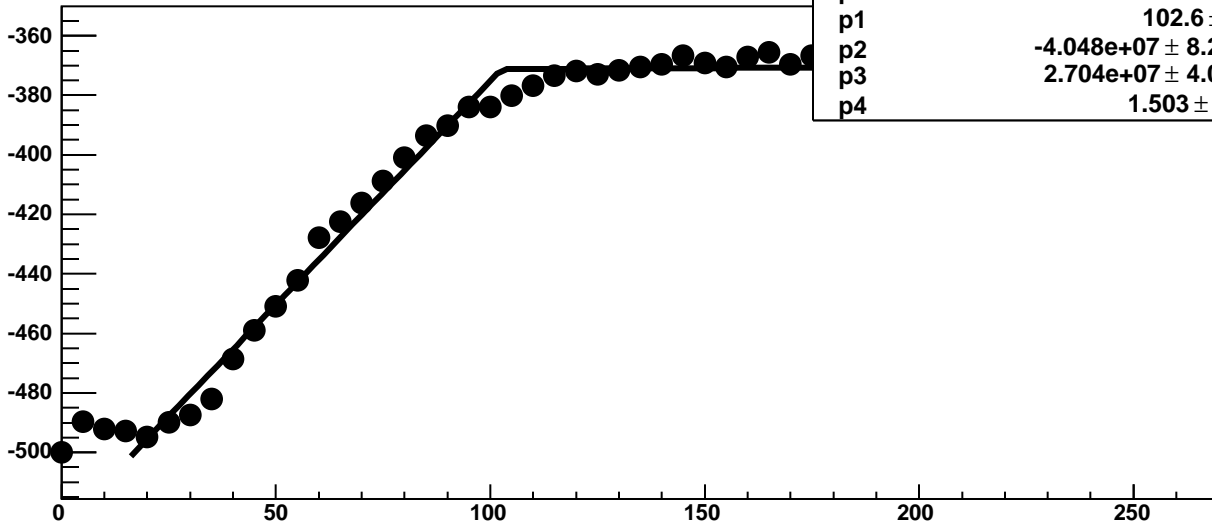
Chip 3, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold

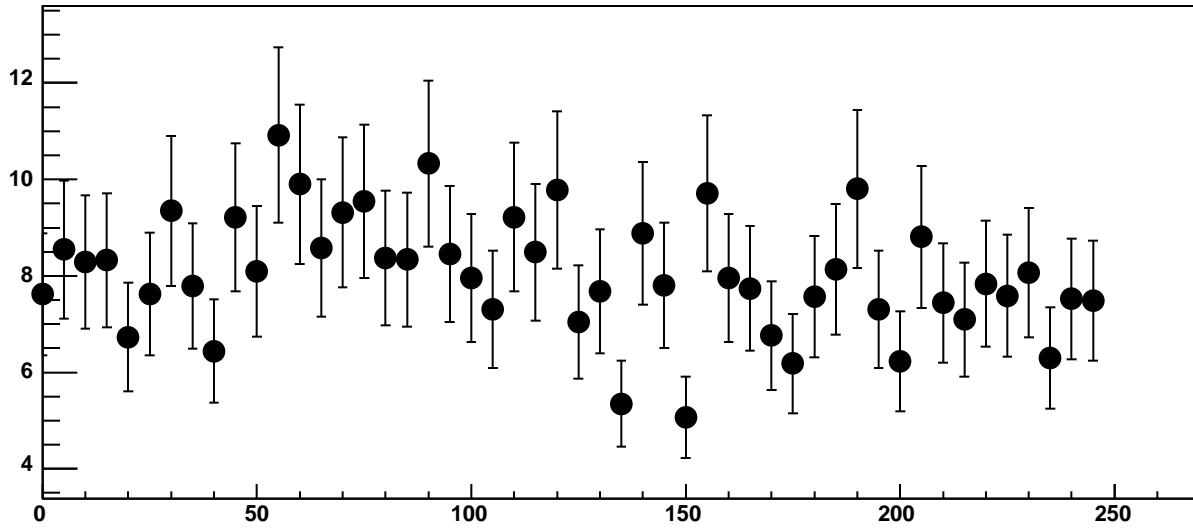


Chip 3, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

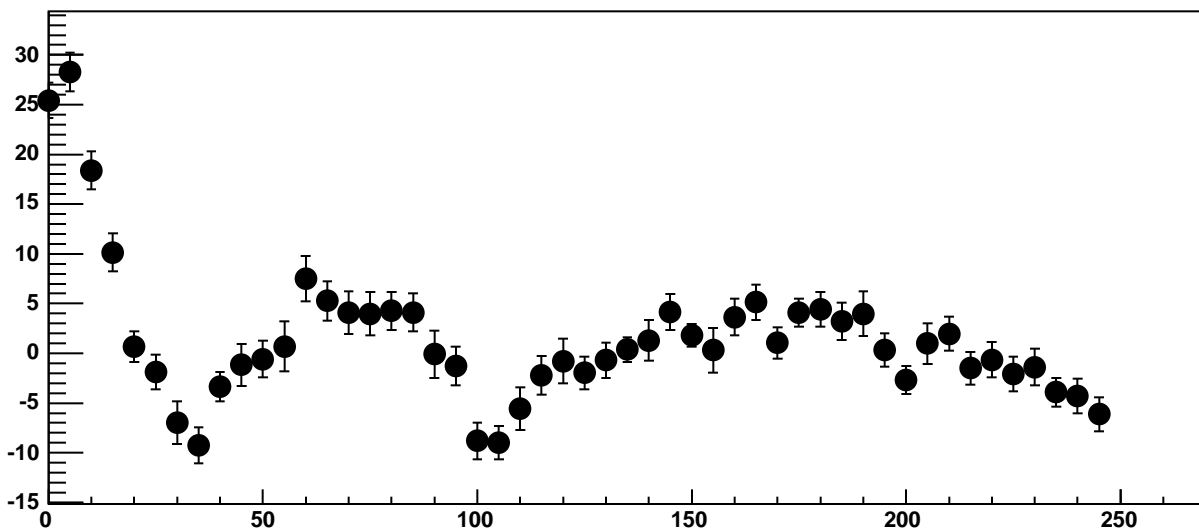


χ^2 / ndf	232.7 / 41
p0	-371.2 ± 0.6591
p1	102.6 ± 0.7599
p2	$-4.048\text{e}+07 \pm 8.268\text{e}+05$
p3	$2.704\text{e}+07 \pm 4.083\text{e}+05$
p4	1.503 ± 0.01699

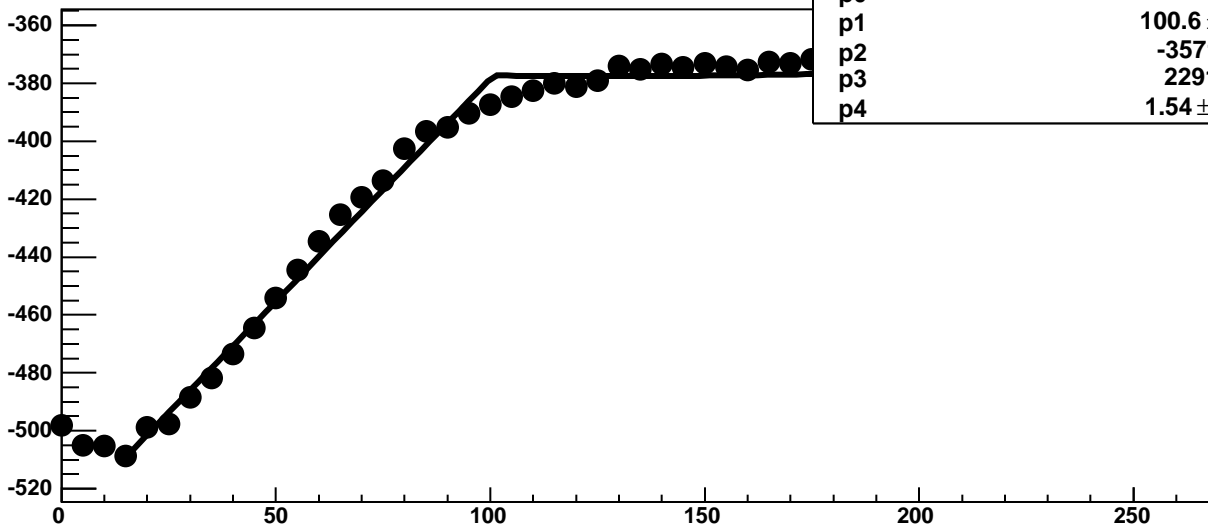
Chip 3, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold

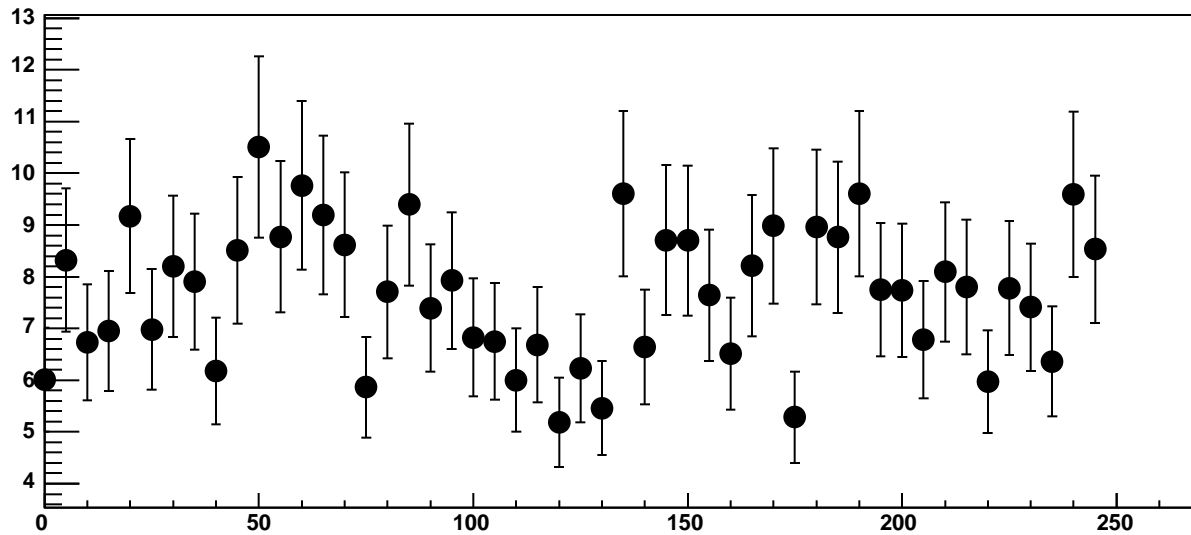


Chip 3, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

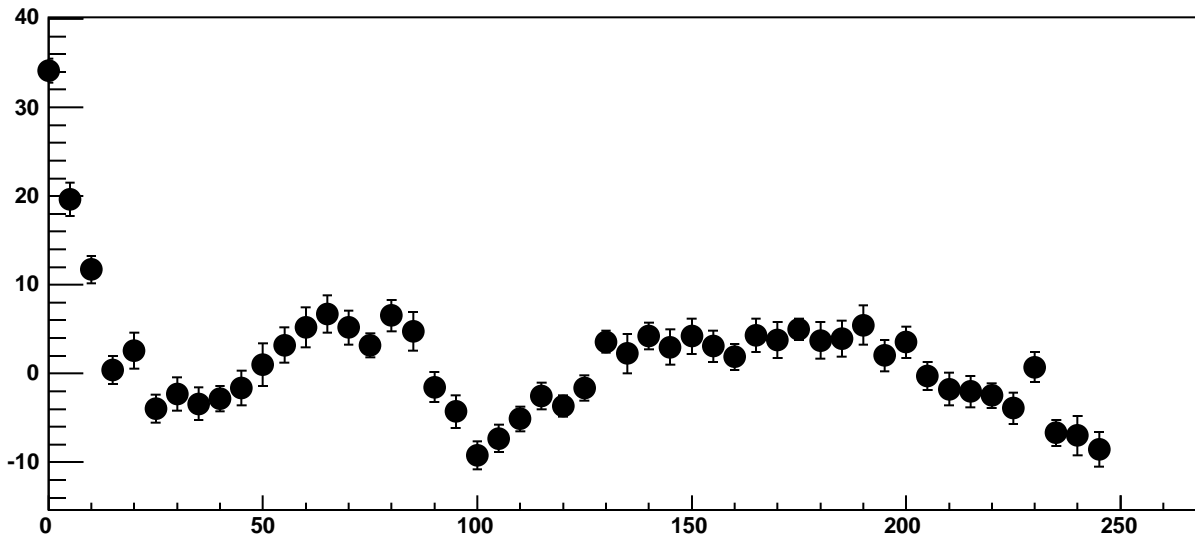


χ^2 / ndf	271.5 / 41
p0	-377.3 ± 0.5789
p1	100.6 ± 0.6315
p2	-3571 ± 75.14
p3	2291 ± 23.86
p4	1.54 ± 0.01561

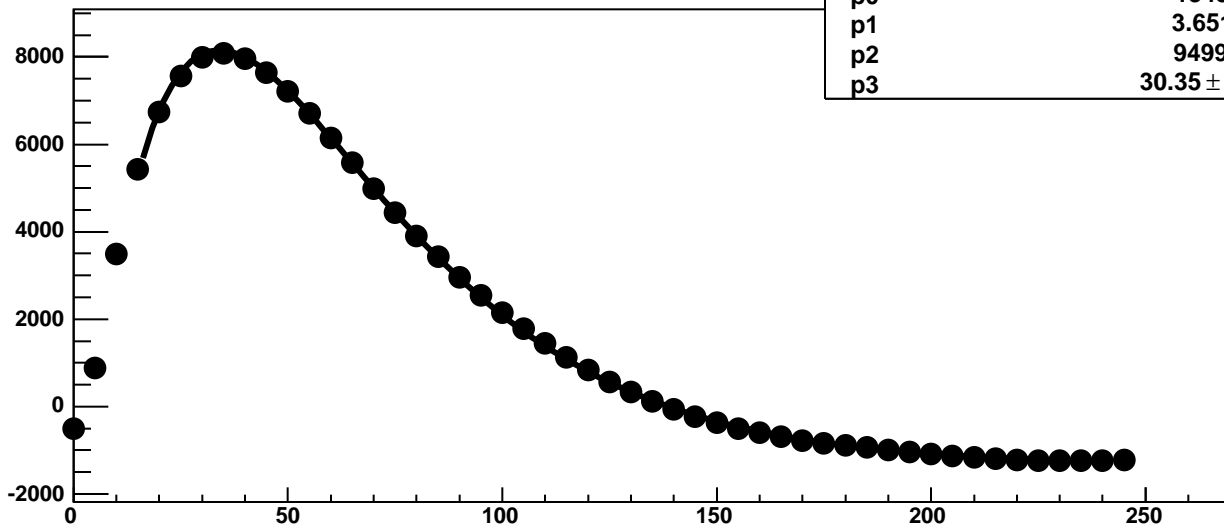
Chip 3, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

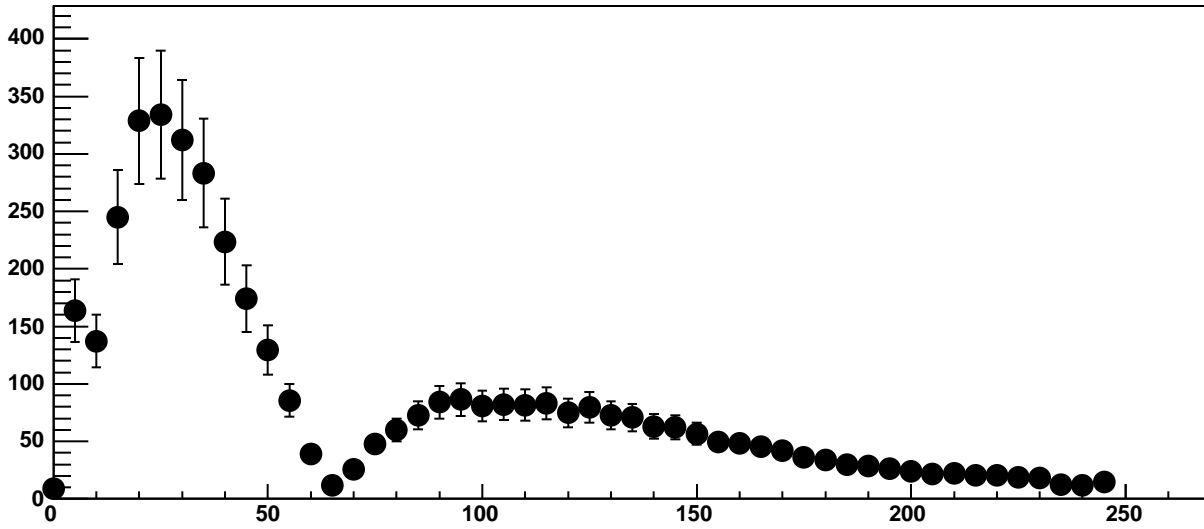


Chip 3, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold

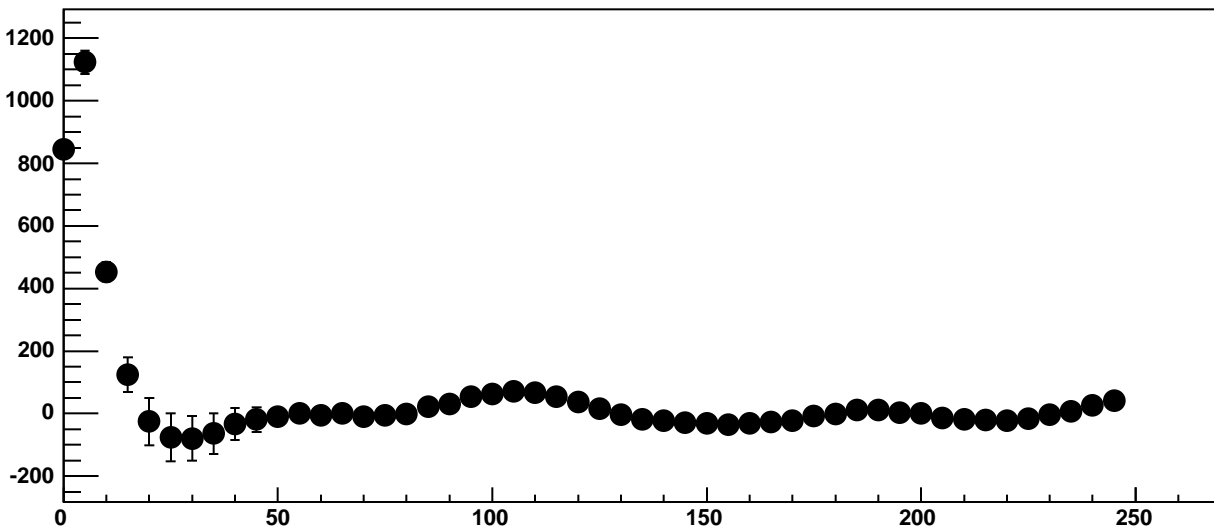


χ^2 / ndf	304.3 / 42
p0	-1343 ± 1.785
p1	3.651 ± 0.113
p2	9499 ± 13.02
p3	30.35 ± 0.04465

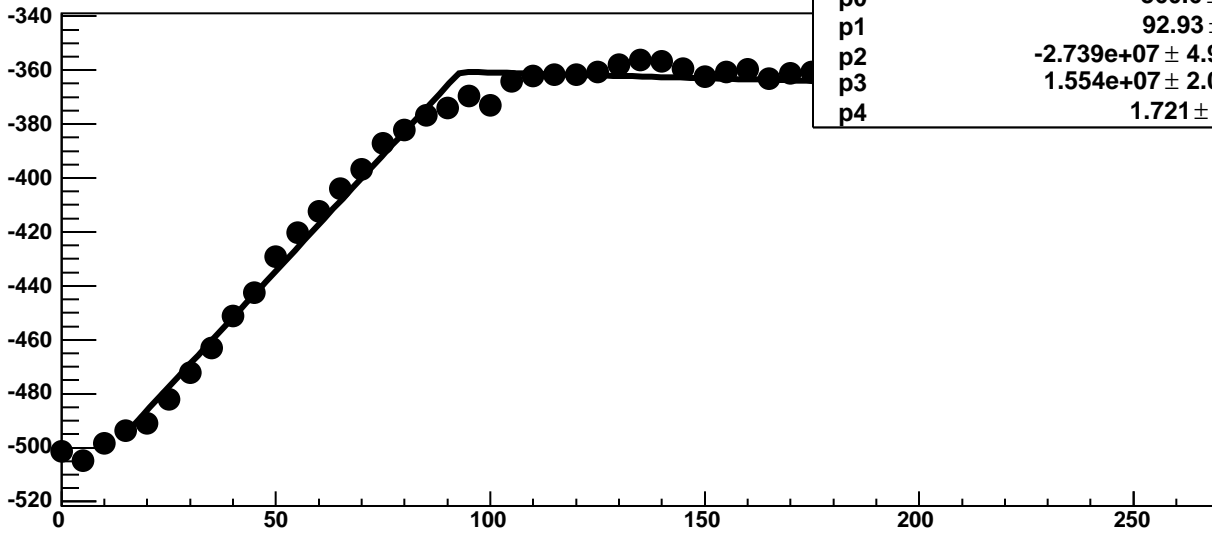
Chip 3, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold

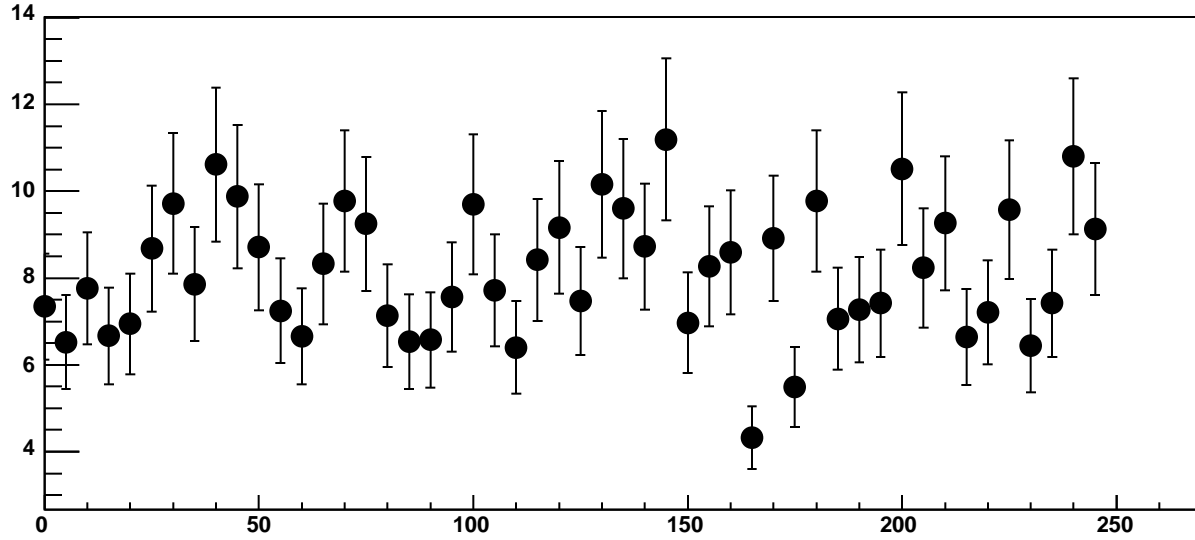


Chip 3, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold

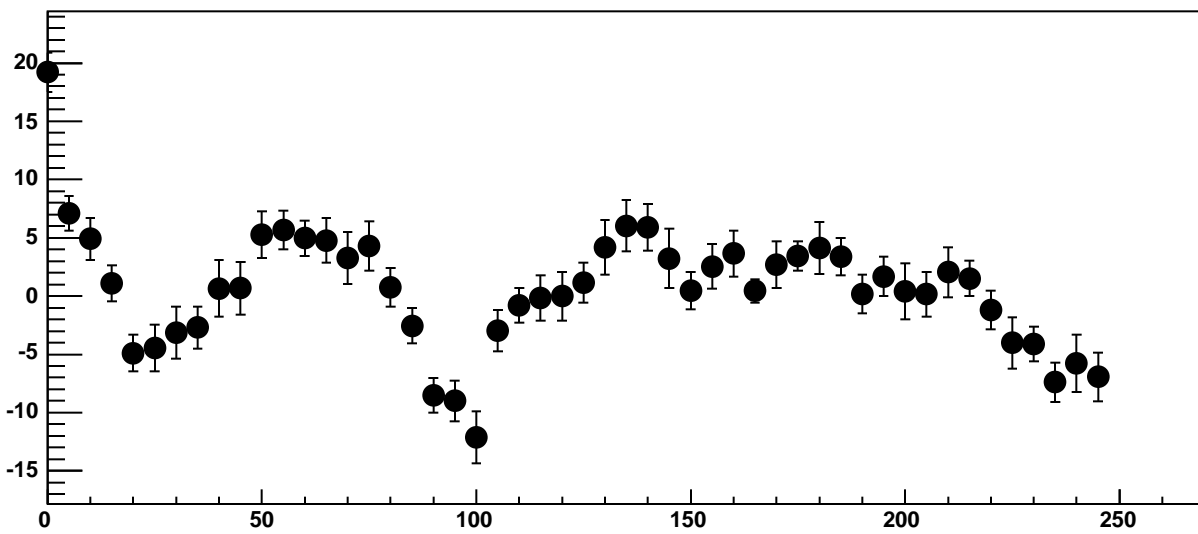


χ^2 / ndf	238.1 / 41
p0	-360.6 ± 0.6534
p1	92.93 ± 0.6078
p2	$-2.739\text{e}+07 \pm 4.912\text{e}+05$
p3	$1.554\text{e}+07 \pm 2.096\text{e}+05$
p4	1.721 ± 0.01814

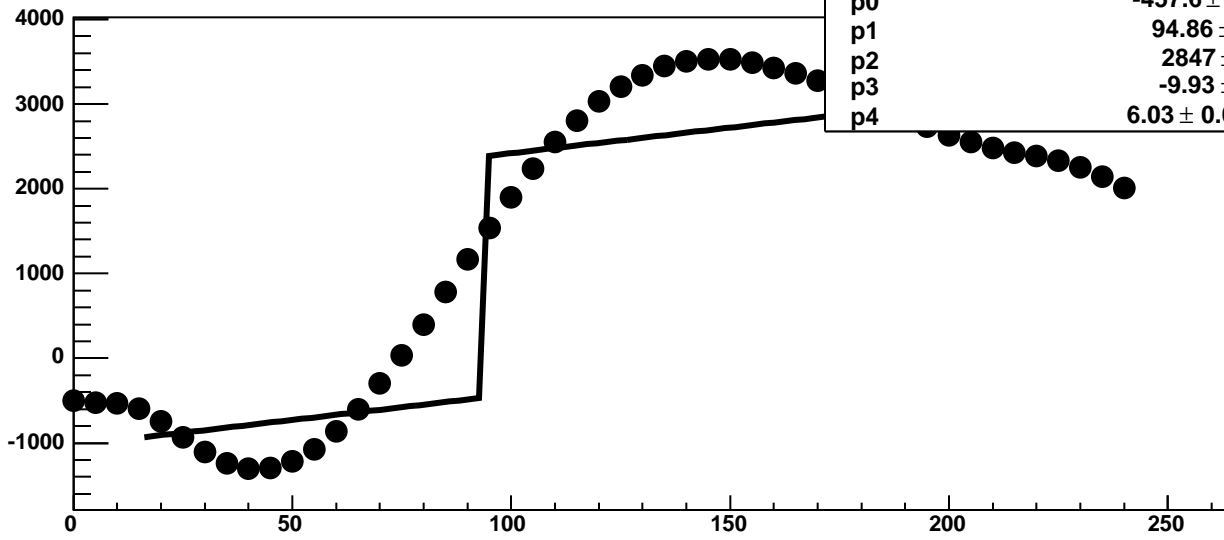
Chip 3, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



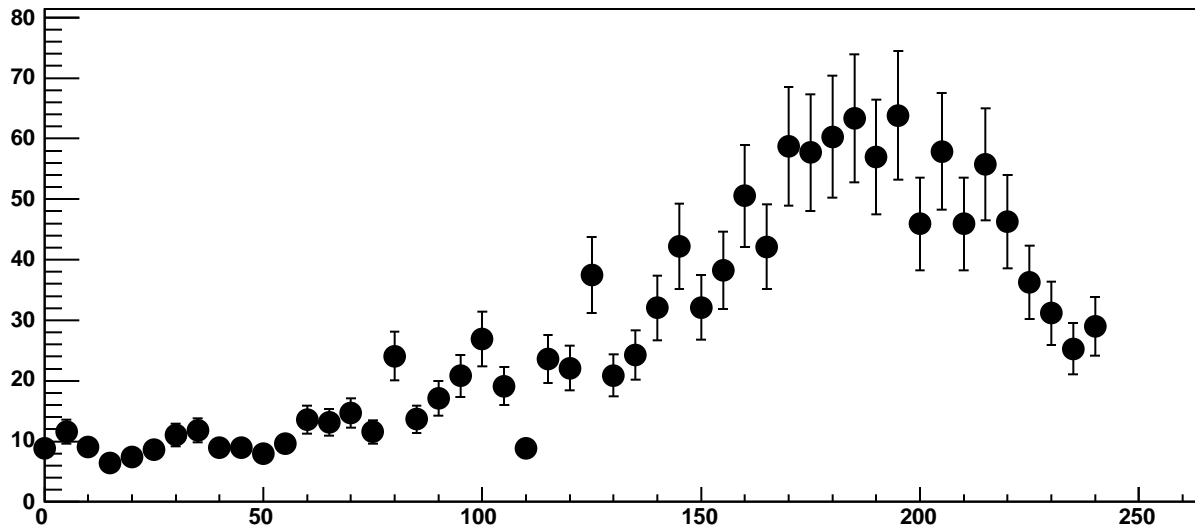
Chip 3, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold



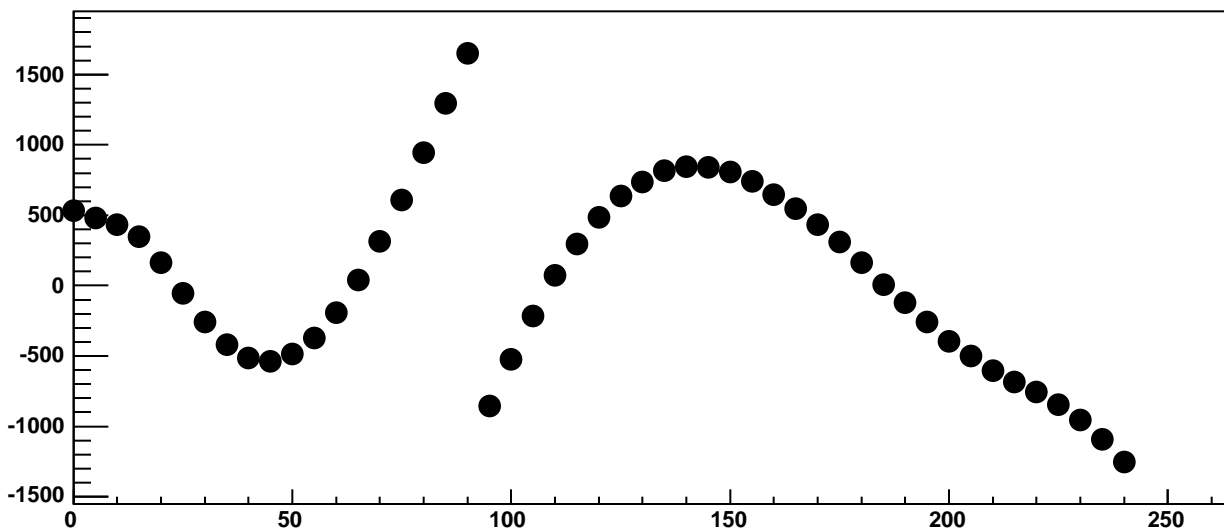
Chip 3, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold



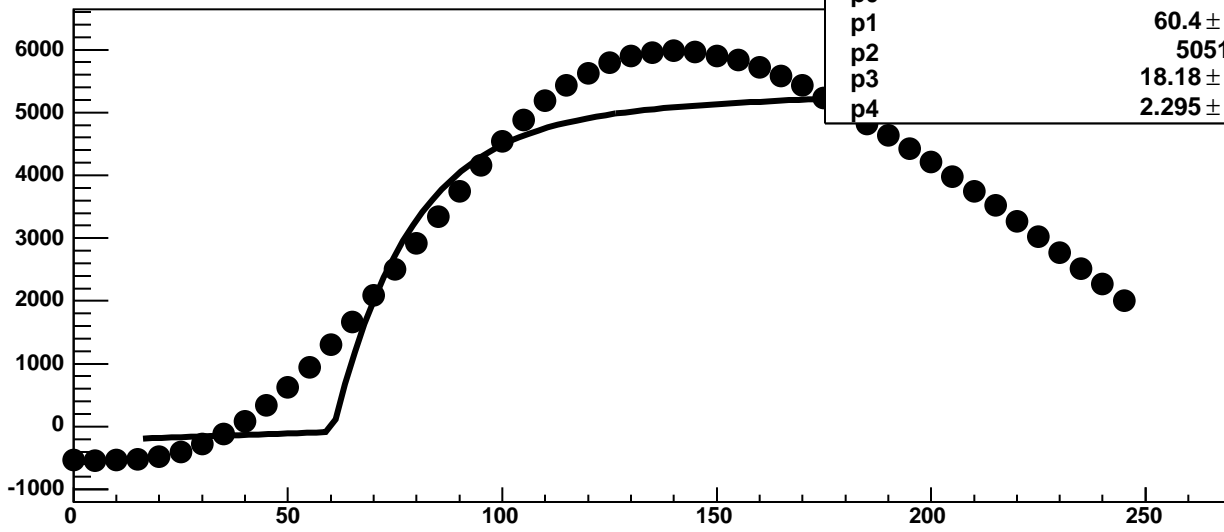
Chip 3, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold



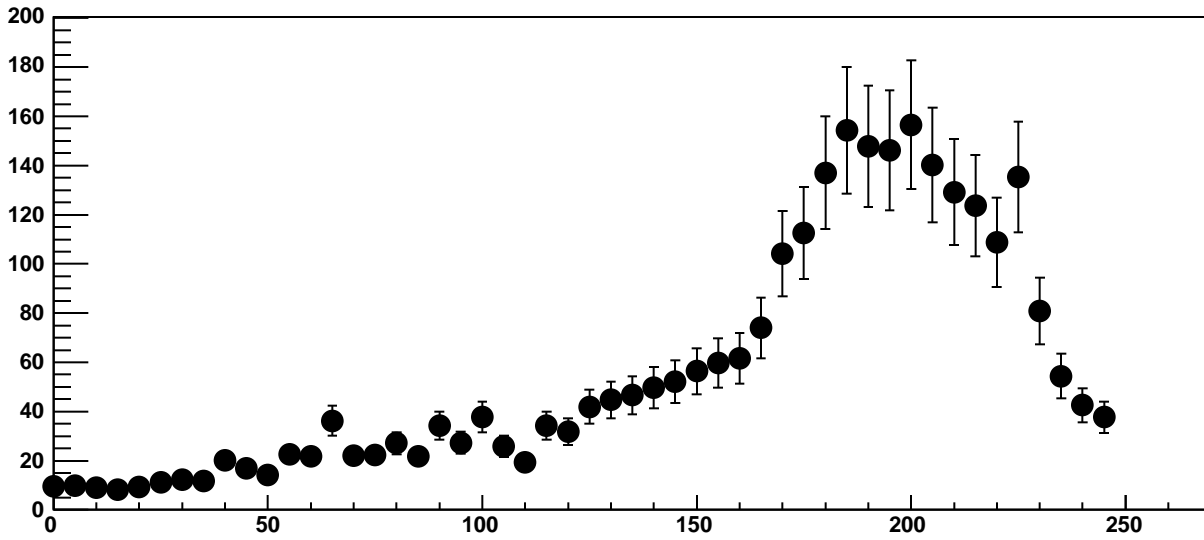
Chip 3, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold



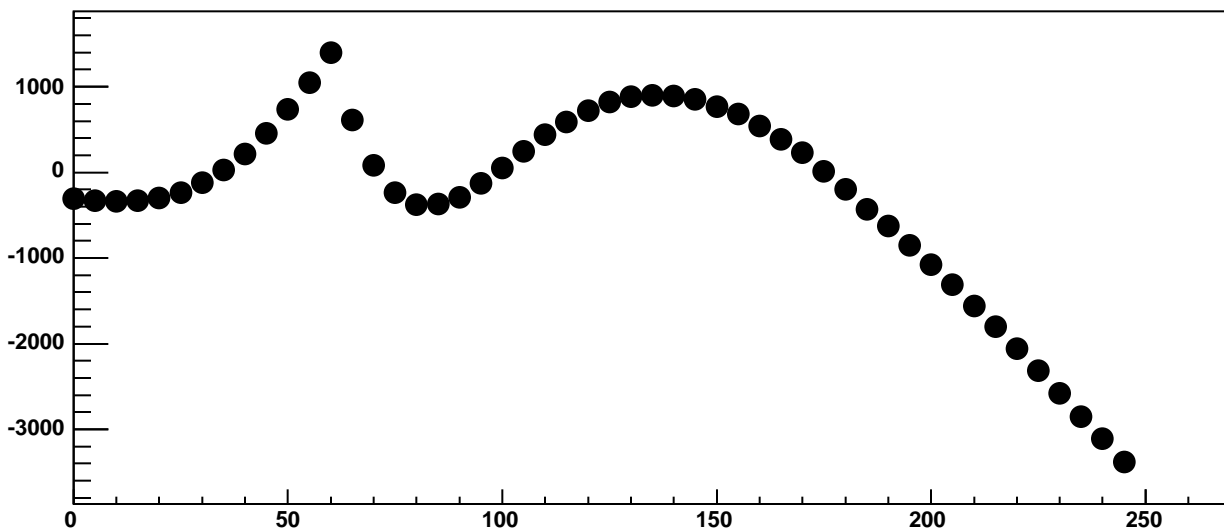
Chip 3, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold



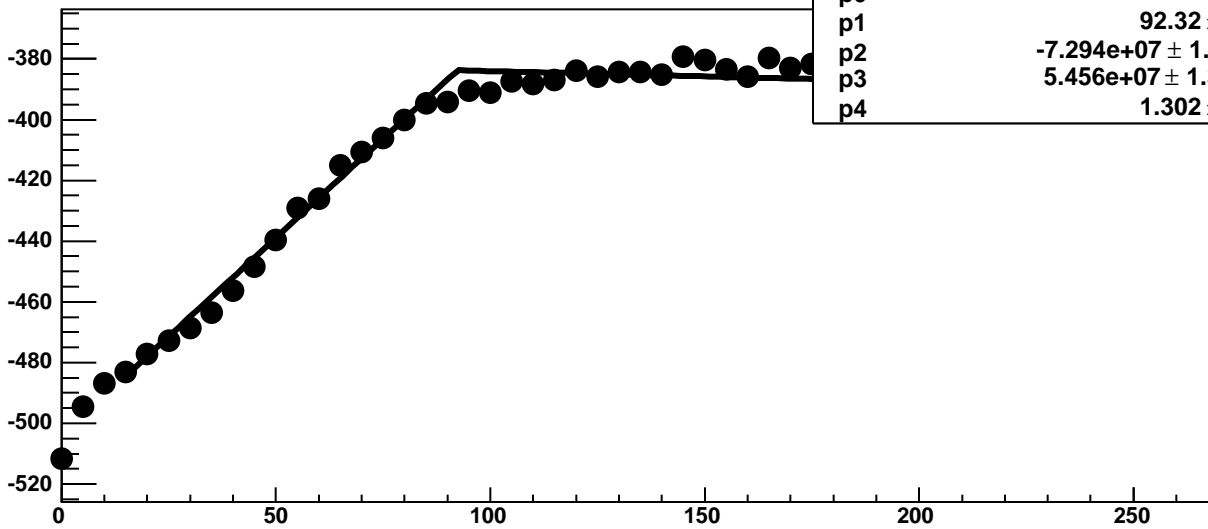
Chip 3, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

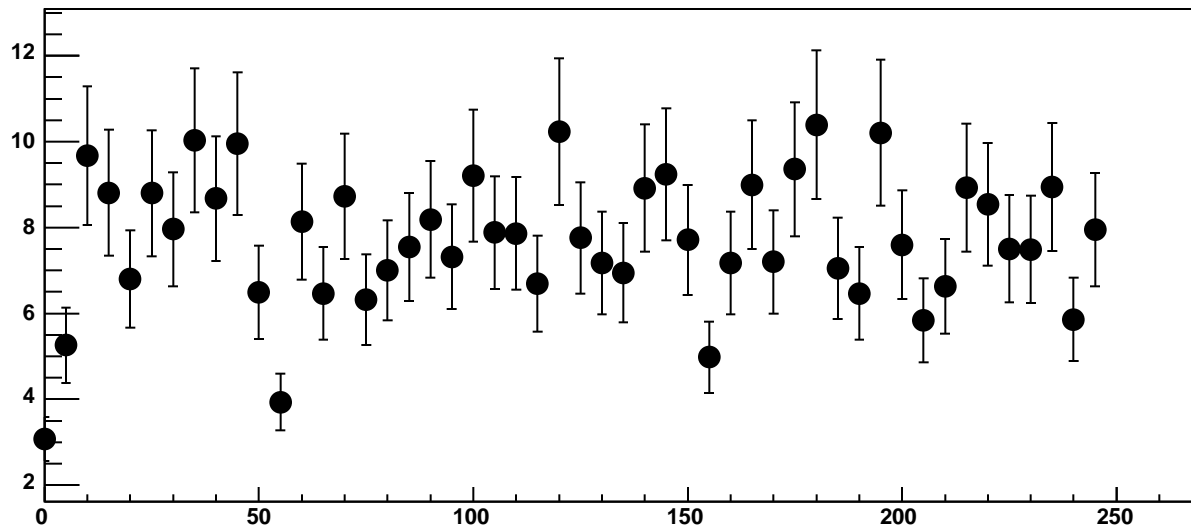


Chip 3, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold

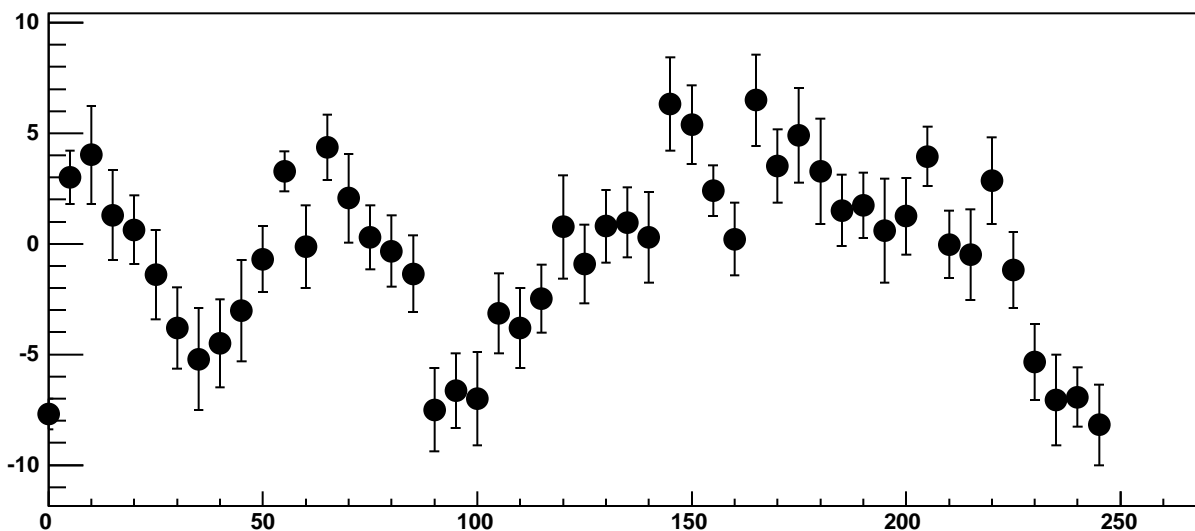


χ^2 / ndf	201.1 / 41
p0	-383.7 ± 0.643
p1	92.32 ± 0.8126
p2	$-7.294\text{e}+07 \pm 1.139\text{e}+06$
p3	$5.456\text{e}+07 \pm 1.345\text{e}+05$
p4	1.302 ± 0.0196

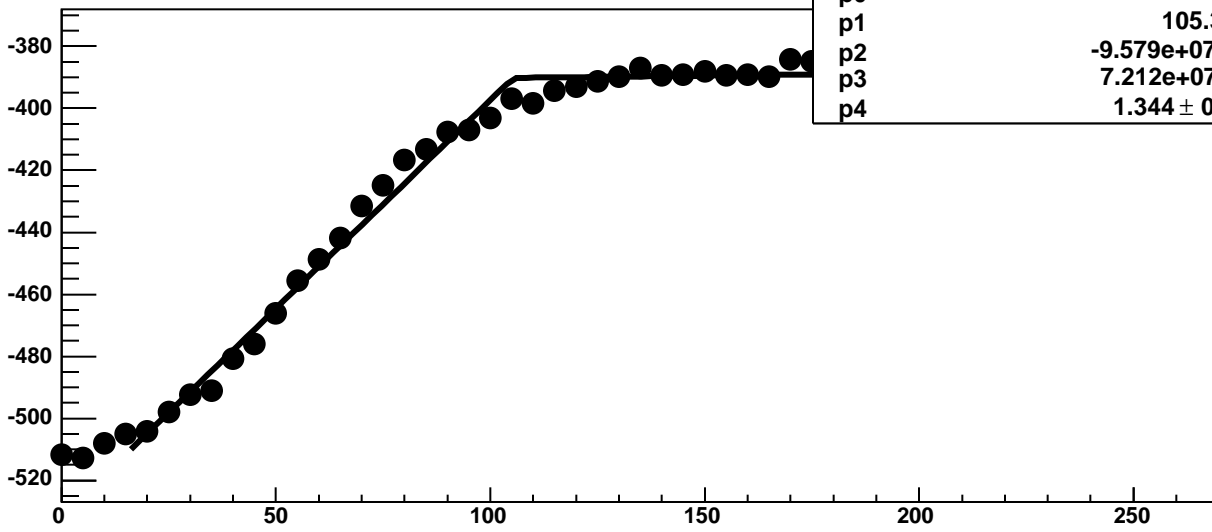
Chip 3, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold

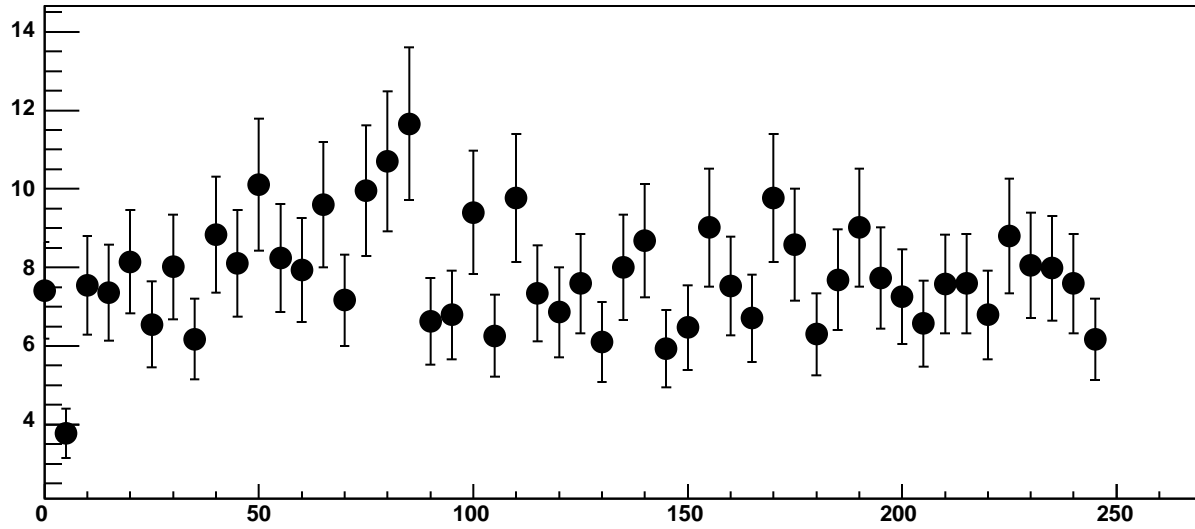


Chip 3, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

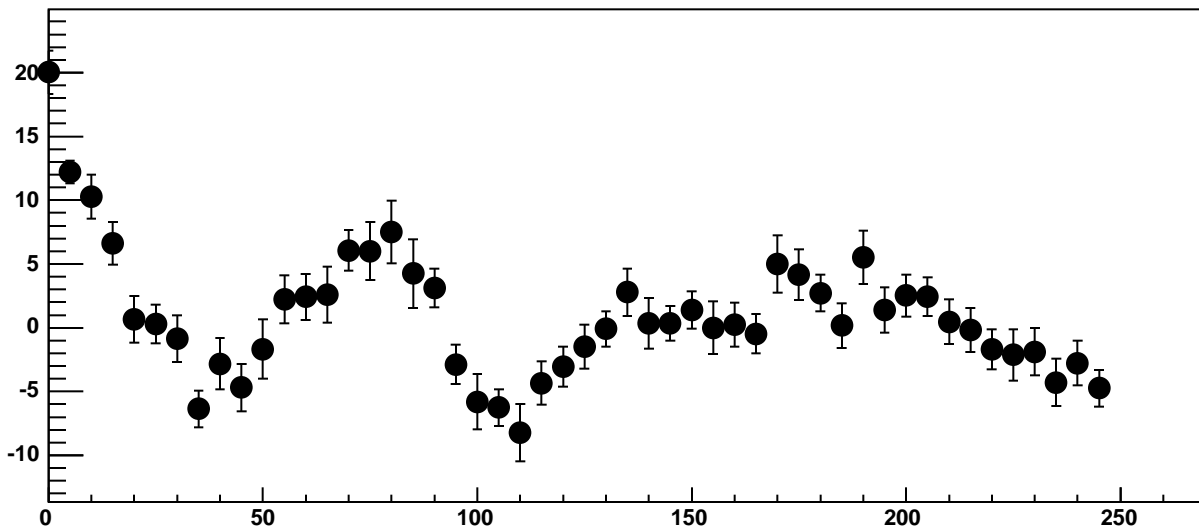


χ^2 / ndf	180.9 / 41
p0	-390.2 ± 0.6126
p1	105.3 ± 0.75
p2	$-9.579\text{e}+07 \pm 1.414$
p3	$7.212\text{e}+07 \pm 1.414$
p4	1.344 ± 0.007358

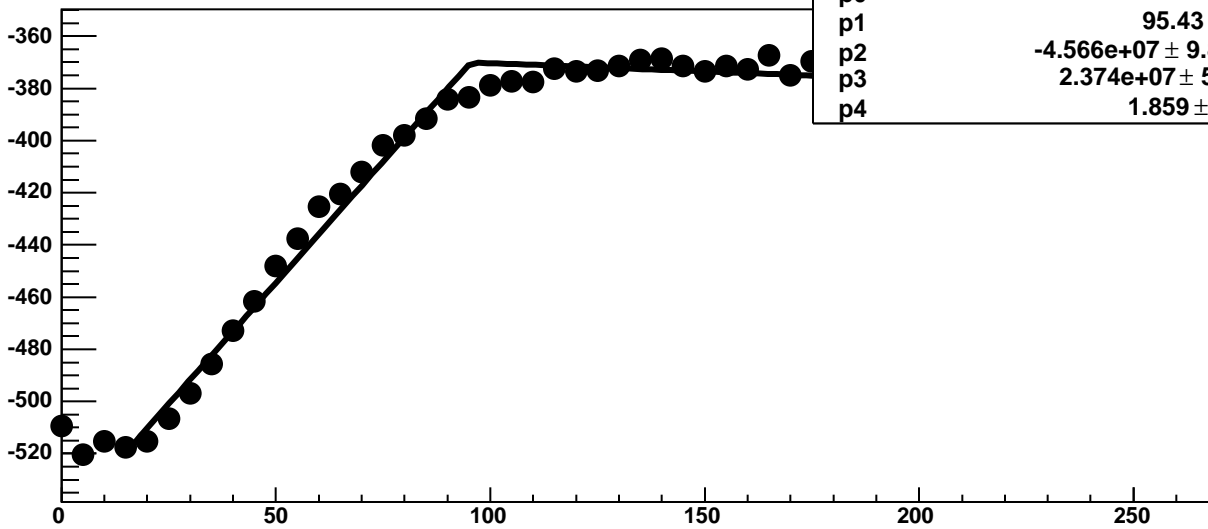
Chip 3, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

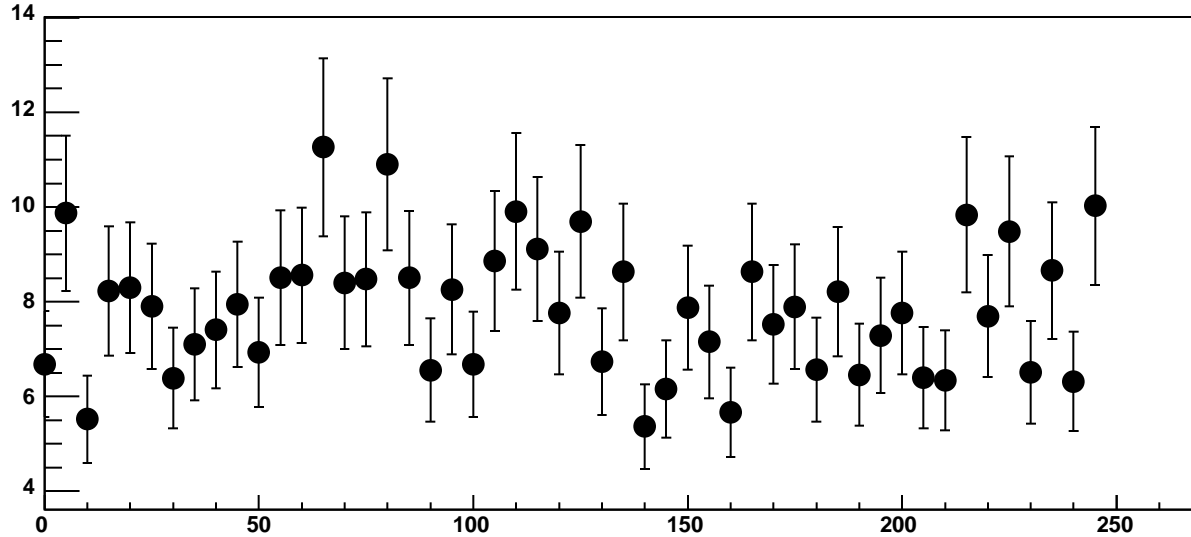


Chip 3, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

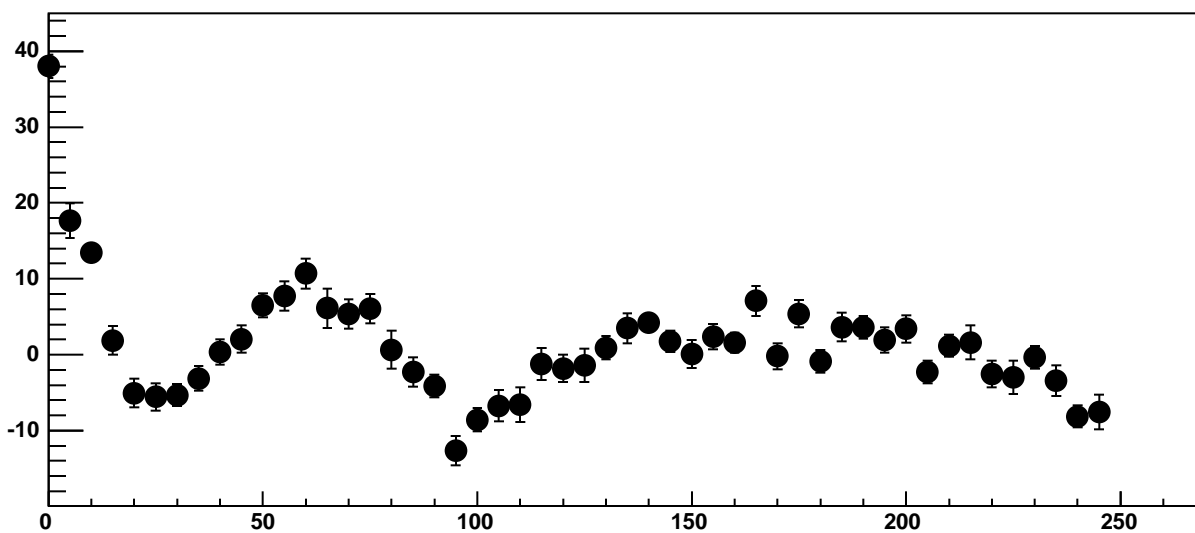


χ^2 / ndf	325.8 / 41
p0	-370 ± 0.6277
p1	95.43 ± 0.5187
p2	$-4.566\text{e}+07 \pm 9.884\text{e}+05$
p3	$2.374\text{e}+07 \pm 5.83\text{e}+05$
p4	1.859 ± 0.01582

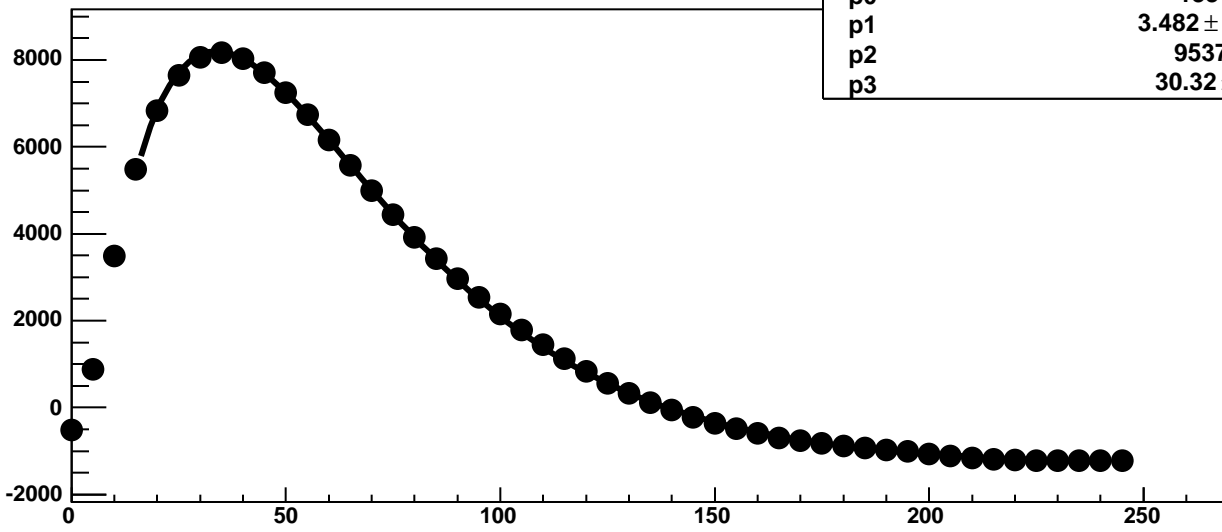
Chip 3, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

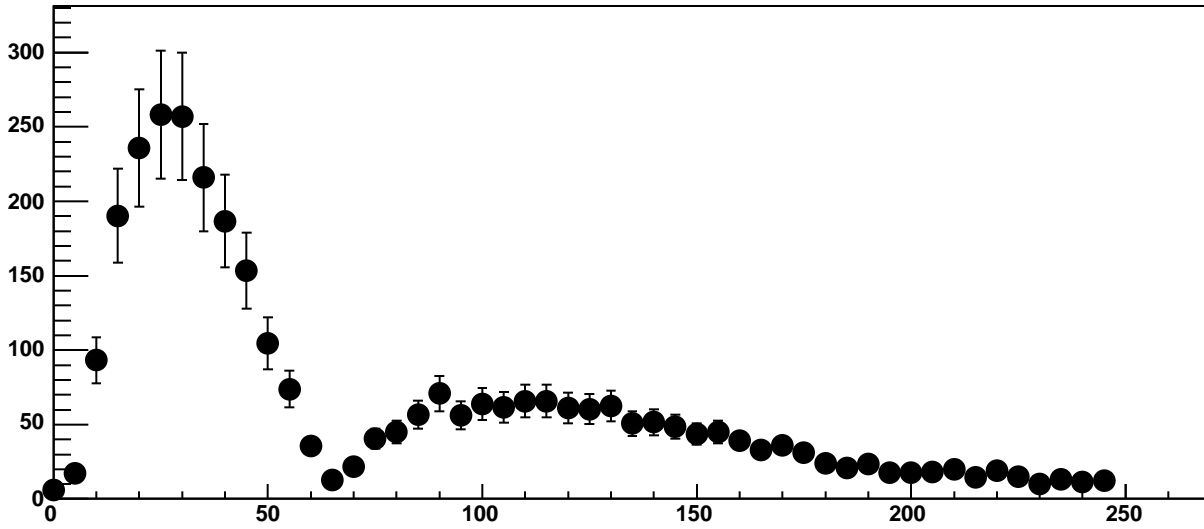


Chip 3, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold

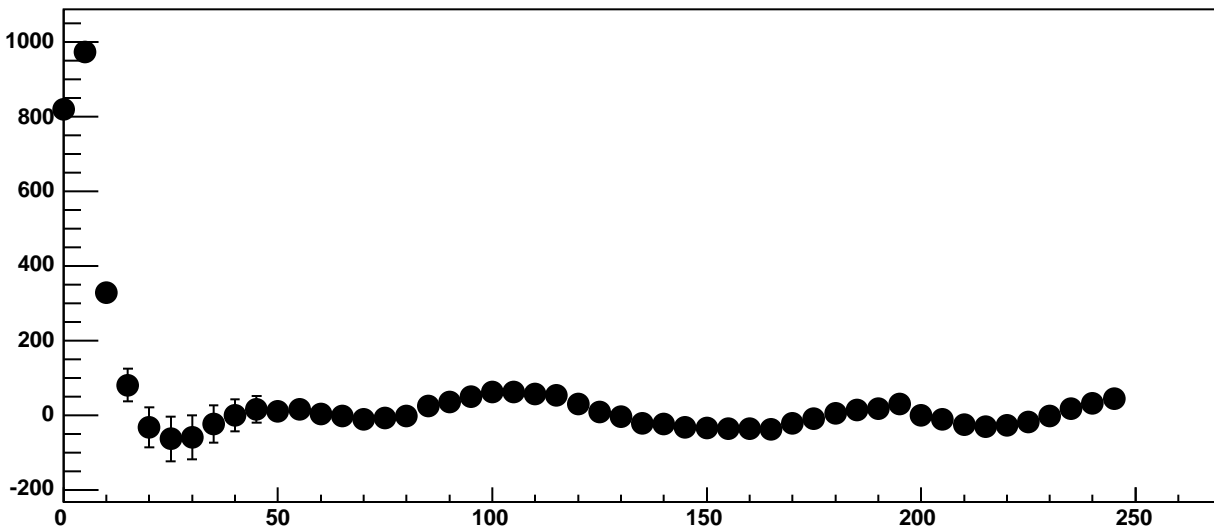


χ^2 / ndf	632.7 / 42
p0	-1331 ± 1.481
p1	3.482 ± 0.08868
p2	9537 ± 10.41
p3	30.32 ± 0.0361

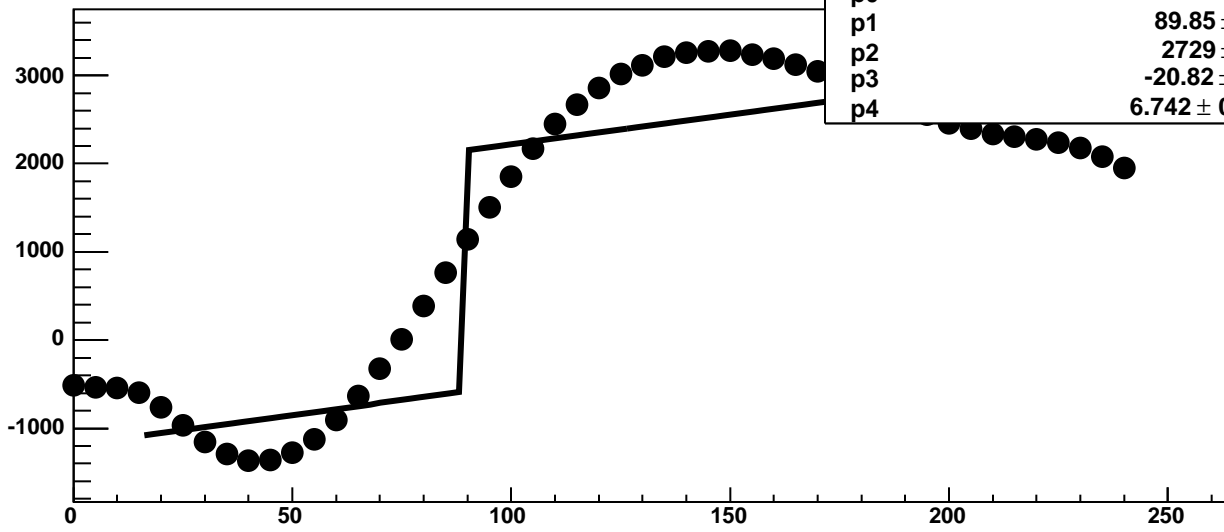
Chip 3, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold



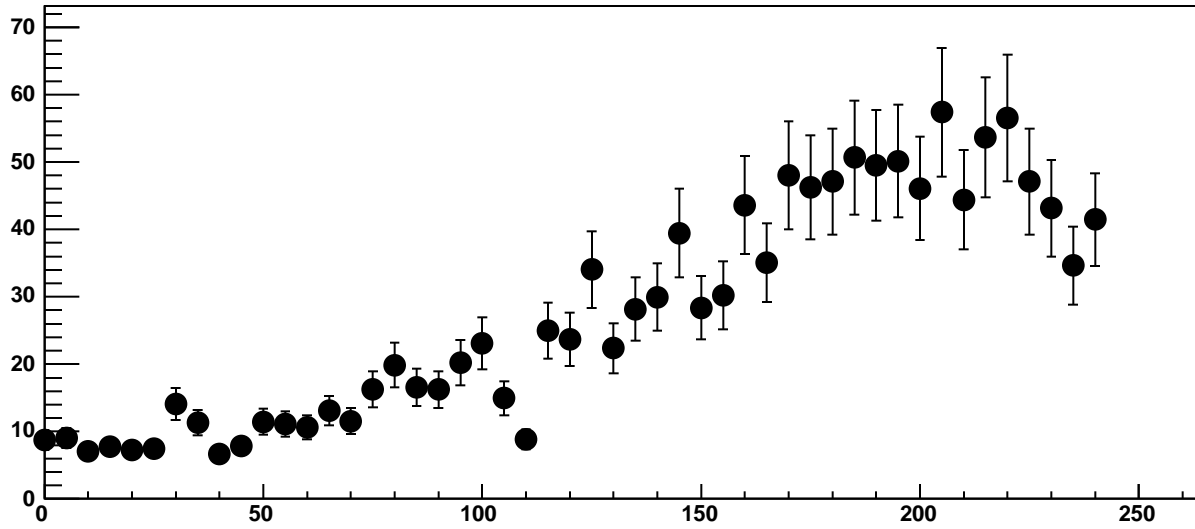
Chip 3, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold



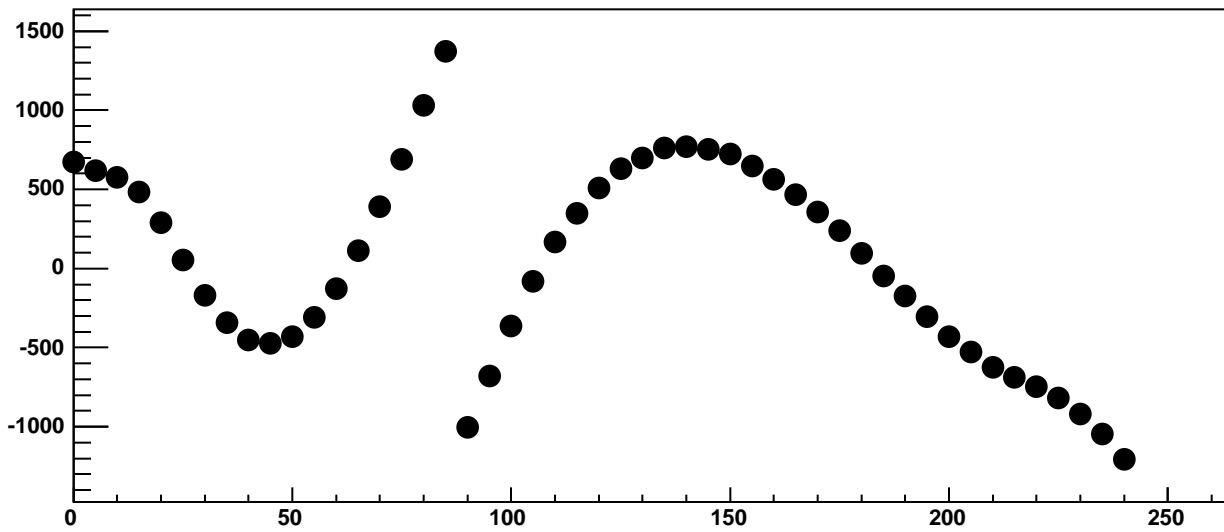
Chip 3, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold



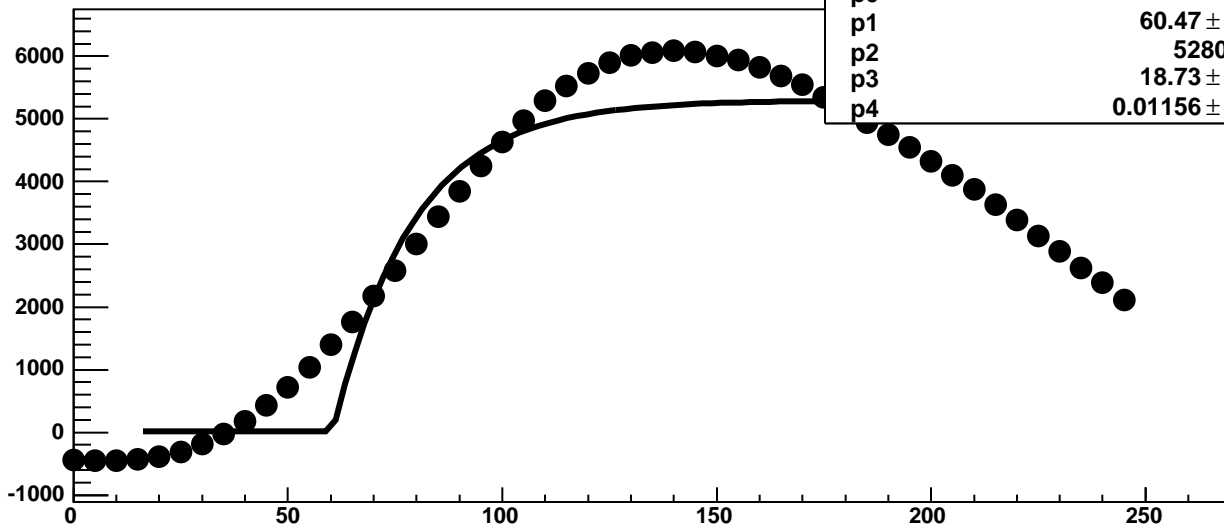
Chip 3, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold

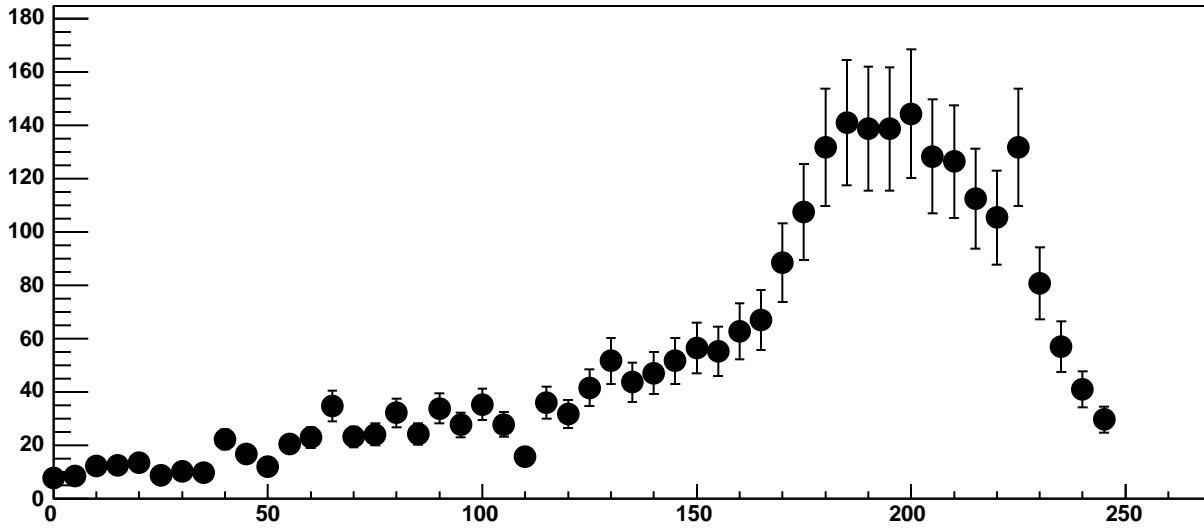


Chip 3, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

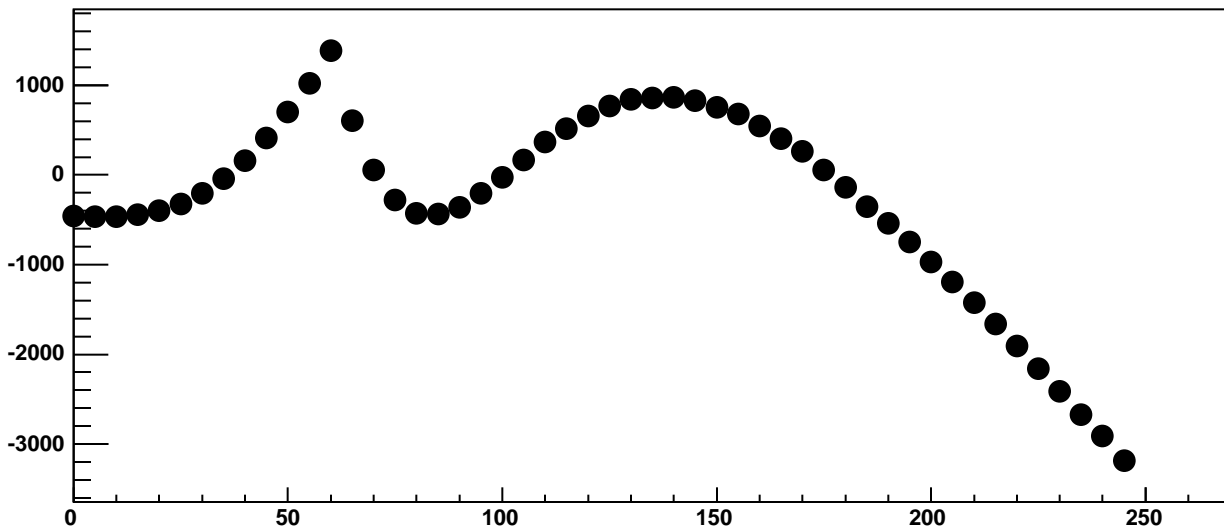


χ^2 / ndf	5.257e+05 / 41
p0	16.24 ± 1.615
p1	60.47 ± 0.02982
p2	5280 ± 6.395
p3	18.73 ± 0.04623
p4	0.01156 ± 0.04749

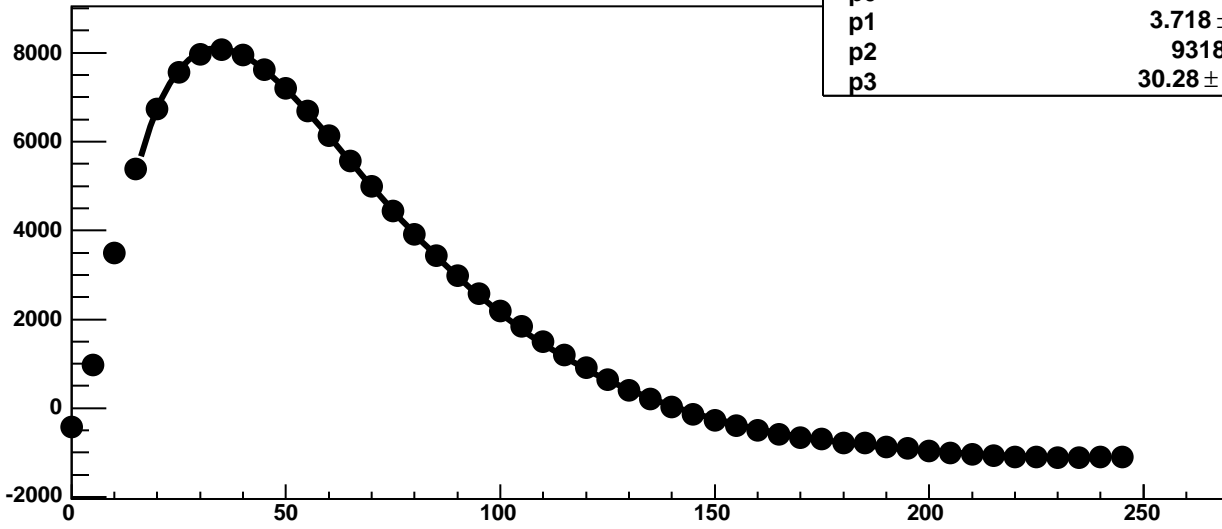
Chip 3, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

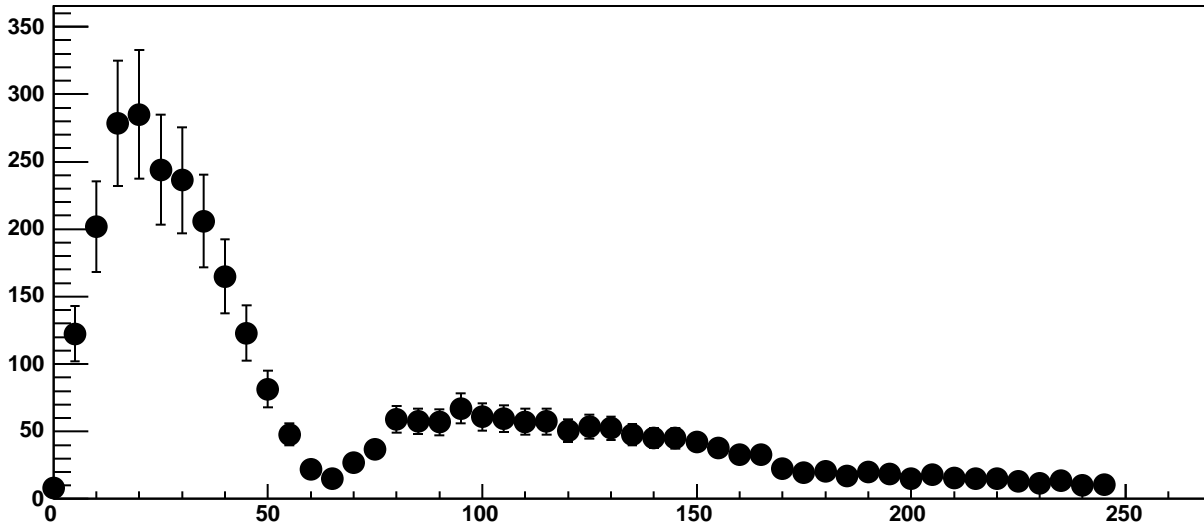


Chip 3, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold

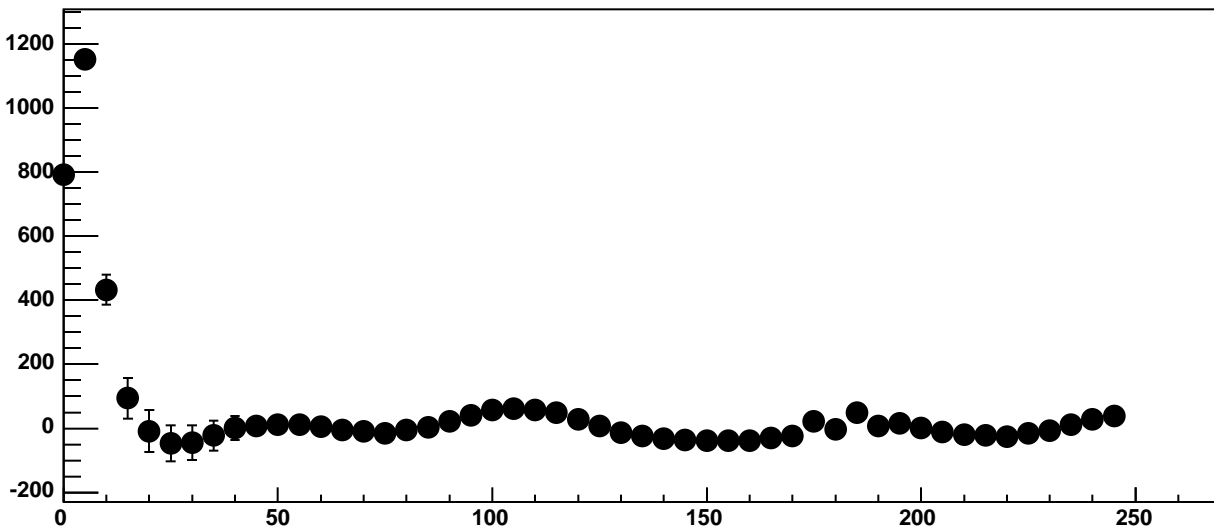


χ^2 / ndf	741.1 / 42
p0	-1214 ± 1.401
p1	3.718 ± 0.1085
p2	9318 ± 11.54
p3	30.28 ± 0.03538

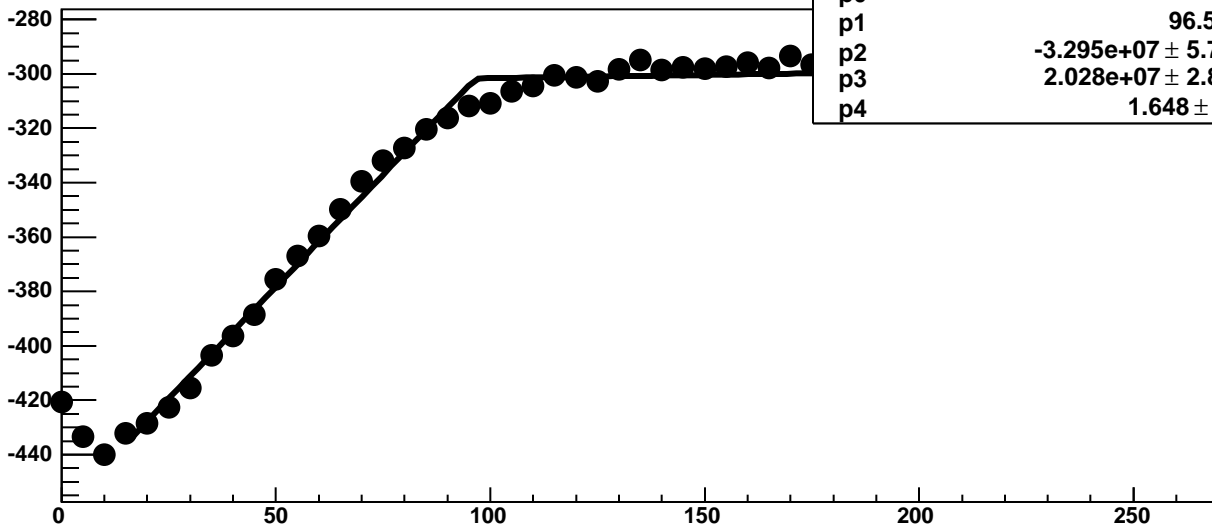
Chip 3, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold

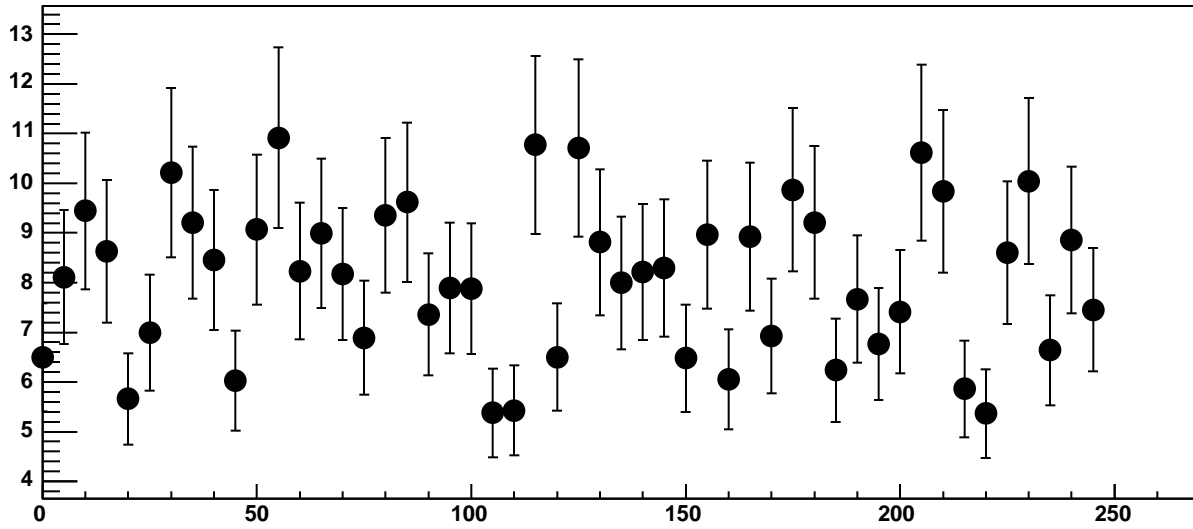


Chip 3, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

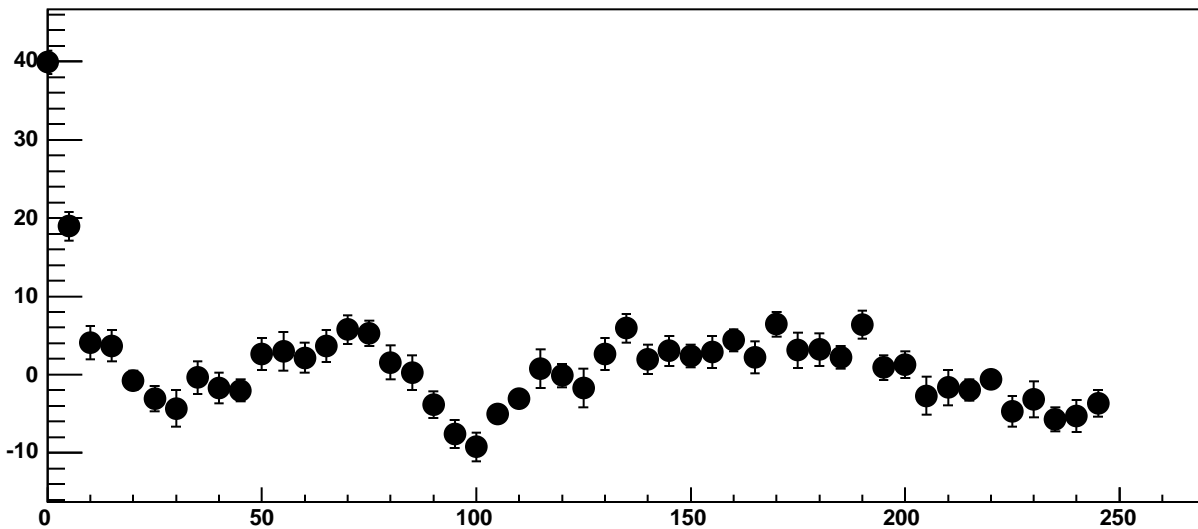


χ^2 / ndf	216.4 / 41
p0	-301.6 ± 0.6283
p1	96.5 ± 0.664
p2	$-3.295e+07 \pm 5.758e+05$
p3	$2.028e+07 \pm 2.868e+05$
p4	1.648 ± 0.01734

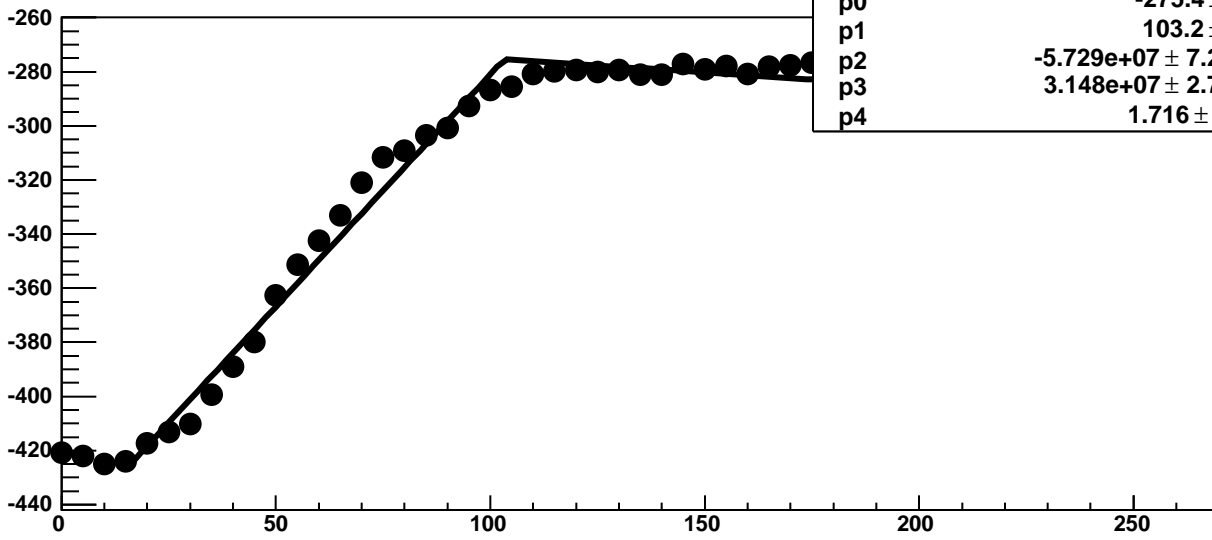
Chip 3, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

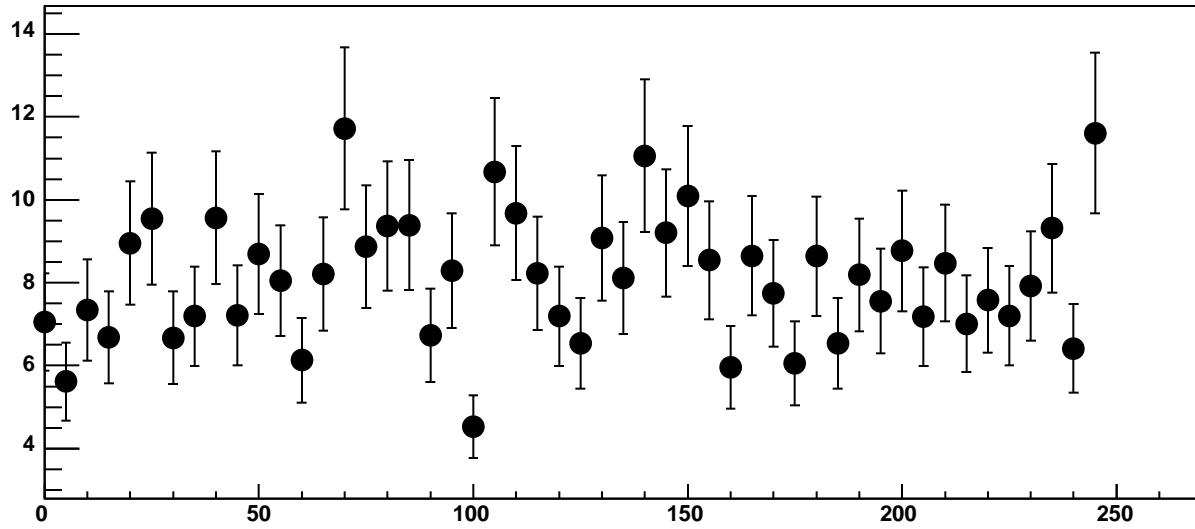


Chip 3, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

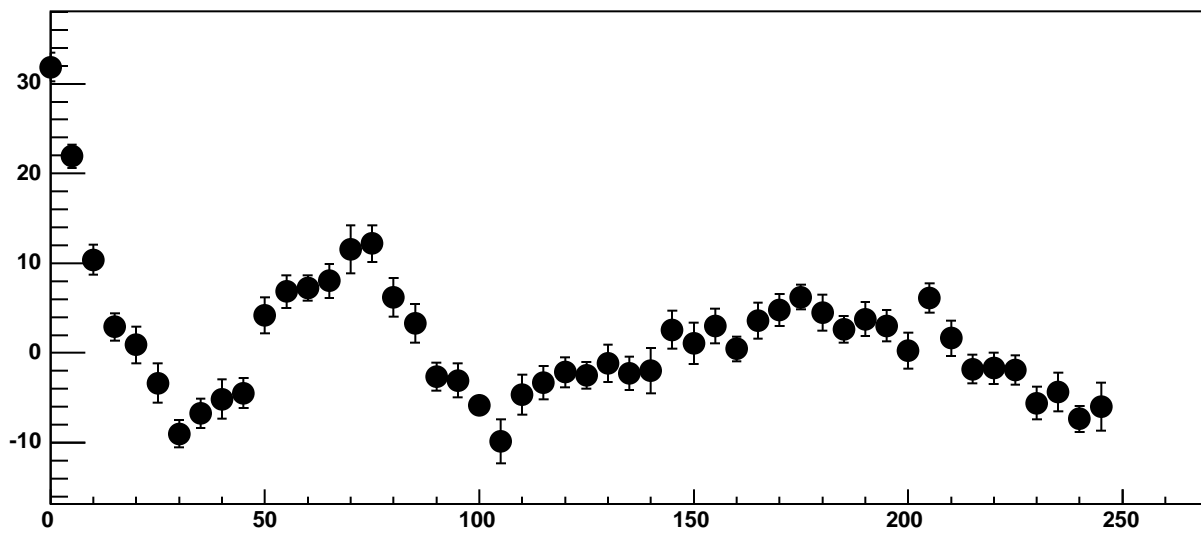


χ^2 / ndf	372.2 / 41
p0	-275.4 ± 0.6759
p1	103.2 ± 0.5669
p2	-5.729e+07 ± 7.224e+05
p3	3.148e+07 ± 2.788e+05
p4	1.716 ± 0.01442

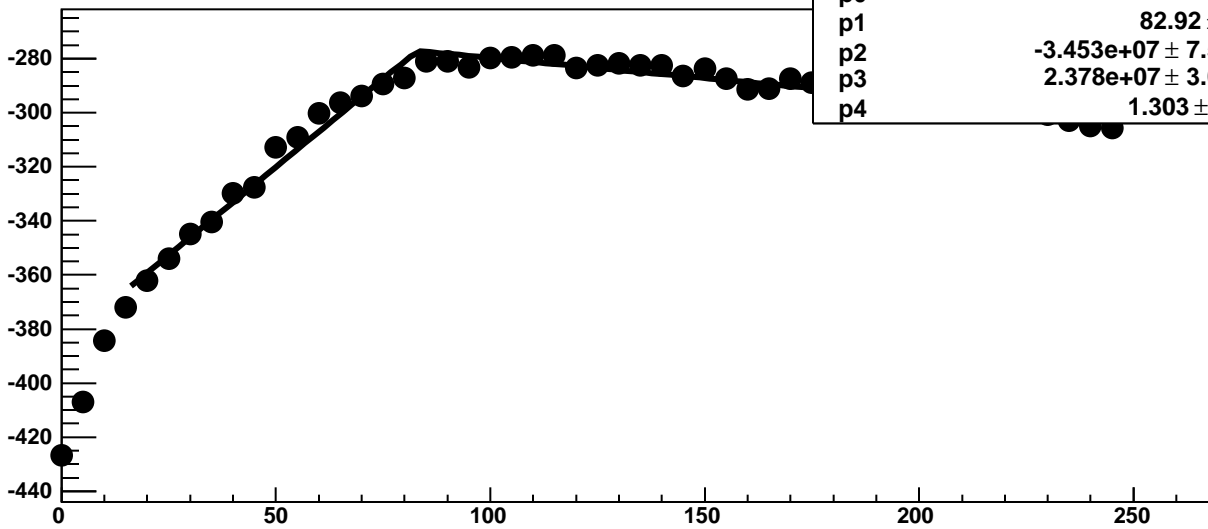
Chip 3, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

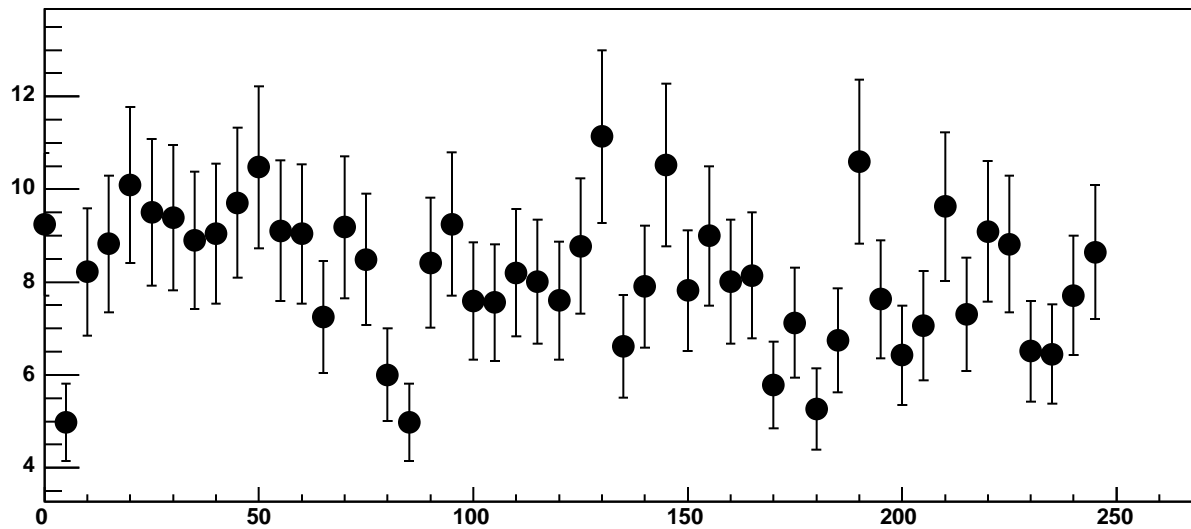


Chip 3, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold

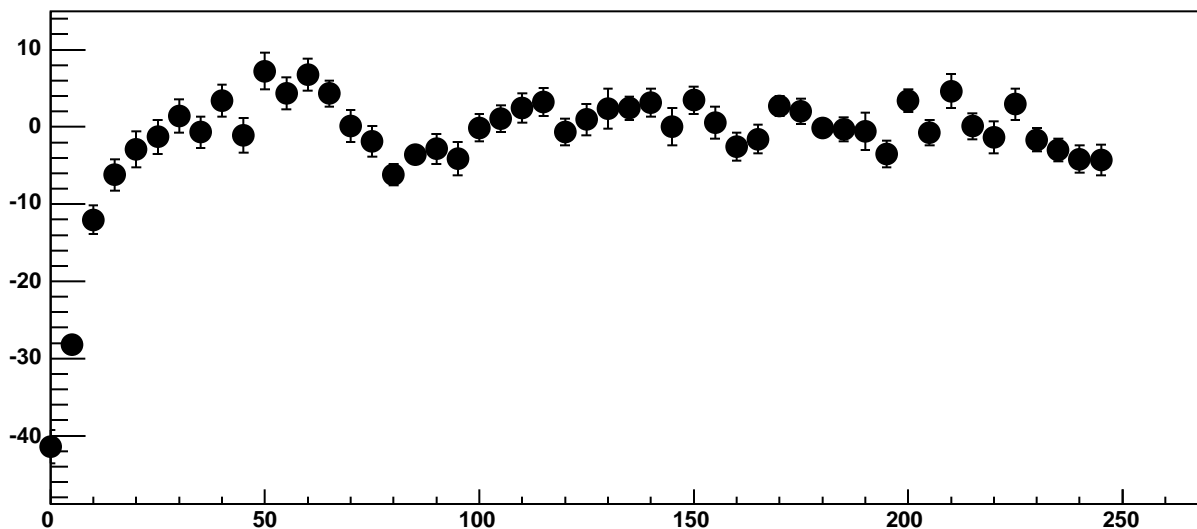


χ^2 / ndf	134.3 / 41
p0	-277.2 ± 0.5442
p1	82.92 ± 0.7704
p2	$-3.453\text{e}+07 \pm 7.599\text{e}+05$
p3	$2.378\text{e}+07 \pm 3.007\text{e}+05$
p4	1.303 ± 0.02499

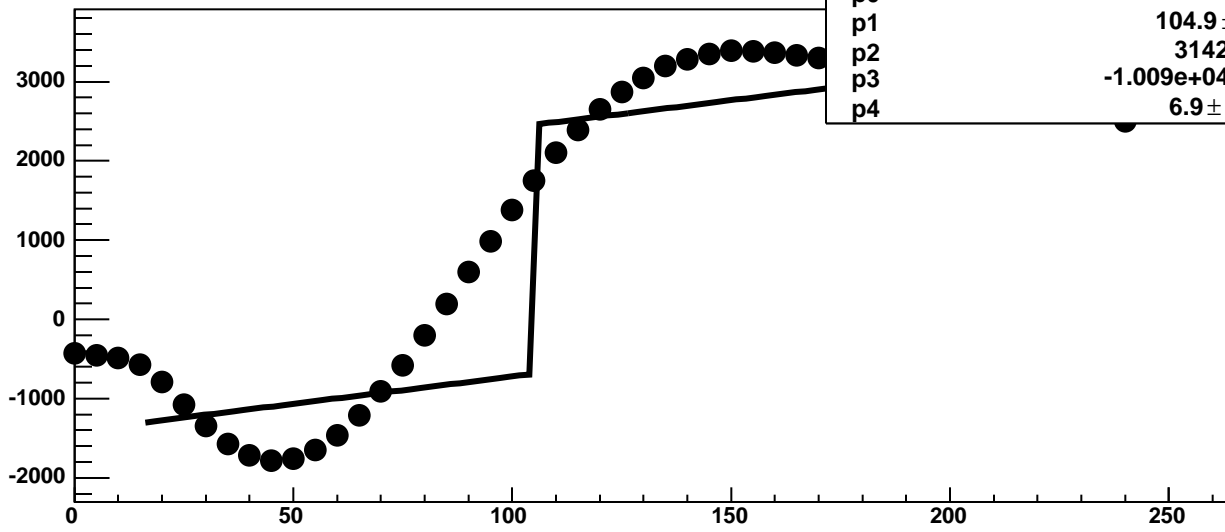
Chip 3, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold



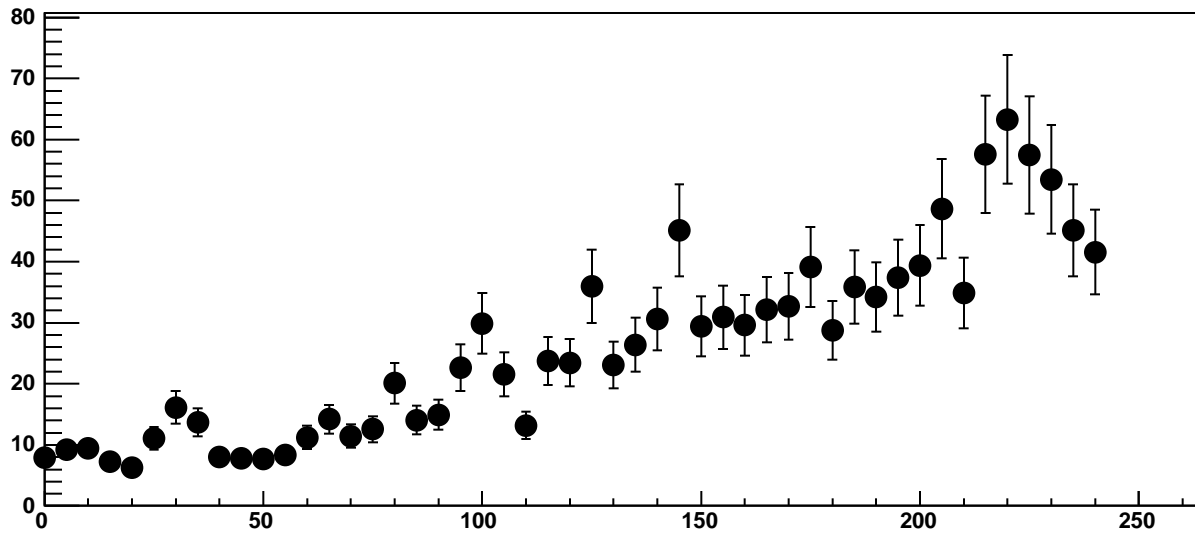
Chip 3, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold



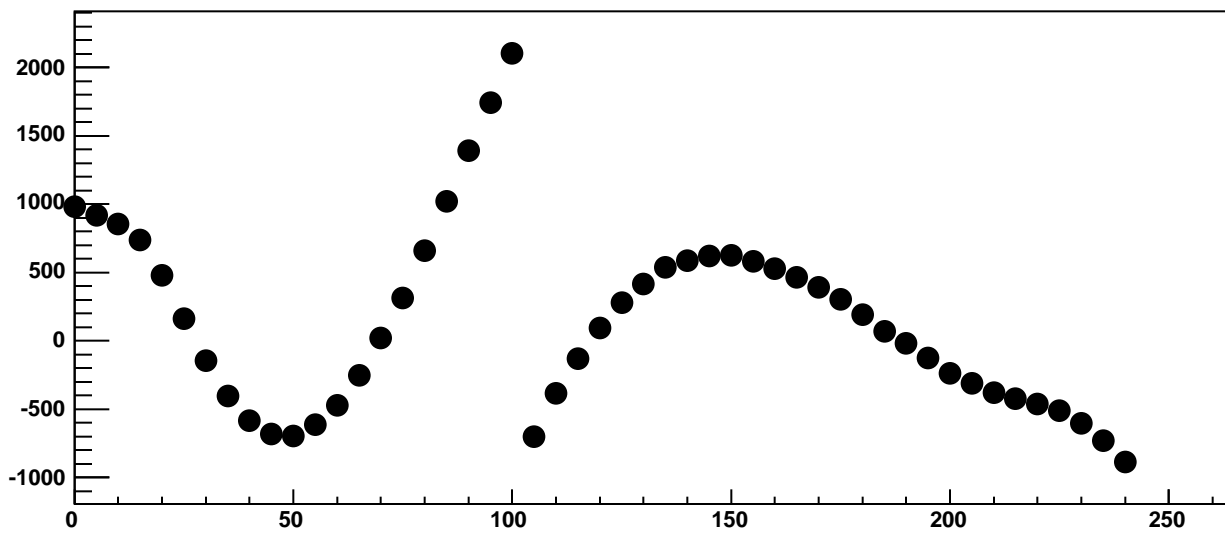
Chip 3, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold



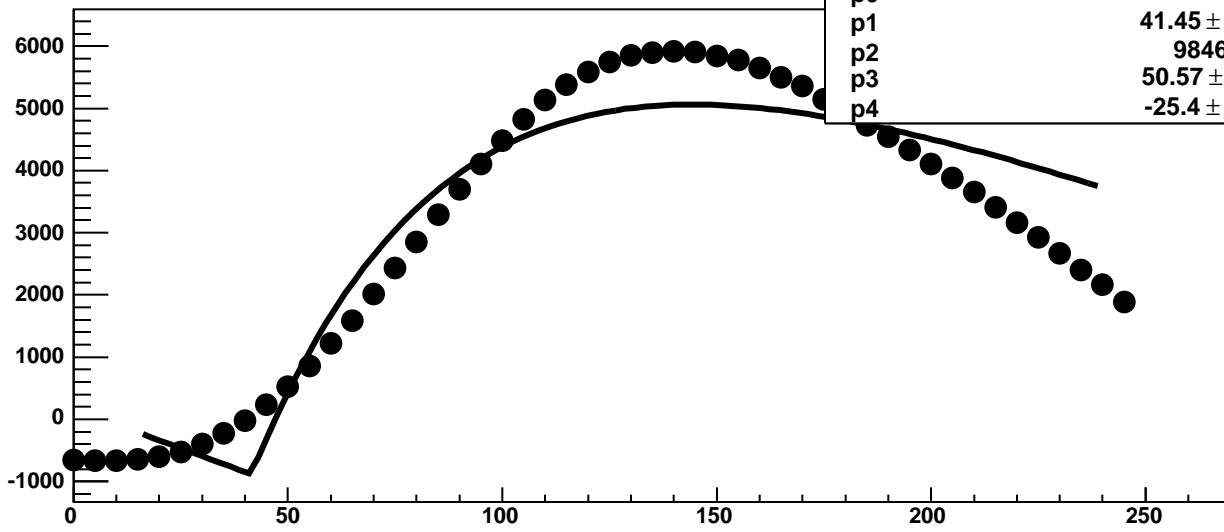
Chip 3, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold

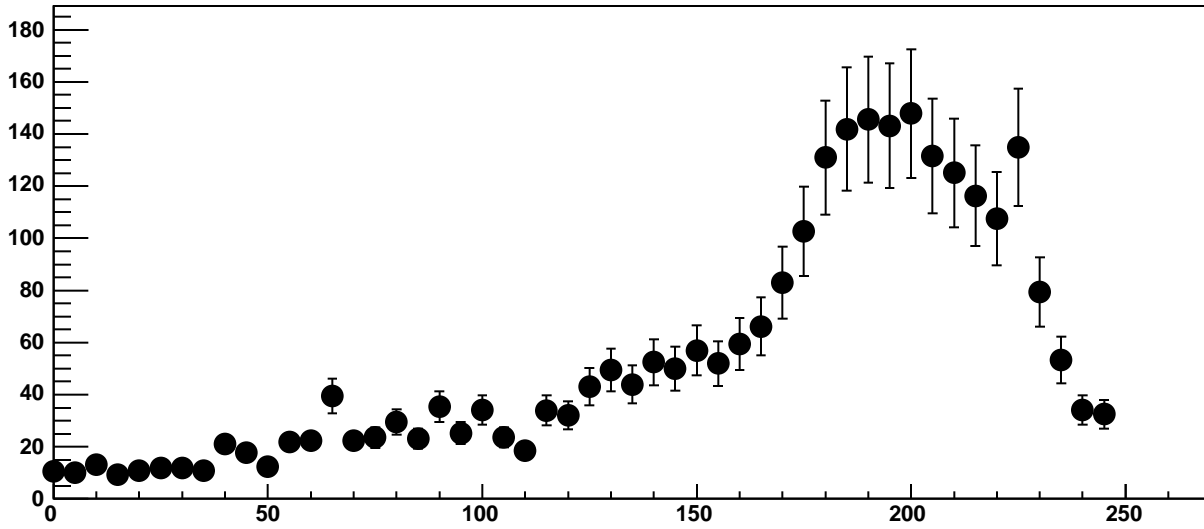


Chip 3, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold

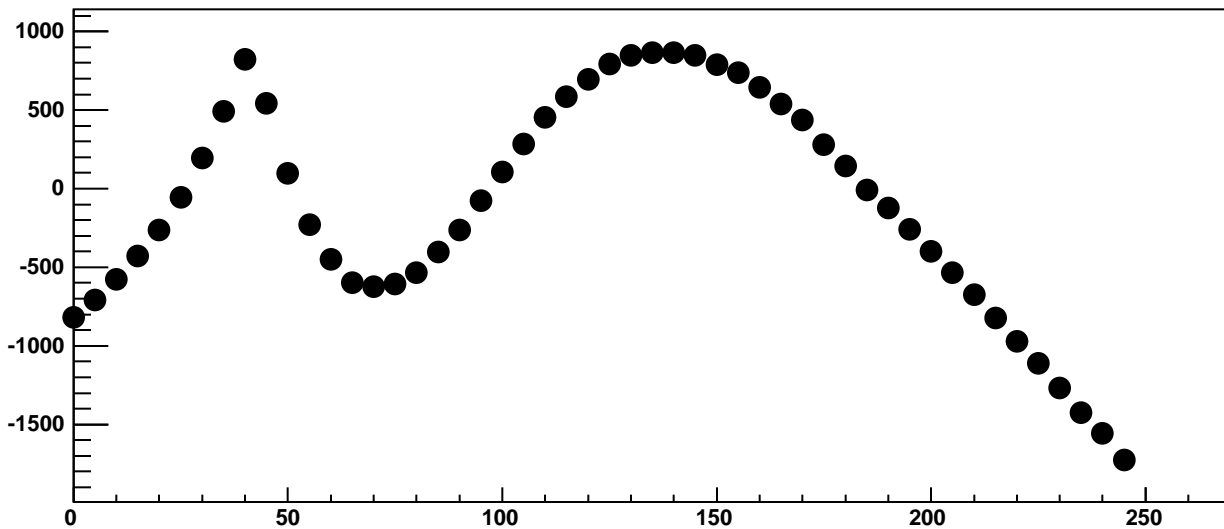


χ^2 / ndf	3.329e+05 / 41
p0	-884.6 ± 1.508
p1	41.45 ± 0.01804
p2	9846 ± 16.03
p3	50.57 ± 0.08771
p4	-25.4 ± 0.07943

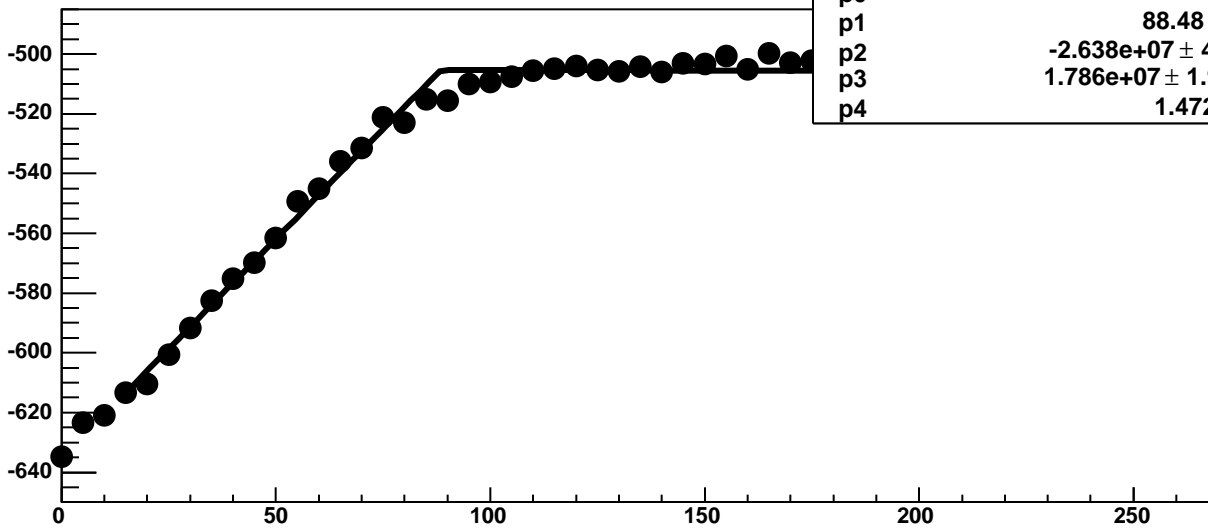
Chip 3, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



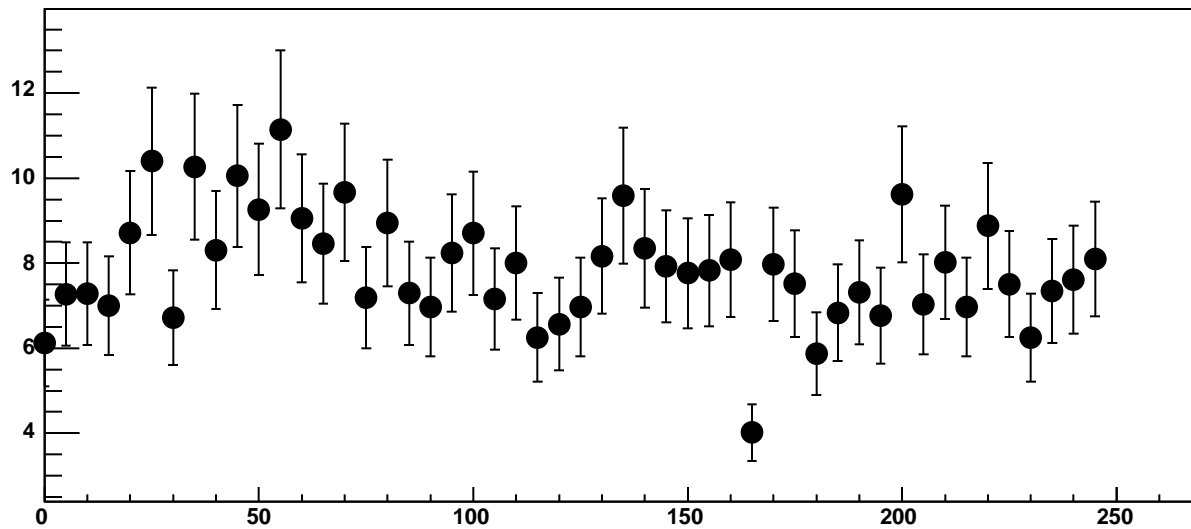
Chip 3, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold



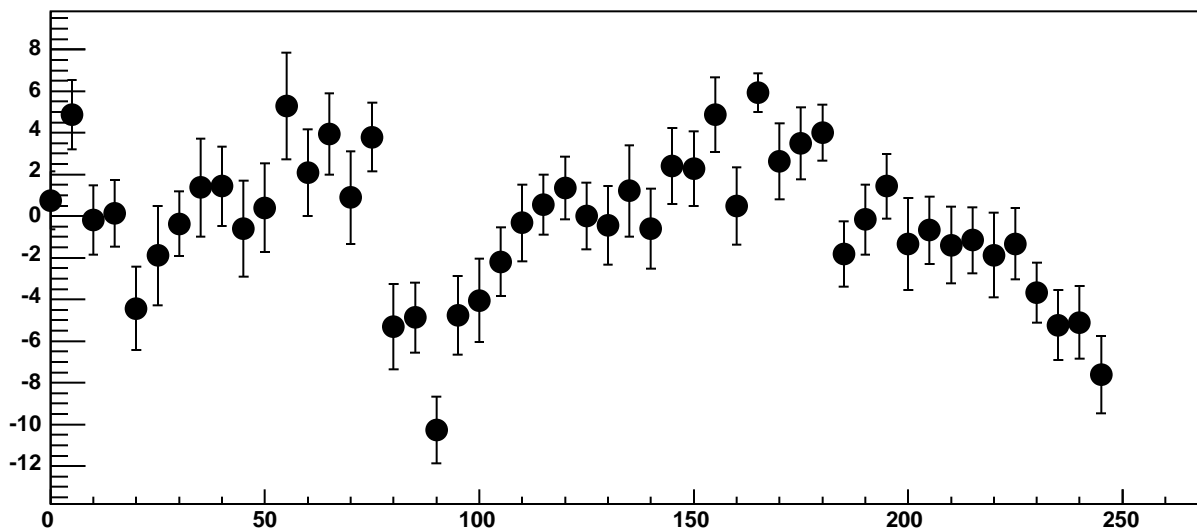
Chip 3, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold



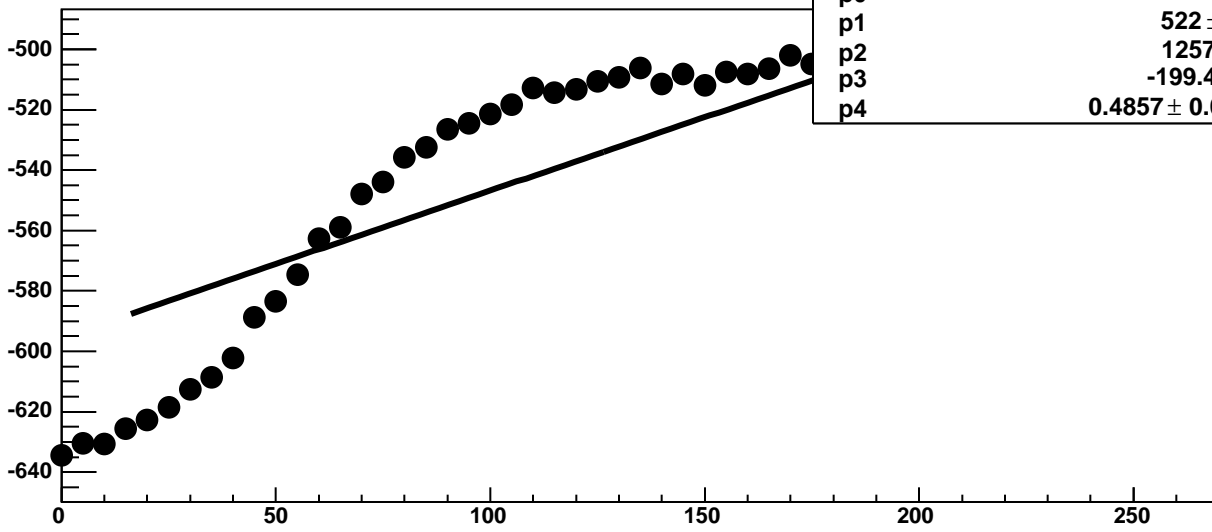
Chip 3, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold

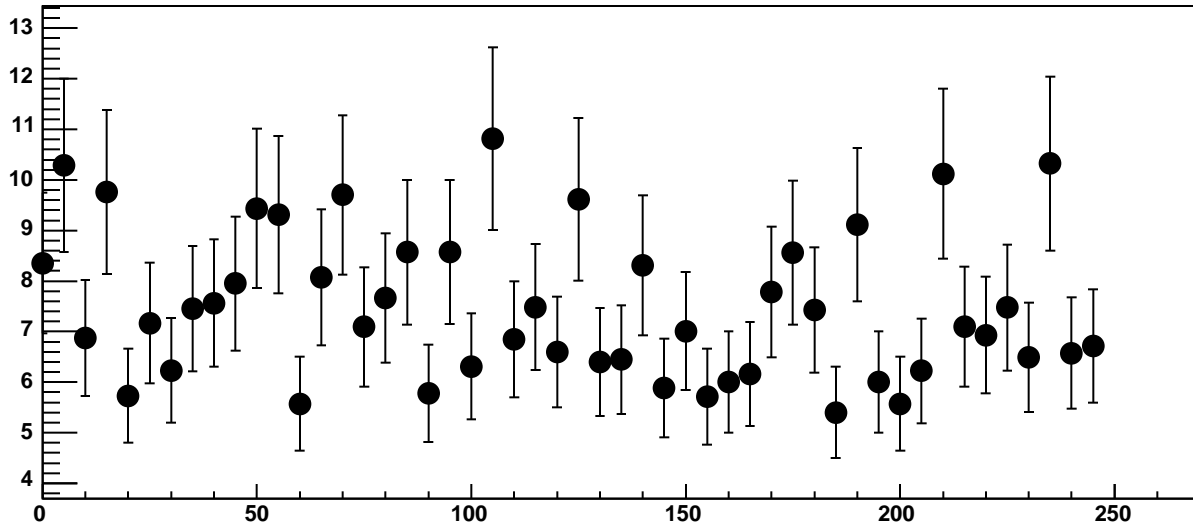


Chip 3, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

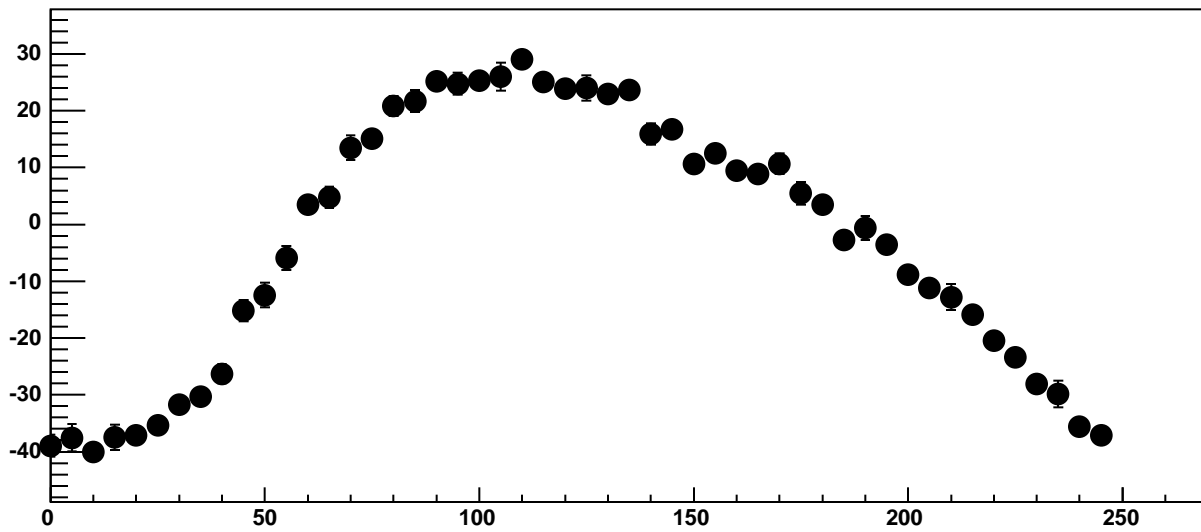


χ^2 / ndf	7648 / 41
p0	-341.8 ± 0.2388
p1	522 ± 0.4916
p2	1257 ± 84.85
p3	-199.4 ± 1.414
p4	0.4857 ± 0.0006016

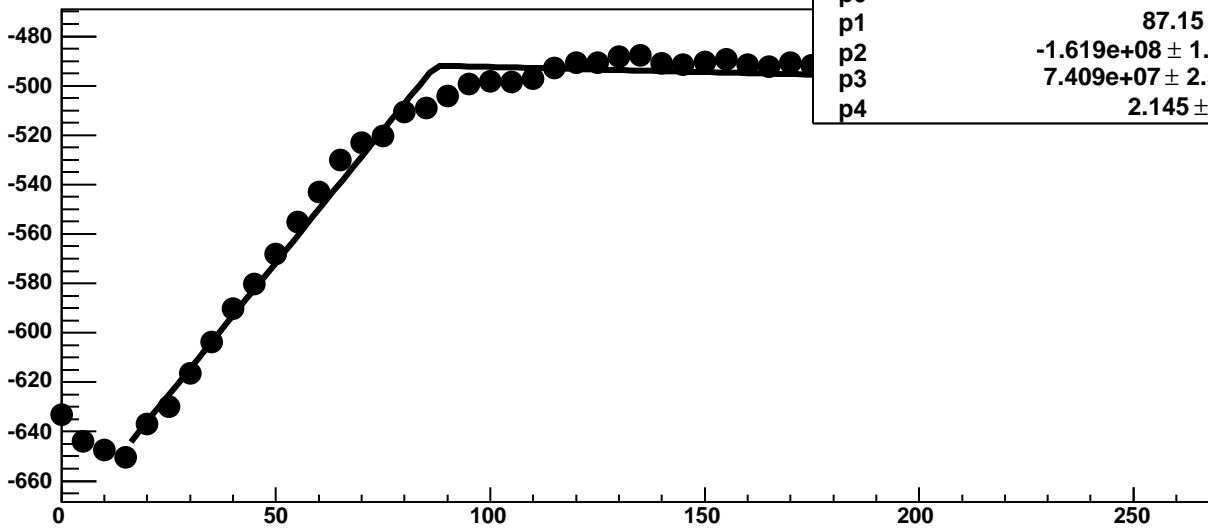
Chip 3, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

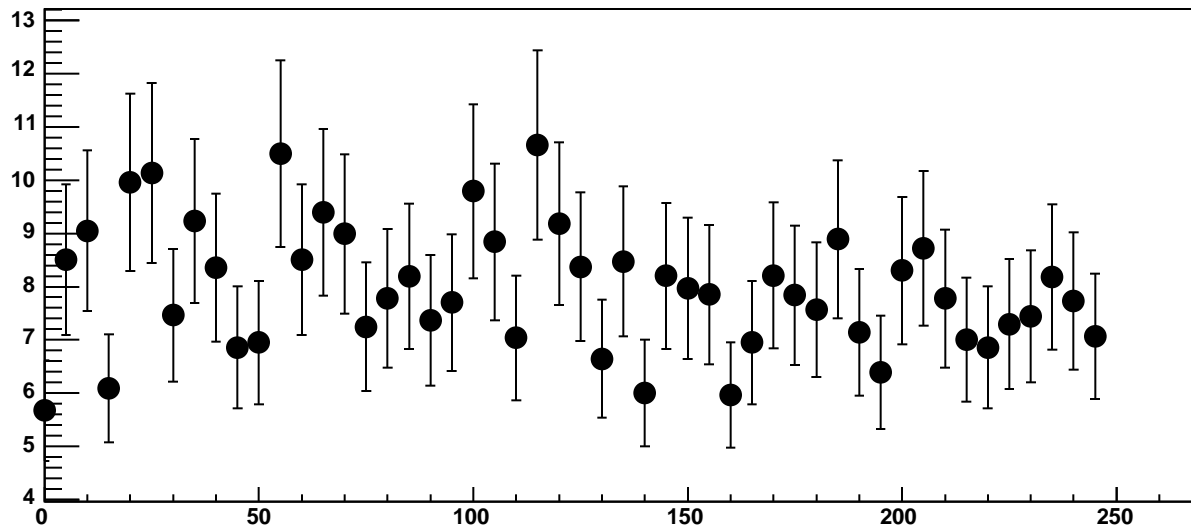


Chip 3, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

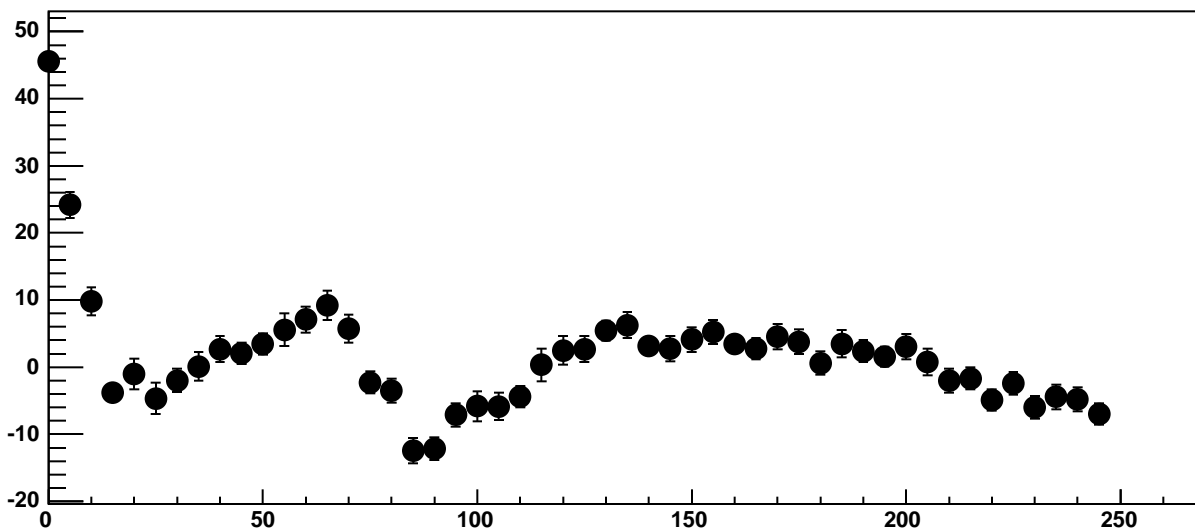


χ^2 / ndf	320.7 / 41
p0	-491.9 ± 0.6439
p1	87.15 ± 0.5258
p2	$-1.619\text{e}+08 \pm 1.753\text{e}+06$
p3	$7.409\text{e}+07 \pm 2.545\text{e}+05$
p4	2.145 ± 0.02152

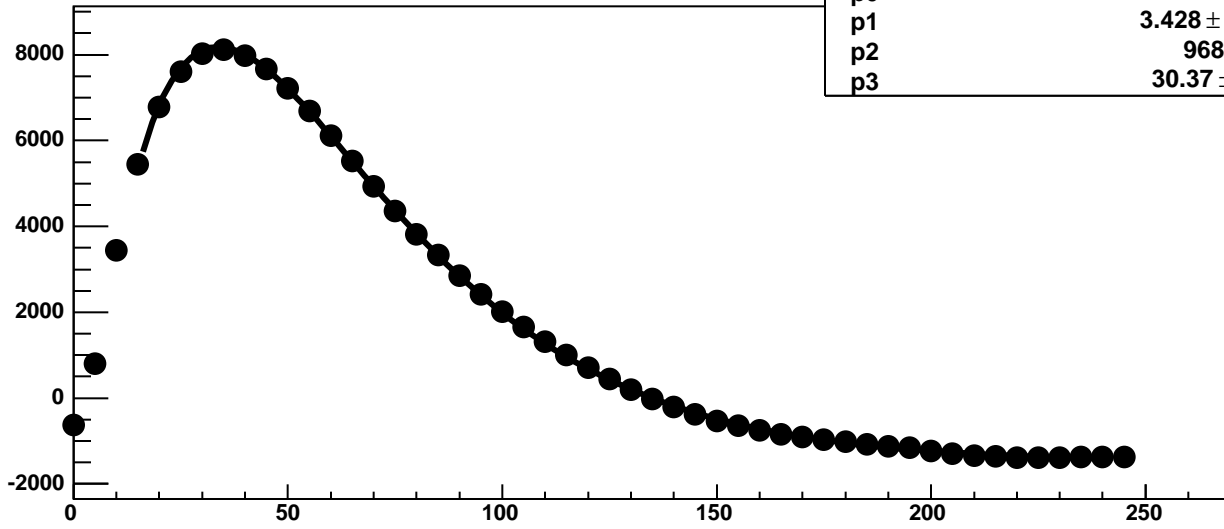
Chip 3, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

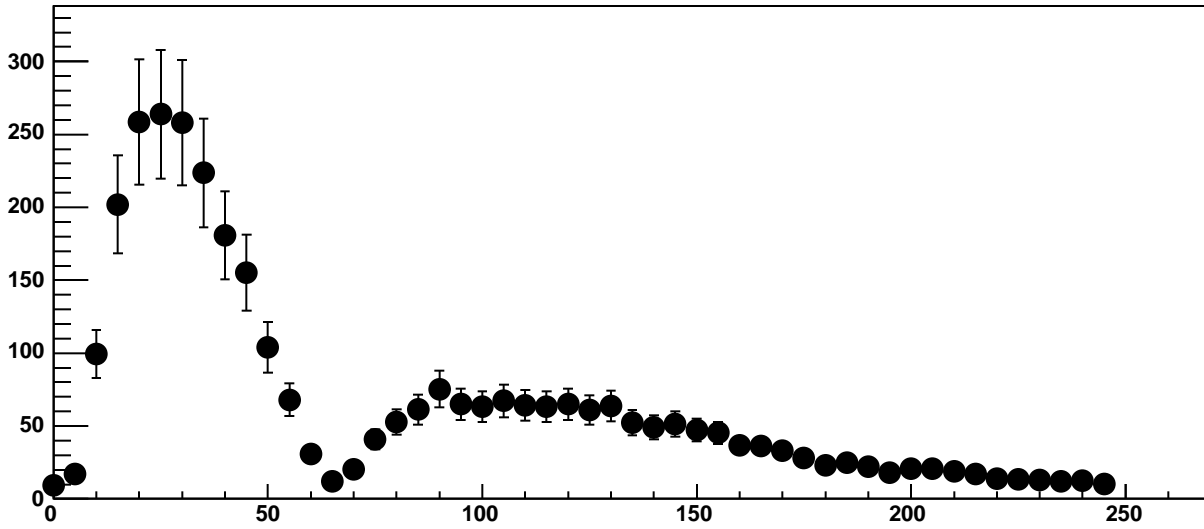


Chip 3, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold

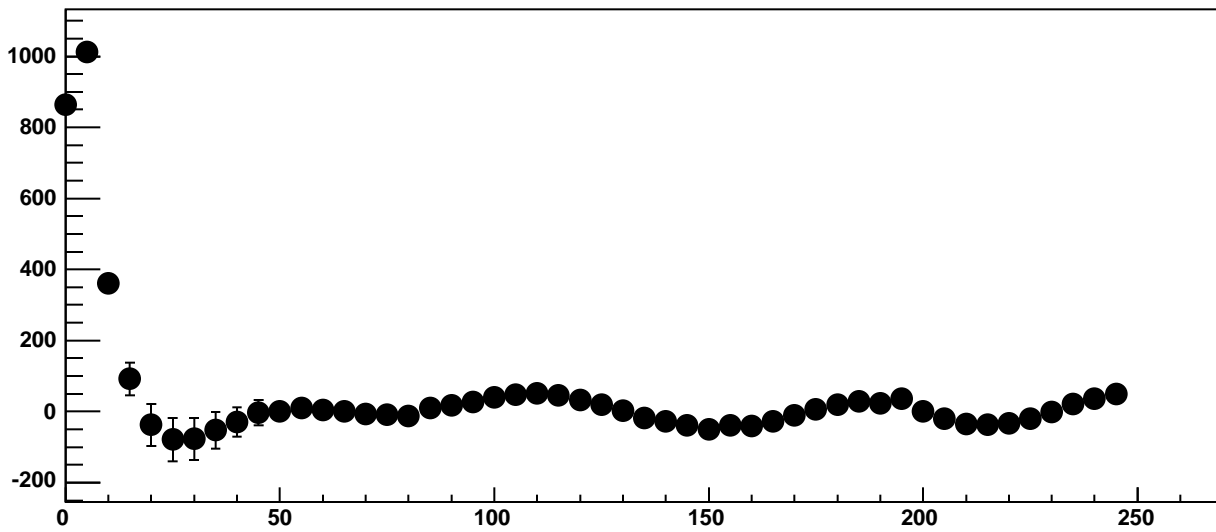


χ^2 / ndf	808.4 / 42
p0	-1498 ± 1.537
p1	3.428 ± 0.09238
p2	9681 ± 10.8
p3	30.37 ± 0.0366

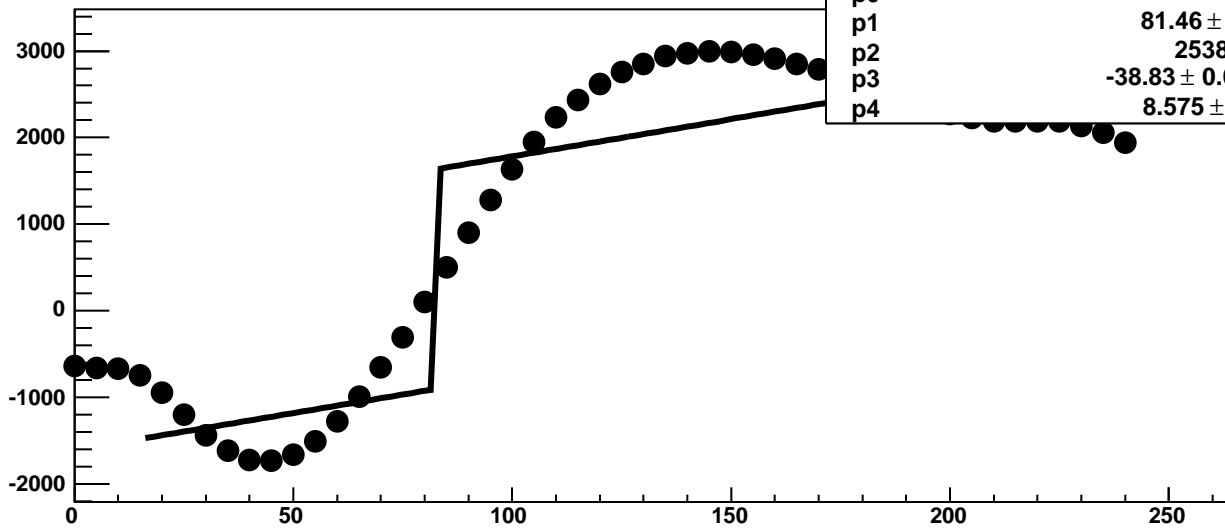
Chip 3, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold



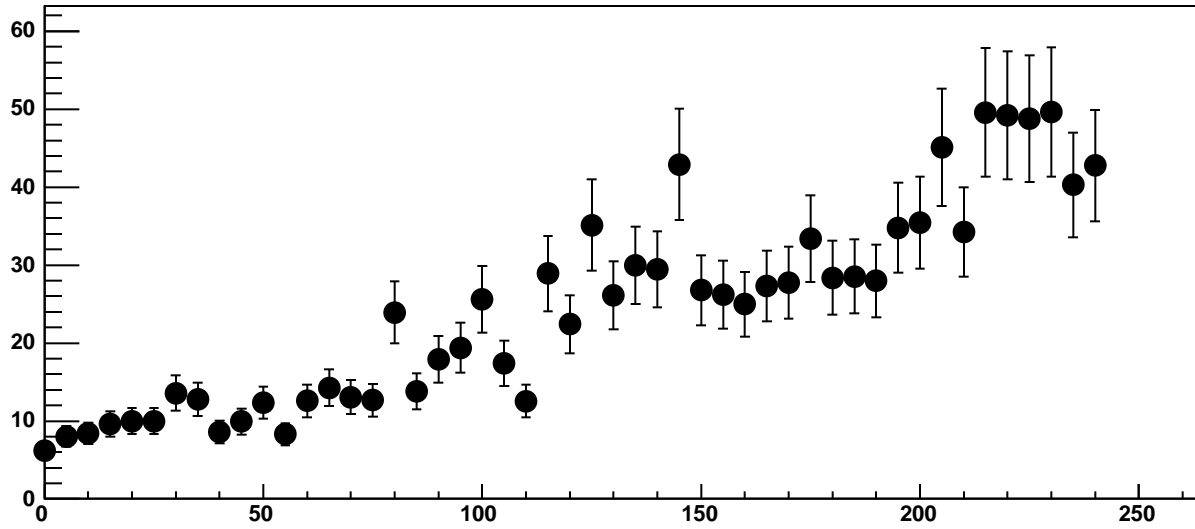
Chip 3, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold



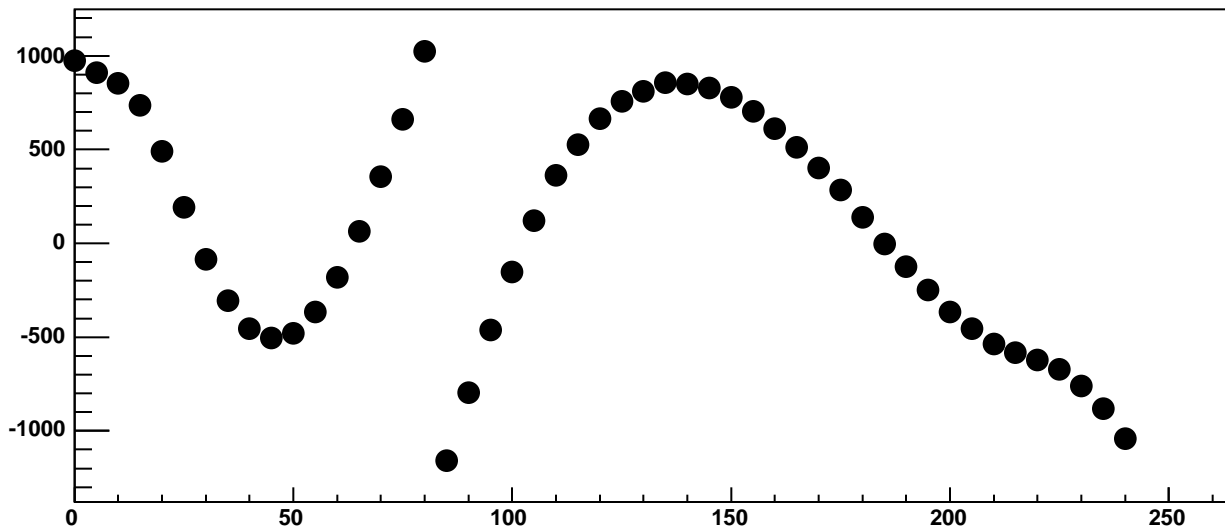
Chip 3, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold



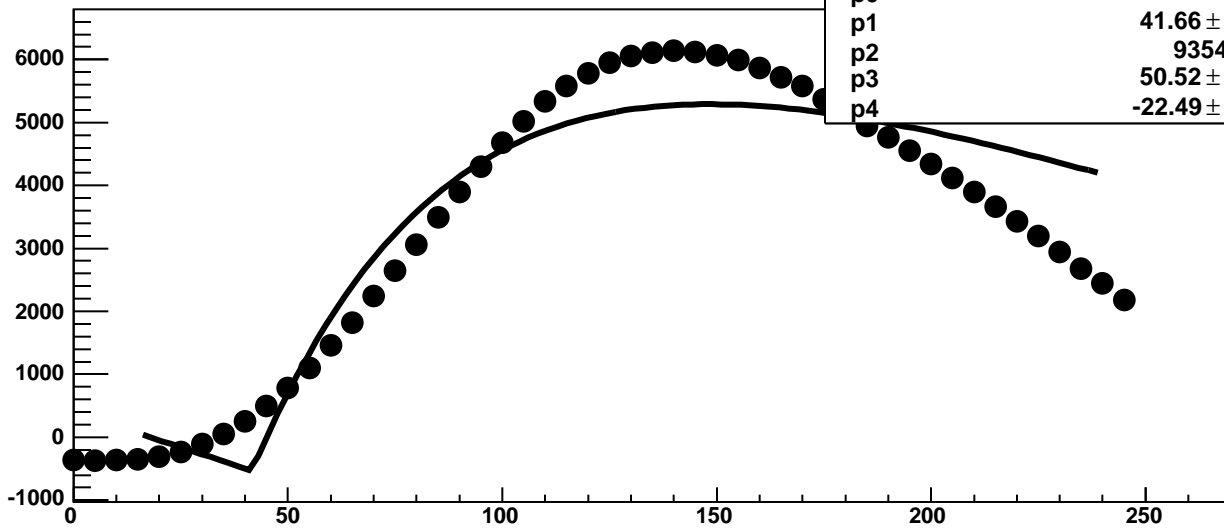
Chip 3, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

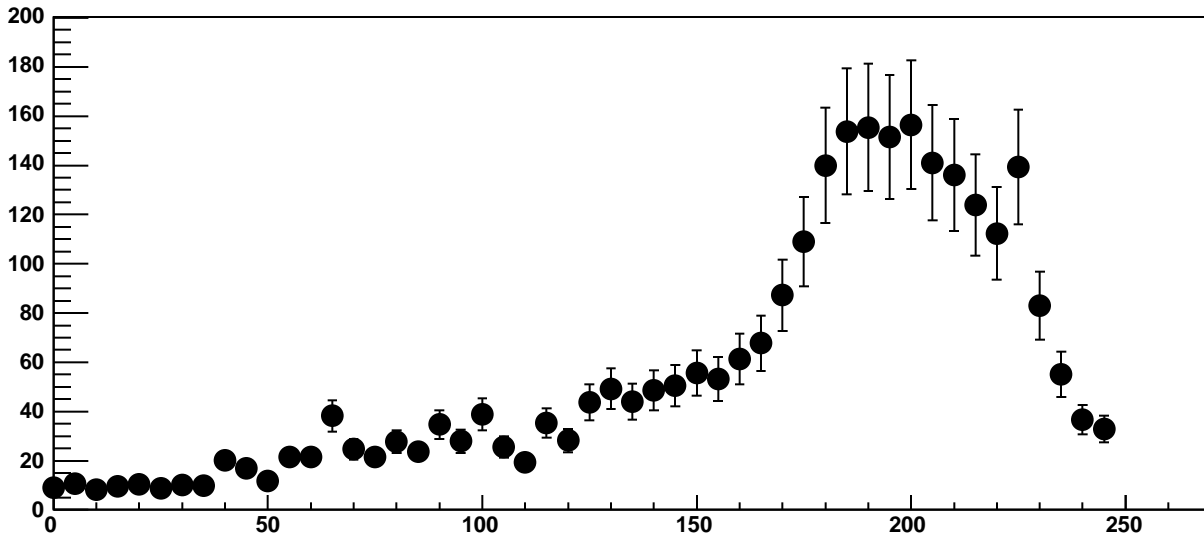


Chip 3, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

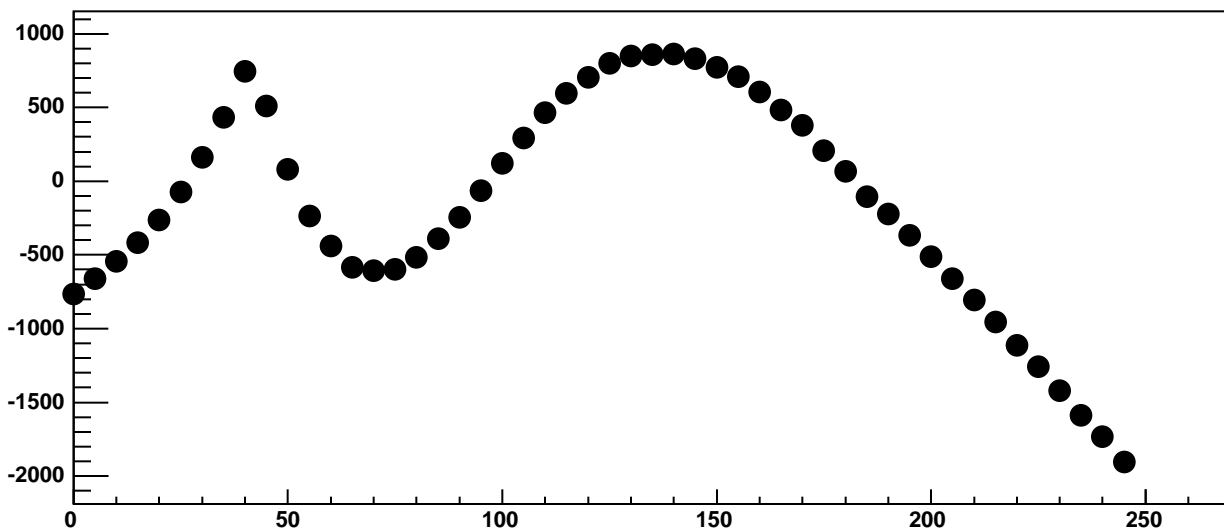


χ^2 / ndf	3.299e+05 / 41
p0	-533 ± 1.434
p1	41.66 ± 0.01835
p2	9354 ± 15.59
p3	50.52 ± 0.08938
p4	-22.49 ± 0.07885

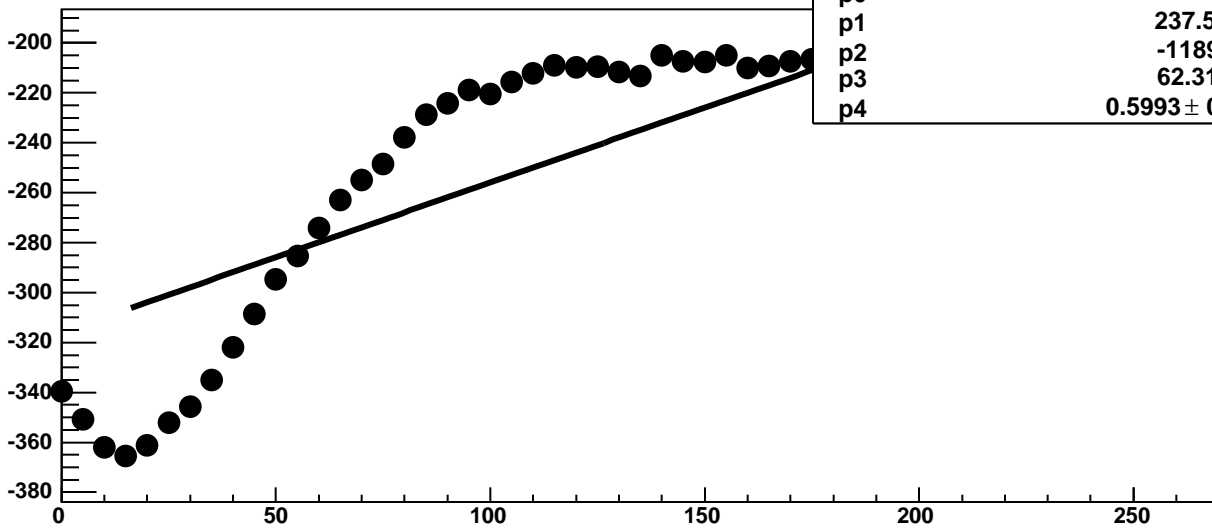
Chip 3, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold

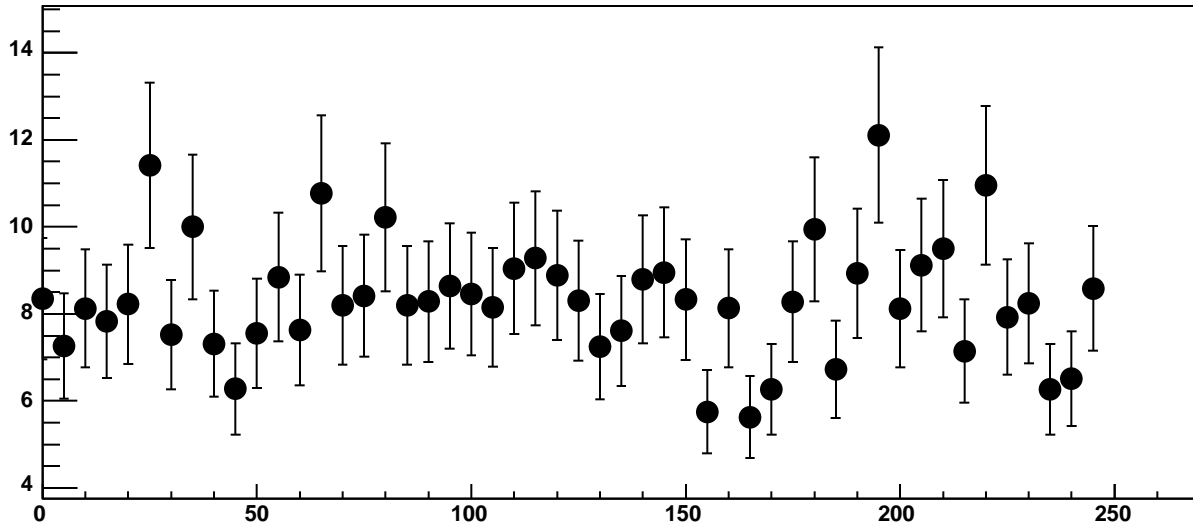


Chip 3, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold

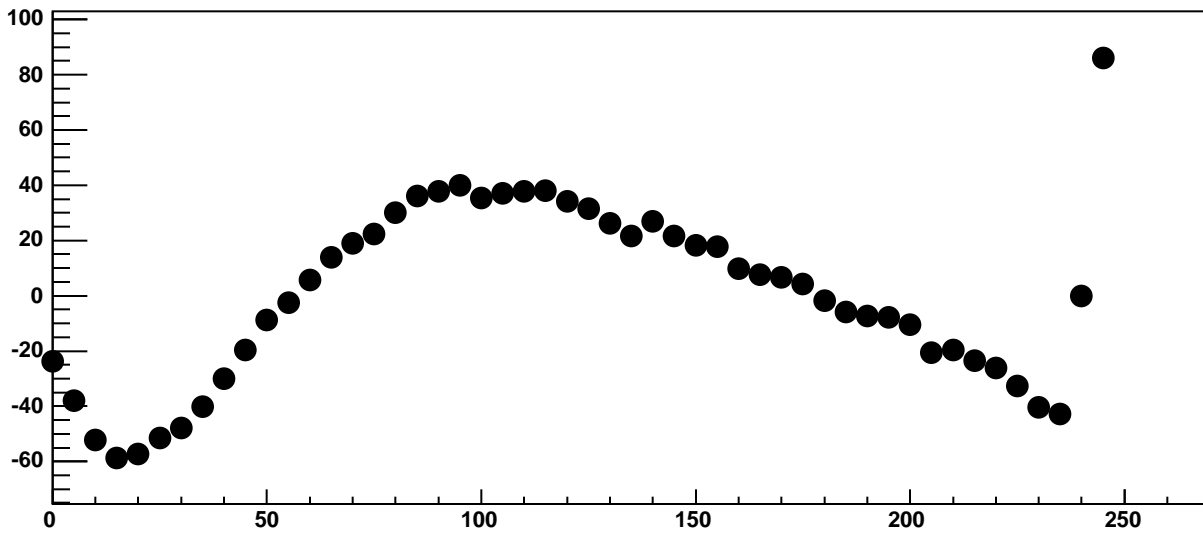


χ^2 / ndf	1.081e+04 / 41
p0	-173.5 ± 0.8606
p1	237.5 ± 1.125
p2	-1189 ± 519.1
p3	62.31 ± 22.77
p4	0.5993 ± 0.004251

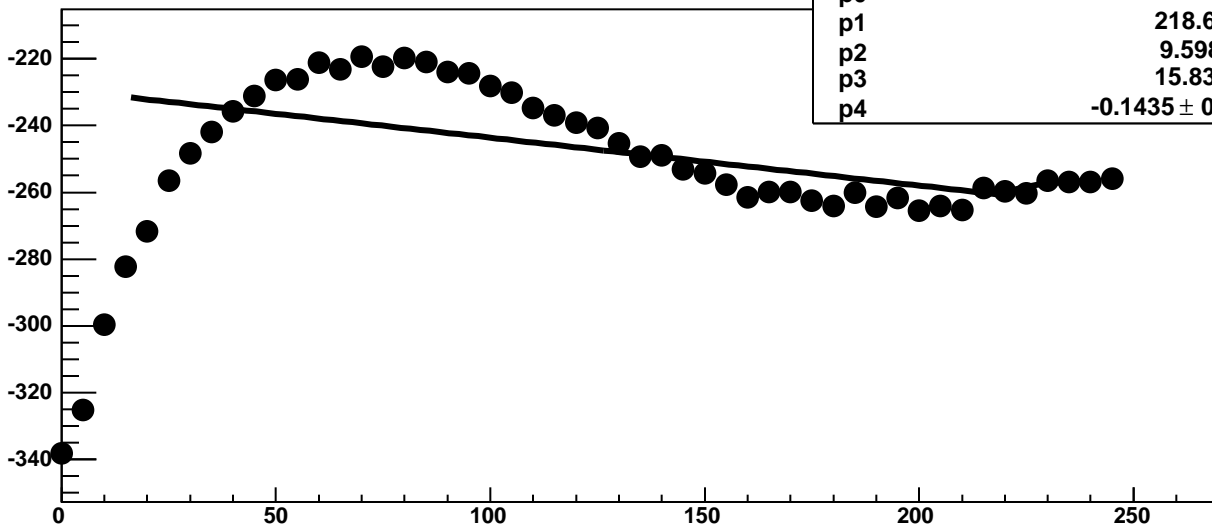
Chip 3, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold

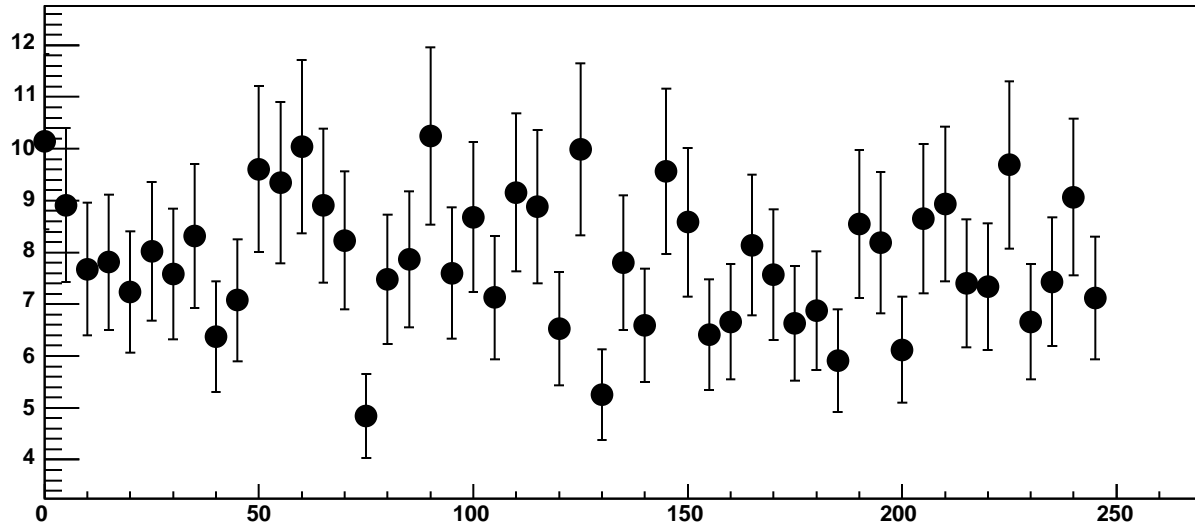


Chip 3, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold

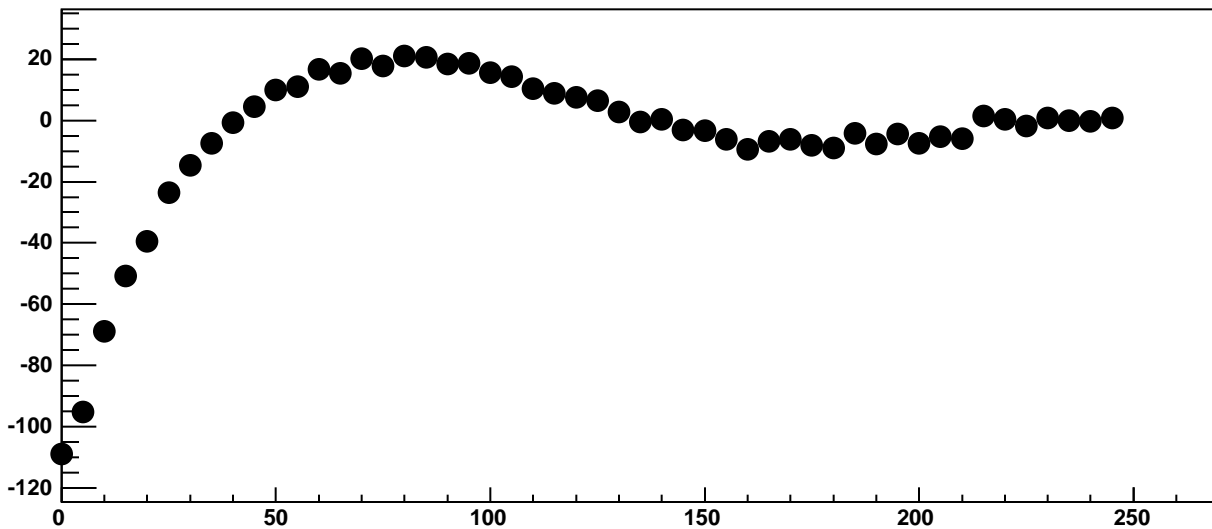


χ^2 / ndf	3094 / 41
p0	-260.7 ± 0.6506
p1	218.6 ± 3.643
p2	9.598 ± 9.111
p3	15.83 ± 27.45
p4	-0.1435 ± 0.004554

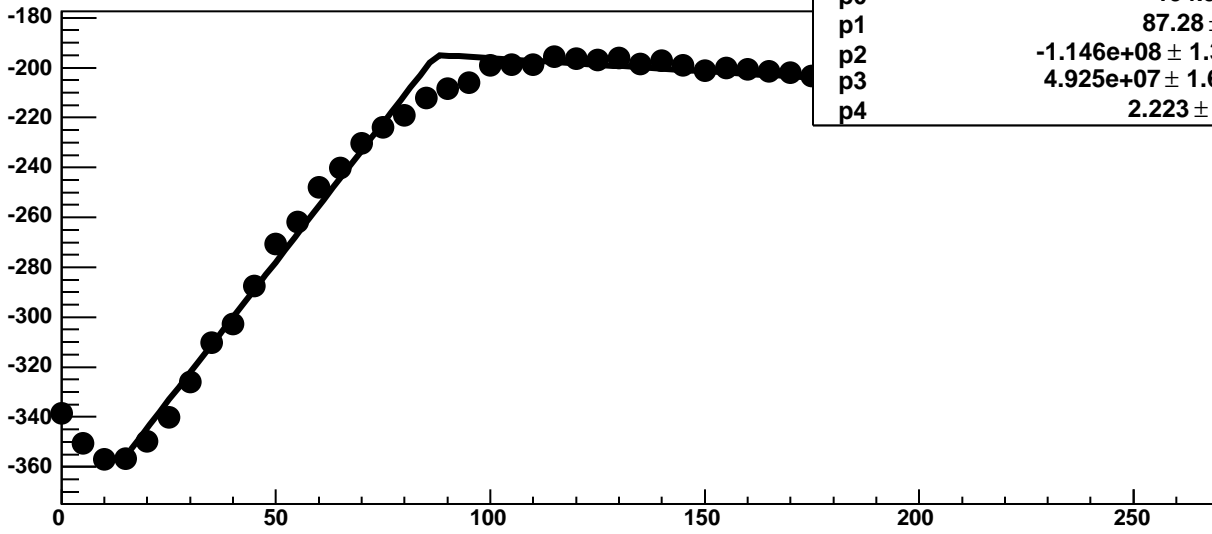
Chip 3, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold

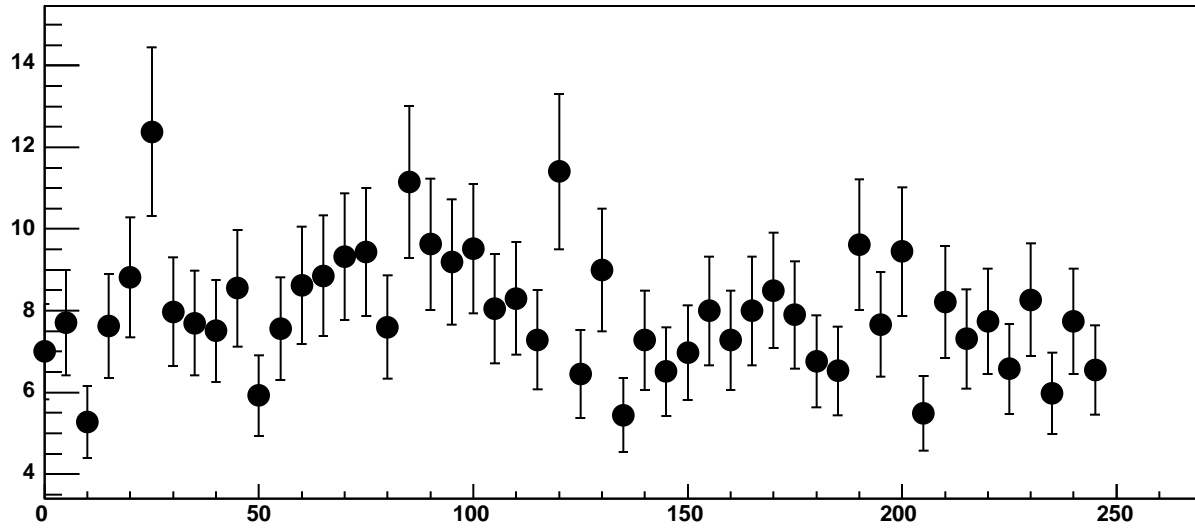


Chip 3, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

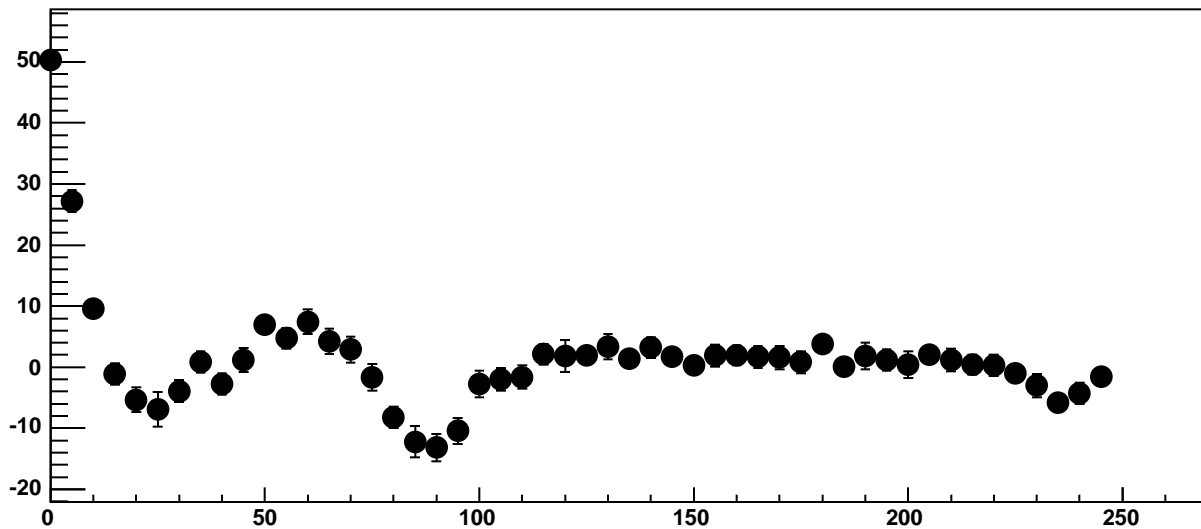


χ^2 / ndf	238.5 / 41
p0	-194.9 ± 0.632
p1	87.28 ± 0.5322
p2	$-1.146\text{e}+08 \pm 1.314\text{e}+06$
p3	$4.925\text{e}+07 \pm 1.684\text{e}+05$
p4	2.223 ± 0.02415

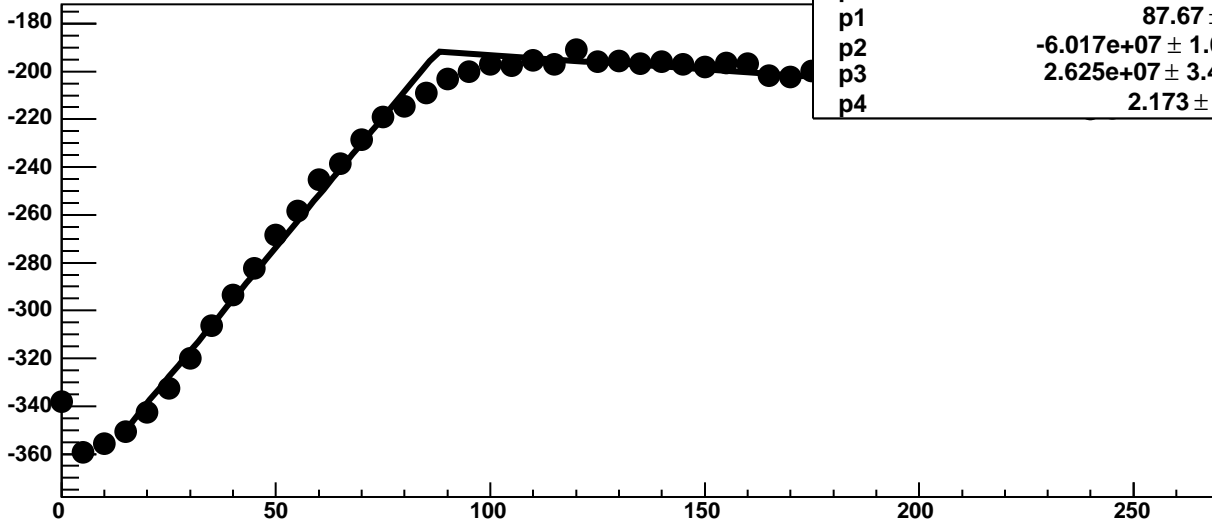
Chip 3, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

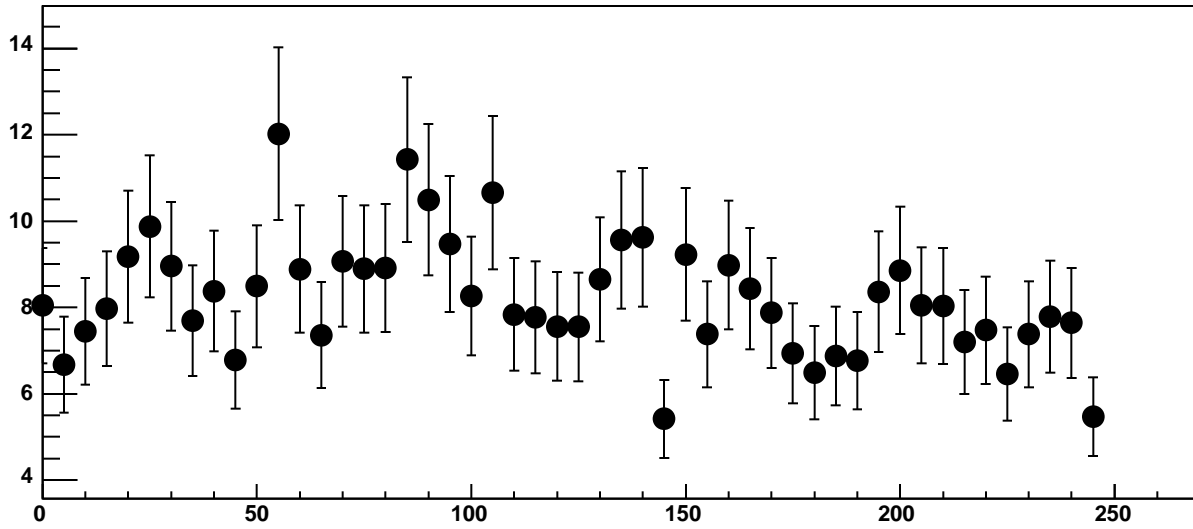


Chip 3, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

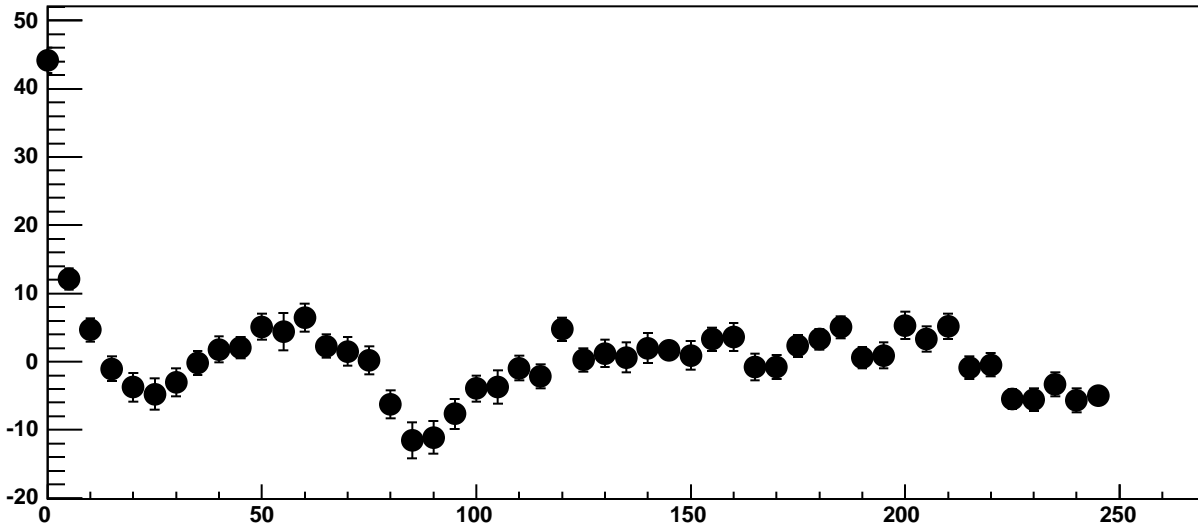


χ^2 / ndf	197.8 / 41
p0	-191.7 ± 0.6531
p1	87.67 ± 0.5608
p2	$-6.017\text{e}+07 \pm 1.018\text{e}+06$
p3	$2.625\text{e}+07 \pm 3.477\text{e}+05$
p4	2.173 ± 0.02462

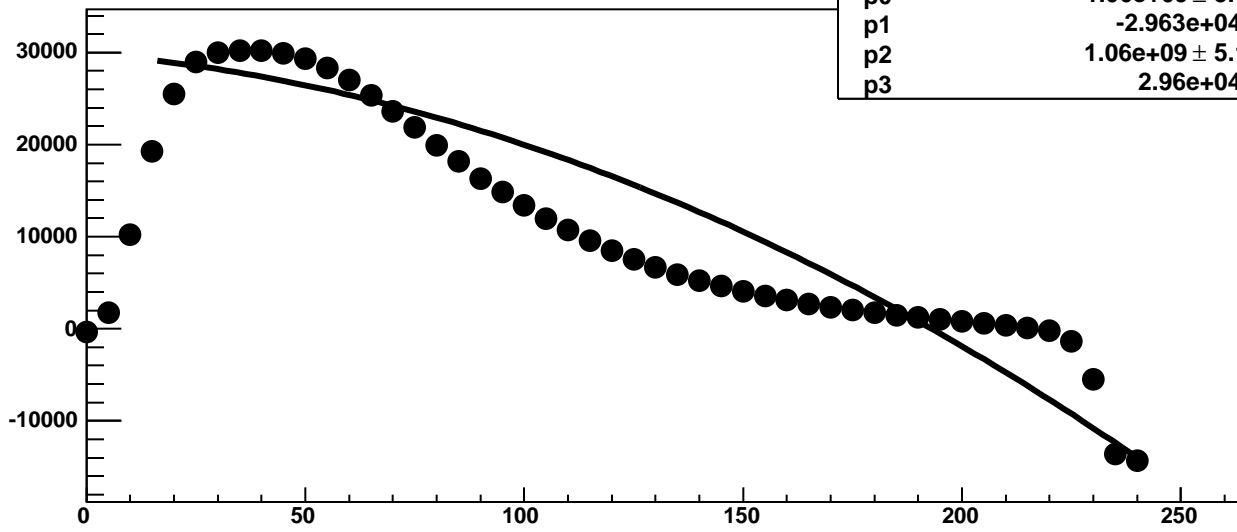
Chip 3, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



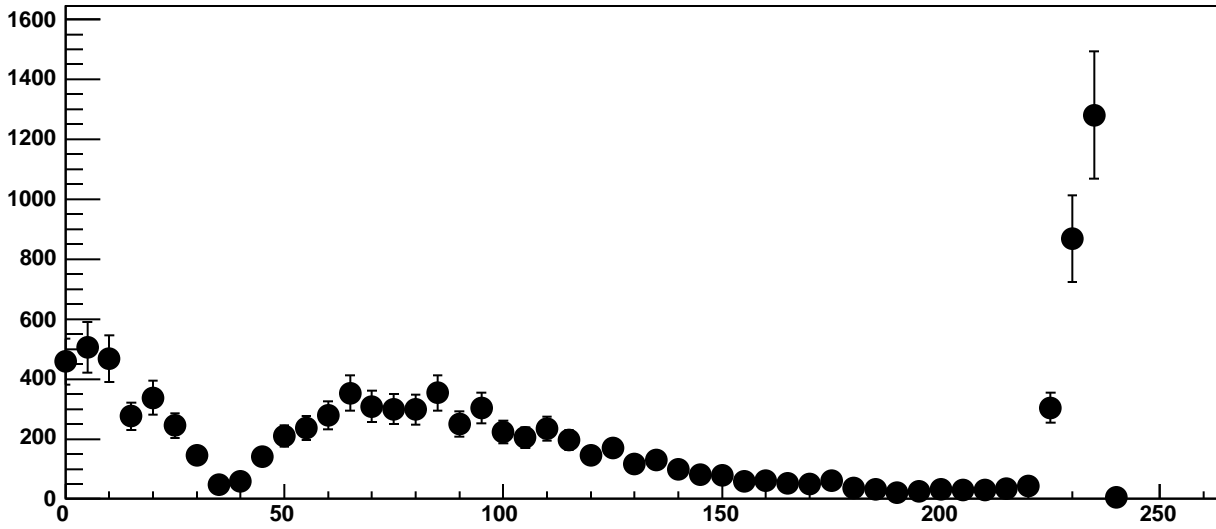
Chip 3, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold



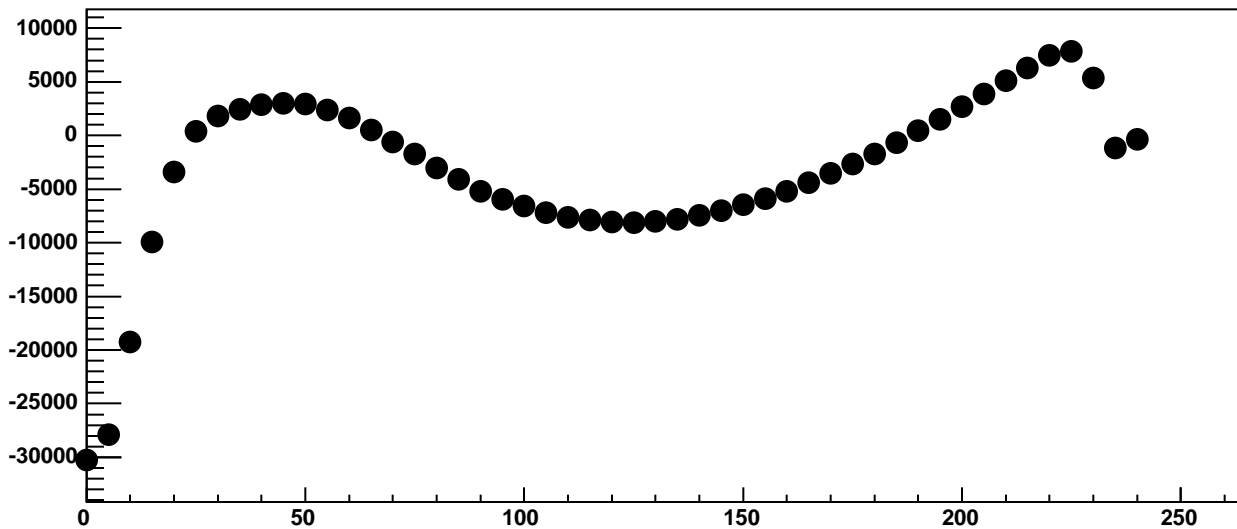
Chip 3, Channel 6, Enable 5!, DAC=4000, ADC Mean vs Hold



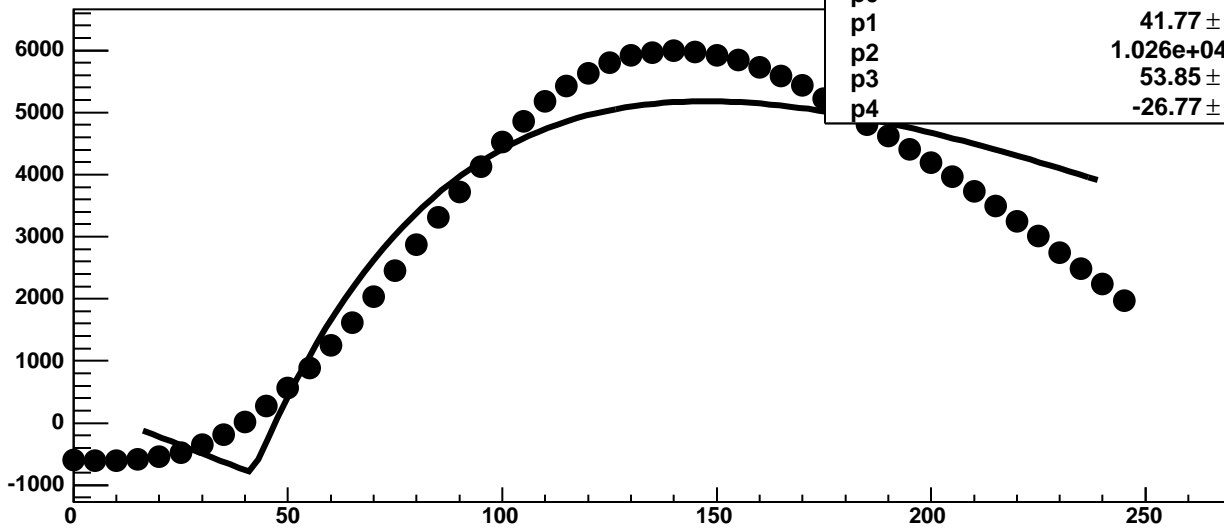
Chip 3, Channel 6, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 6, Enable 5!, DAC=4000, ADC Residuals vs Hold

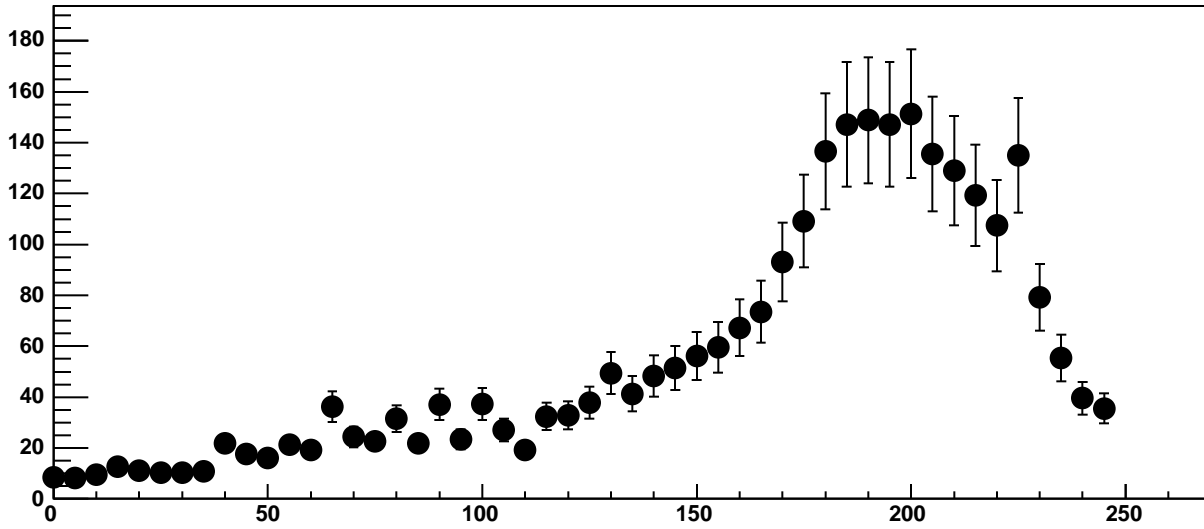


Chip 3, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

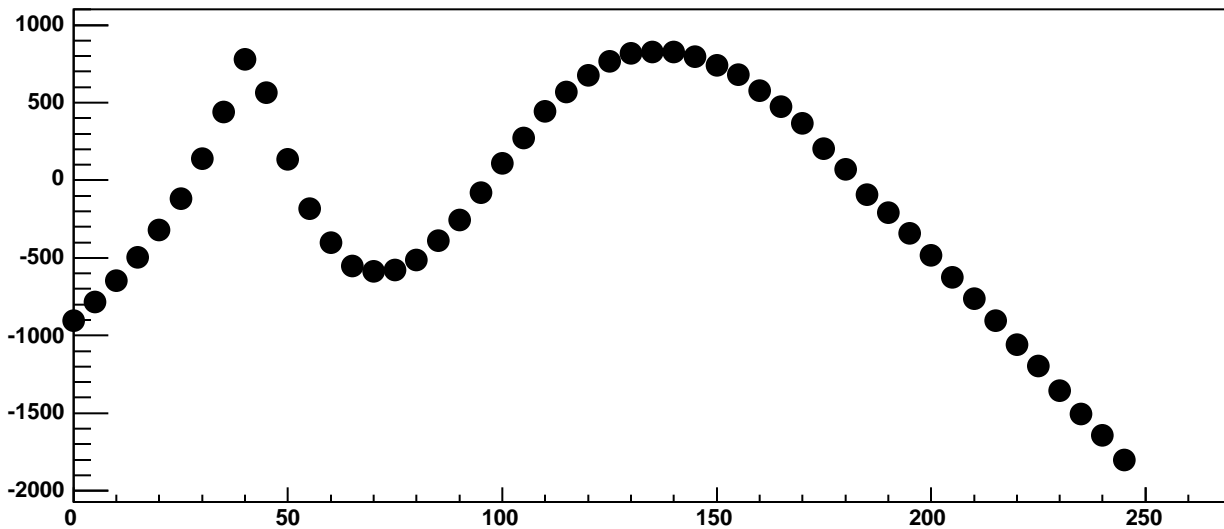


χ^2 / ndf	3.024e+05 / 41
p0	-806.2 ± 1.515
p1	41.77 ± 0.01908
p2	1.026e+04 ± 17.86
p3	53.85 ± 0.09605
p4	-26.77 ± 0.08816

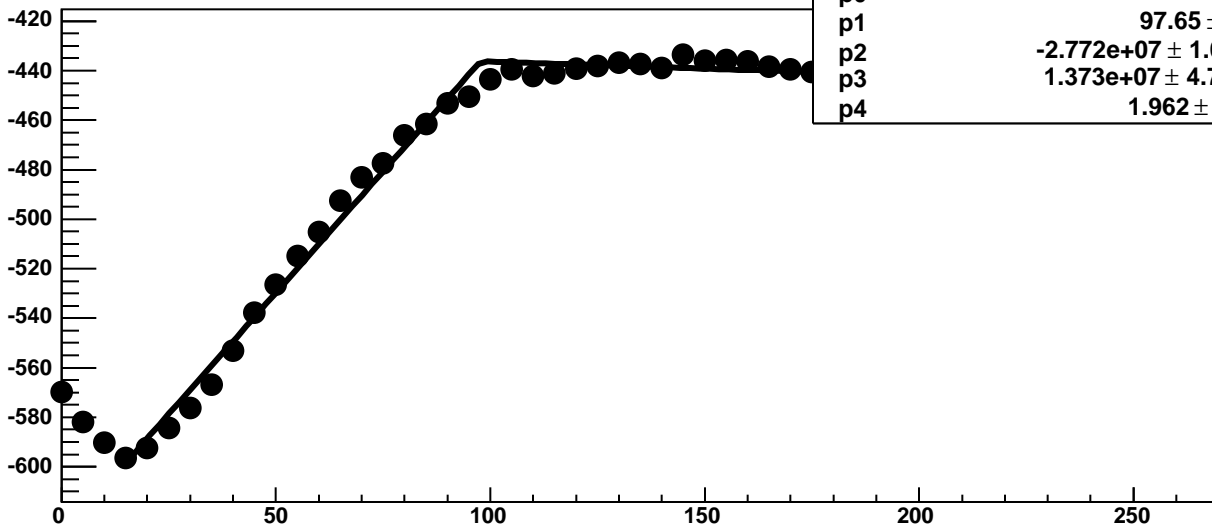
Chip 3, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

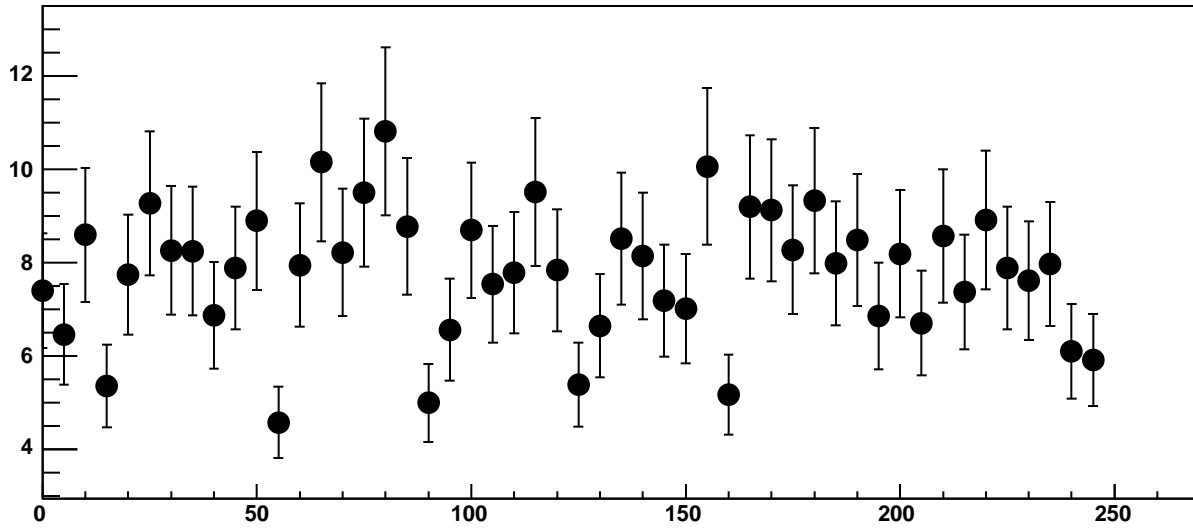


Chip 3, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold

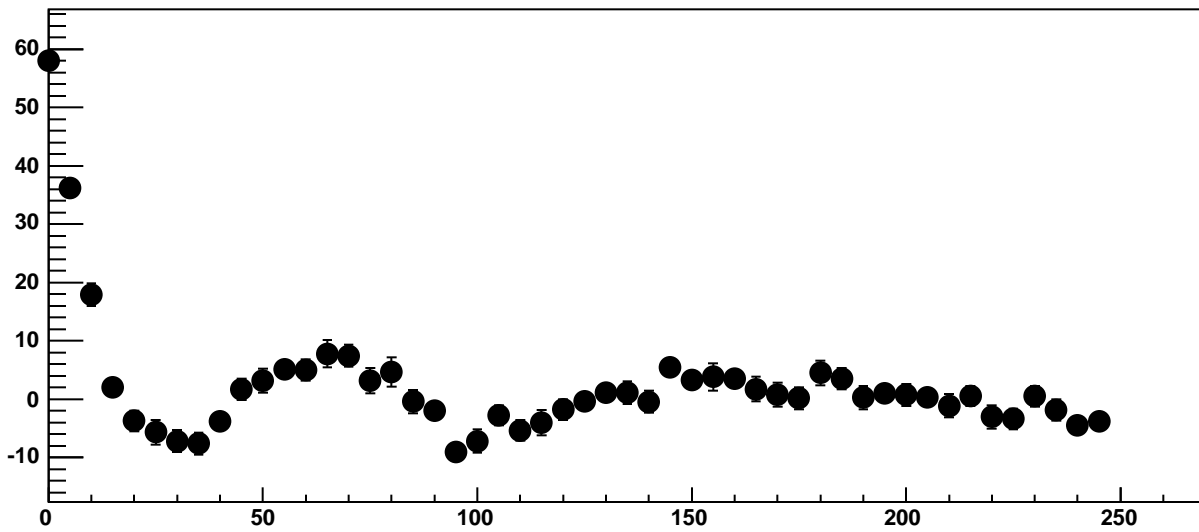


χ^2 / ndf	240.6 / 41
p0	-436.2 ± 0.6261
p1	97.65 ± 0.4974
p2	$-2.772\text{e}+07 \pm 1.008\text{e}+06$
p3	$1.373\text{e}+07 \pm 4.769\text{e}+05$
p4	1.962 ± 0.01542

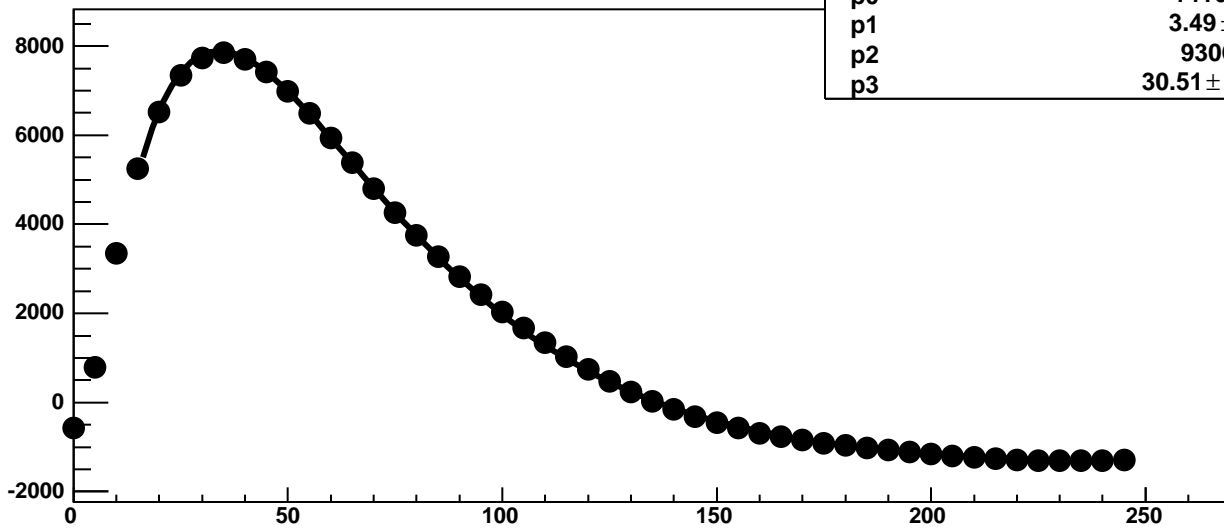
Chip 3, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

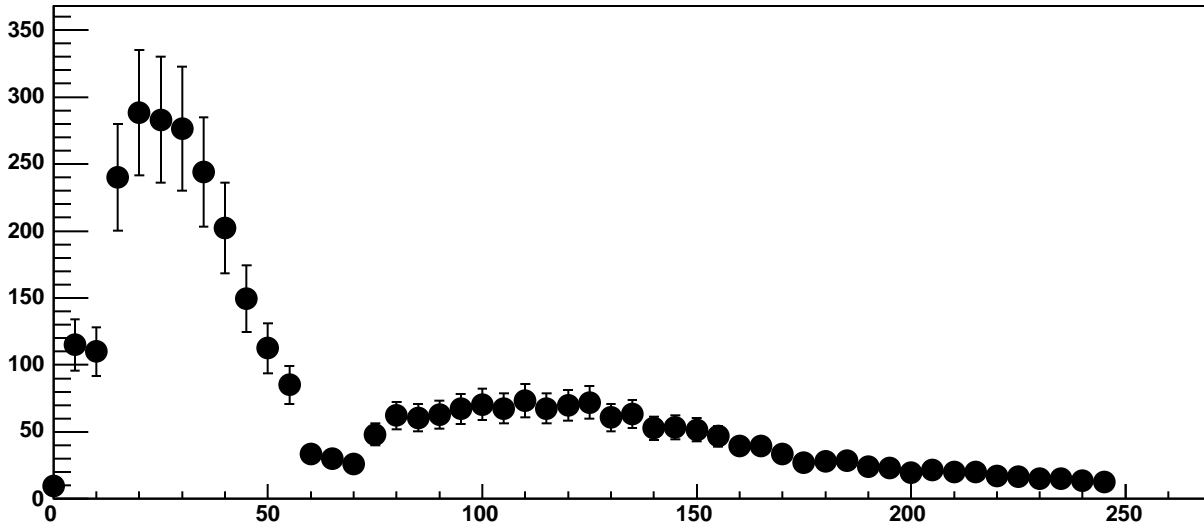


Chip 3, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold

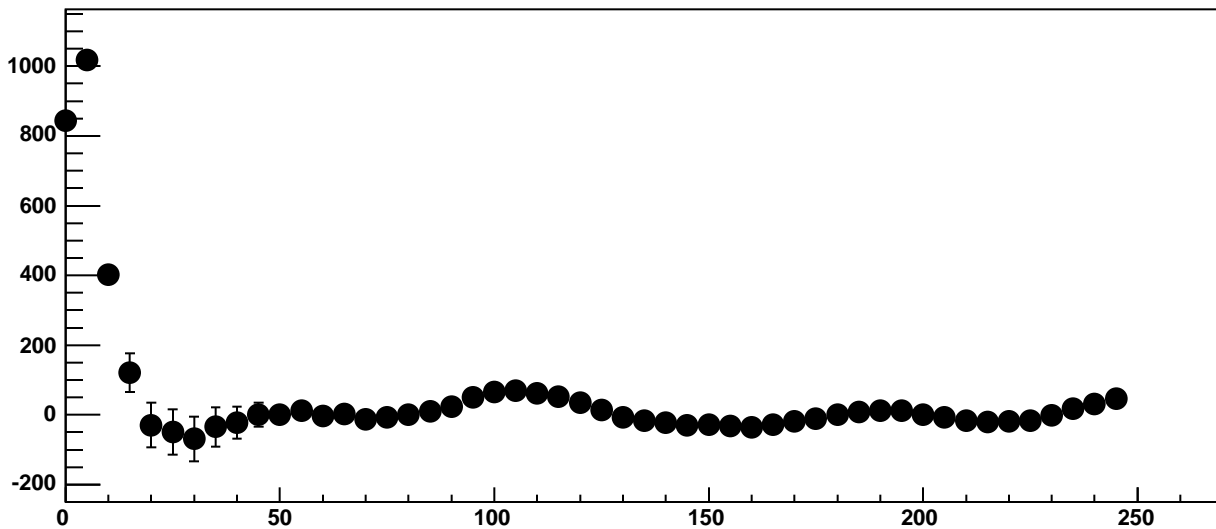


χ^2 / ndf	359.5 / 42
p0	-1416 ± 1.765
p1	3.49 ± 0.1107
p2	9306 ± 12.3
p3	30.51 ± 0.04265

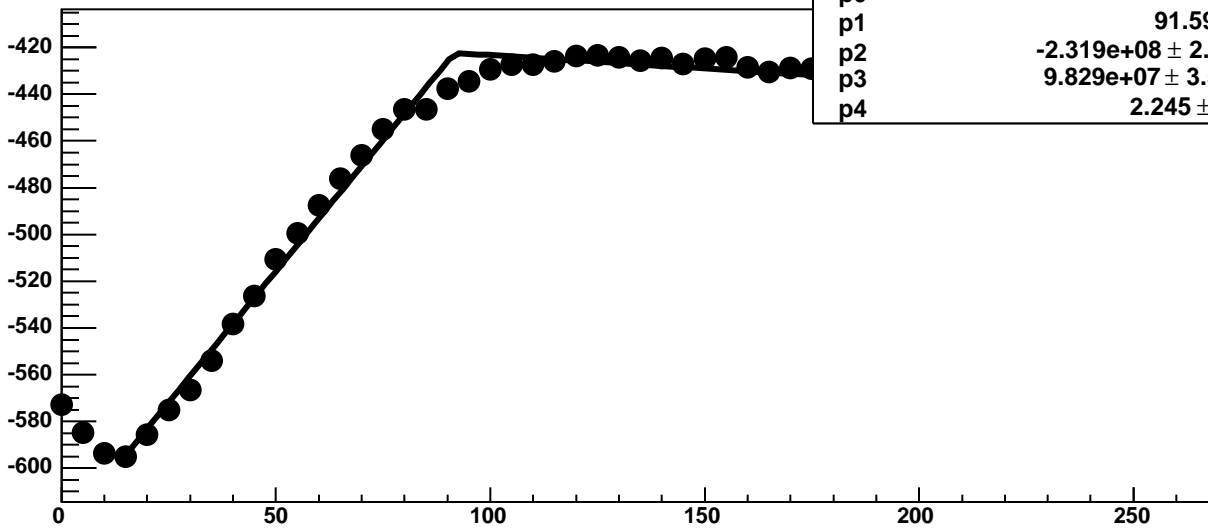
Chip 3, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold

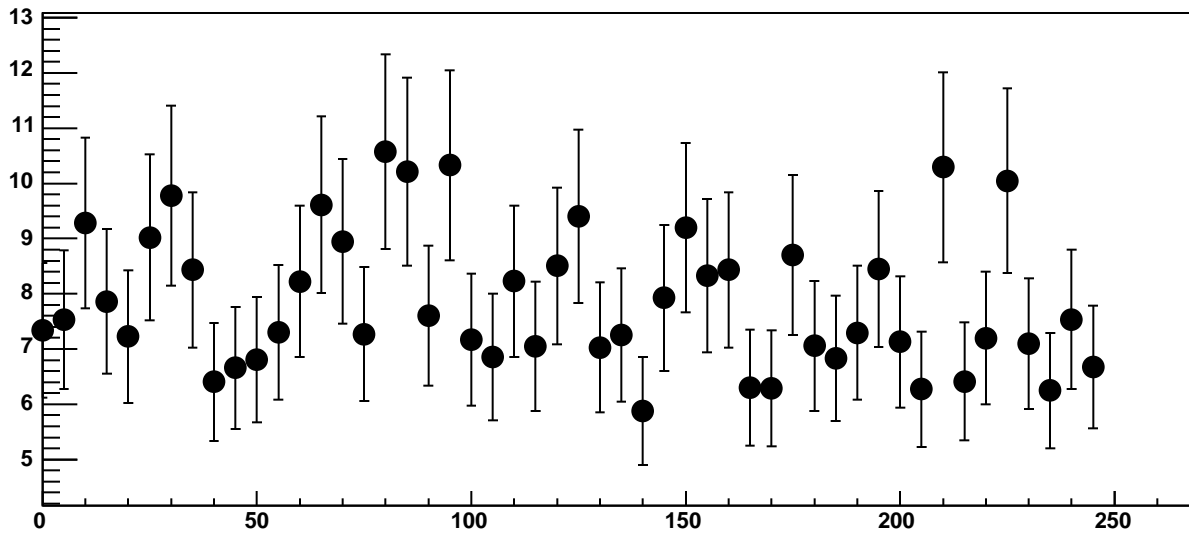


Chip 3, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

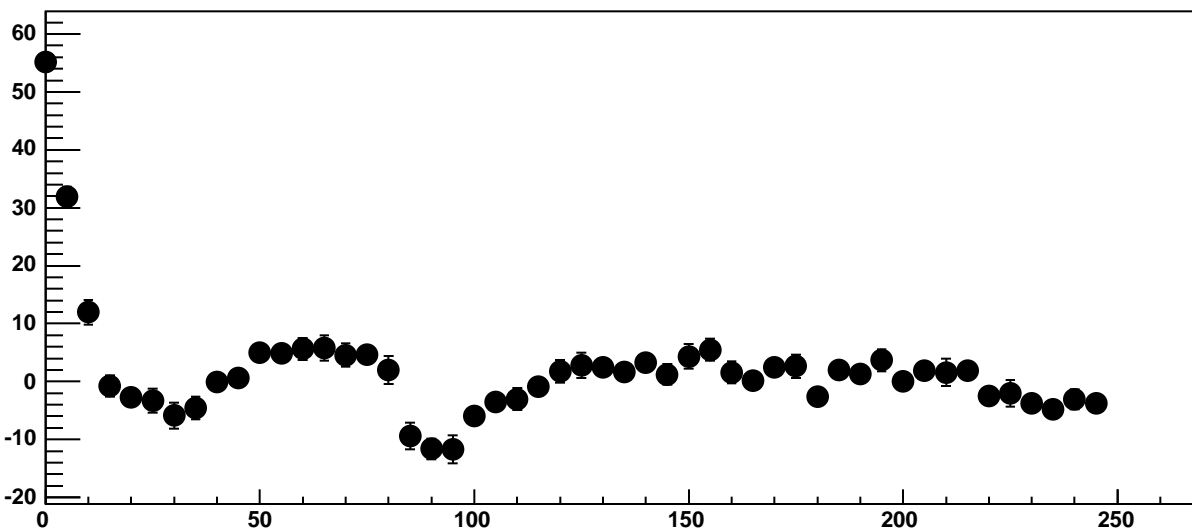


χ^2 / ndf	236.9 / 41
p0	-422.3 ± 0.6138
p1	91.59 ± 0.496
p2	$-2.319\text{e}+08 \pm 2.471\text{e}+06$
p3	$9.829\text{e}+07 \pm 3.514\text{e}+05$
p4	2.245 ± 0.02071

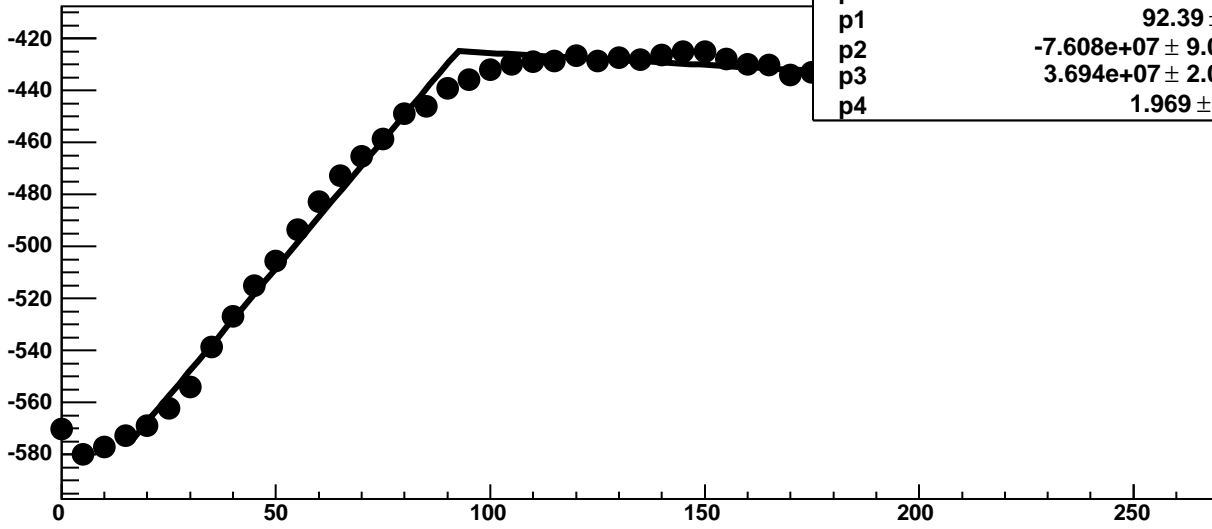
Chip 3, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

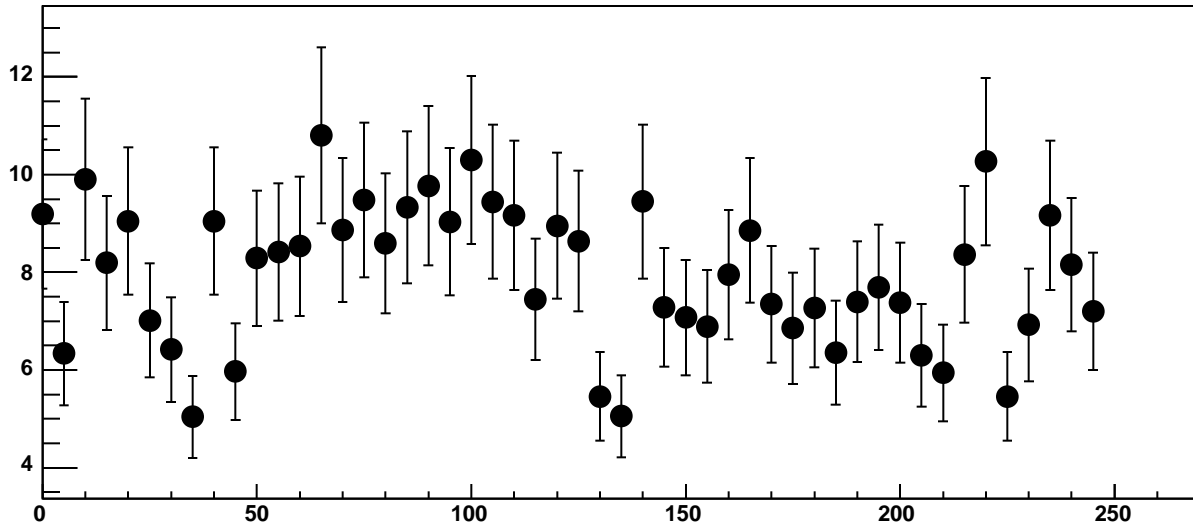


Chip 3, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

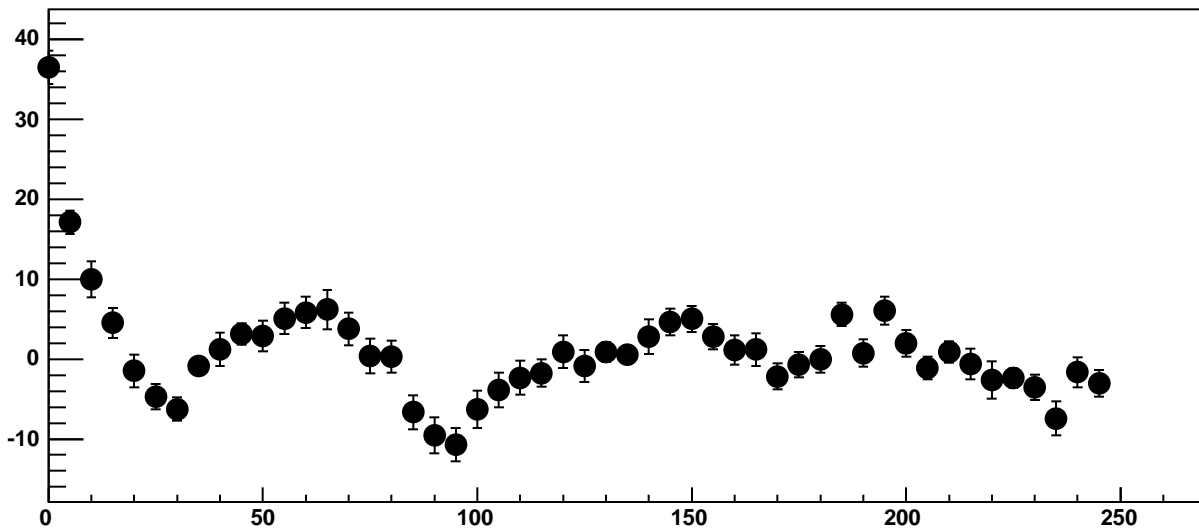


χ^2 / ndf	212.1 / 41
p0	-424.9 ± 0.6348
p1	92.39 ± 0.5943
p2	$-7.608e+07 \pm 9.033e+05$
p3	$3.694e+07 \pm 2.029e+05$
p4	1.969 ± 0.02081

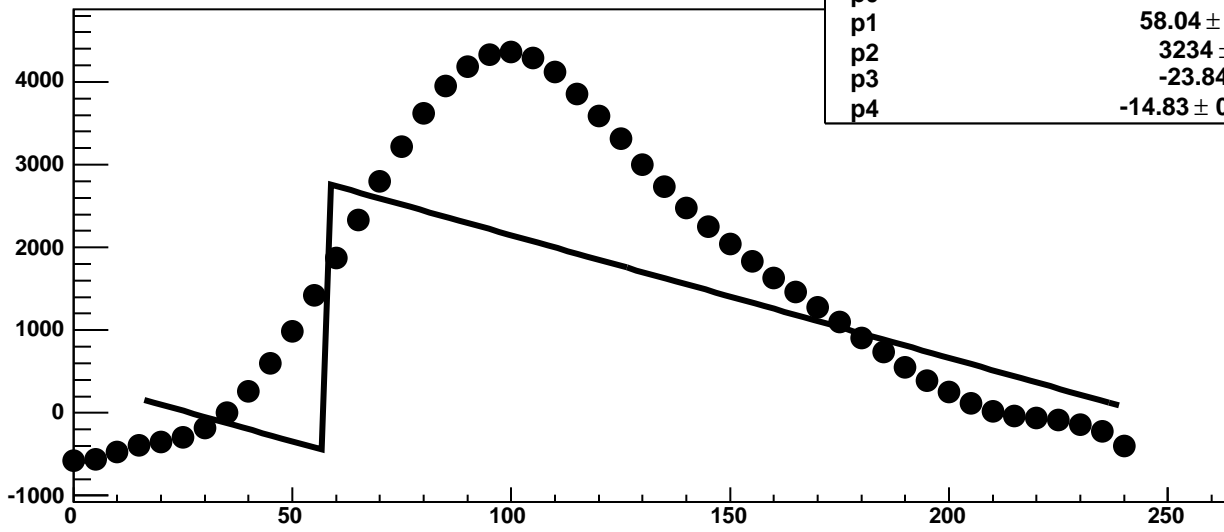
Chip 3, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

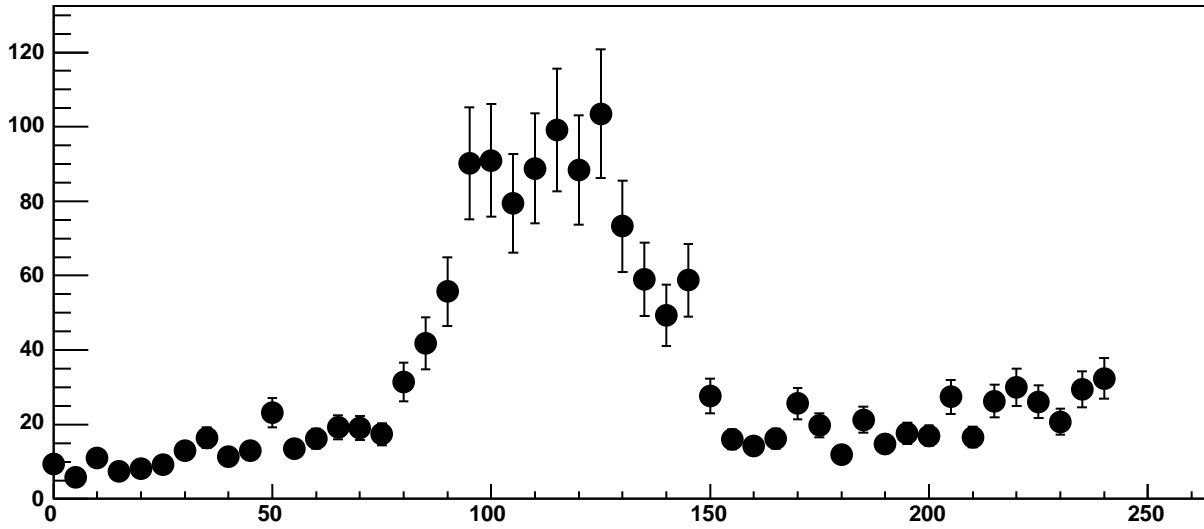


Chip 3, Channel 7, Enable 5, DAC=4000, ADC Mean vs Hold

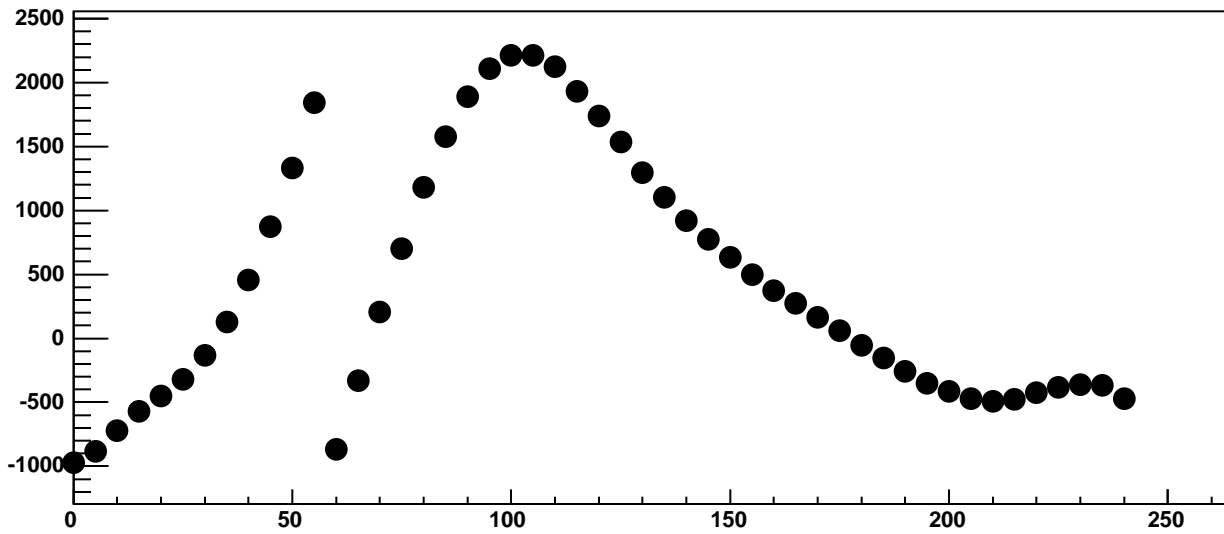


χ^2 / ndf	1.107e+06 / 41
p0	-464.1 ± 0.2016
p1	58.04 ± 0.05946
p2	3234 ± 0.2205
p3	-23.84 ± 2.956
p4	-14.83 ± 0.008792

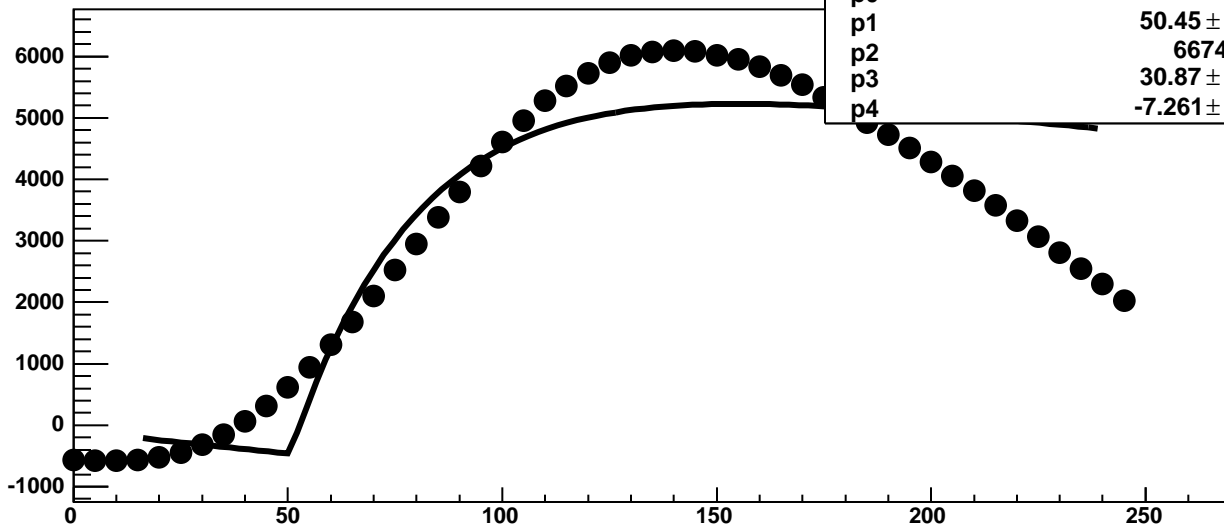
Chip 3, Channel 7, Enable 5, DAC=4000, ADC Noise vs Hold



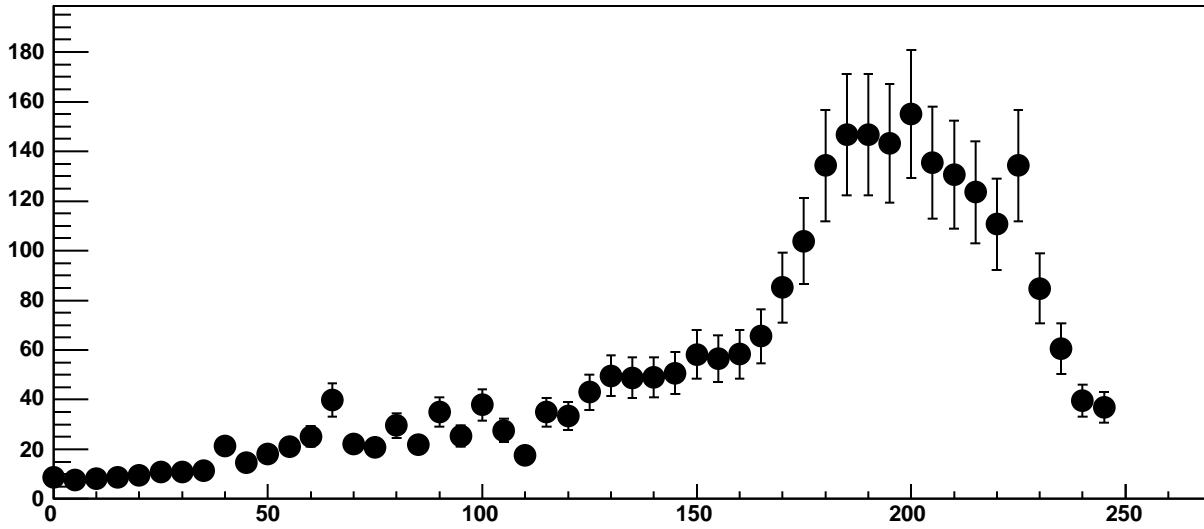
Chip 3, Channel 7, Enable 5, DAC=4000, ADC Residuals vs Hold



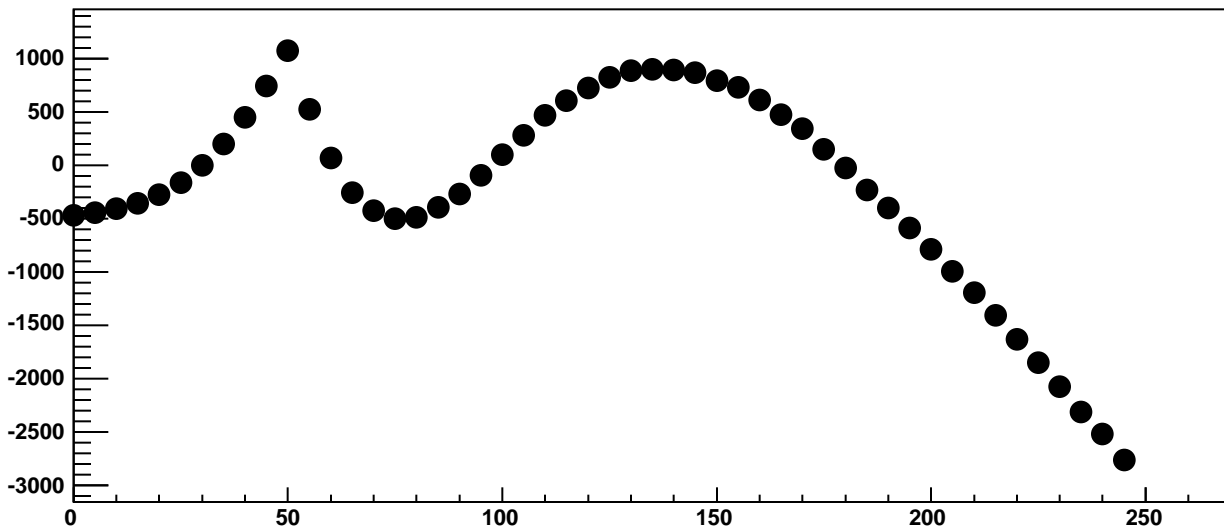
Chip 3, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold



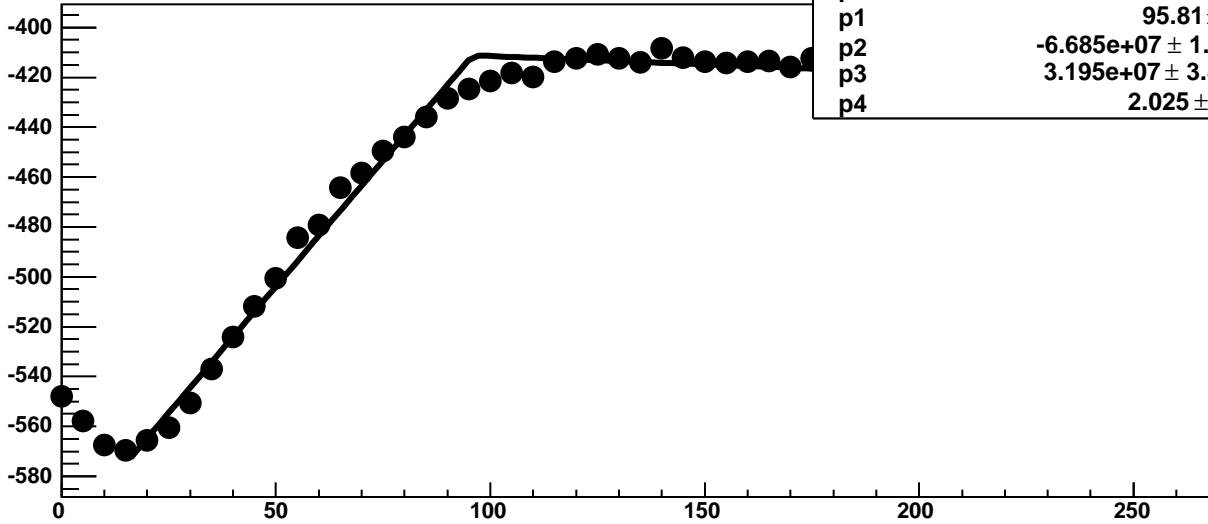
Chip 3, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

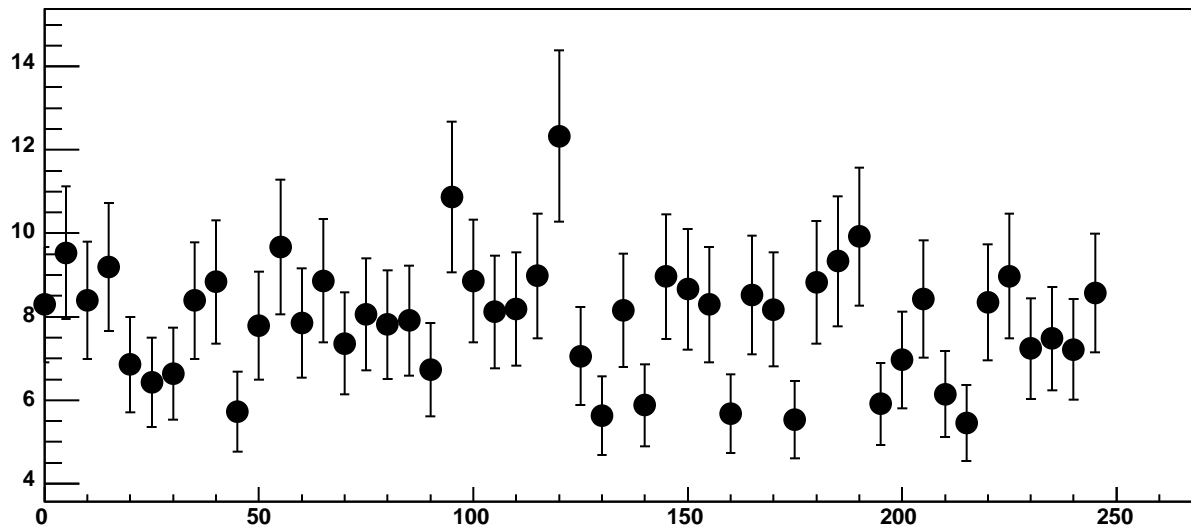


Chip 3, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

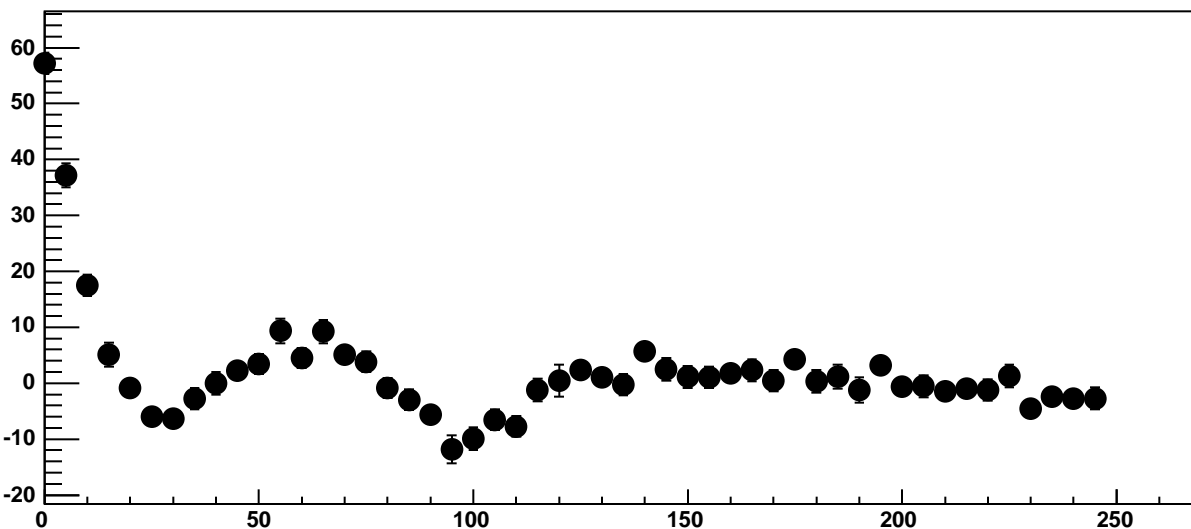


χ^2 / ndf	256.8 / 41
p0	-411.2 ± 0.6564
p1	95.81 ± 0.5254
p2	$-6.685\text{e}+07 \pm 1.008\text{e}+06$
p3	$3.195\text{e}+07 \pm 3.881\text{e}+05$
p4	2.025 ± 0.01764

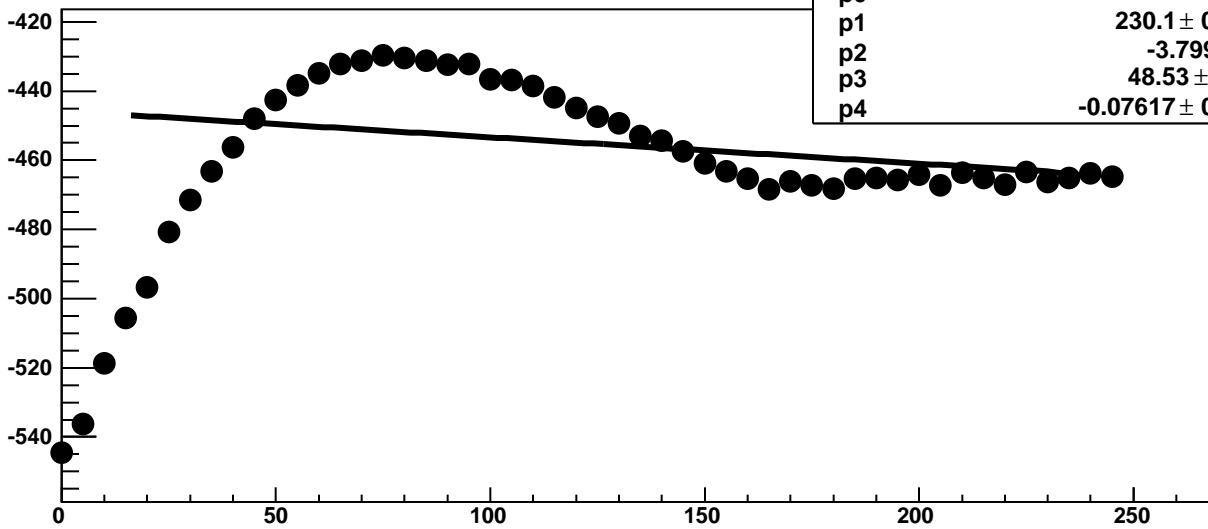
Chip 3, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

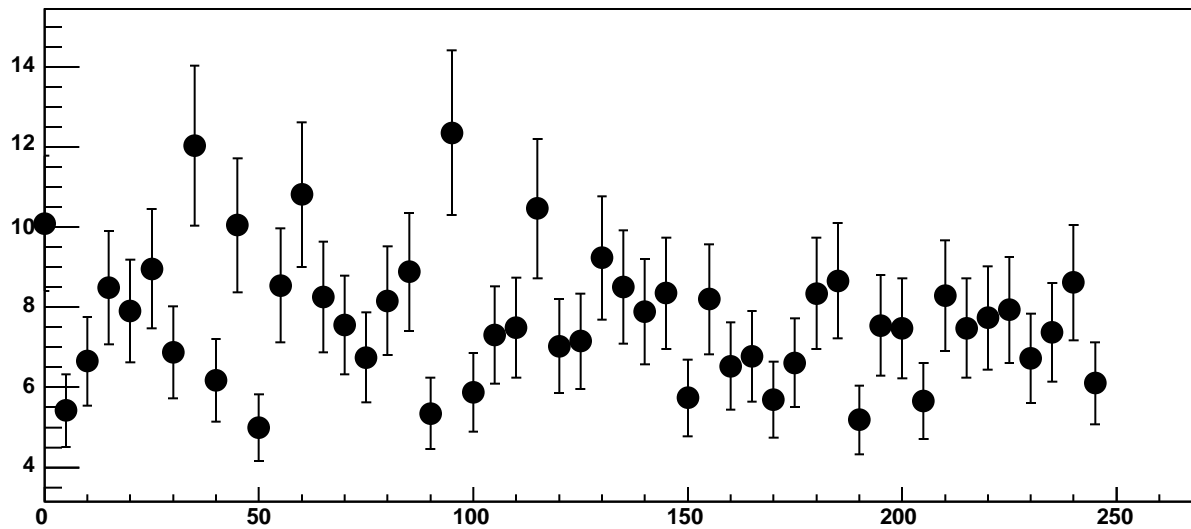


Chip 3, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold

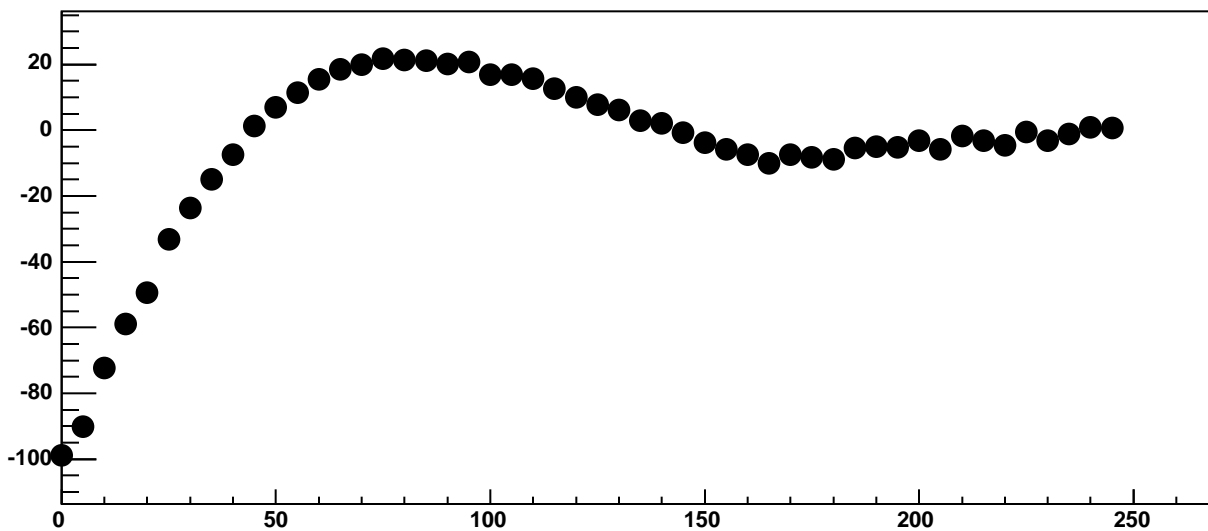


χ^2 / ndf	4021 / 41
p0	-463.3 ± 0.4813
p1	230.1 ± 0.001617
p2	-3.799 ± 9.759
p3	48.53 ± 0.08667
p4	-0.07617 ± 0.003997

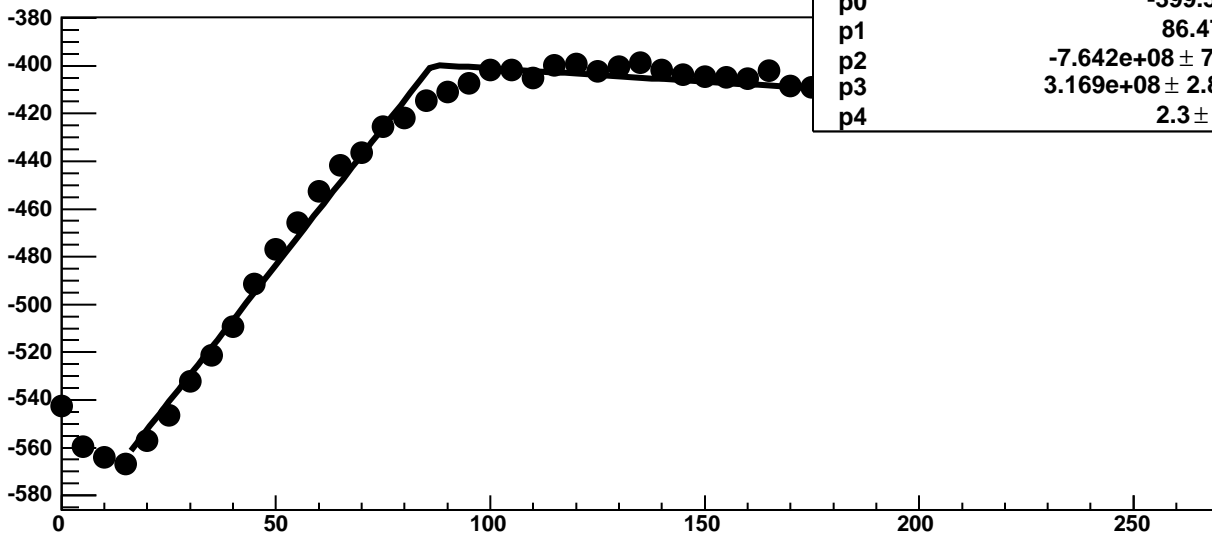
Chip 3, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold

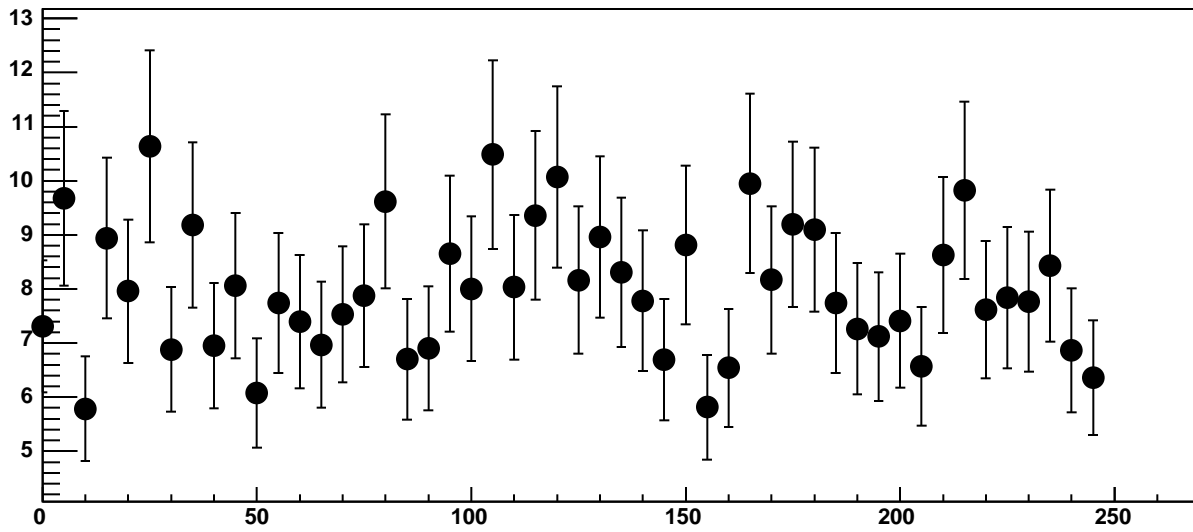


Chip 3, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

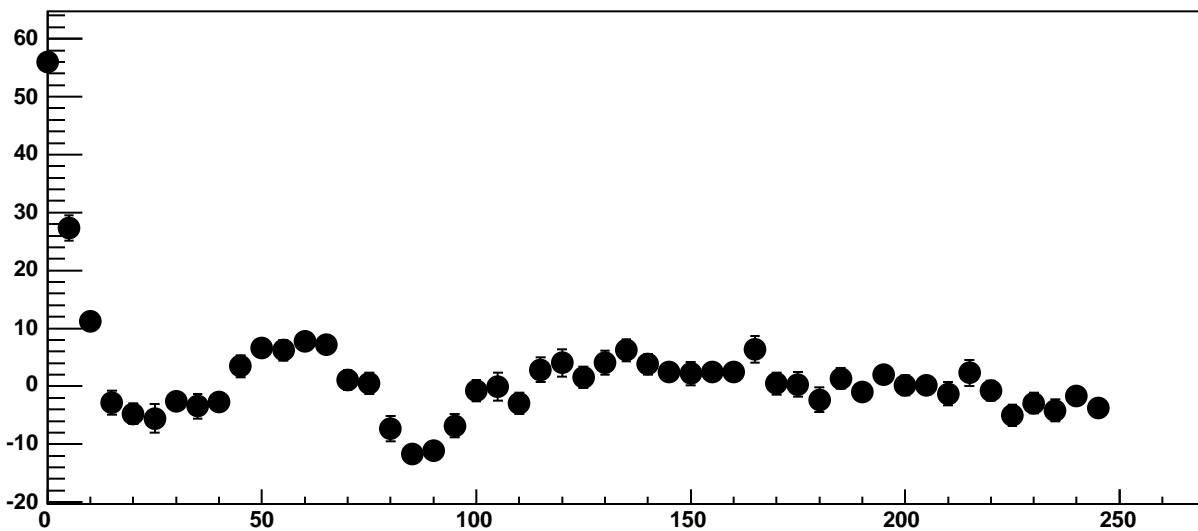


χ^2 / ndf	298.9 / 41
p0	-399.5 ± 0.648
p1	86.47 ± 0.46
p2	$-7.642\text{e}+08 \pm 7.33\text{e}+06$
p3	$3.169\text{e}+08 \pm 2.883\text{e}+05$
p4	2.3 ± 0.02185

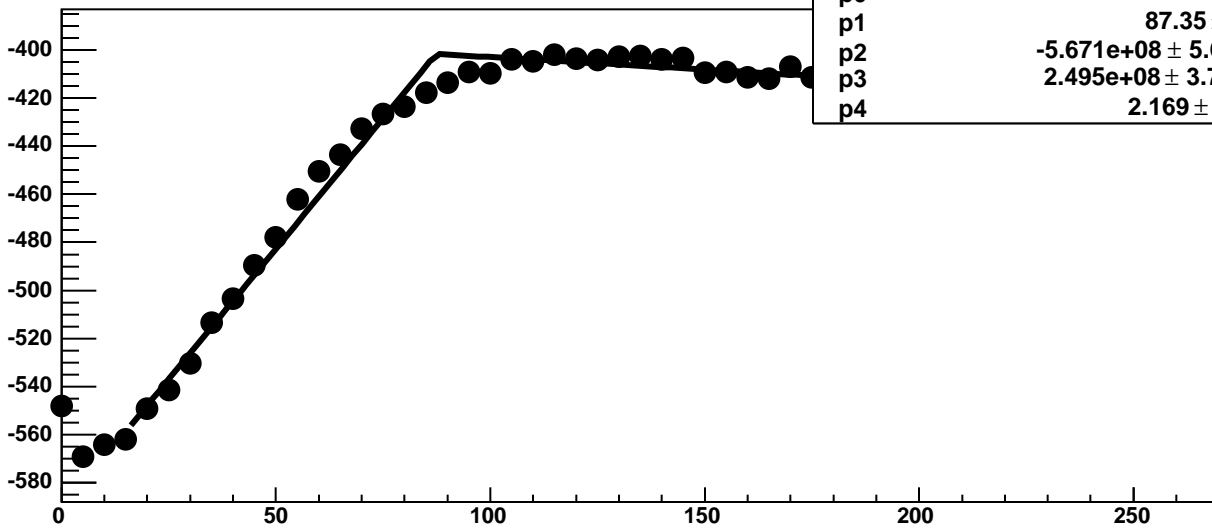
Chip 3, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

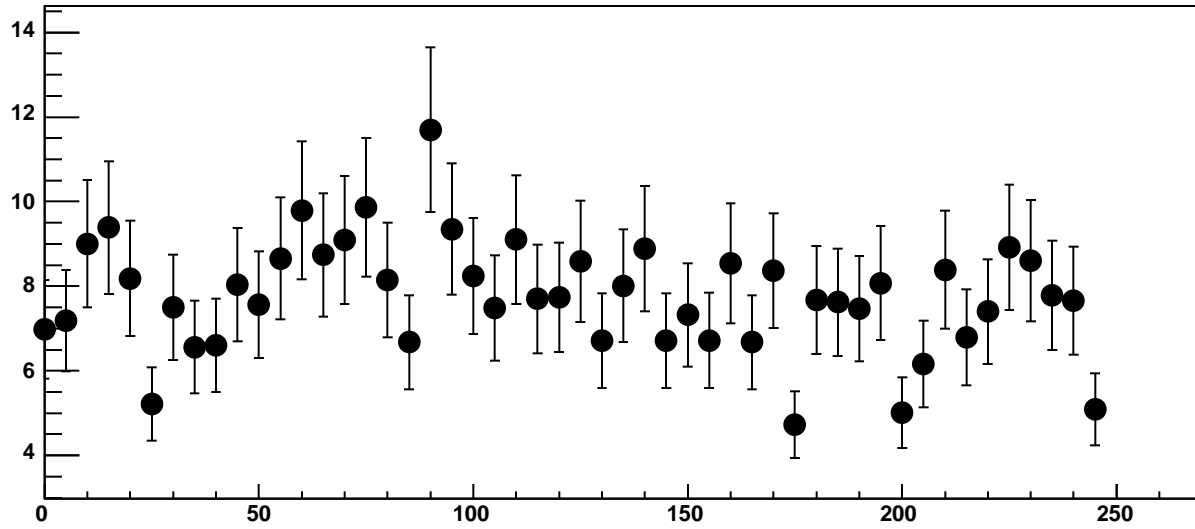


Chip 3, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

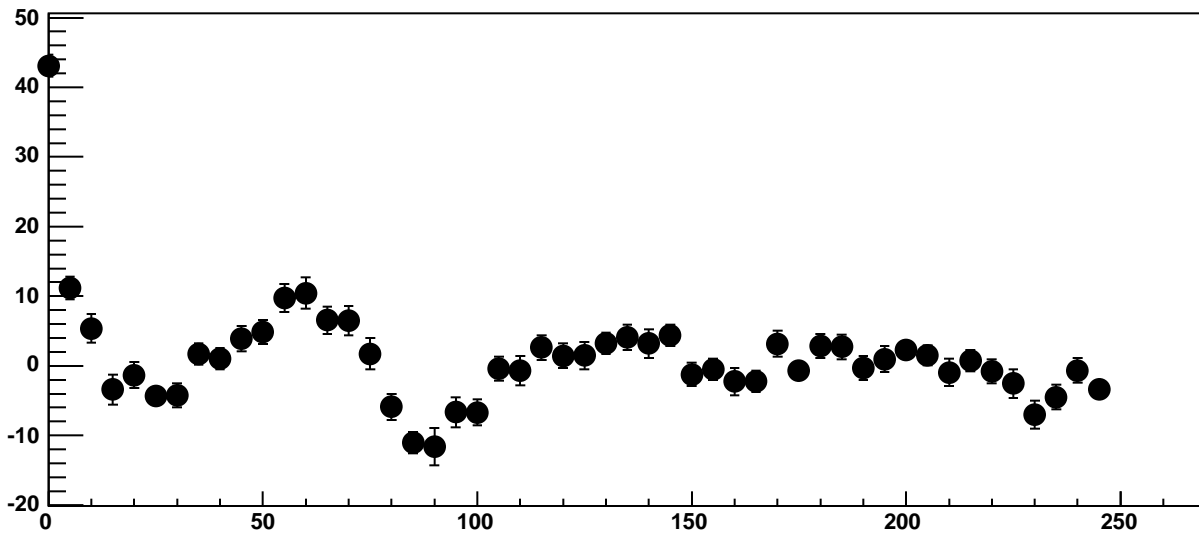


χ^2 / ndf	268.3 / 41
p0	-401.7 ± 0.6351
p1	87.35 ± 0.5131
p2	$-5.671\text{e}+08 \pm 5.621\text{e}+06$
p3	$2.495\text{e}+08 \pm 3.795\text{e}+05$
p4	2.169 ± 0.02068

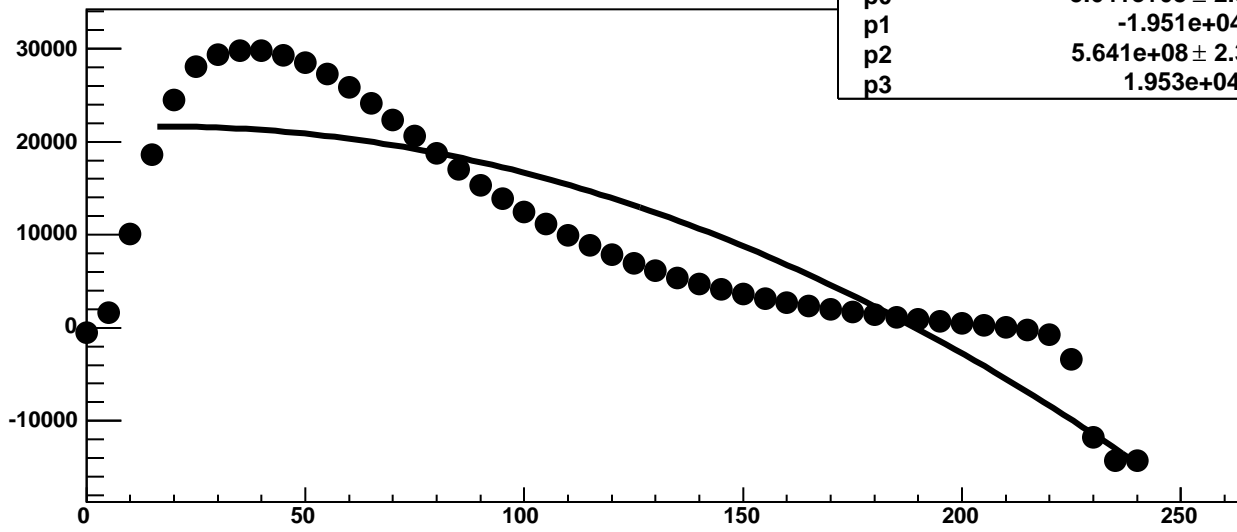
Chip 3, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



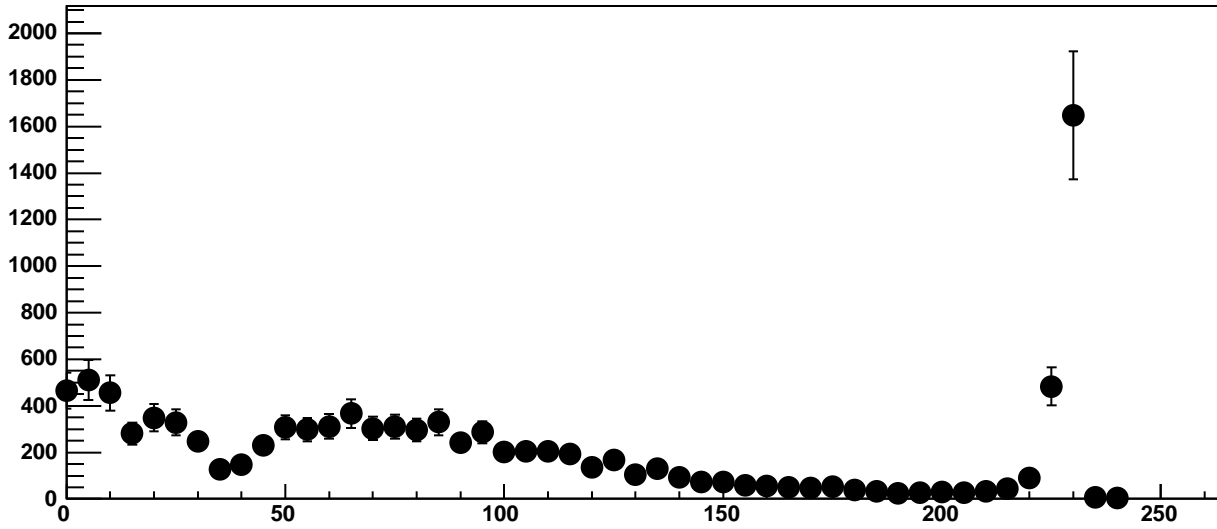
Chip 3, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold



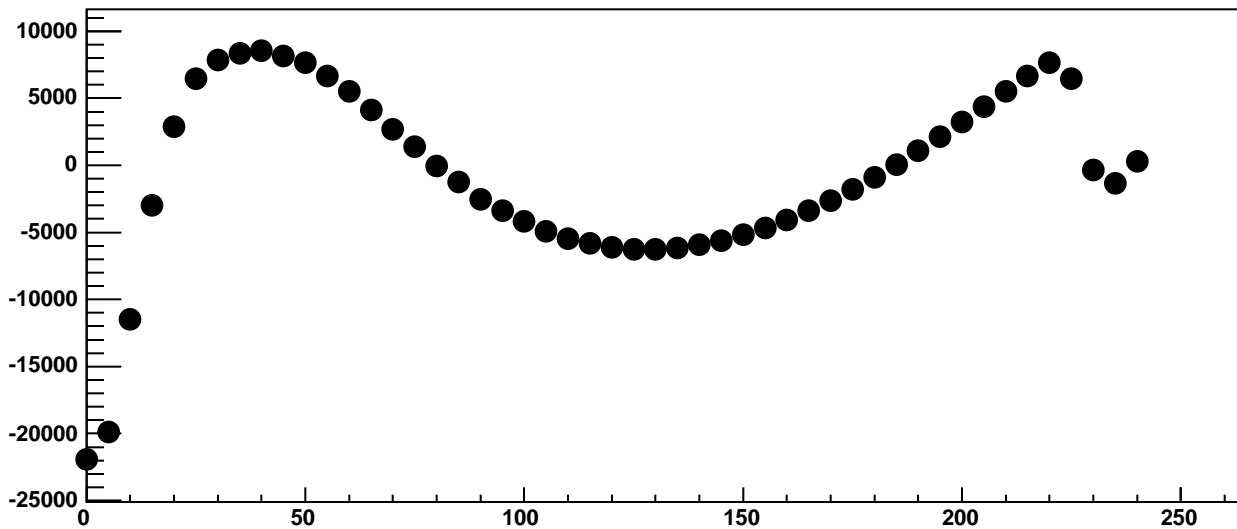
Chip 3, Channel 8, Enable 5!, DAC=4000, ADC Mean vs Hold



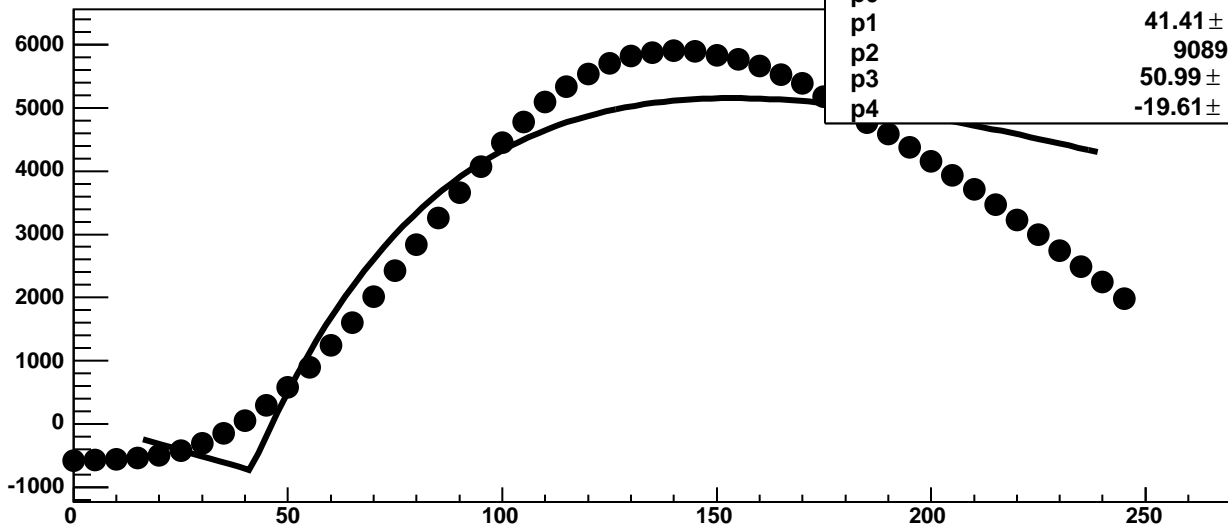
Chip 3, Channel 8, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 8, Enable 5!, DAC=4000, ADC Residuals vs Hold

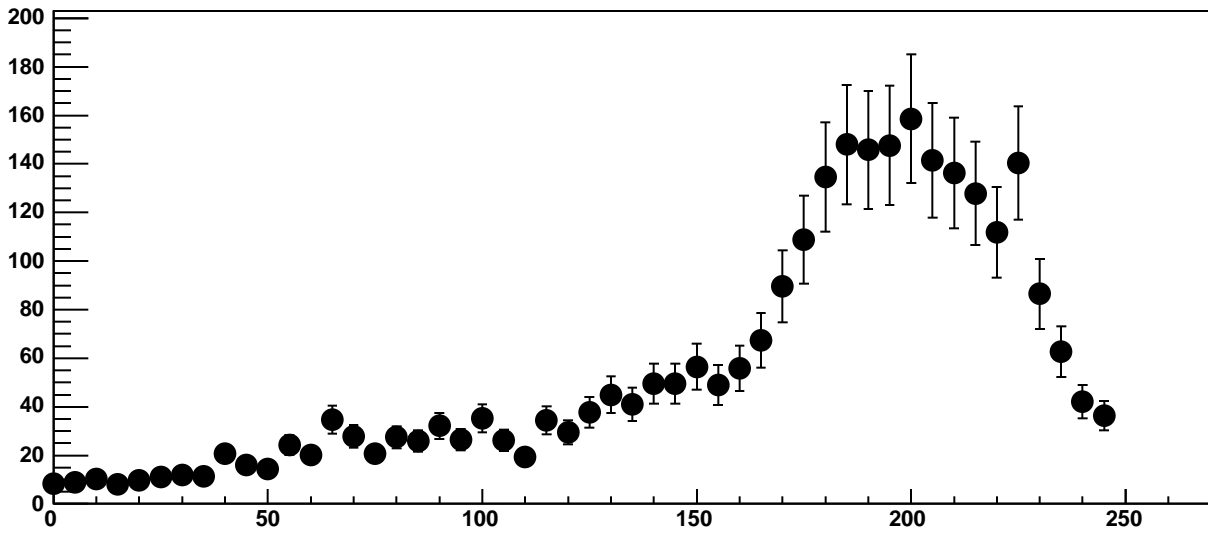


Chip 3, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold

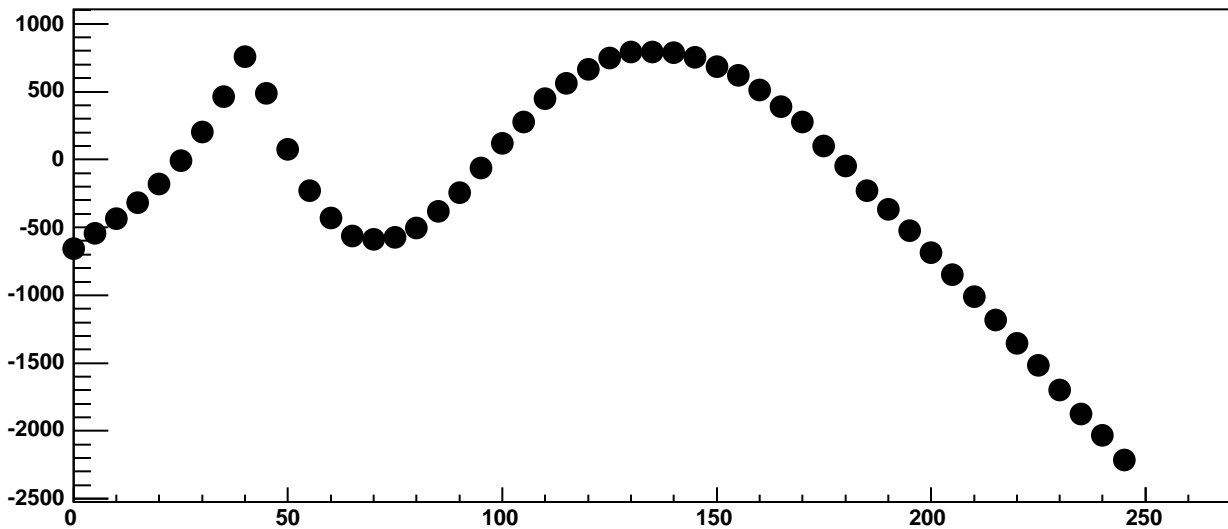


χ^2 / ndf	3.128e+05 / 41
p0	-730.9 ± 1.58
p1	41.41 ± 0.01971
p2	9089 ± 15.92
p3	50.99 ± 0.09315
p4	-19.61 ± 0.08086

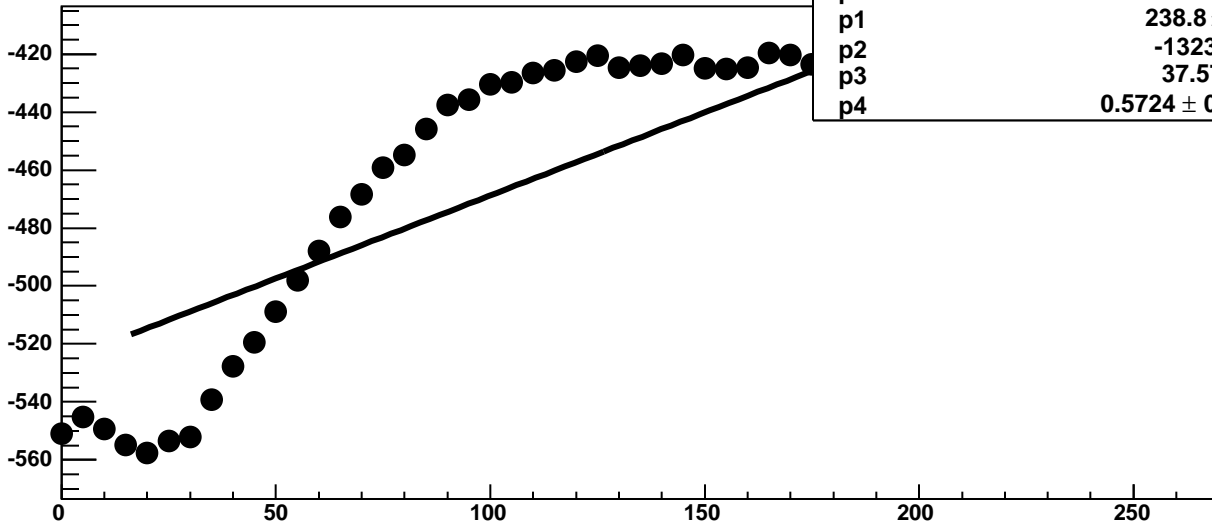
Chip 3, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold

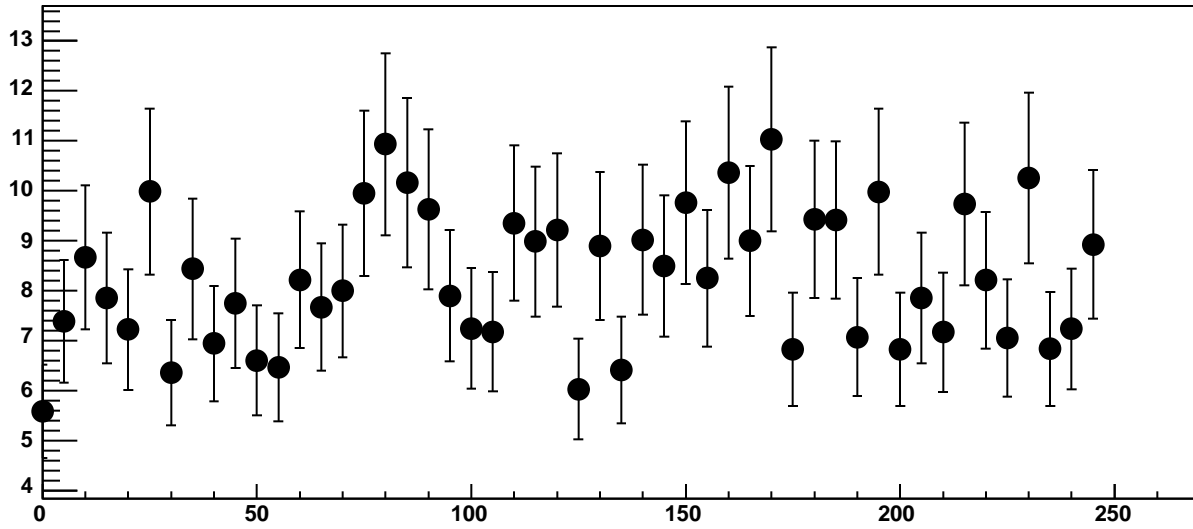


Chip 3, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

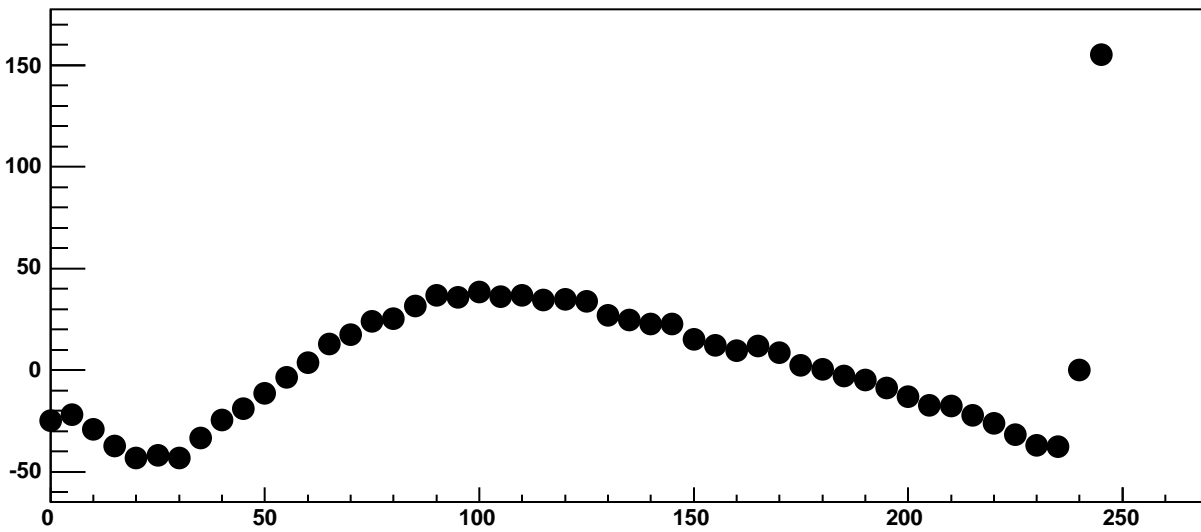


χ^2 / ndf	9372 / 41
p0	-389.3 ± 0.6527
p1	238.8 ± 0.6201
p2	-1323 ± 667.5
p3	37.57 ± 15.8
p4	0.5724 ± 0.004112

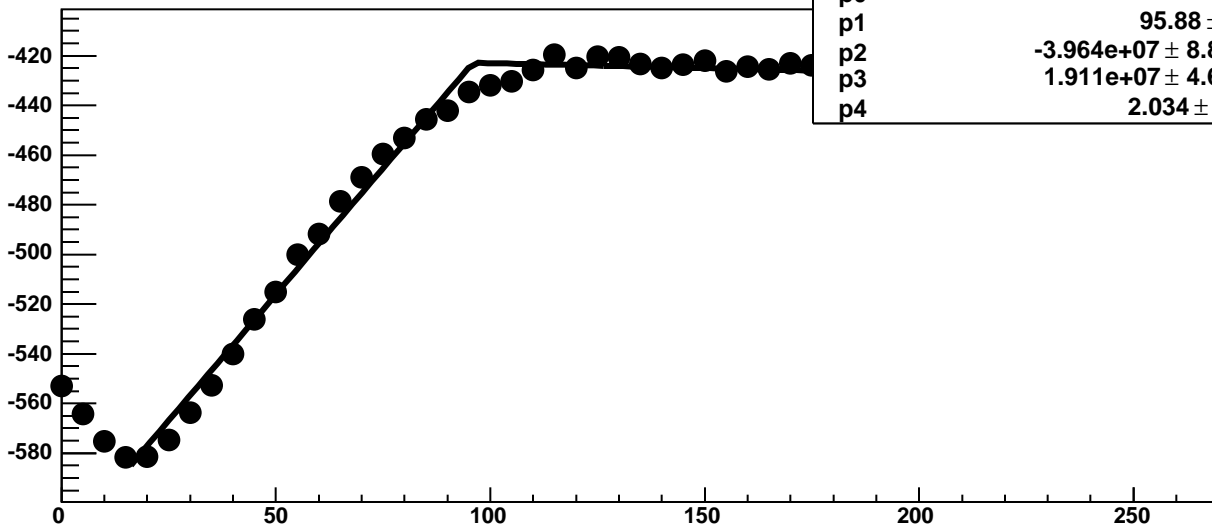
Chip 3, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

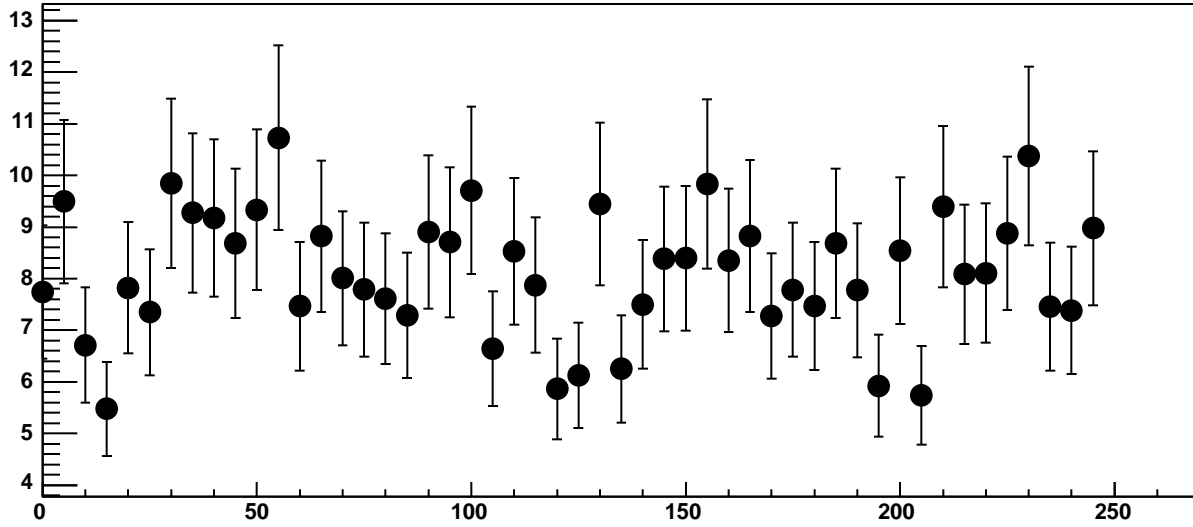


Chip 3, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

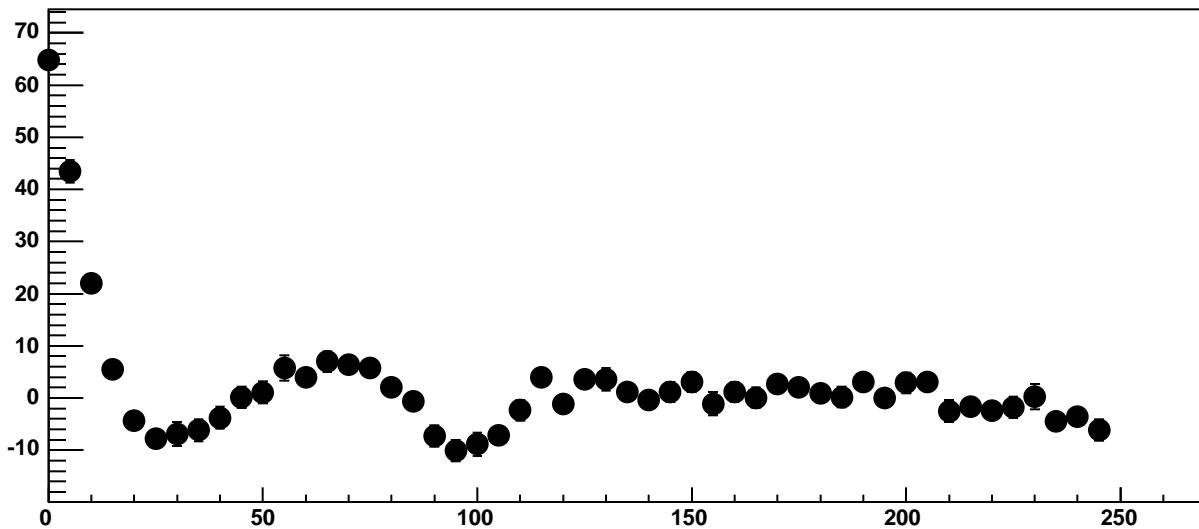


χ^2 / ndf	242.9 / 41
p0	-422.8 ± 0.6243
p1	95.88 ± 0.4855
p2	$-3.964\text{e}+07 \pm 8.823\text{e}+05$
p3	$1.911\text{e}+07 \pm 4.639\text{e}+05$
p4	2.034 ± 0.01555

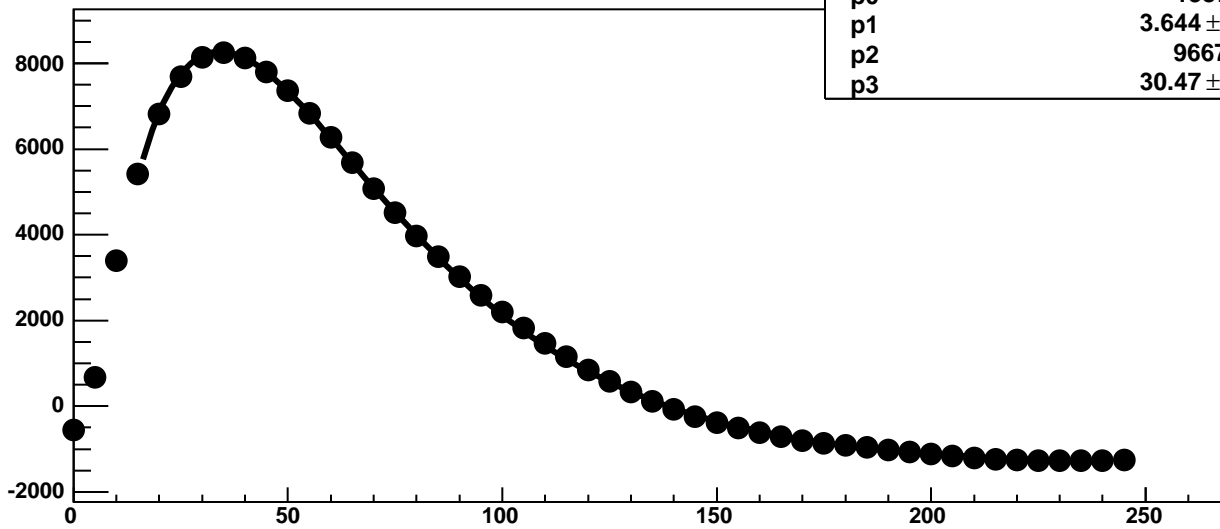
Chip 3, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold

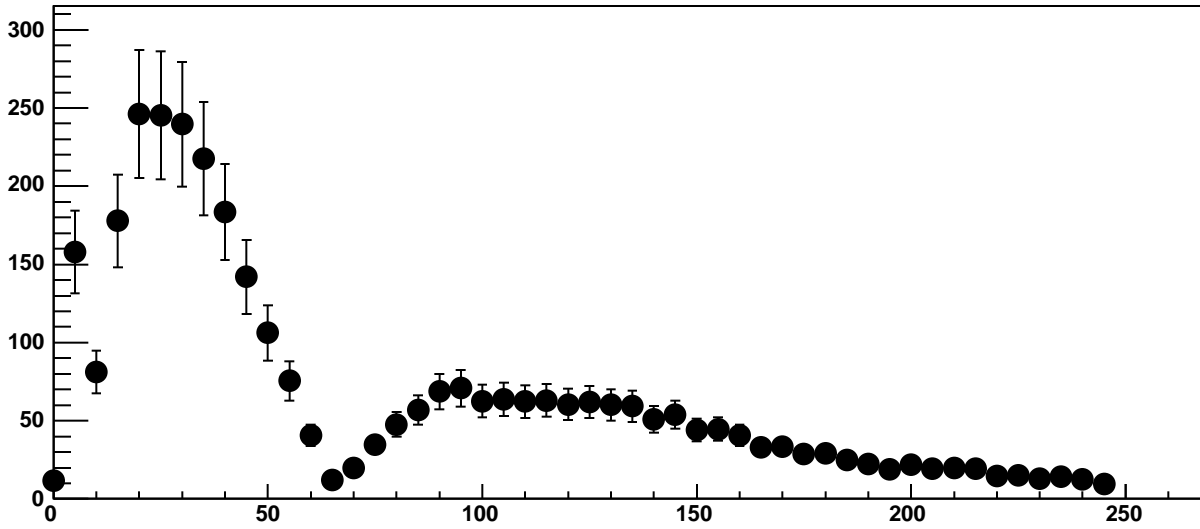


Chip 3, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold

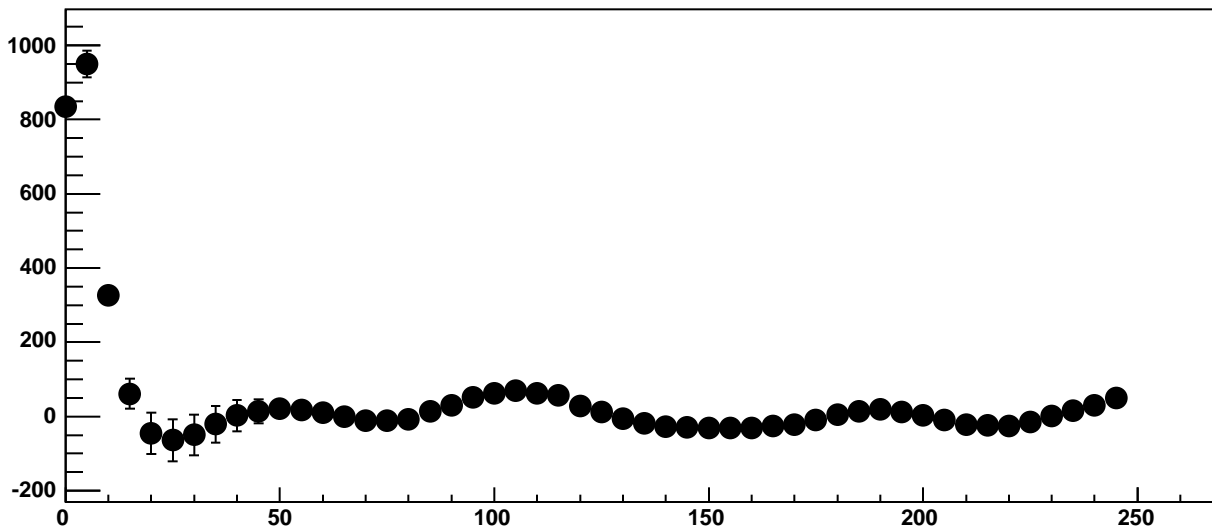


χ^2 / ndf	467.8 / 42
p0	-1387 ± 1.592
p1	3.644 ± 0.08396
p2	9667 ± 9.975
p3	30.47 ± 0.03614

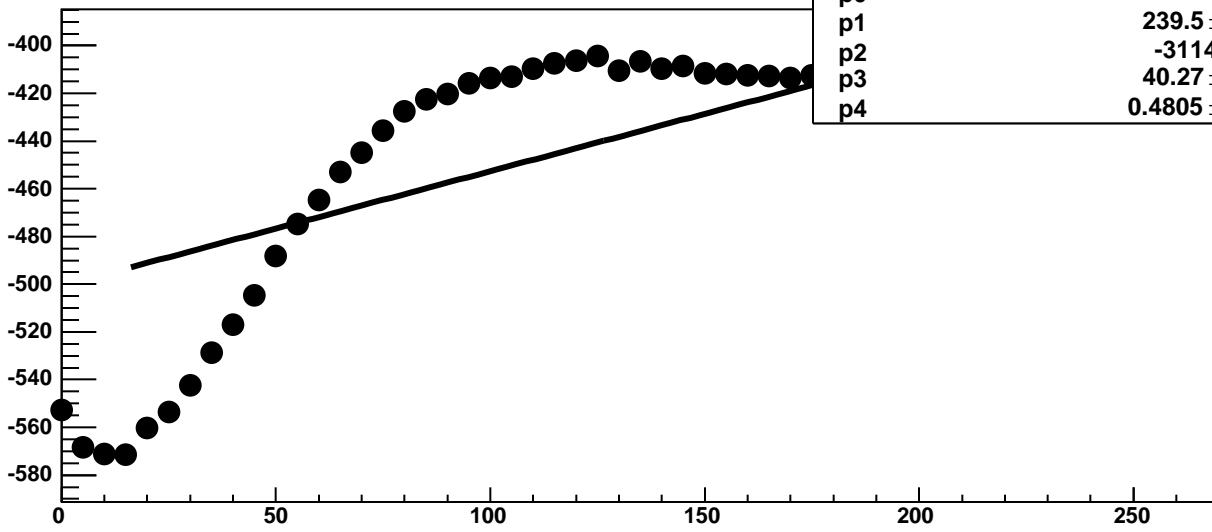
Chip 3, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold

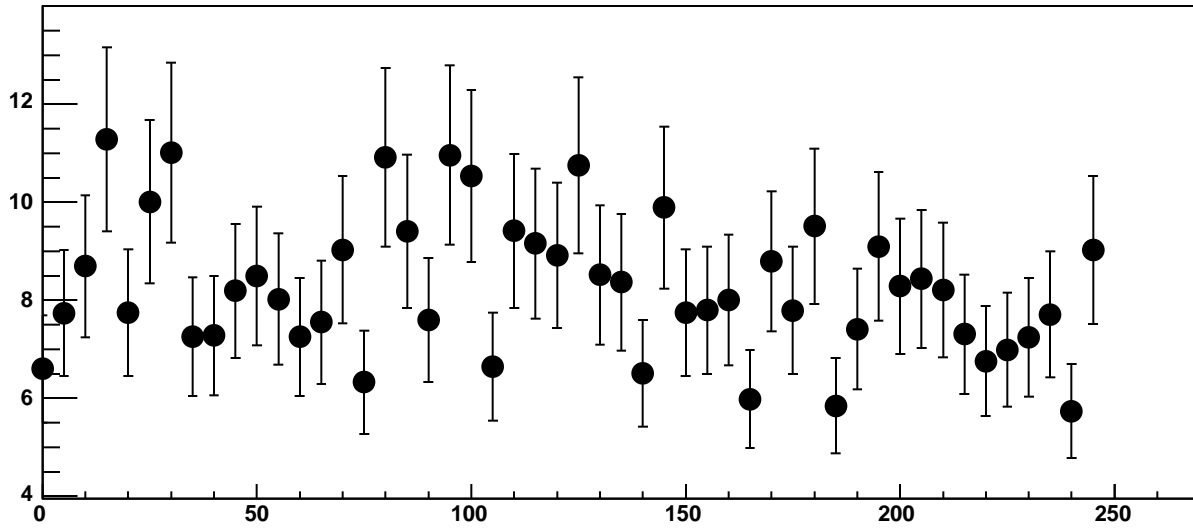


Chip 3, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

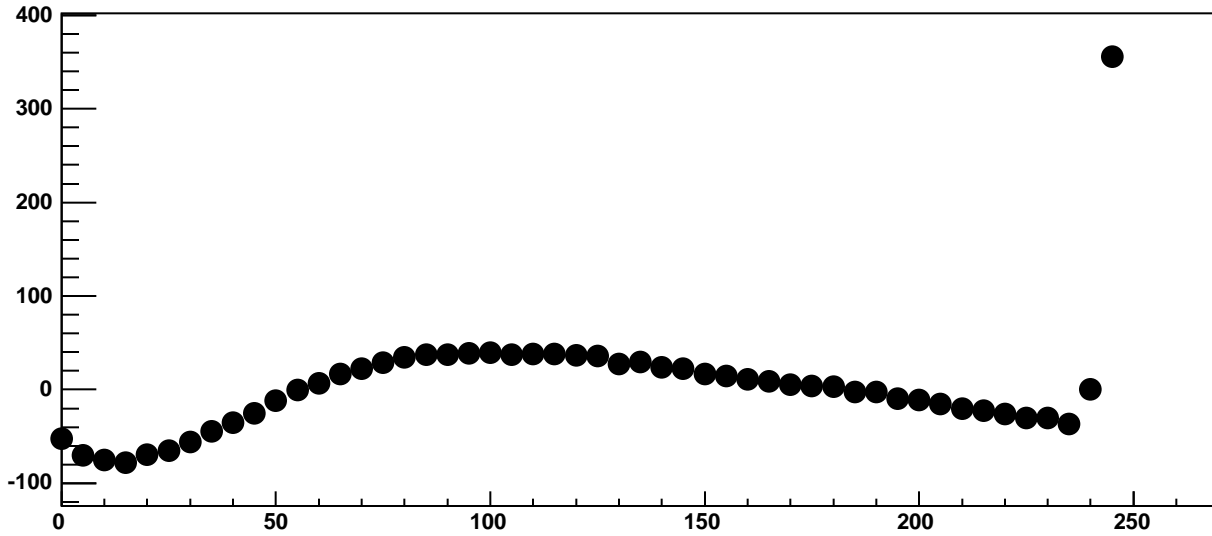


χ^2 / ndf	1.181e+04 / 41
p0	-385.6 ± 0.5279
p1	239.5 ± 0.0193
p2	-3114 ± 8.426
p3	40.27 ± 0.1109
p4	0.4805 ± 0.0042

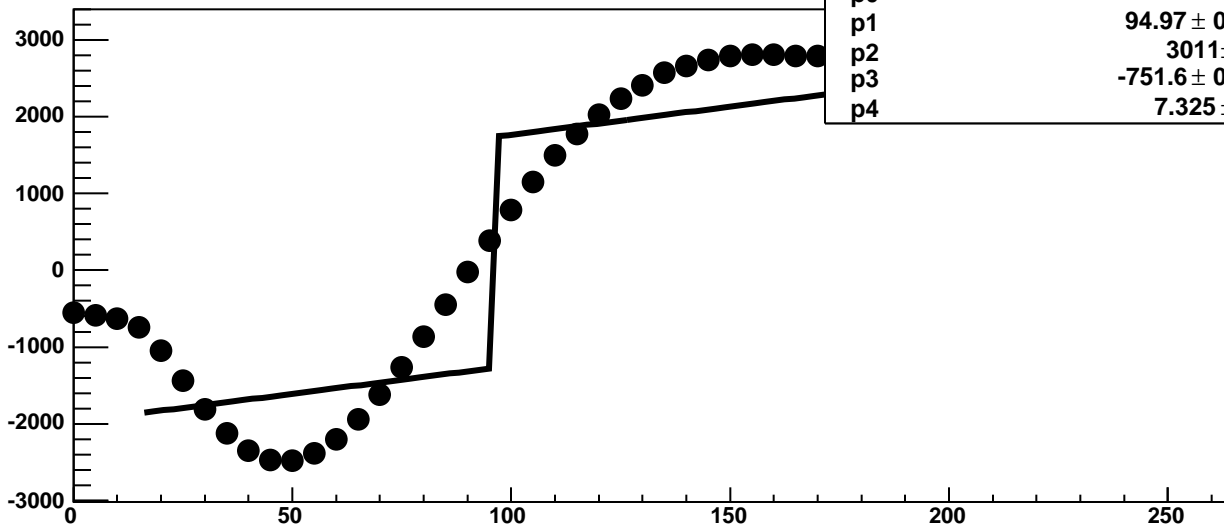
Chip 3, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

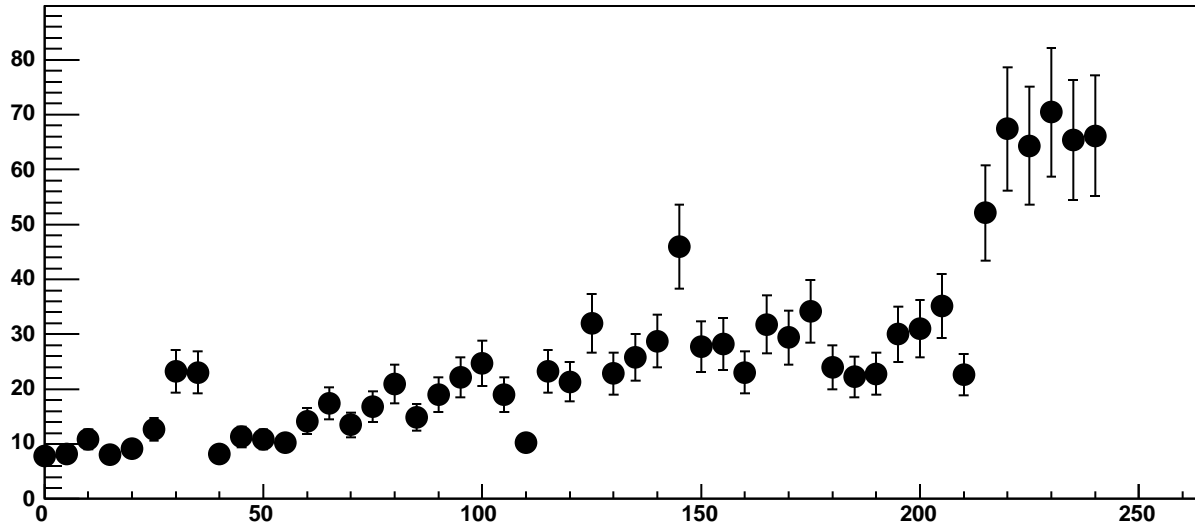


Chip 3, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold

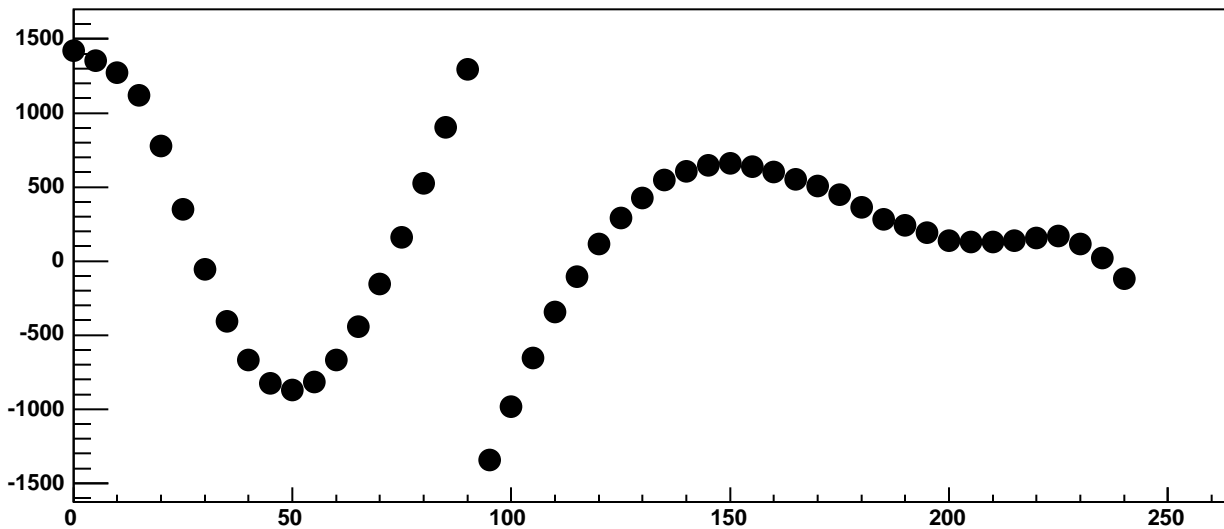


χ^2 / ndf	1.461e+06 / 41
p0	-1281 ± 0.679
p1	94.97 ± 0.001883
p2	3011 ± 0.8083
p3	-751.6 ± 0.005349
p4	7.325 ± 0.0112

Chip 3, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold

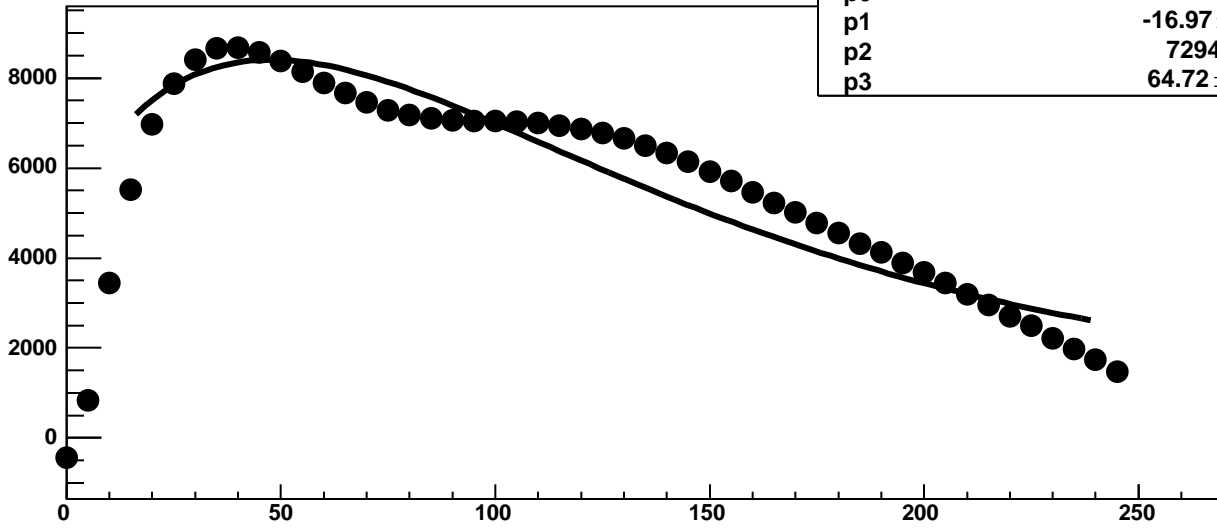


Chip 3, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold

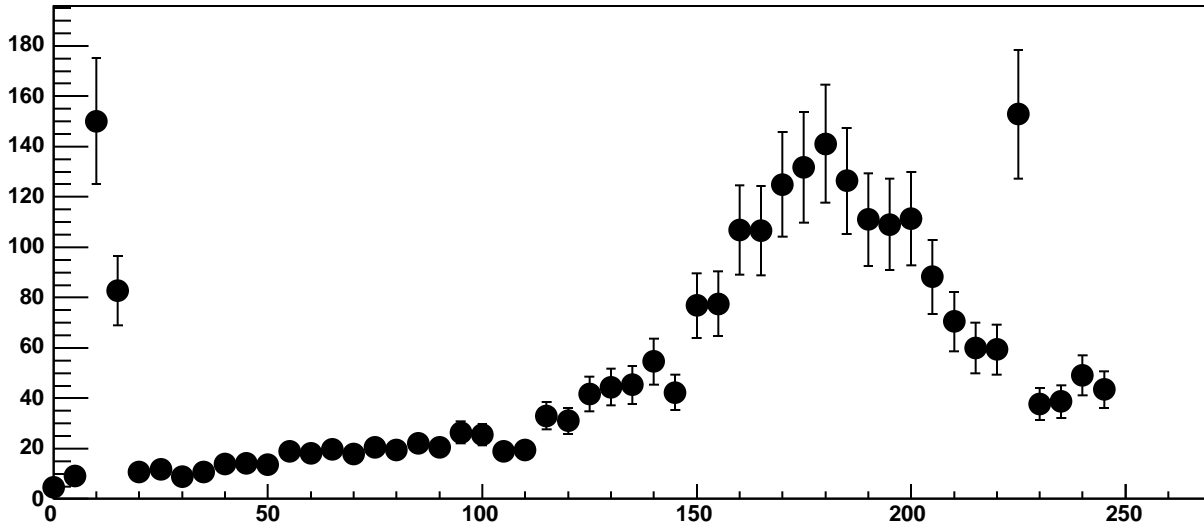


Chip 3, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold

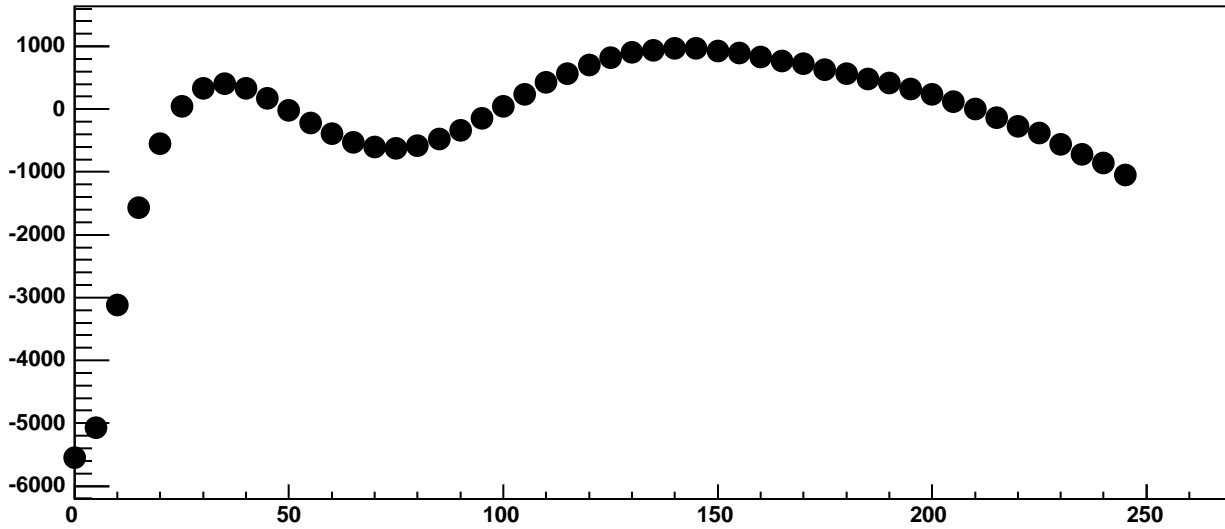
χ^2 / ndf	3.138e+05 / 42
p0	1114 ± 13.83
p1	-16.97 ± 0.1251
p2	7294 ± 13.18
p3	64.72 ± 0.1293



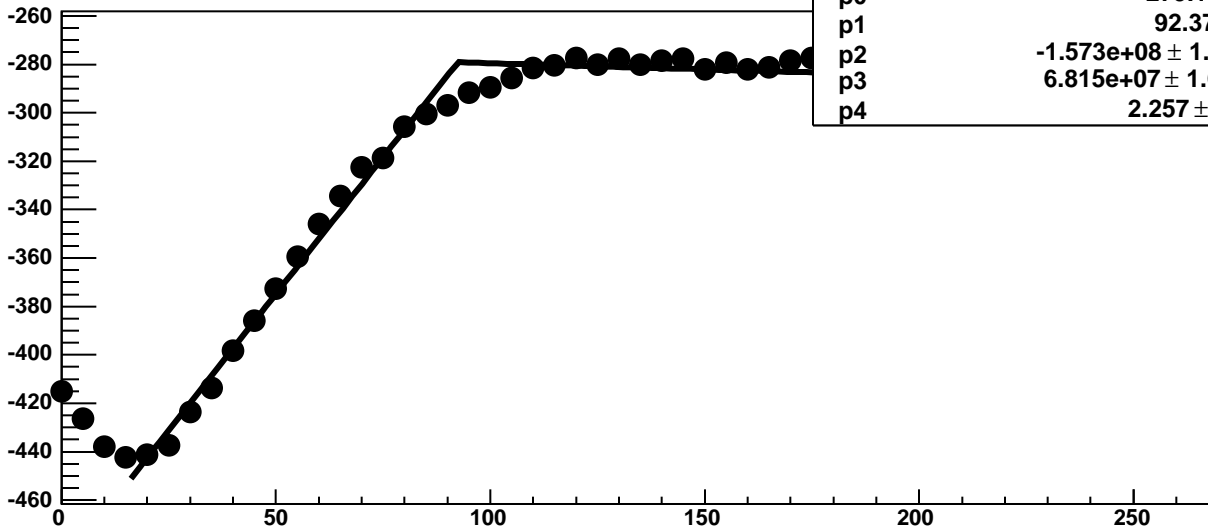
Chip 3, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold

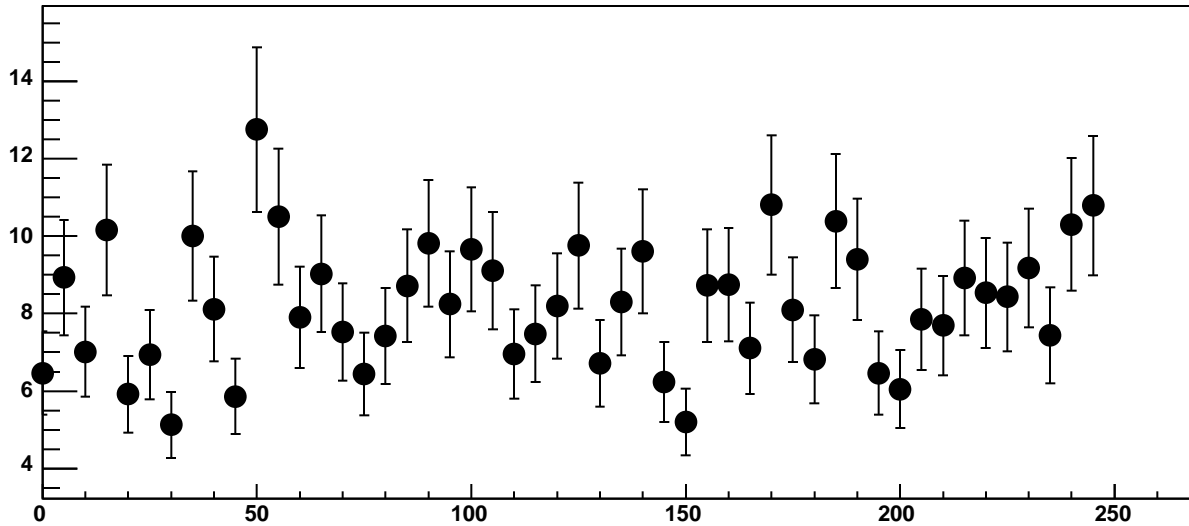


Chip 3, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

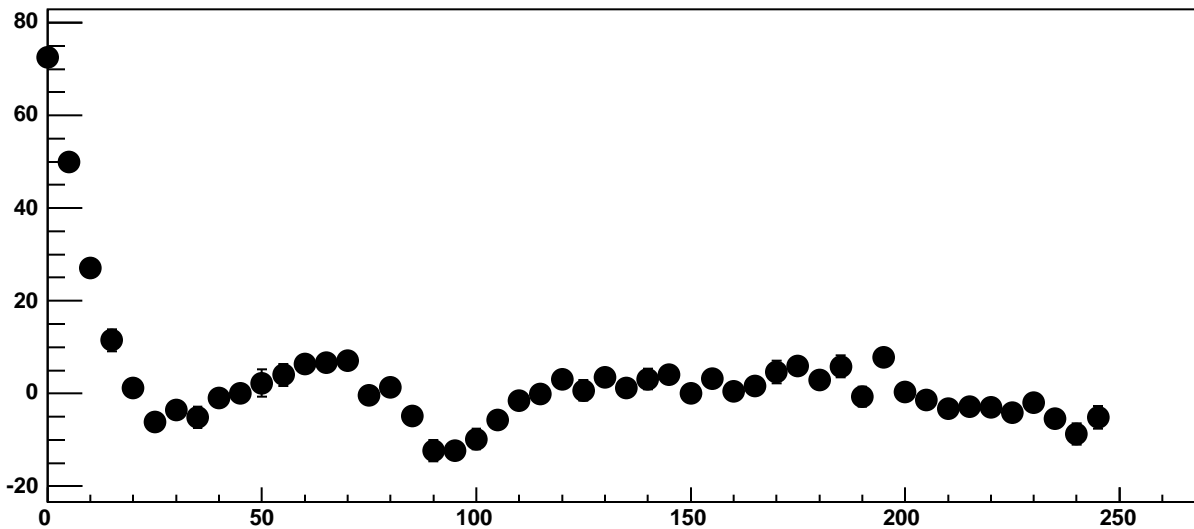


χ^2 / ndf	315.4 / 41
p0	-279.1 ± 0.6608
p1	92.37 ± 0.495
p2	-1.573e+08 ± 1.445e+06
p3	6.815e+07 ± 1.601e+05
p4	2.257 ± 0.01897

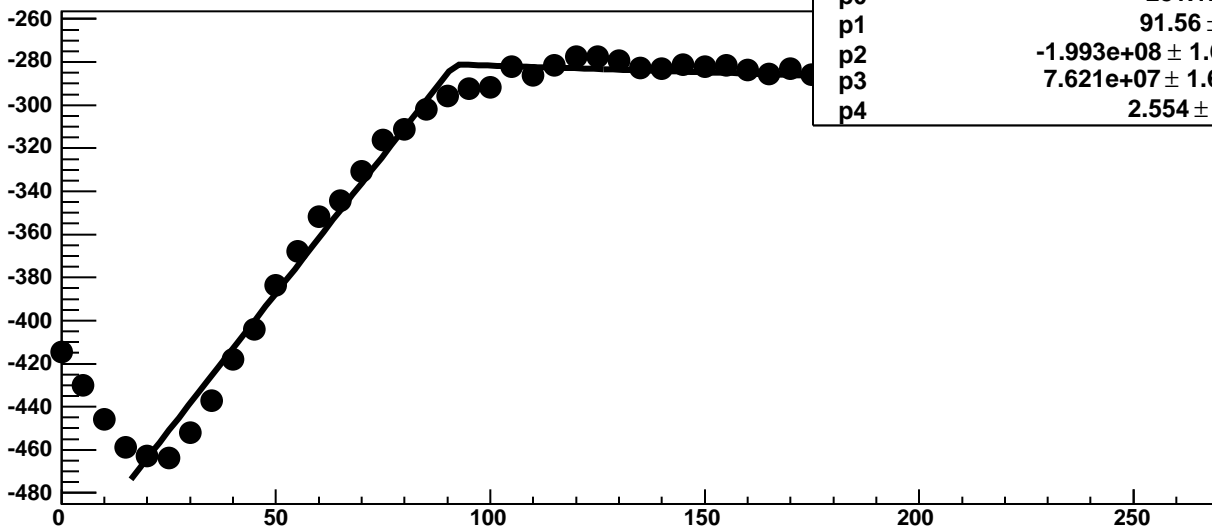
Chip 3, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

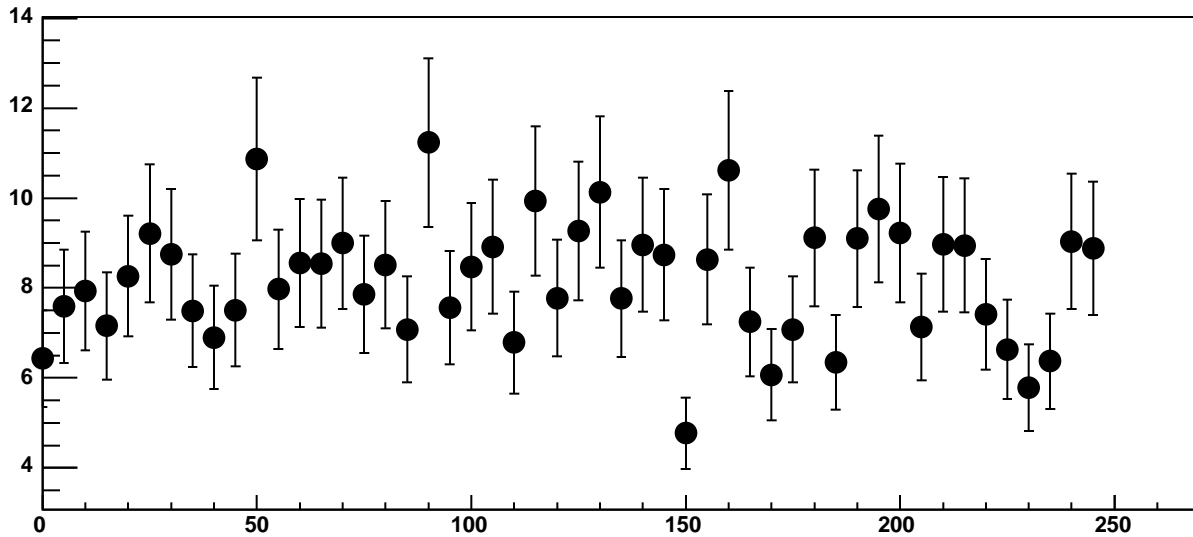


Chip 3, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

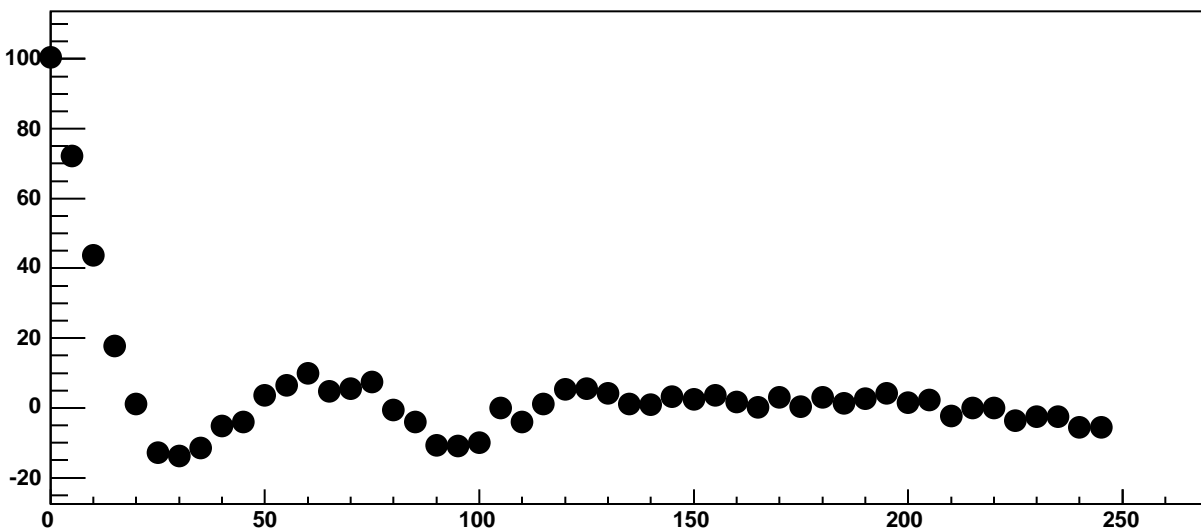


χ^2 / ndf	495.9 / 41
p0	-281.1 ± 0.6543
p1	91.56 ± 0.4454
p2	-1.993e+08 ± 1.682e+06
p3	7.621e+07 ± 1.625e+05
p4	2.554 ± 0.02043

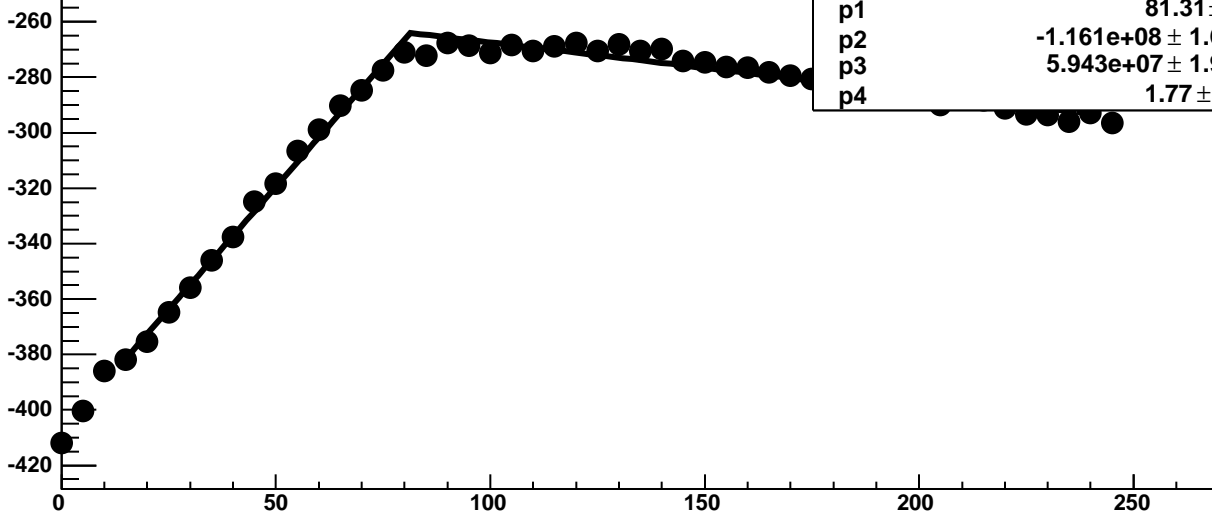
Chip 3, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

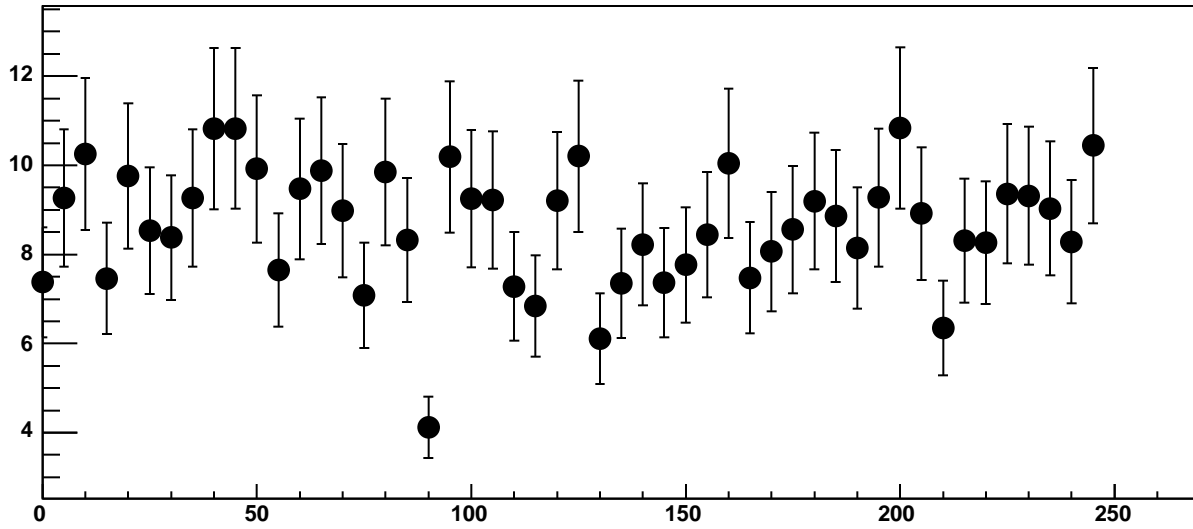


Chip 3, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold

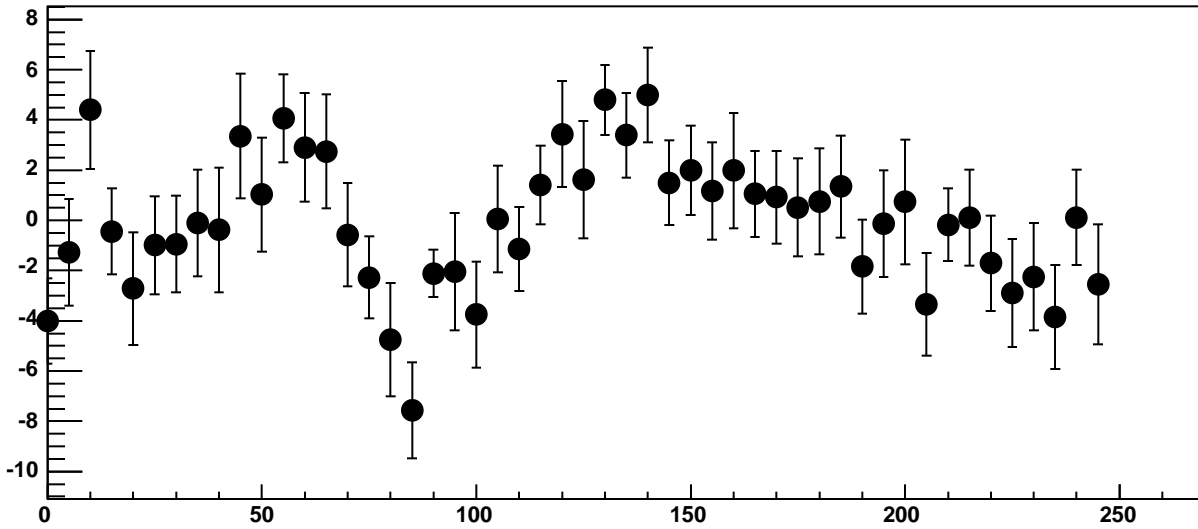


χ^2 / ndf	86.24 / 41
p0	-264 ± 0.5444
p1	81.31 ± 0.6093
p2	$-1.161\text{e}+08 \pm 1.611\text{e}+06$
p3	$5.943\text{e}+07 \pm 1.911\text{e}+05$
p4	1.77 ± 0.02581

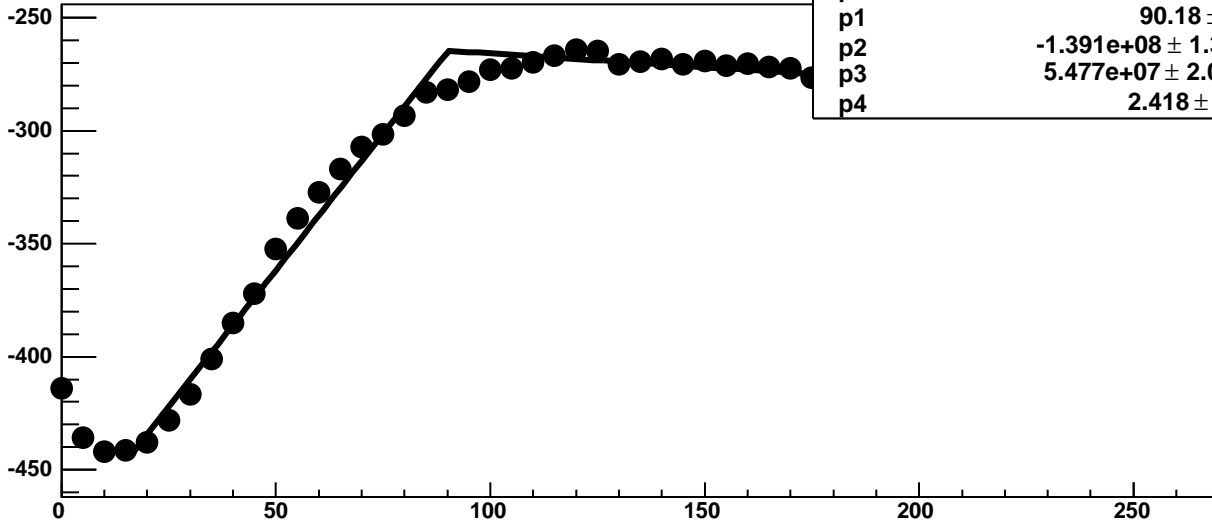
Chip 3, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold

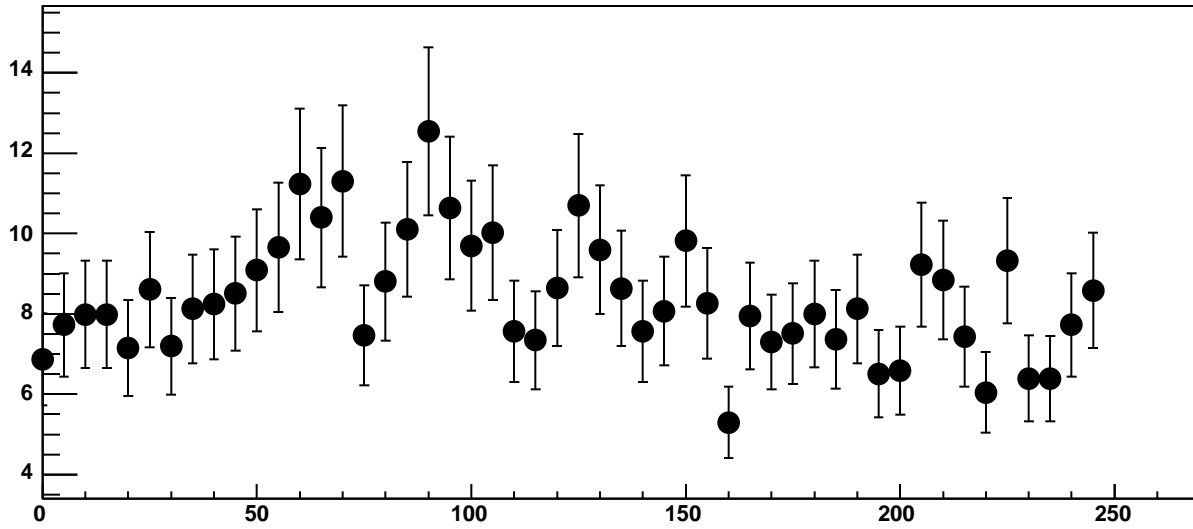


Chip 3, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

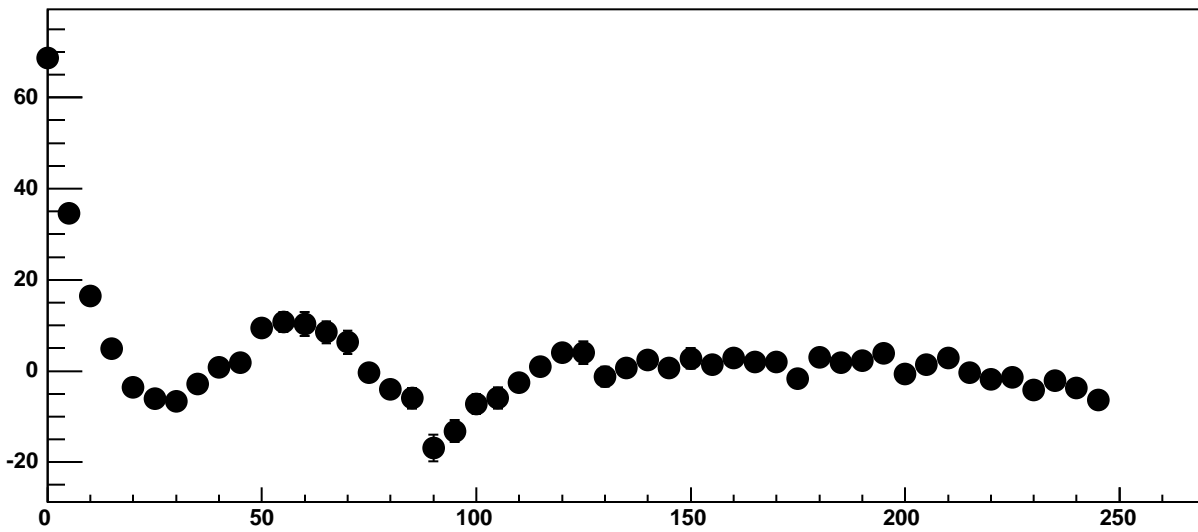


χ^2 / ndf	264.9 / 41
p0	-264.6 ± 0.6961
p1	90.18 ± 0.5055
p2	$-1.391\text{e}+08 \pm 1.357\text{e}+06$
p3	$5.477\text{e}+07 \pm 2.055\text{e}+05$
p4	2.418 ± 0.02198

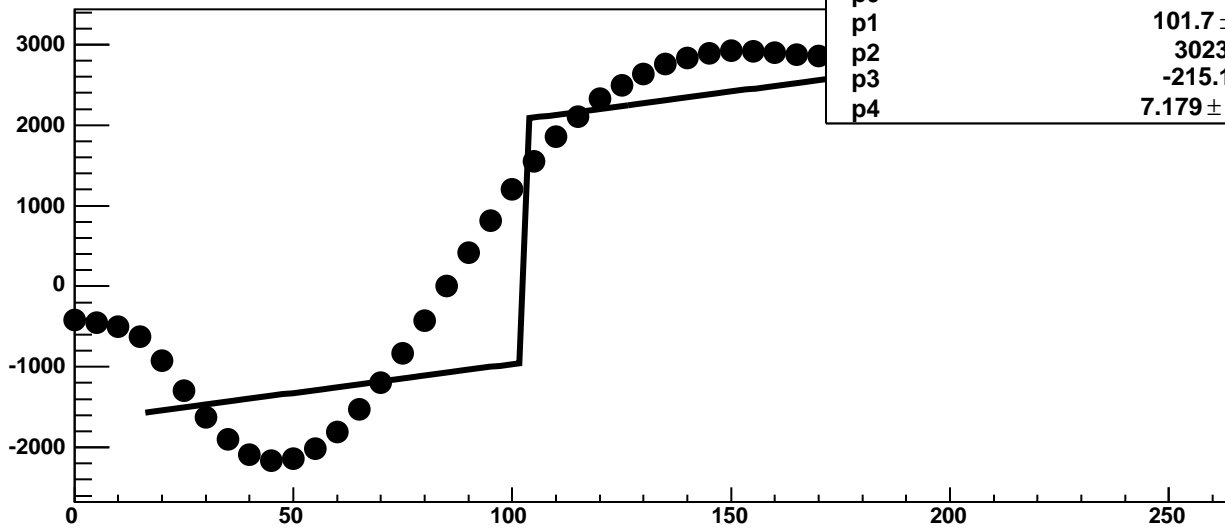
Chip 3, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

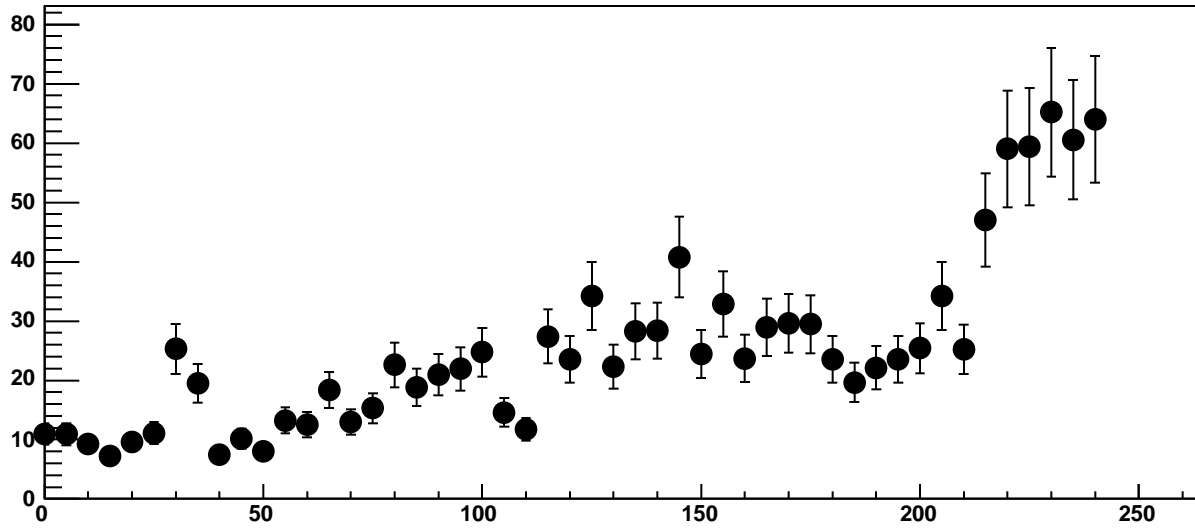


Chip 3, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold

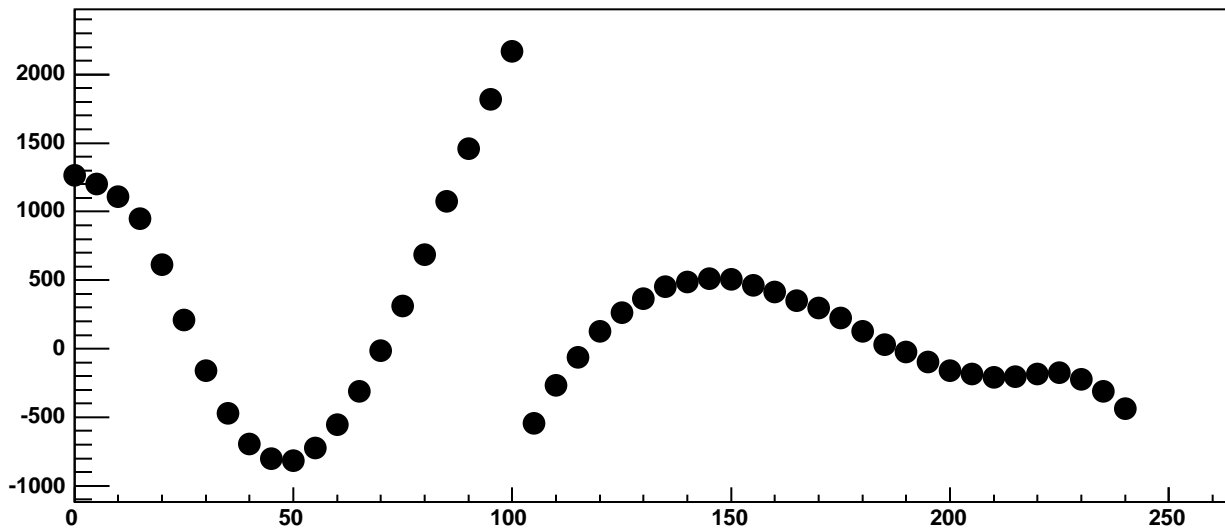


χ^2 / ndf	1.546e+06 / 41
p0	-952.9 ± 1.128
p1	101.7 ± 0.8296
p2	3023 ± 4.986
p3	-215.1 ± 4.019
p4	7.179 ± 0.06965

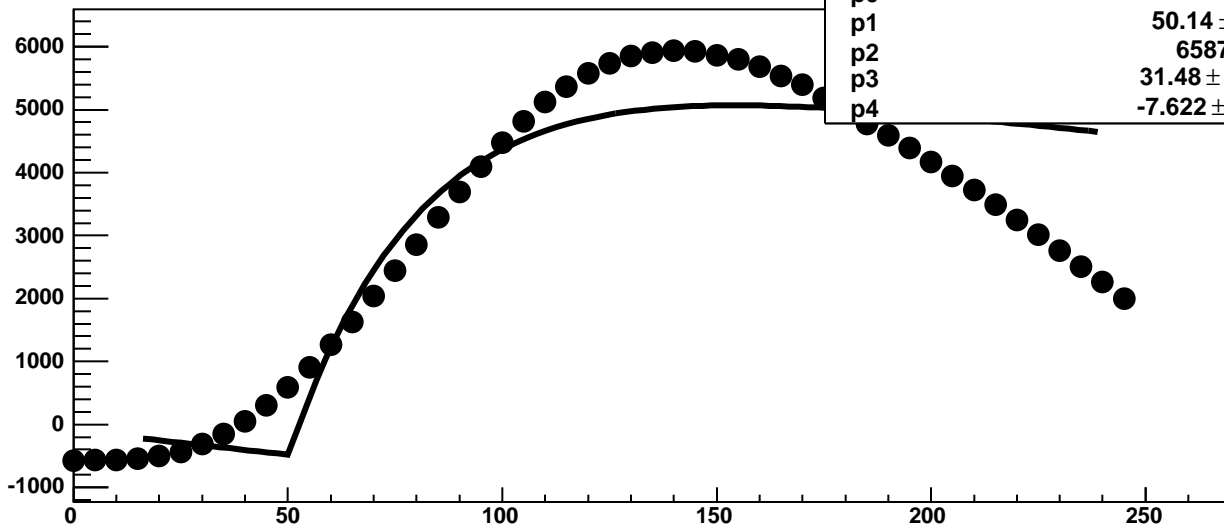
Chip 3, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

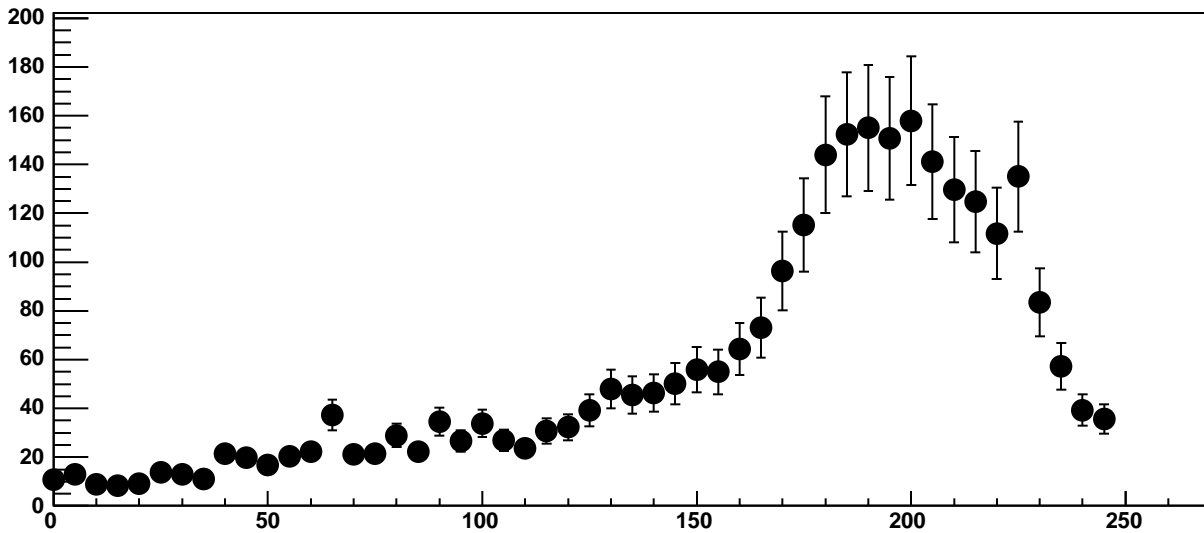


Chip 3, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold

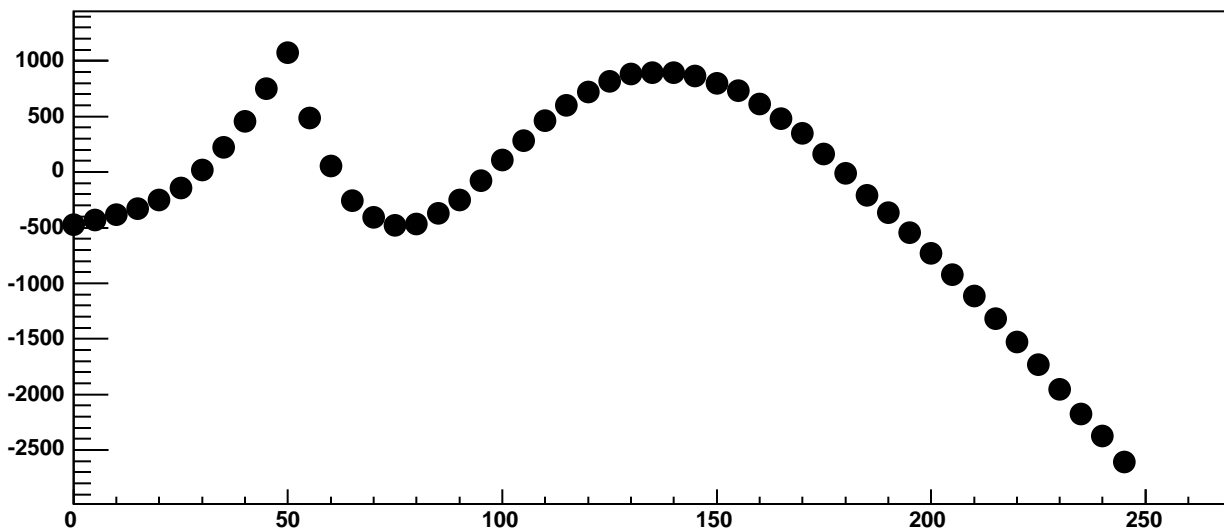


χ^2 / ndf	4.012e+05 / 41
p0	-482.8 ± 1.622
p1	50.14 ± 0.0245
p2	6587 ± 9.271
p3	31.48 ± 0.06054
p4	-7.622 ± 0.05711

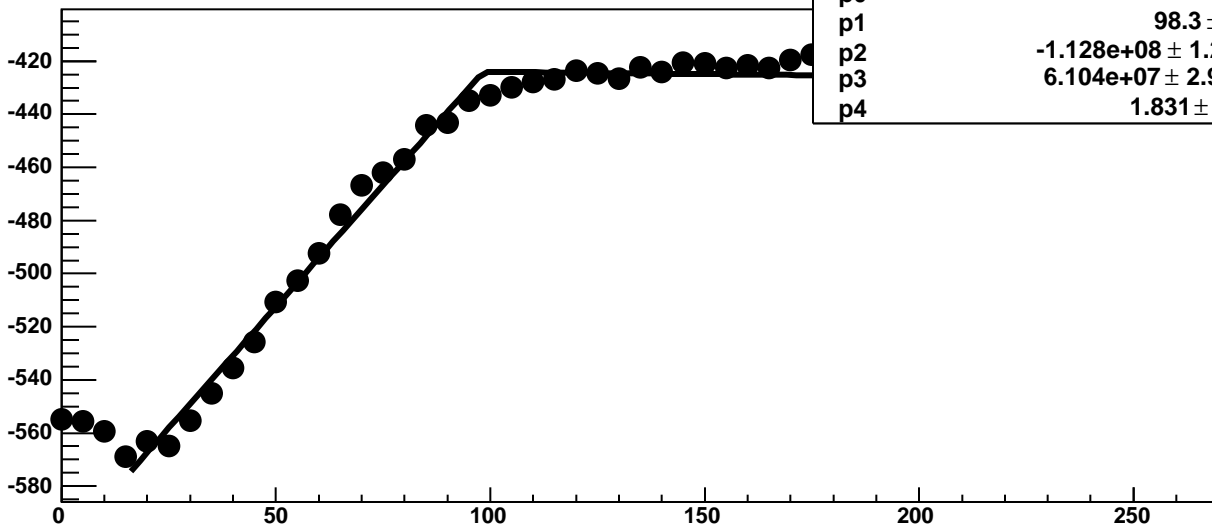
Chip 3, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold

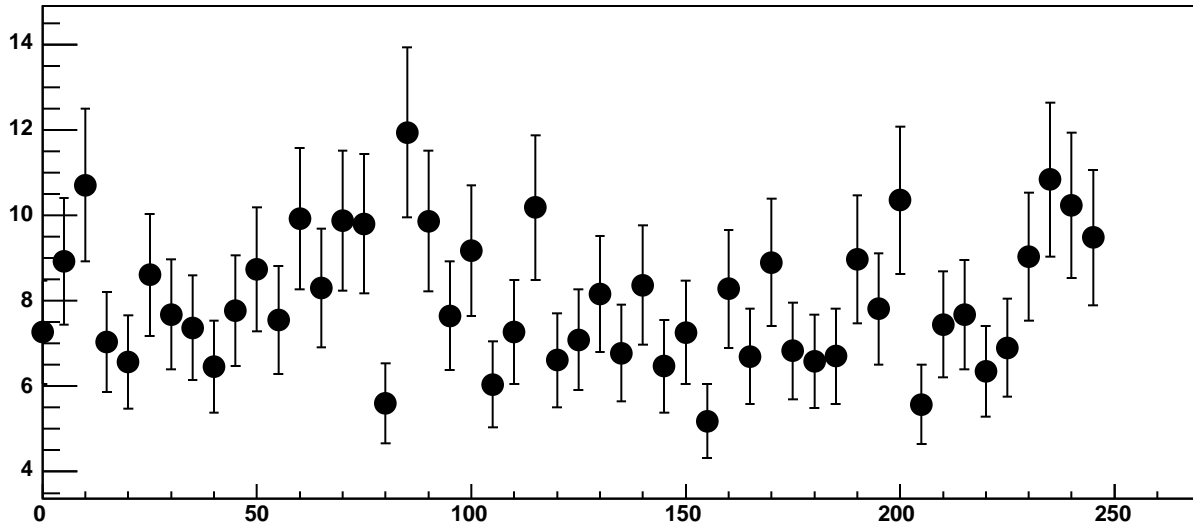


Chip 3, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

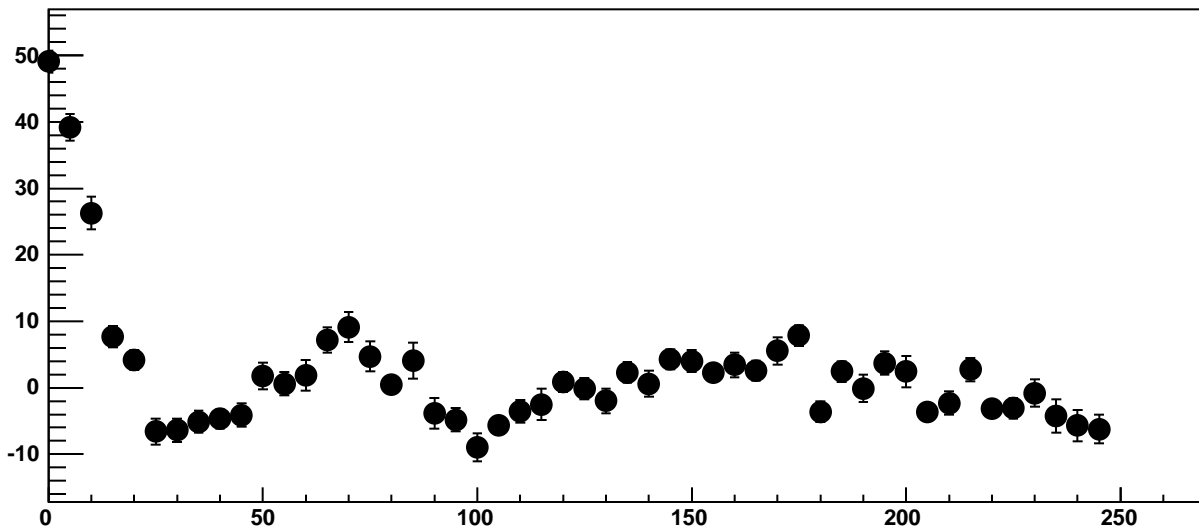


χ^2 / ndf	271.8 / 41
p0	-424 ± 0.6228
p1	98.3 ± 0.6046
p2	-1.128e+08 ± 1.282e+06
p3	6.104e+07 ± 2.935e+05
p4	1.831 ± 0.01759

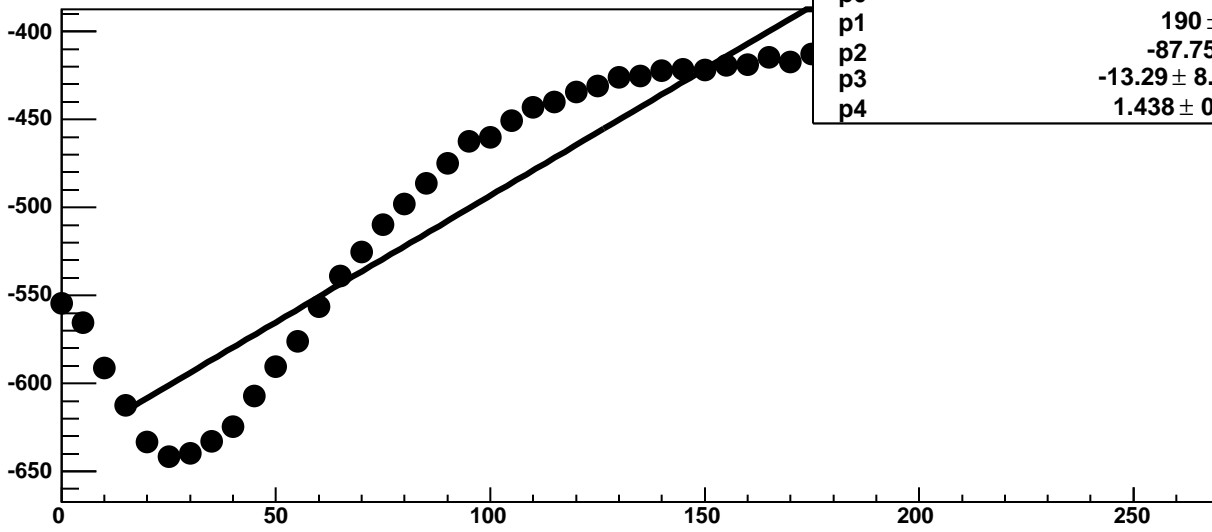
Chip 3, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



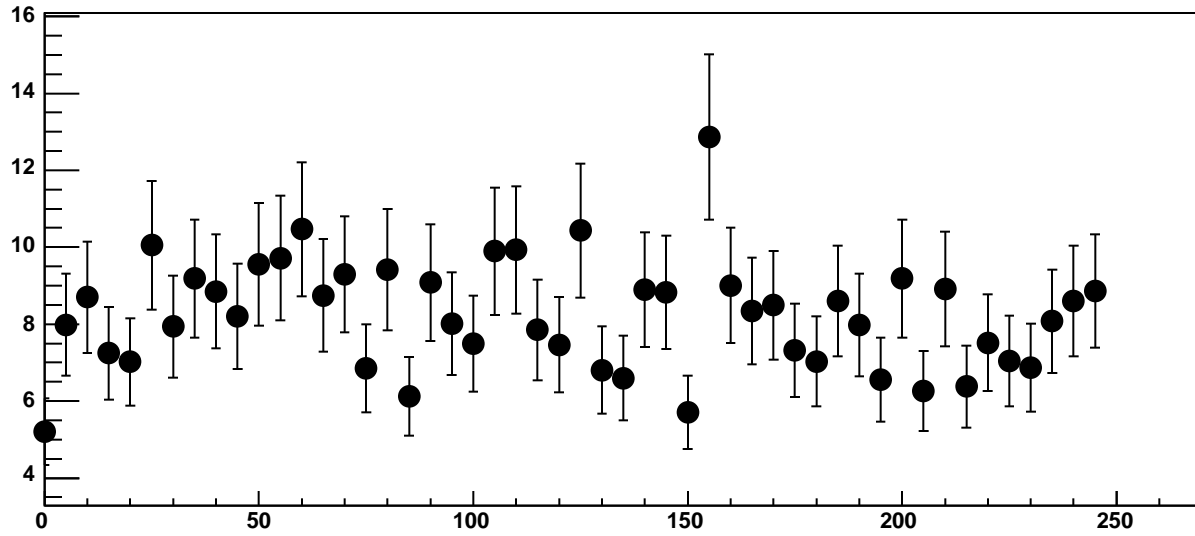
Chip 3, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold



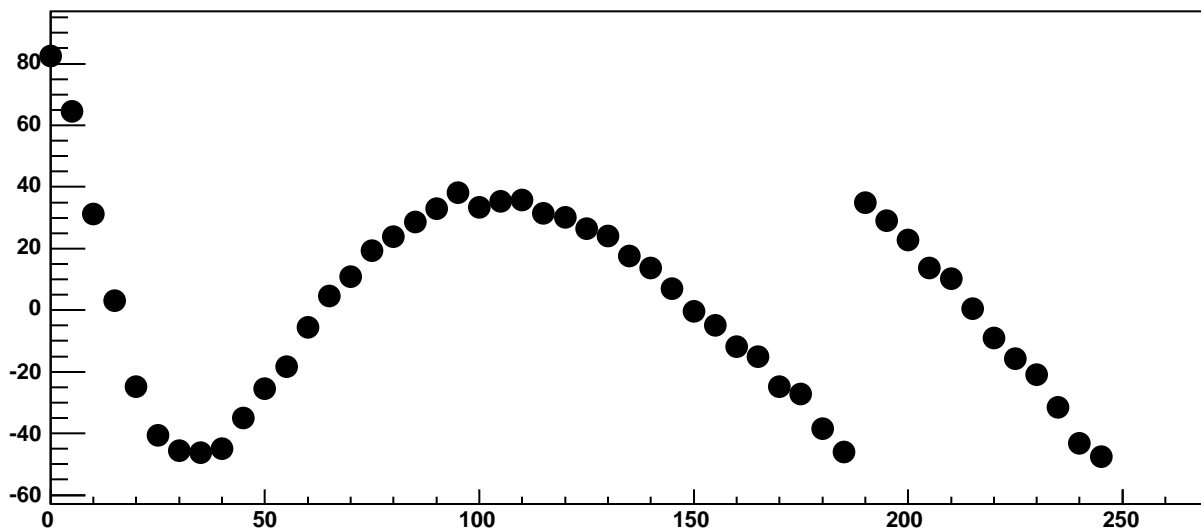
Chip 3, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold



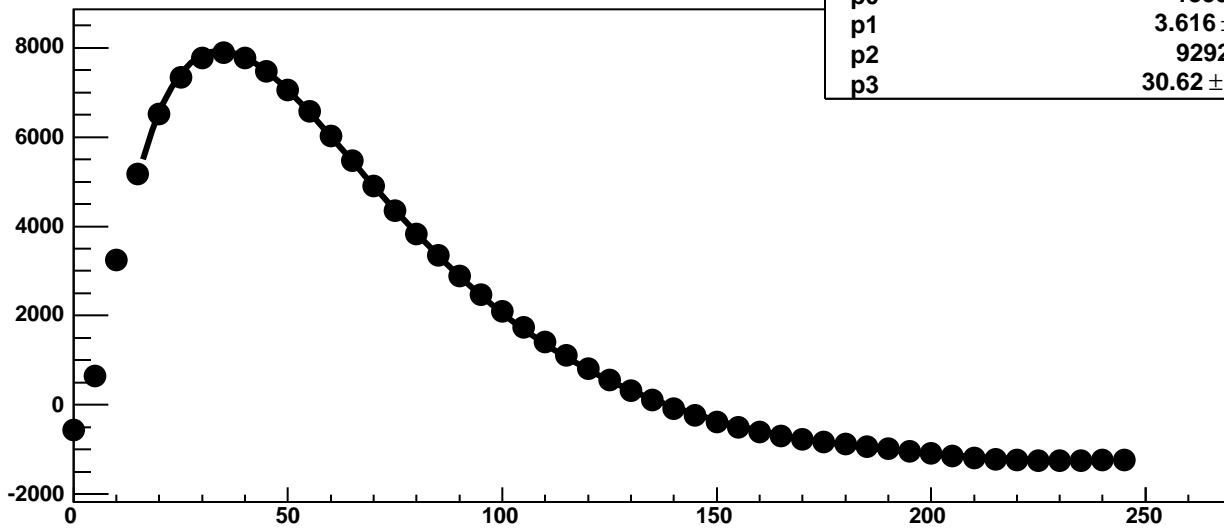
Chip 3, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

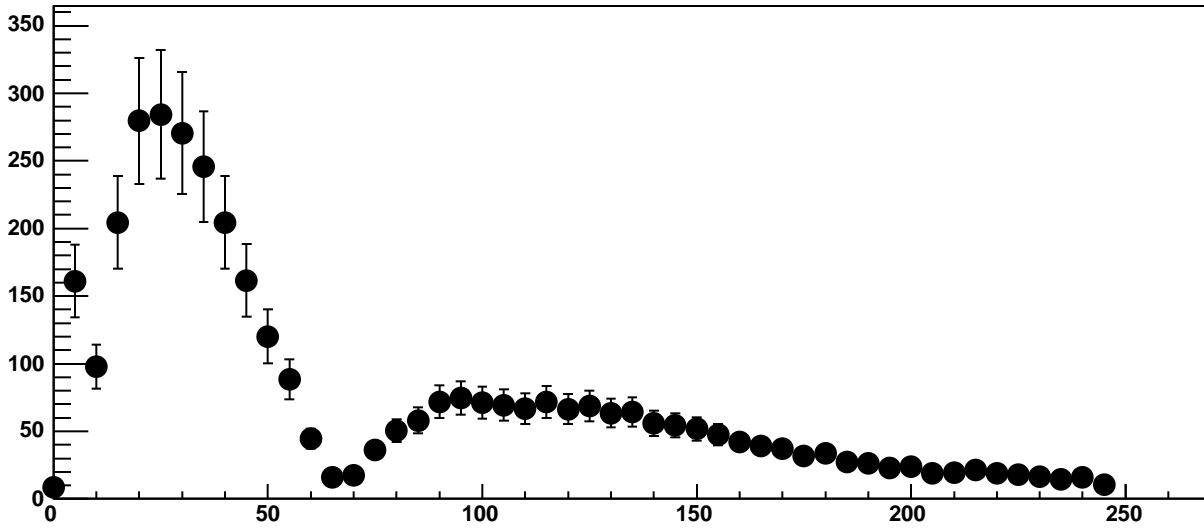


Chip 3, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold

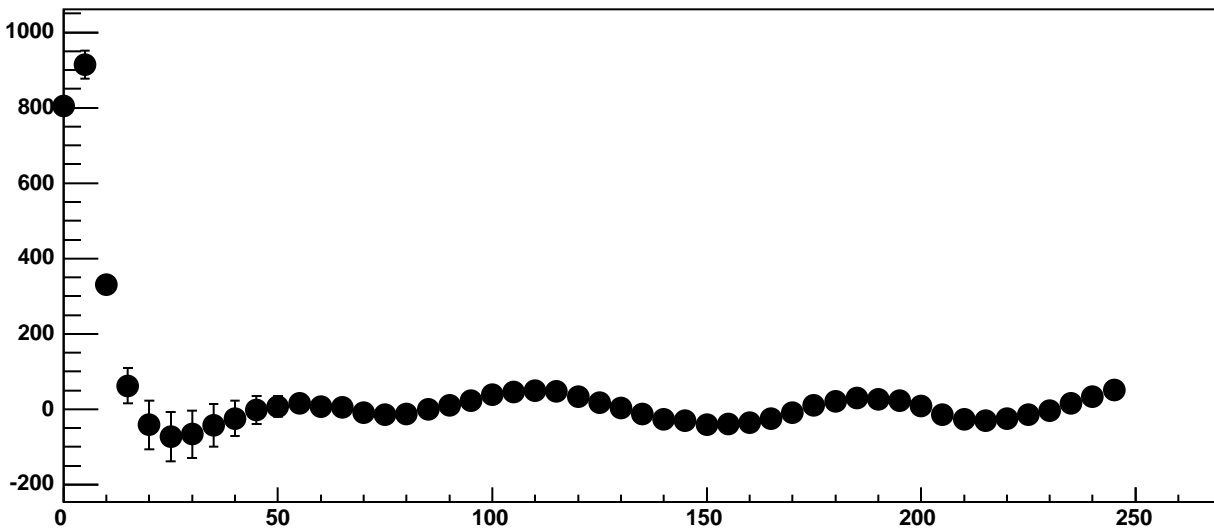


χ^2 / ndf	420.5 / 42
p0	-1359 ± 1.854
p1	3.616 ± 0.1002
p2	9292 ± 11.33
p3	30.62 ± 0.04241

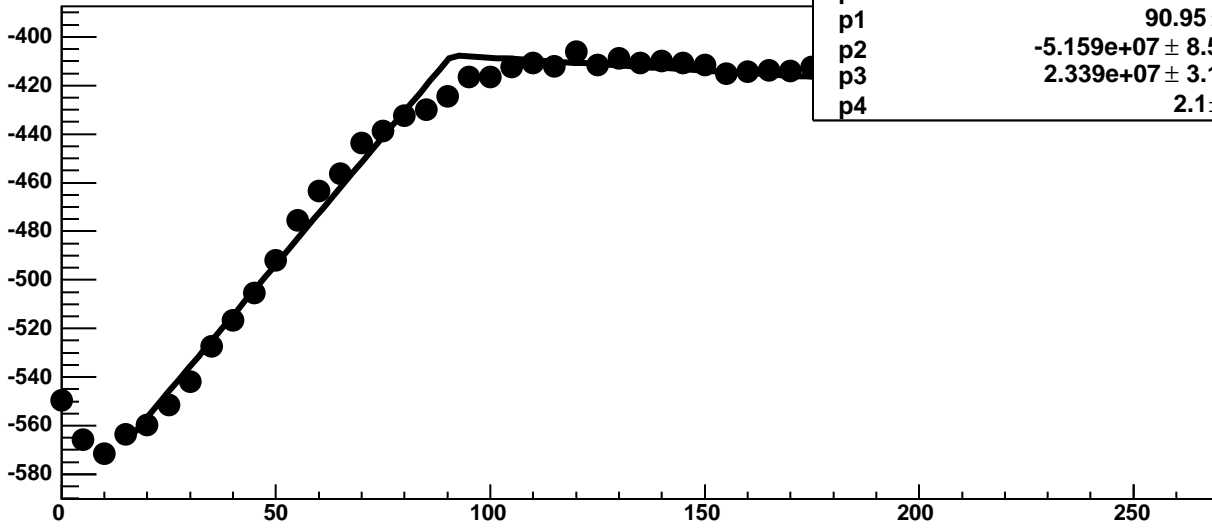
Chip 3, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold

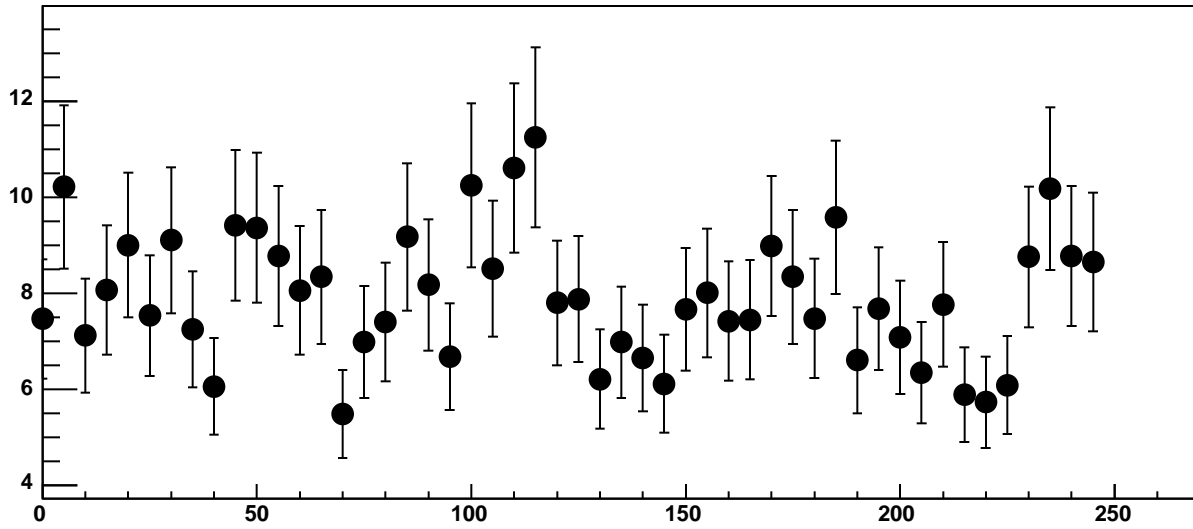


Chip 3, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

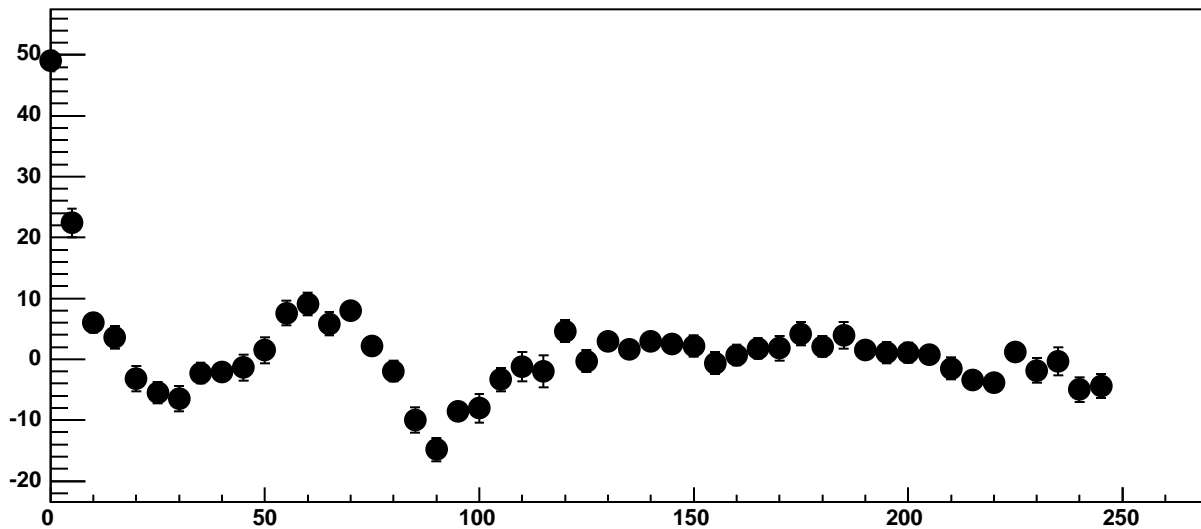


χ^2 / ndf	310.4 / 41
p0	-407.6 ± 0.6415
p1	90.95 ± 0.4941
p2	$-5.159\text{e}+07 \pm 8.594\text{e}+05$
p3	$2.339\text{e}+07 \pm 3.187\text{e}+05$
p4	2.1 ± 0.0197

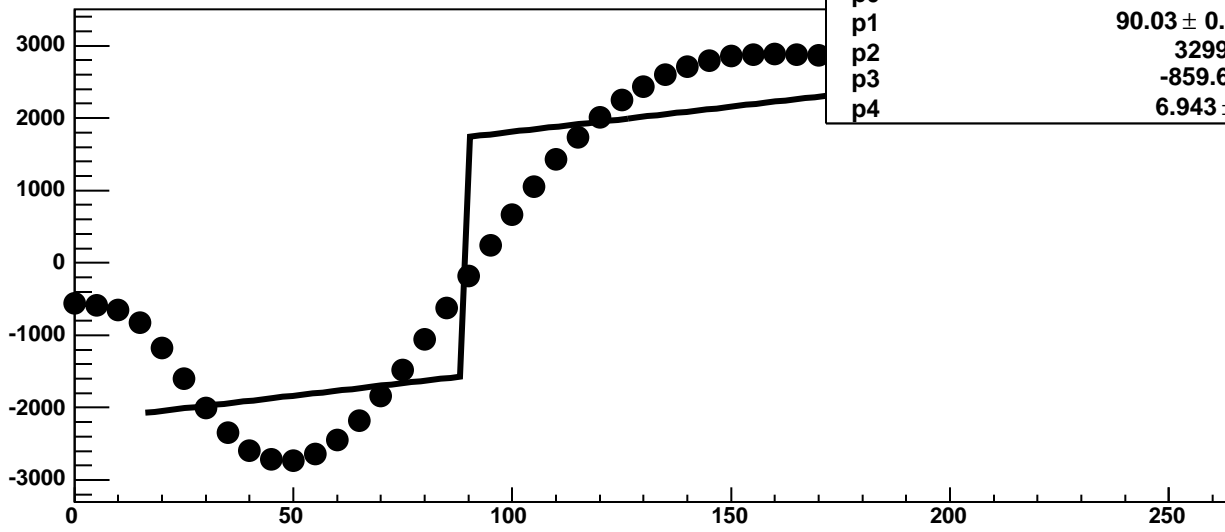
Chip 3, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

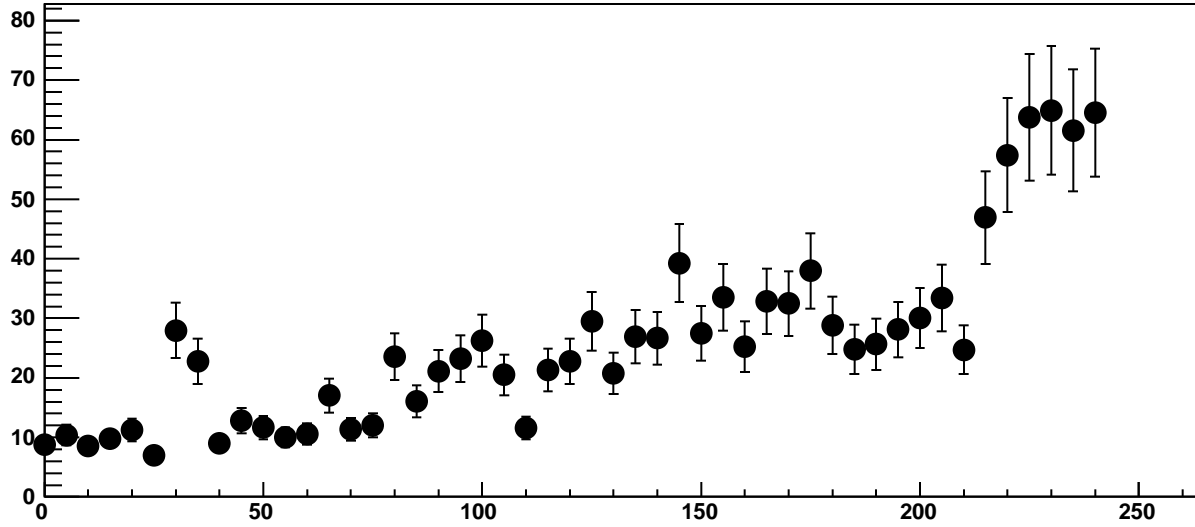


Chip 3, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold

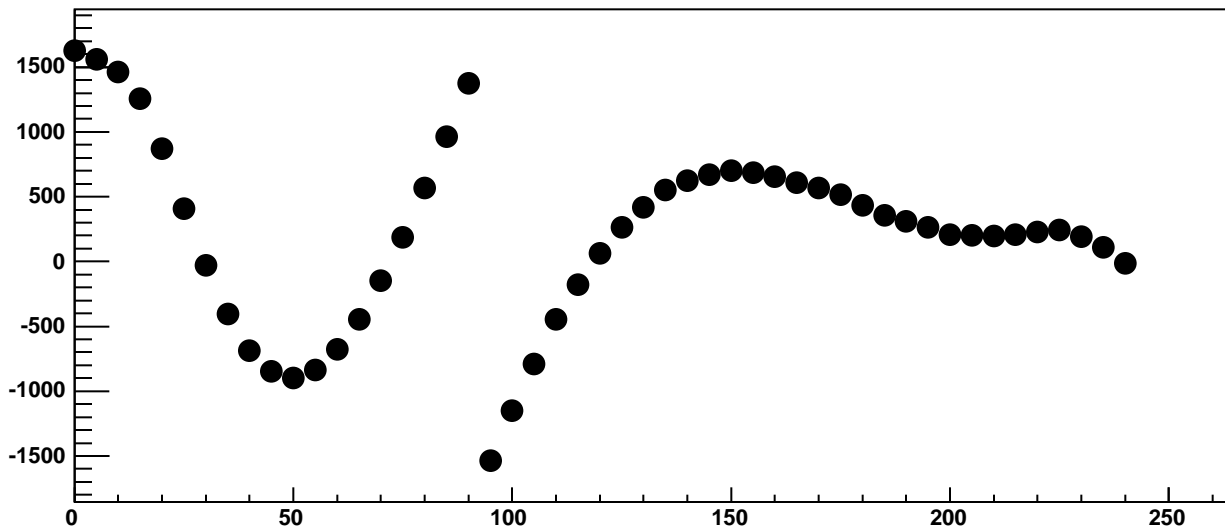


χ^2 / ndf	1.474e+06 / 41
p0	-1558 ± 6.778
p1	90.03 ± 0.0002121
p2	3299 ± 9.235
p3	-859.6 ± 19.74
p4	6.943 ± 0.1229

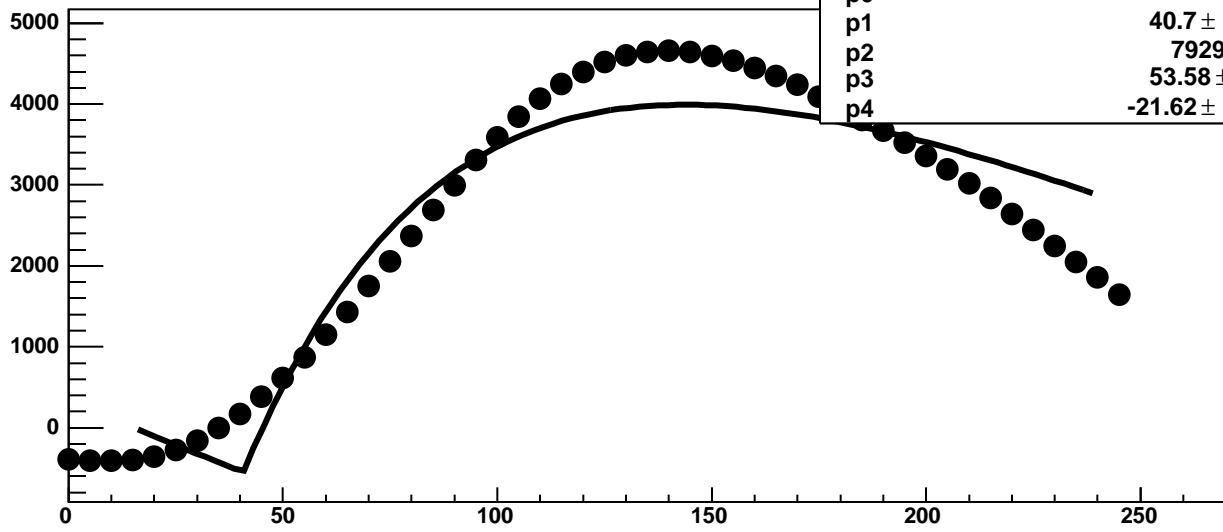
Chip 3, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

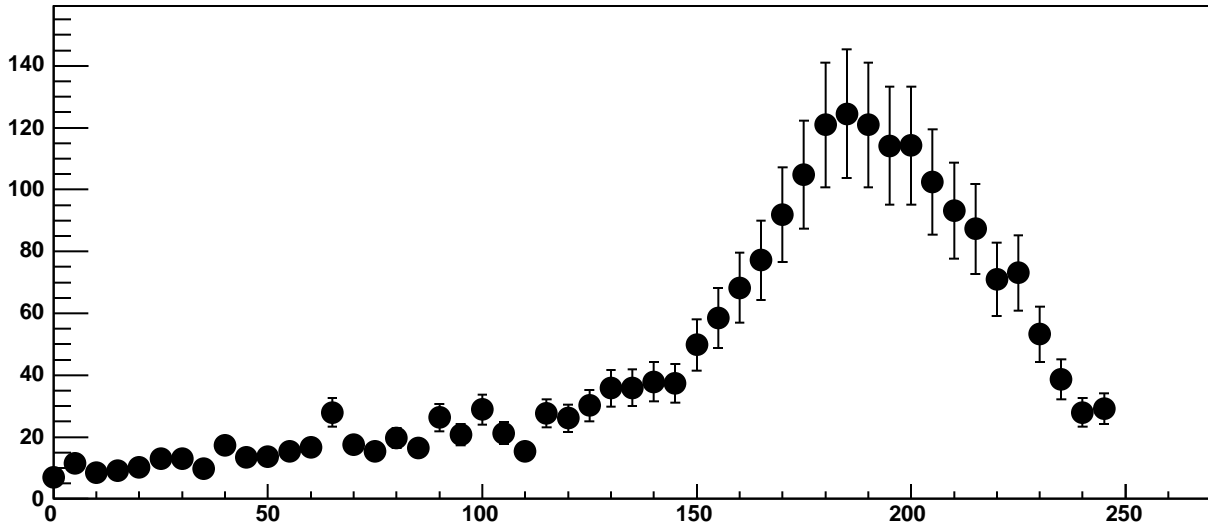


Chip 3, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

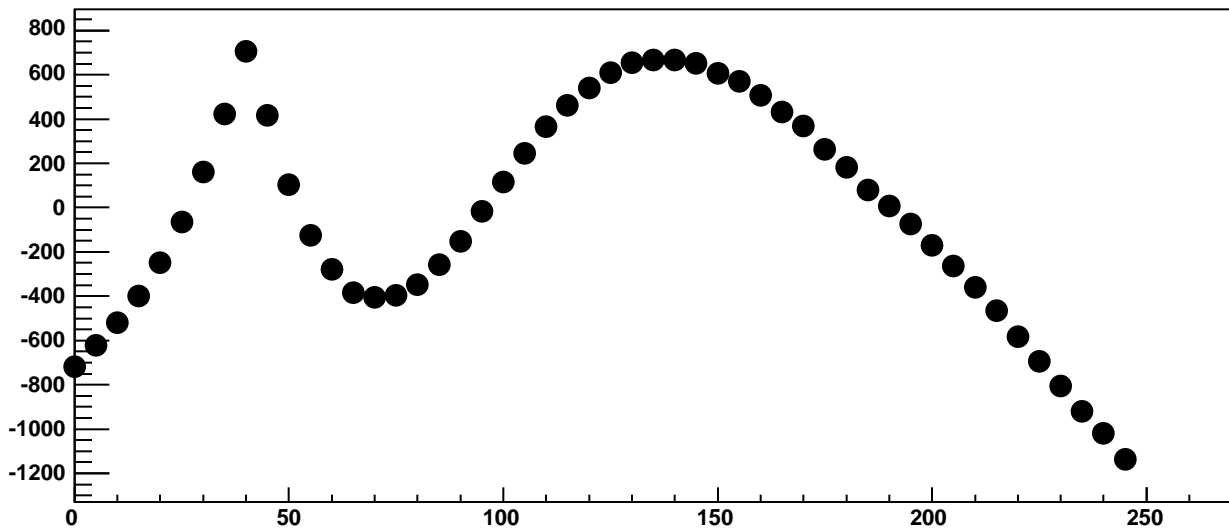


χ^2 / ndf	2.912e+05 / 41
p0	-551.9 ± 1.318
p1	40.7 ± 0.02082
p2	7929 ± 14.51
p3	53.58 ± 0.1008
p4	-21.62 ± 0.06917

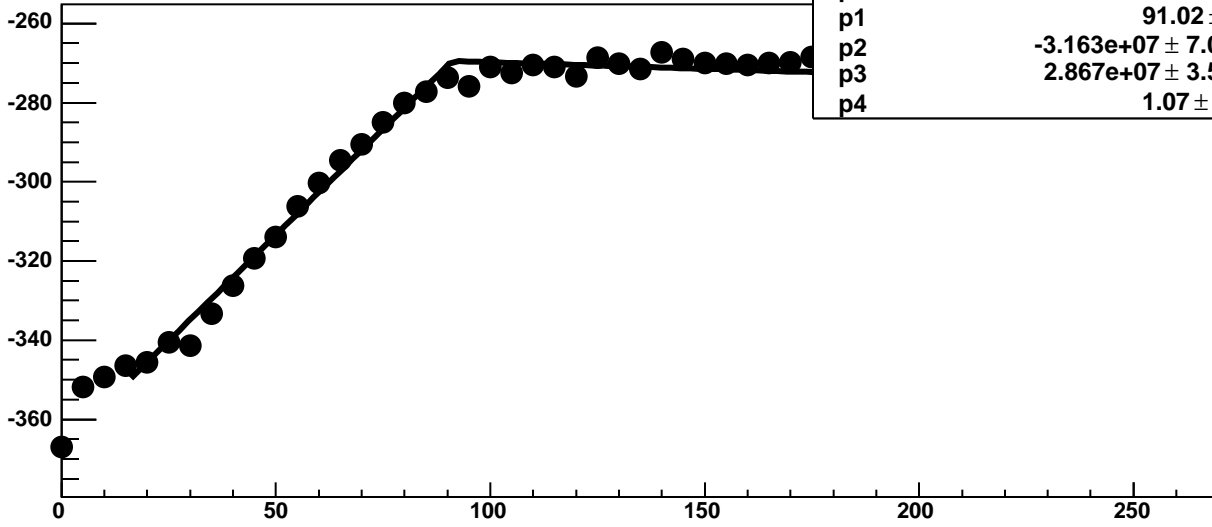
Chip 3, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

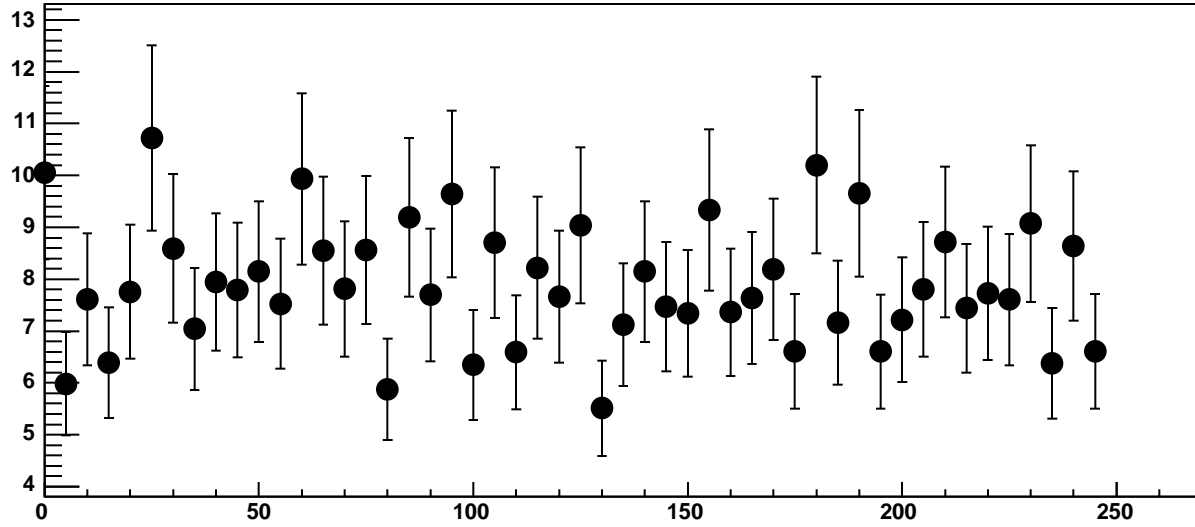


Chip 3, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold

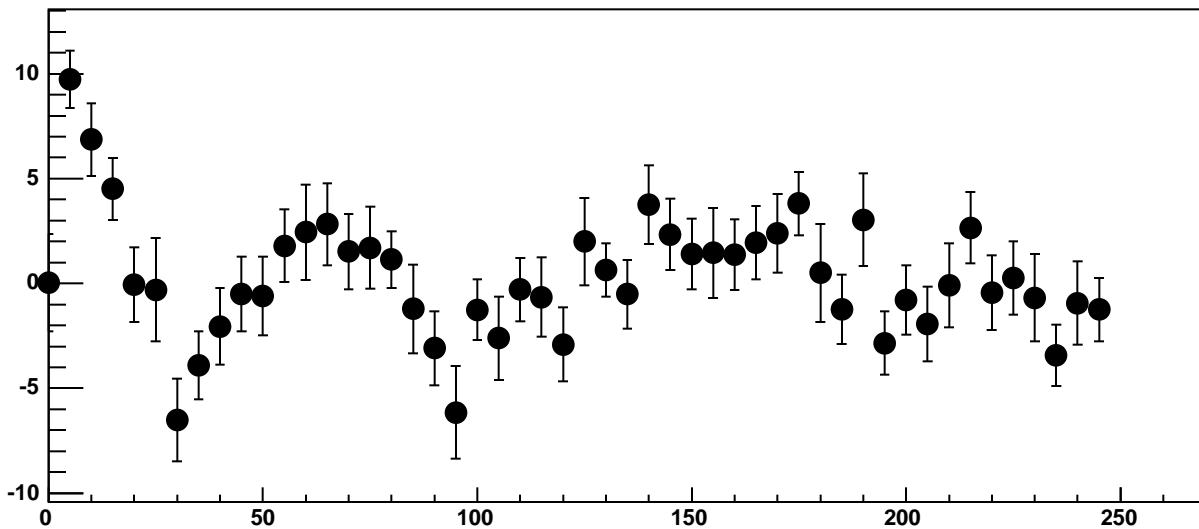


χ^2 / ndf	84.45 / 41
p0	-269.5 ± 0.6228
p1	91.02 ± 0.9692
p2	$-3.163\text{e}+07 \pm 7.095\text{e}+05$
p3	$2.867\text{e}+07 \pm 3.553\text{e}+05$
p4	1.07 ± 0.01886

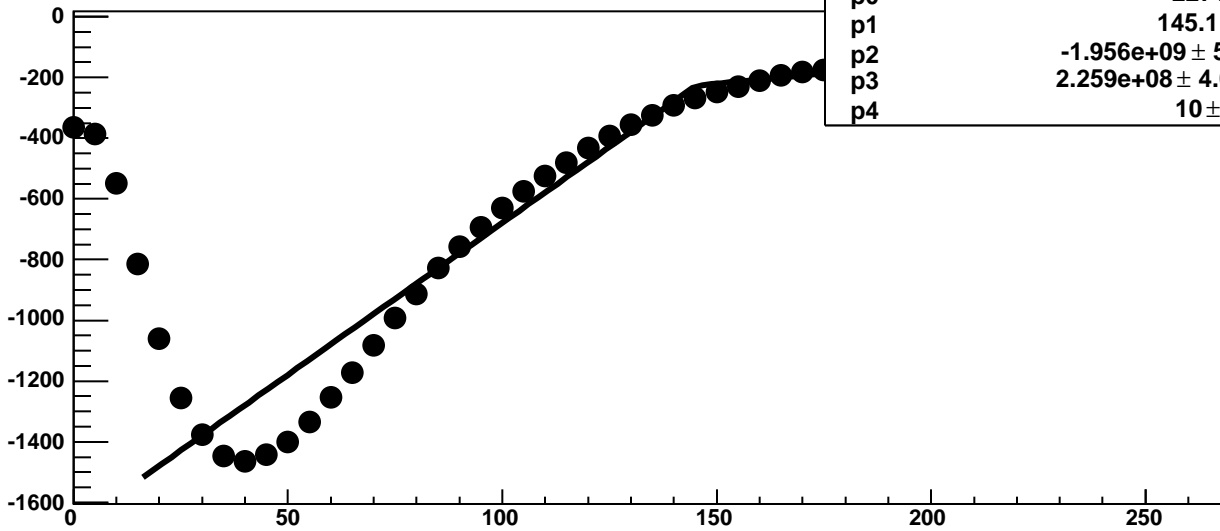
Chip 3, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold

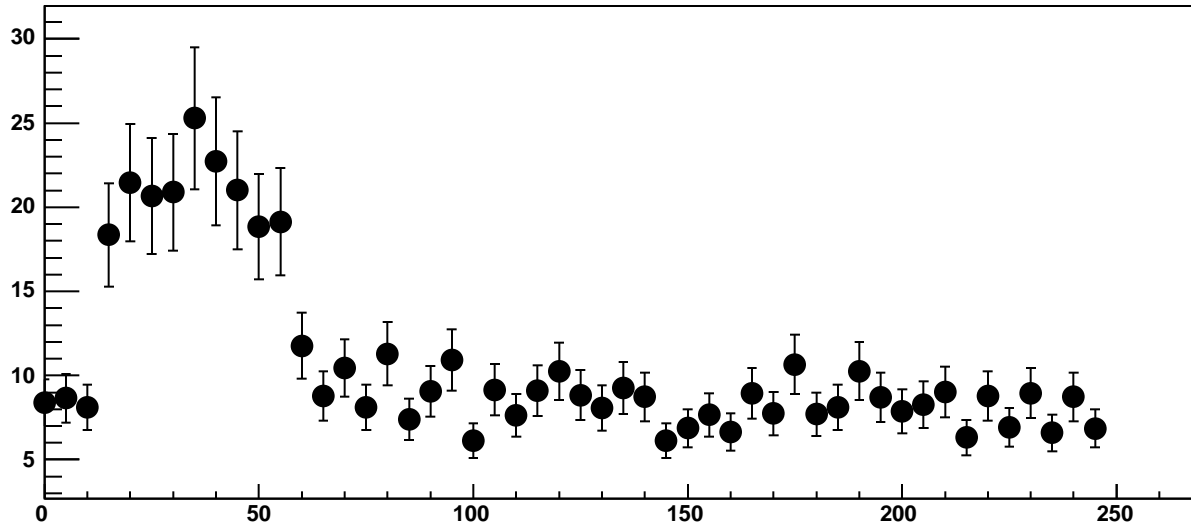


Chip 3, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

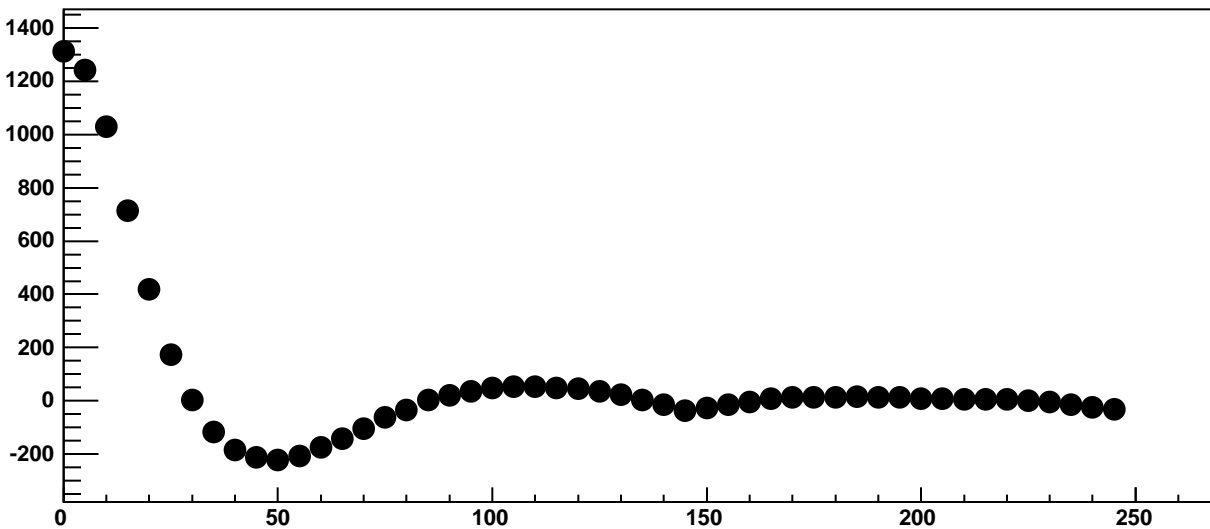


χ^2 / ndf	6.521e+04 / 41
p0	-227 ± 0.9704
p1	145.1 ± 0.1307
p2	-1.956e+09 ± 5.77e+06
p3	2.259e+08 ± 4.072e+05
p4	10 ± 0.01403

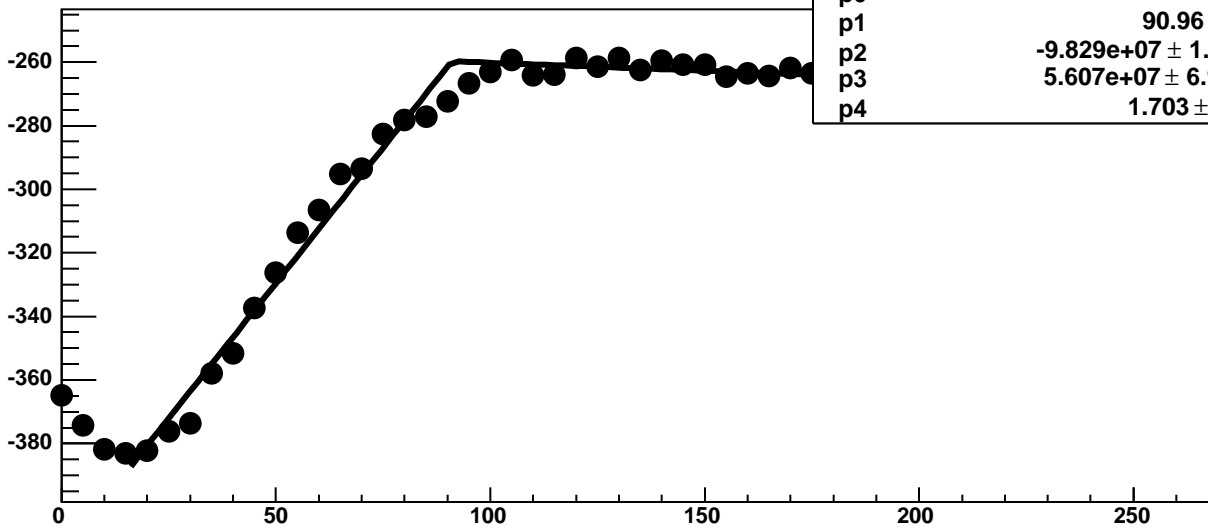
Chip 3, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

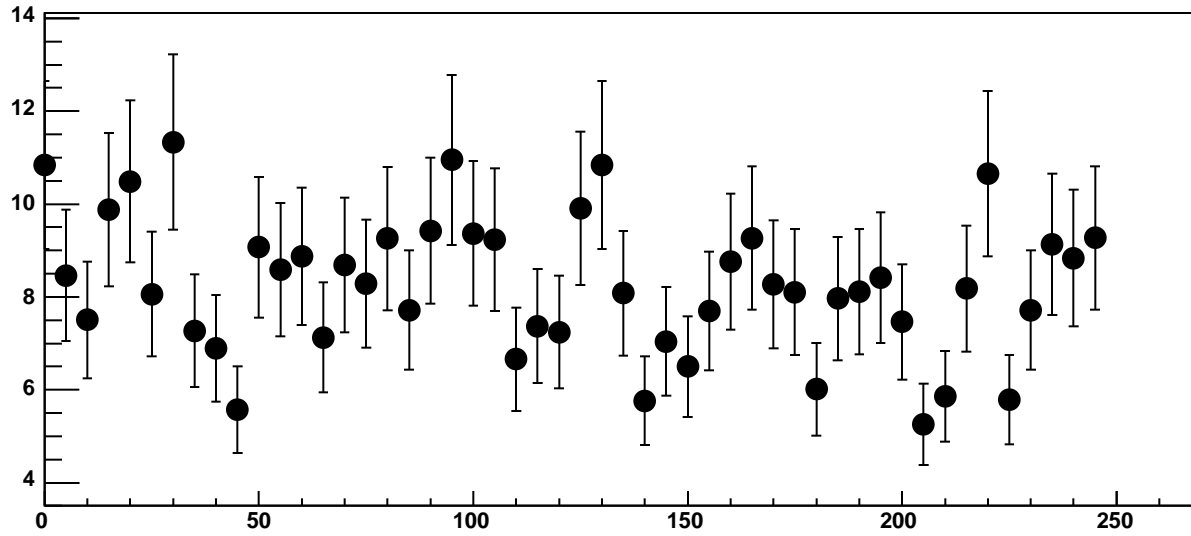


Chip 3, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

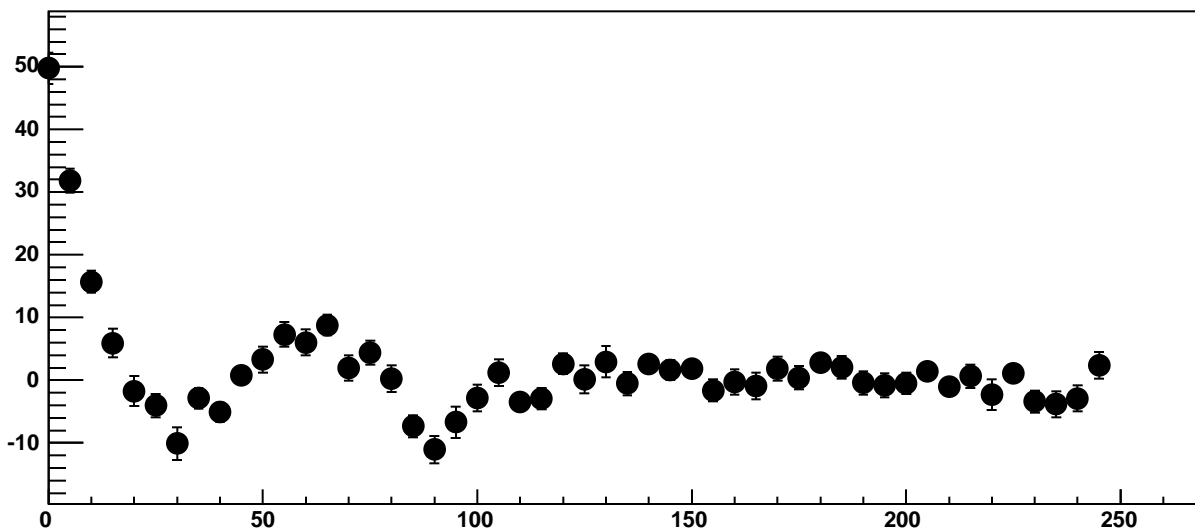


χ^2 / ndf	192.7 / 41
p0	-259.7 ± 0.6738
p1	90.96 ± 0.6779
p2	$-9.829\text{e}+07 \pm 1.814\text{e}+06$
p3	$5.607\text{e}+07 \pm 6.904\text{e}+05$
p4	1.703 ± 0.02208

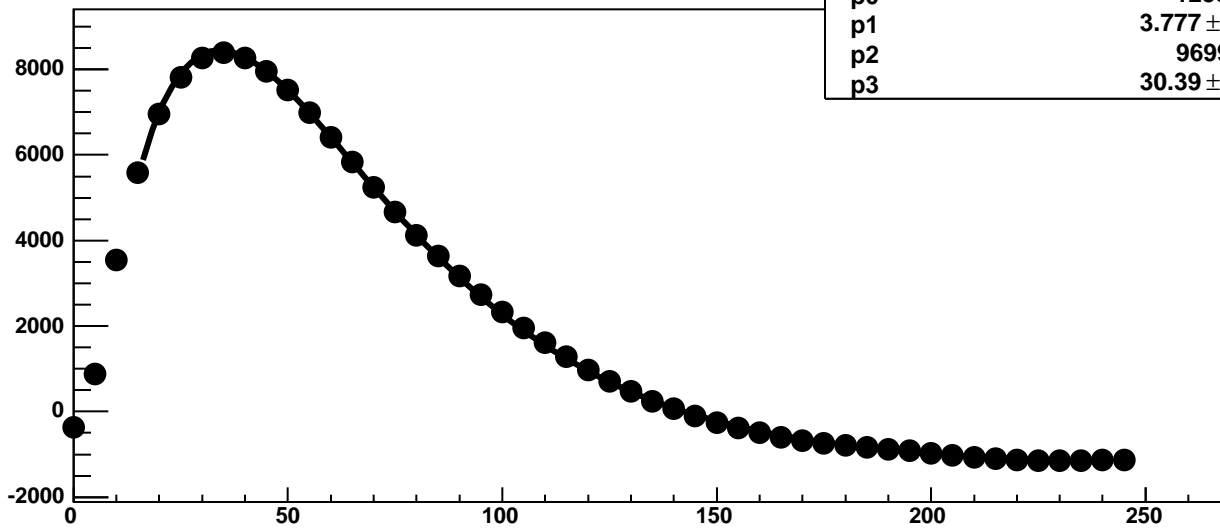
Chip 3, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

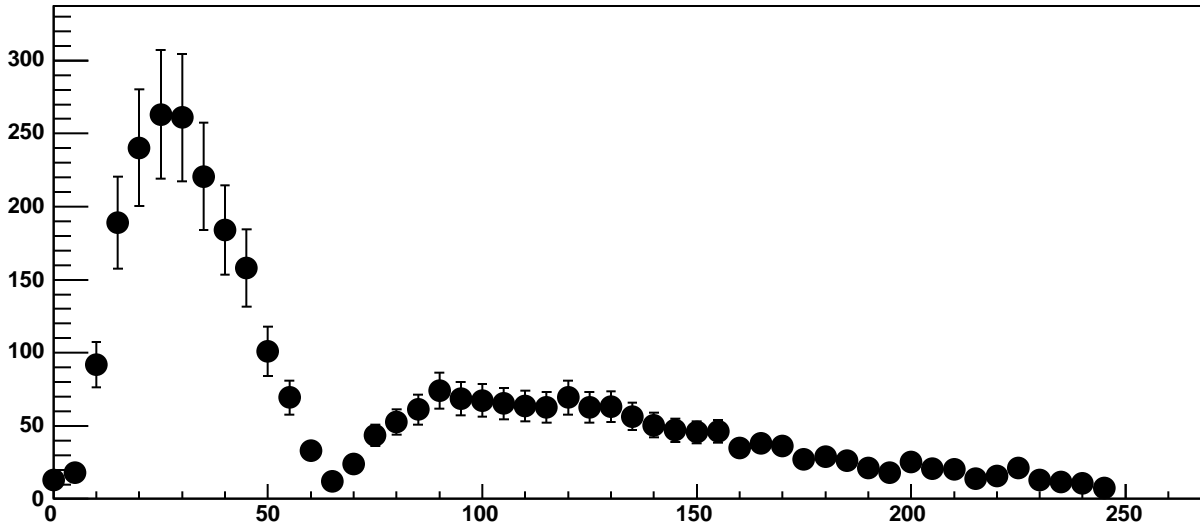


Chip 3, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold

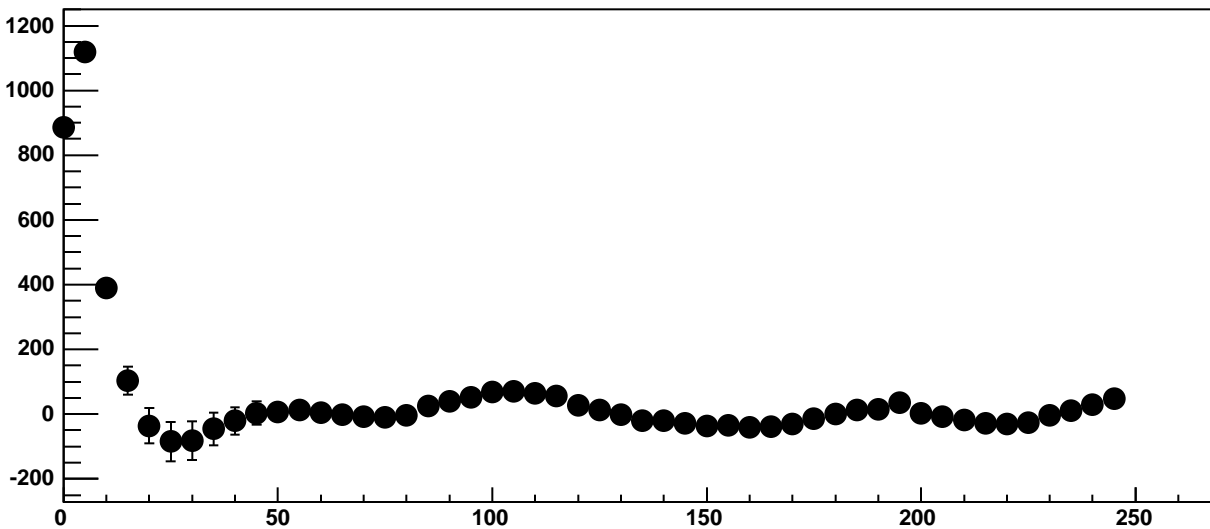


χ^2 / ndf	672.8 / 42
p0	-1253 ± 1.526
p1	3.777 ± 0.08577
p2	9699 ± 10.21
p3	30.39 ± 0.03585

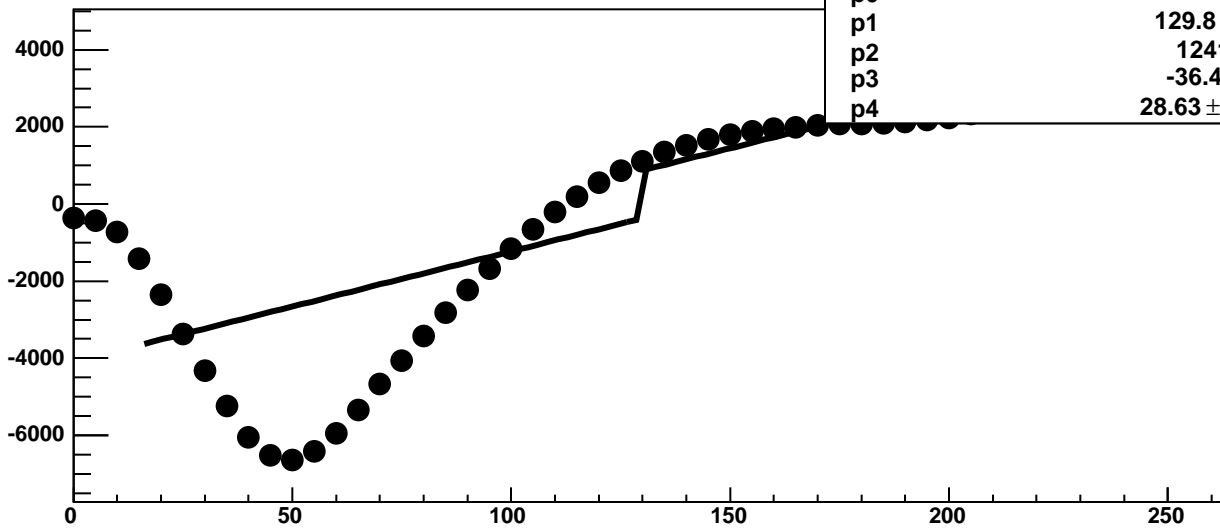
Chip 3, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold



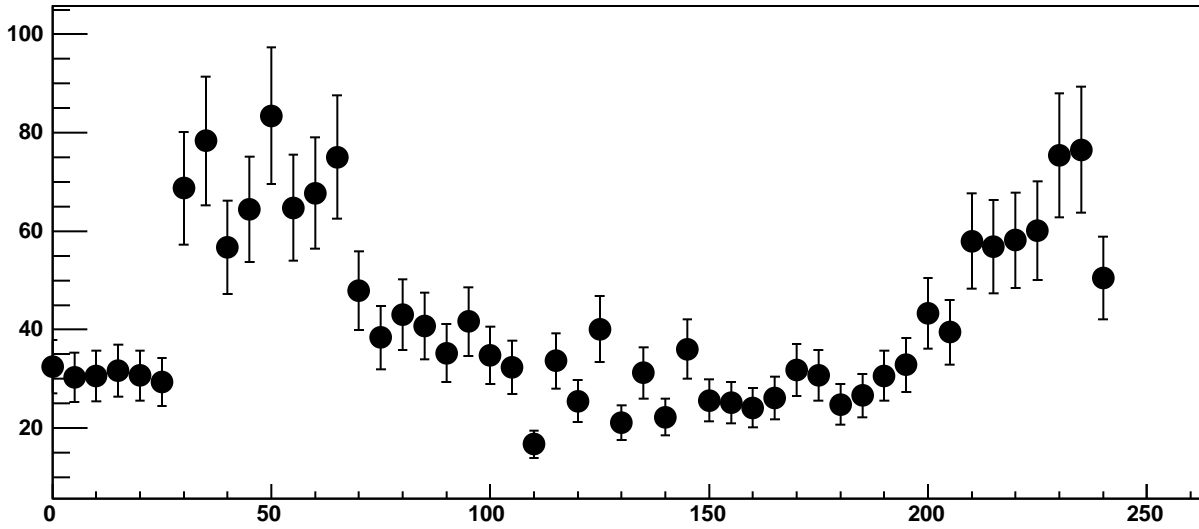
Chip 3, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold



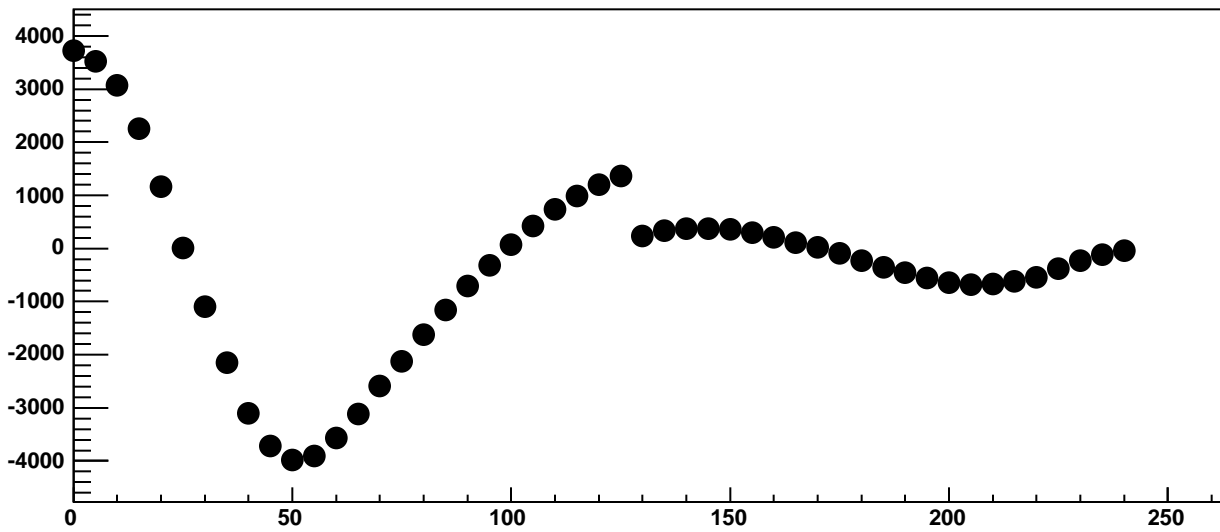
Chip 3, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold



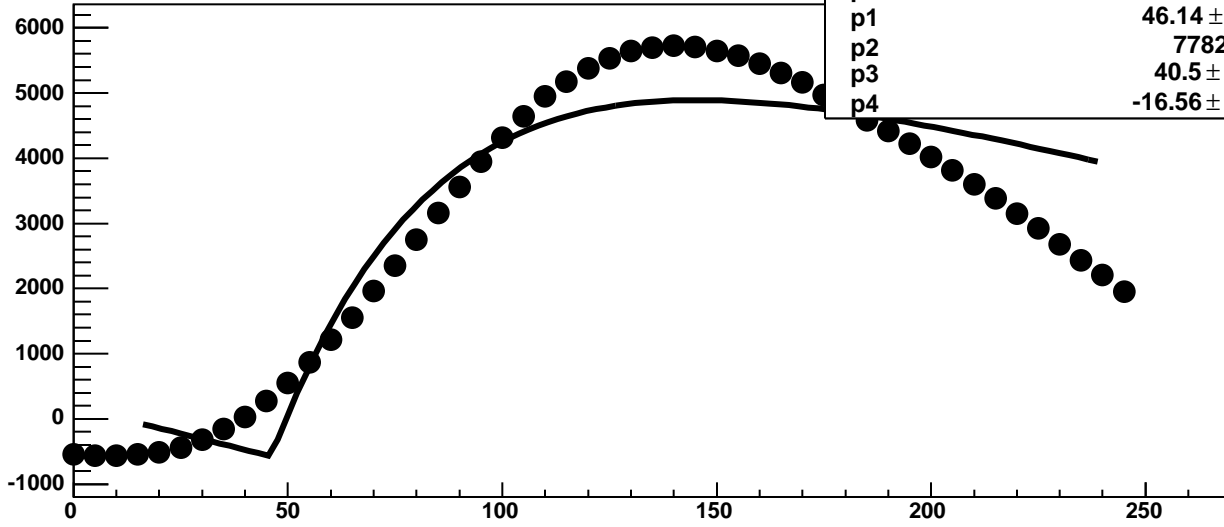
Chip 3, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold

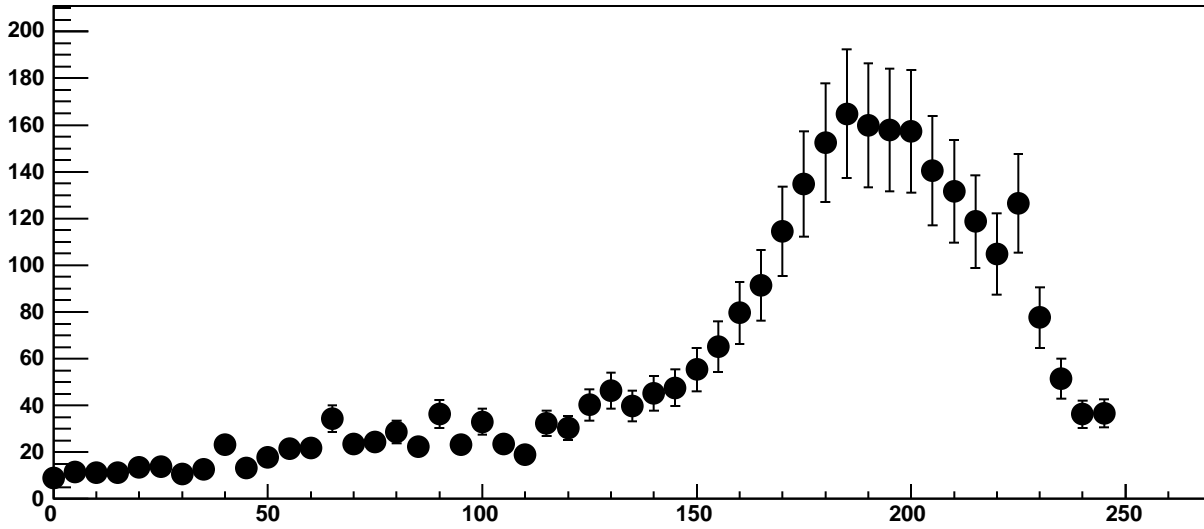


Chip 3, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold

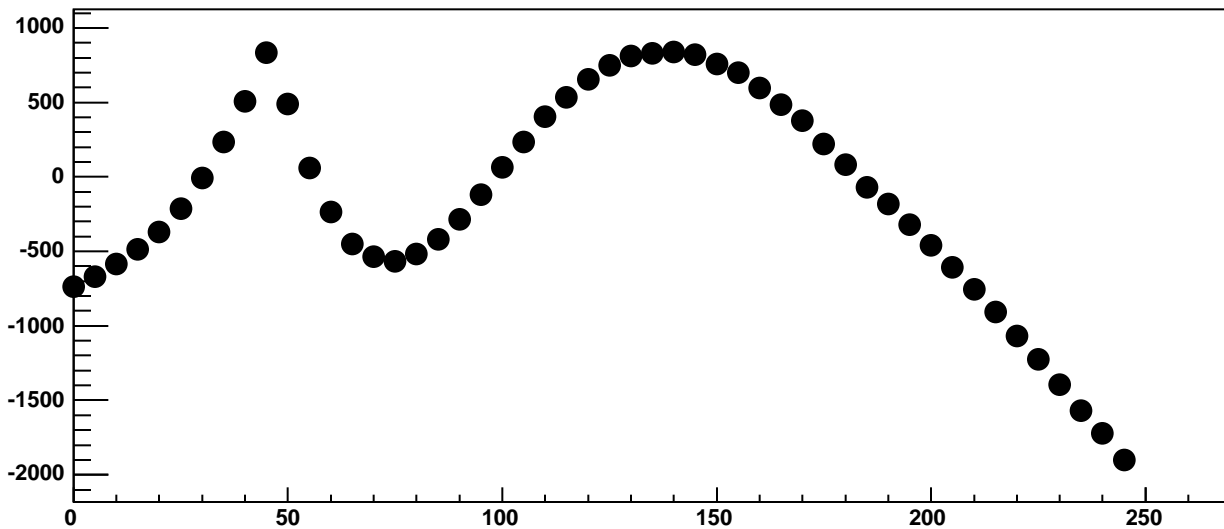


χ^2 / ndf	3.378e+05 / 41
p0	-575.8 ± 1.536
p1	46.14 ± 0.02141
p2	7782 ± 12.07
p3	40.5 ± 0.07559
p4	-16.56 ± 0.06732

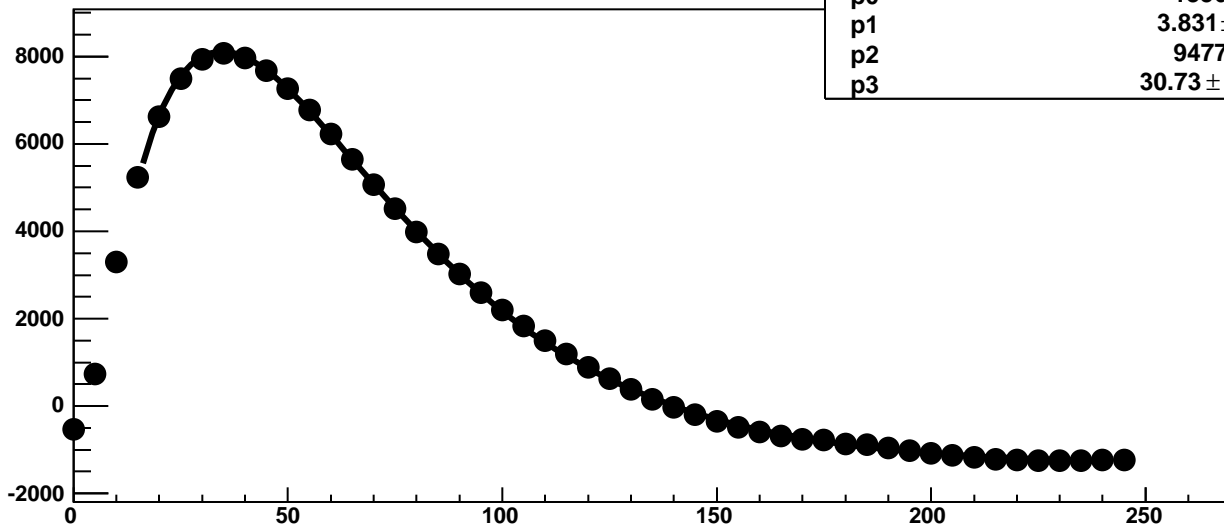
Chip 3, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold

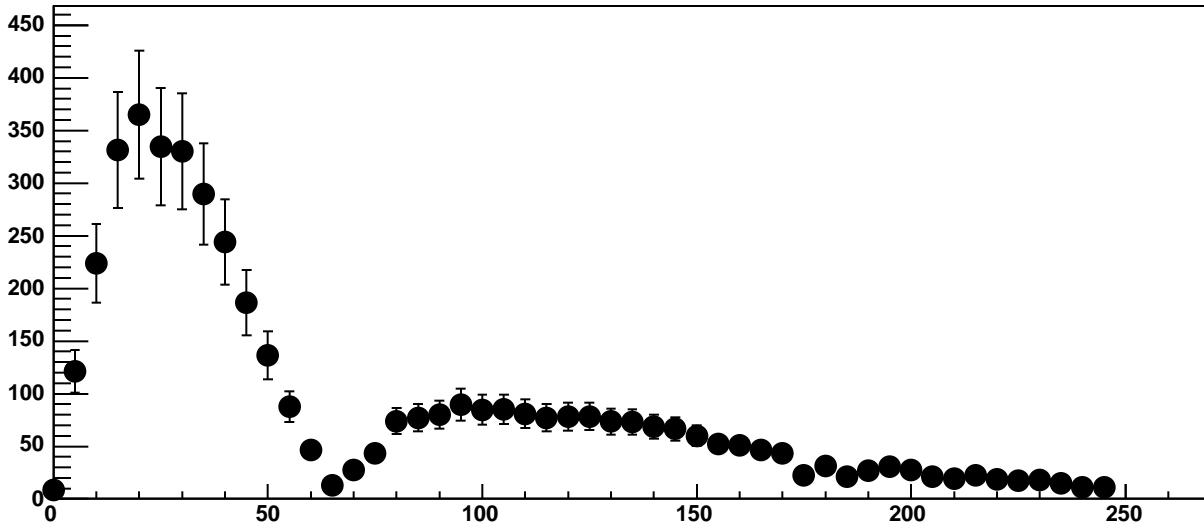


Chip 3, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold

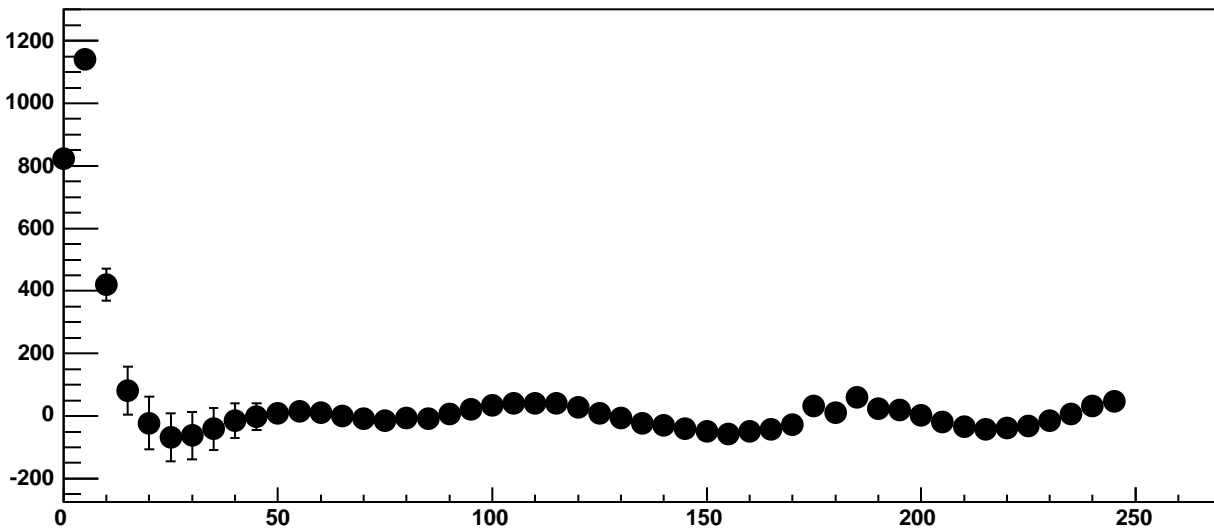


χ^2 / ndf	777.4 / 42
p0	-1356 ± 1.859
p1	3.831 ± 0.1339
p2	9477 ± 14.72
p3	30.73 ± 0.04686

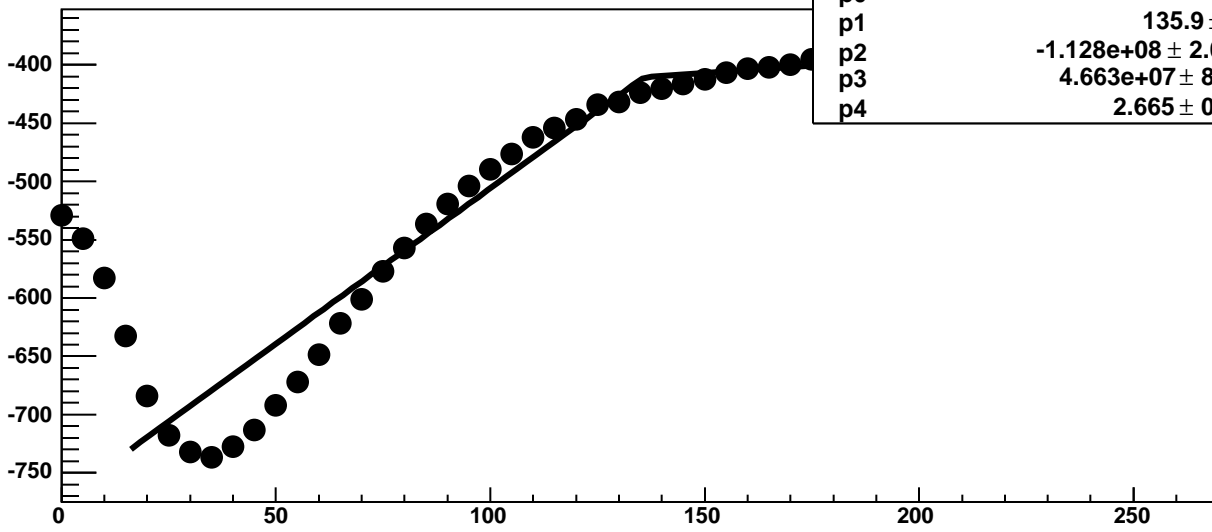
Chip 3, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold



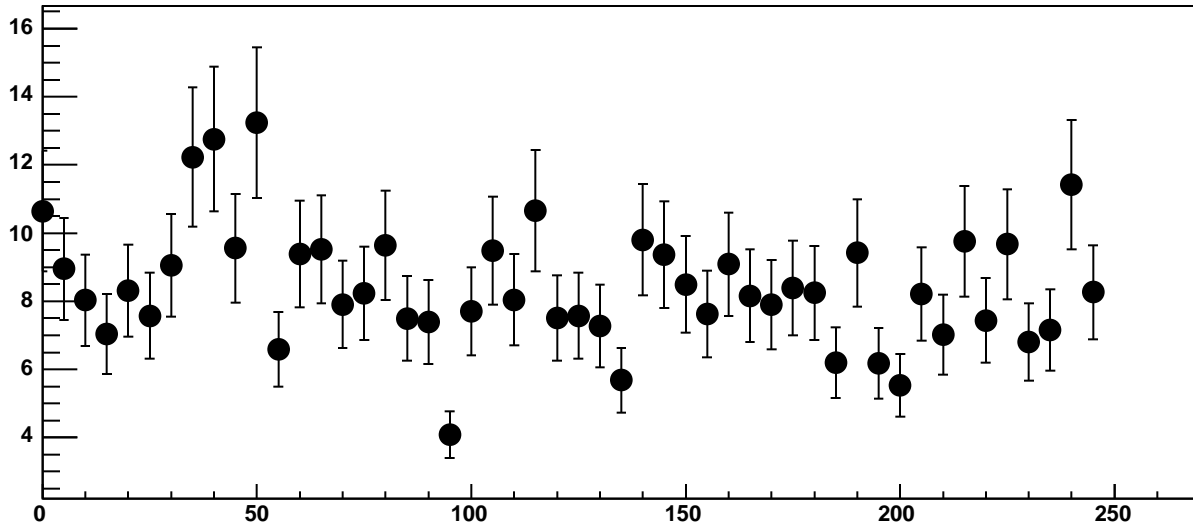
Chip 3, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold



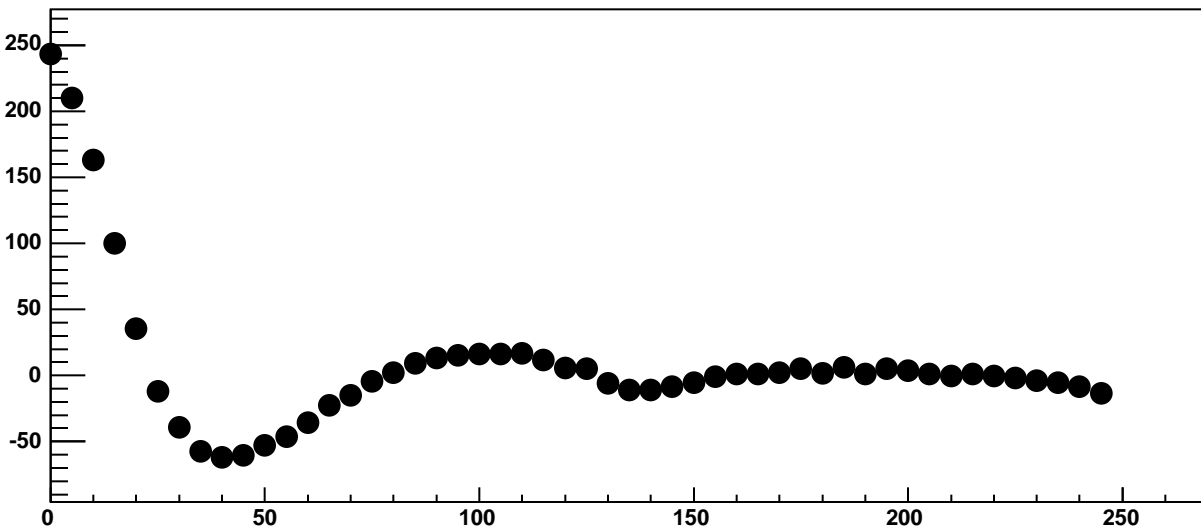
Chip 3, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold



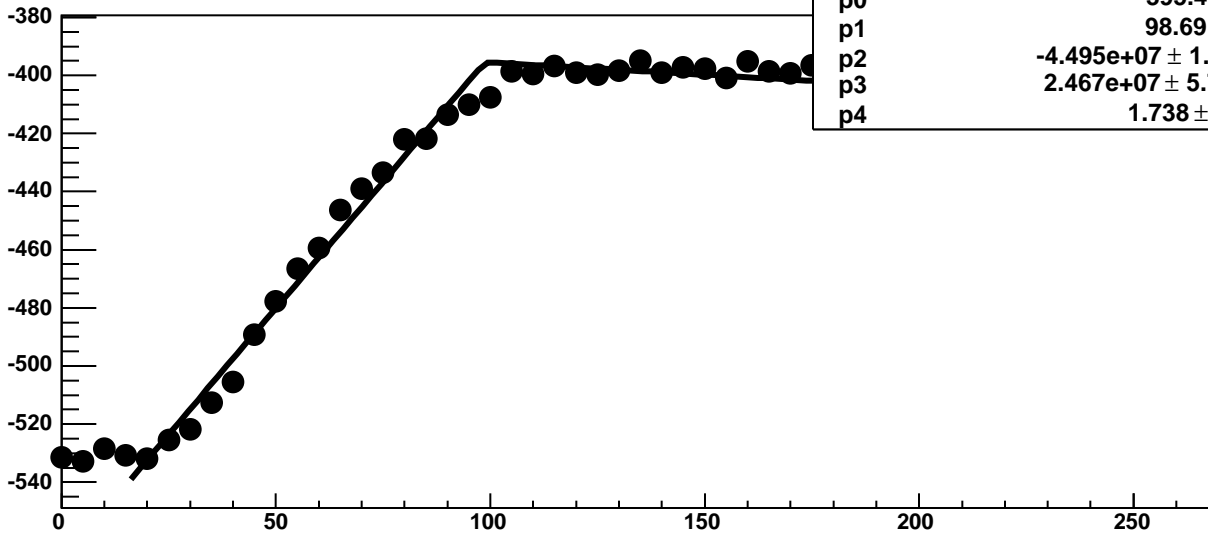
Chip 3, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

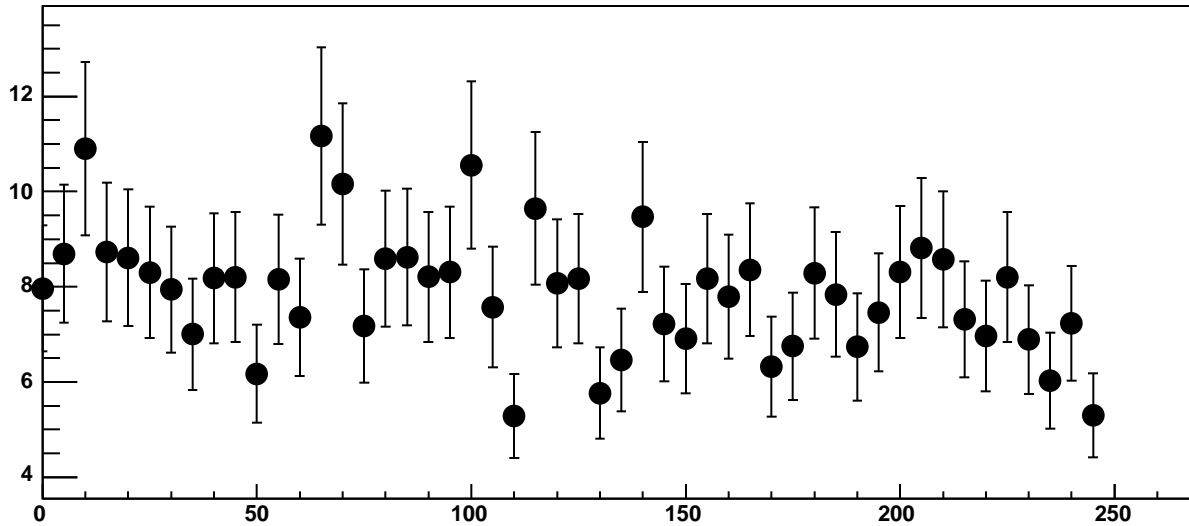


Chip 3, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

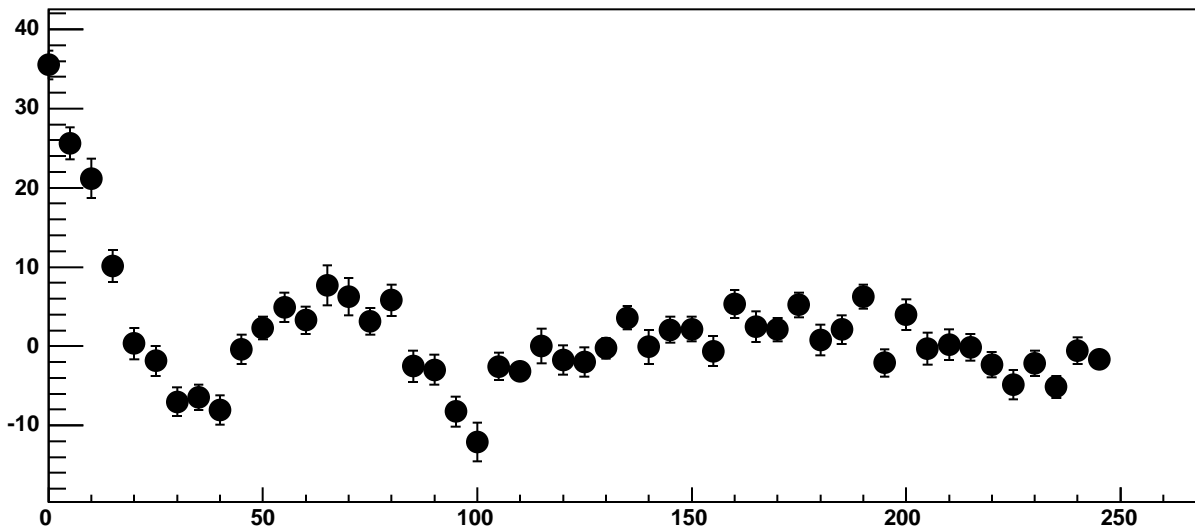


χ^2 / ndf	258.6 / 41
p0	-395.4 ± 0.5891
p1	98.69 ± 0.6201
p2	$-4.495\text{e}+07 \pm 1.152\text{e}+06$
p3	$2.467\text{e}+07 \pm 5.702\text{e}+05$
p4	1.738 ± 0.01894

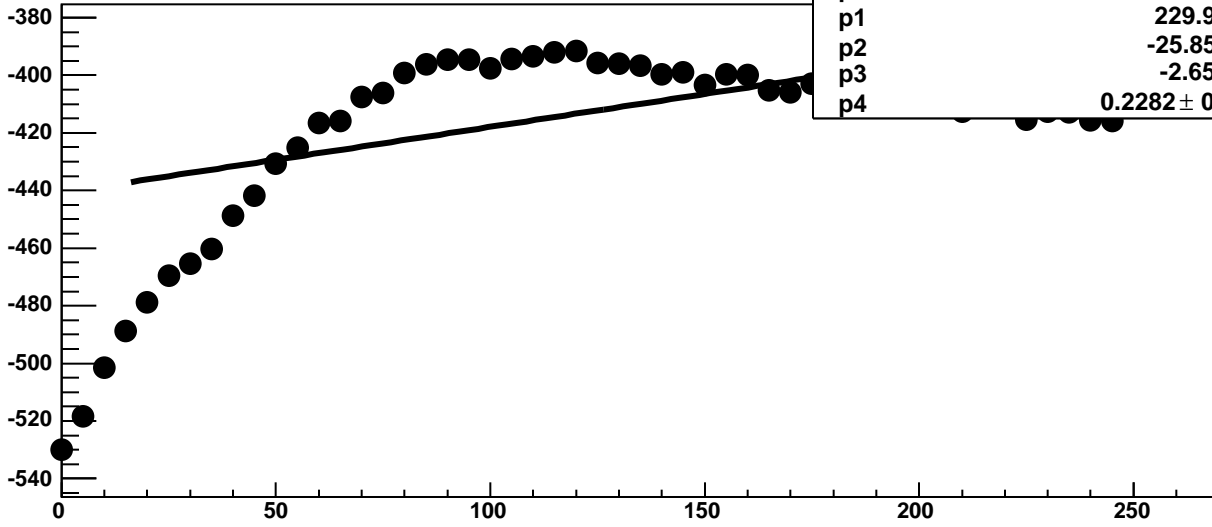
Chip 3, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

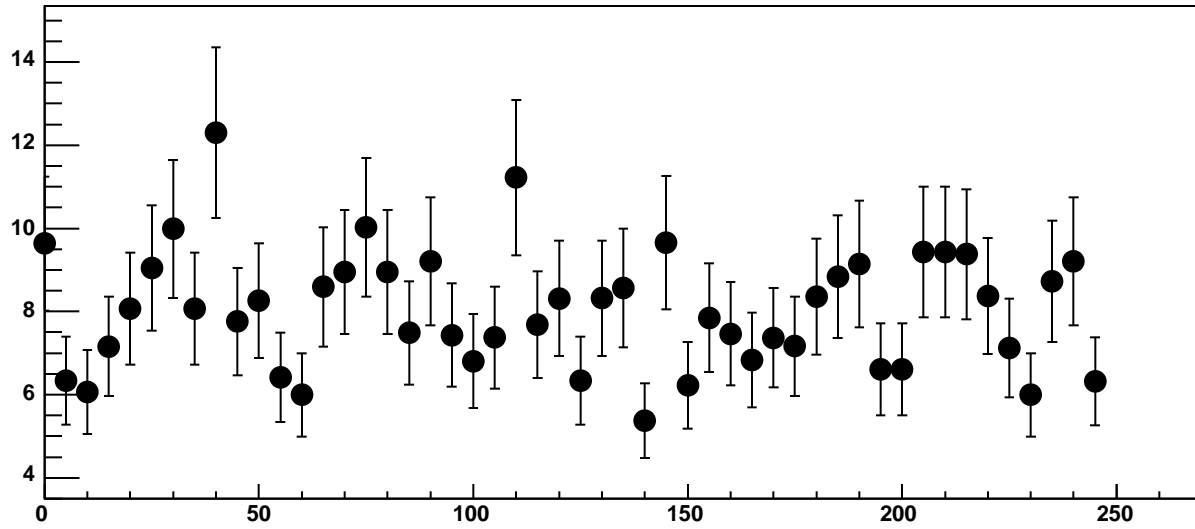


Chip 3, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold

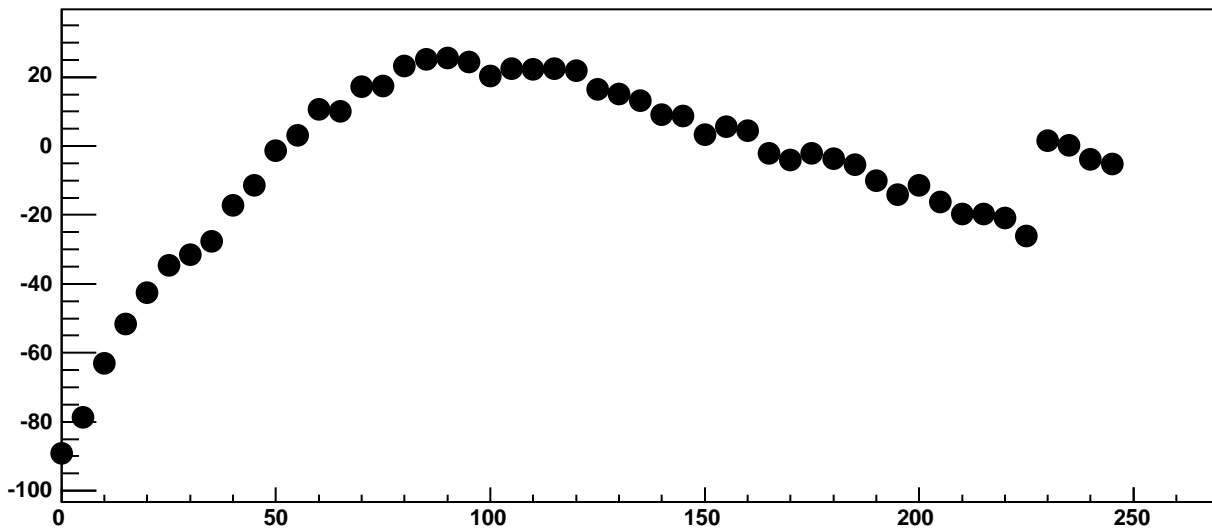


χ^2 / ndf	5025 / 41
p0	-388.3 ± 0.6727
p1	229.9 ± 10.96
p2	-25.85 ± 1.035
p3	-2.65 ± 6.626
p4	0.2282 ± 0.003027

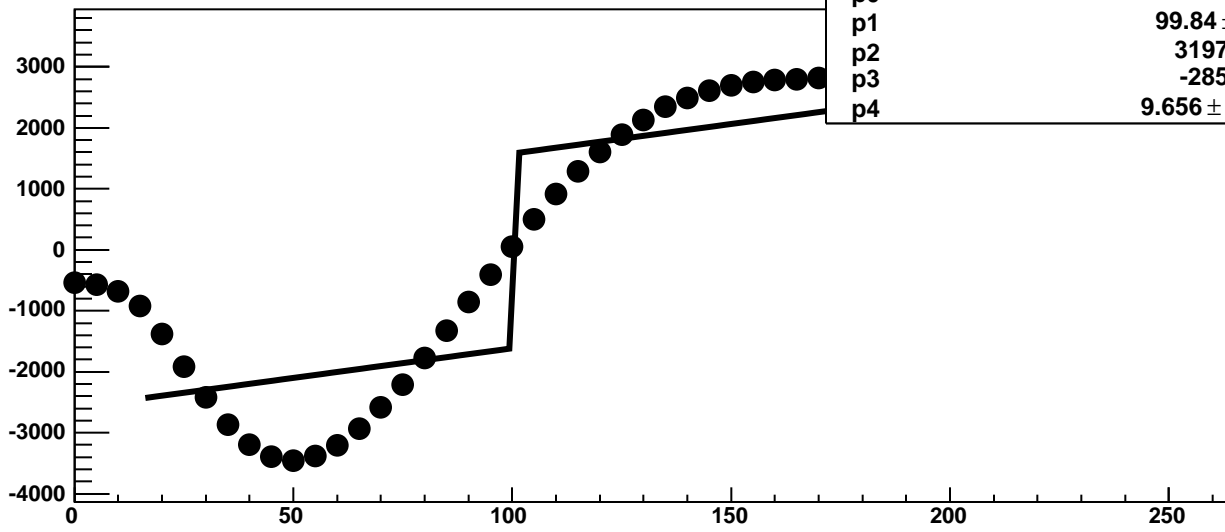
Chip 3, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold

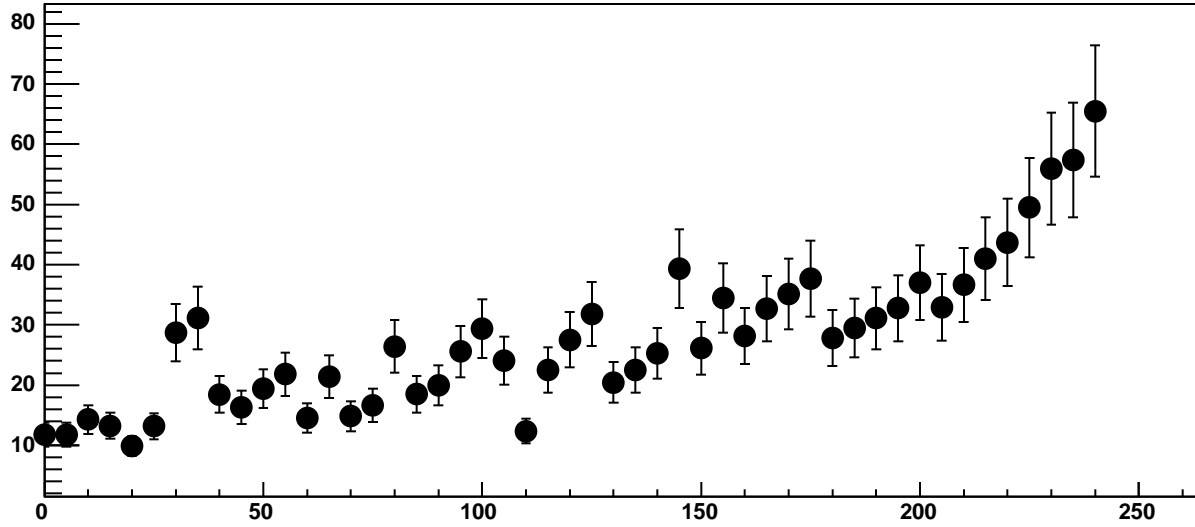


Chip 3, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

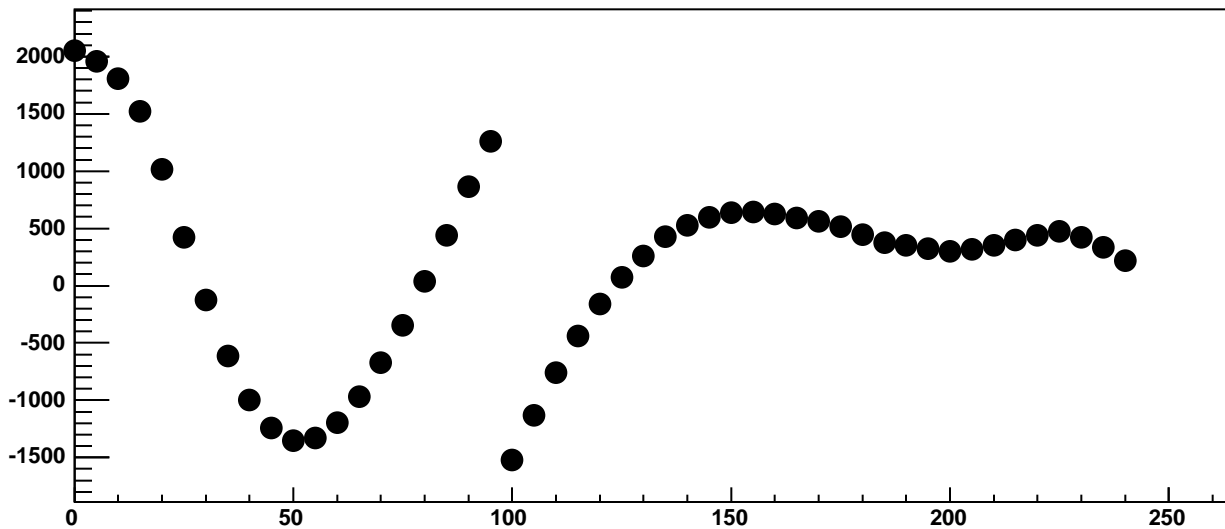


χ^2 / ndf	1.377e+06 / 41
p0	-1620 ± 1.302
p1	99.84 ± 0.7899
p2	3197 ± 4.836
p3	-285 ± 4.005
p4	9.656 ± 0.07403

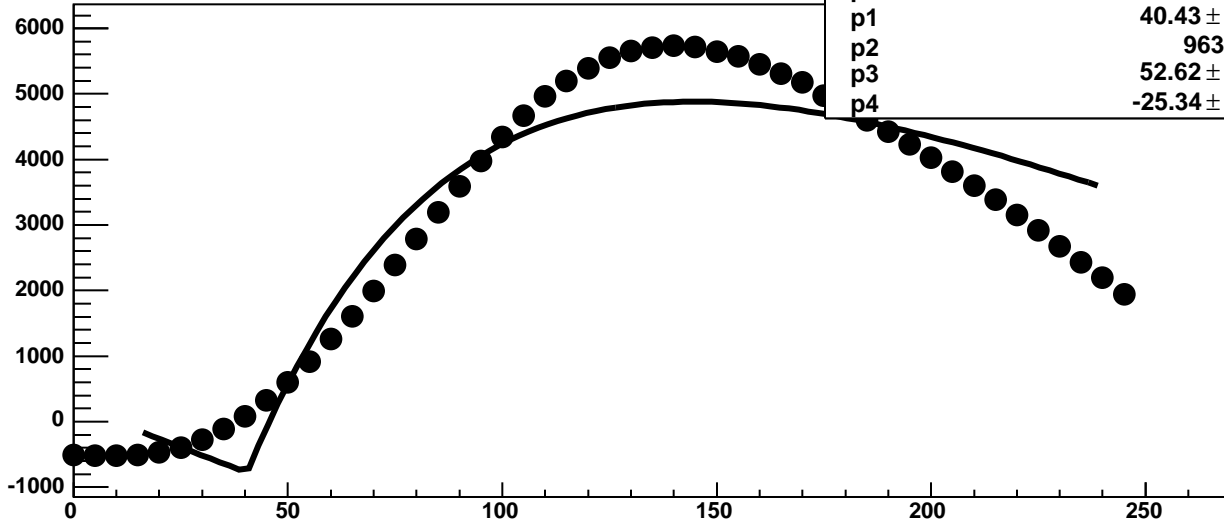
Chip 3, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold

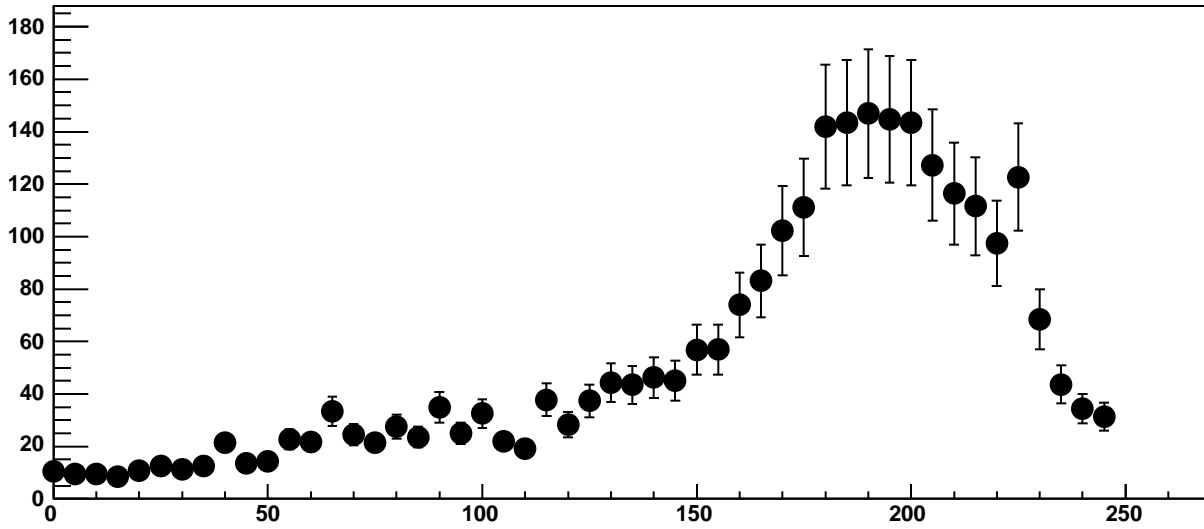


Chip 3, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

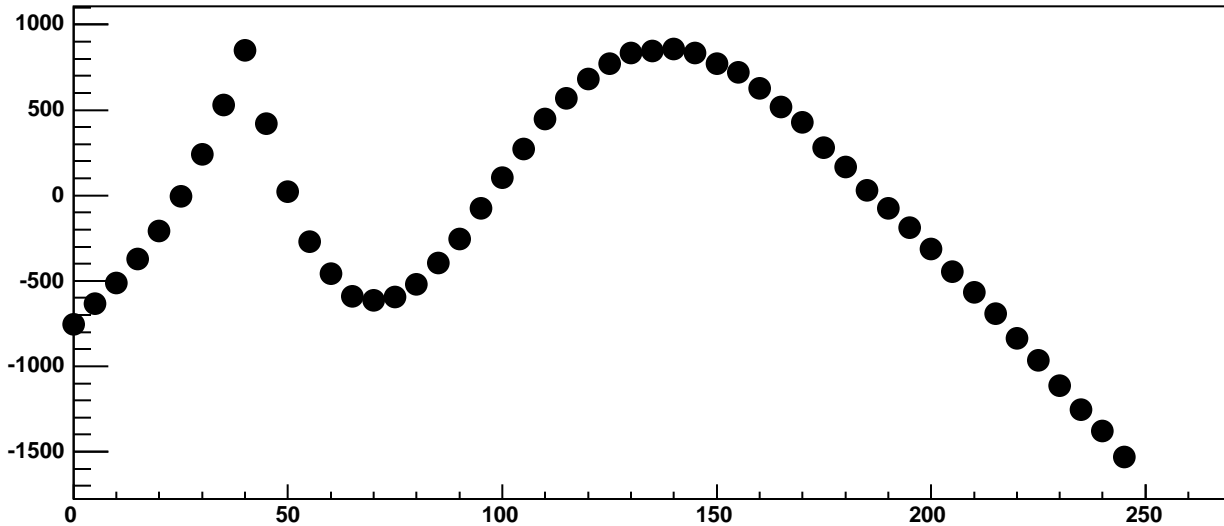


χ^2 / ndf	3.203e+05 / 41
p0	-779.1 ± 1.48
p1	40.43 ± 0.01864
p2	9631 ± 16.2
p3	52.62 ± 0.09305
p4	-25.34 ± 0.07823

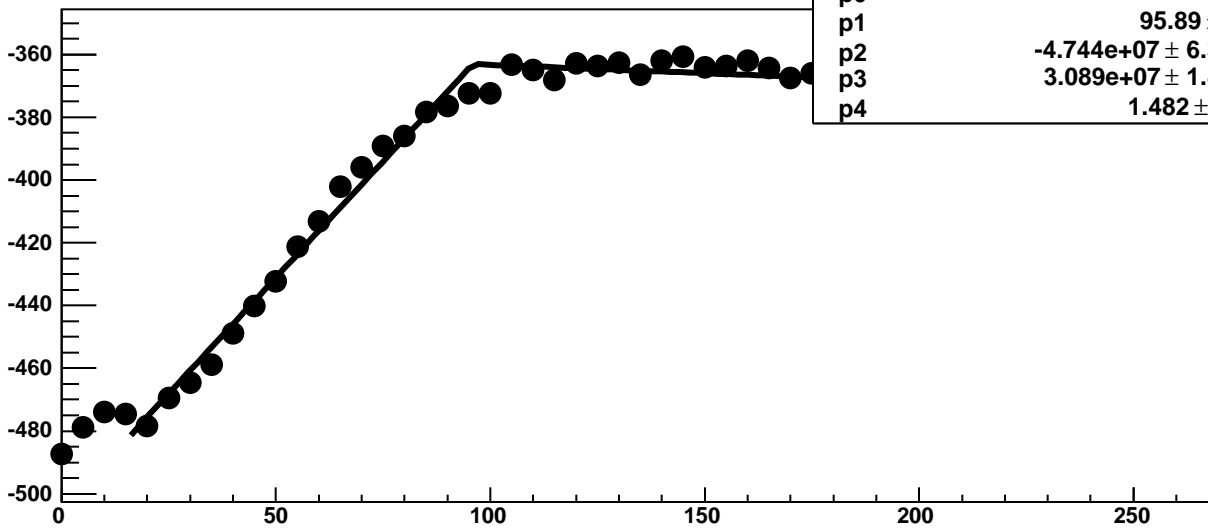
Chip 3, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

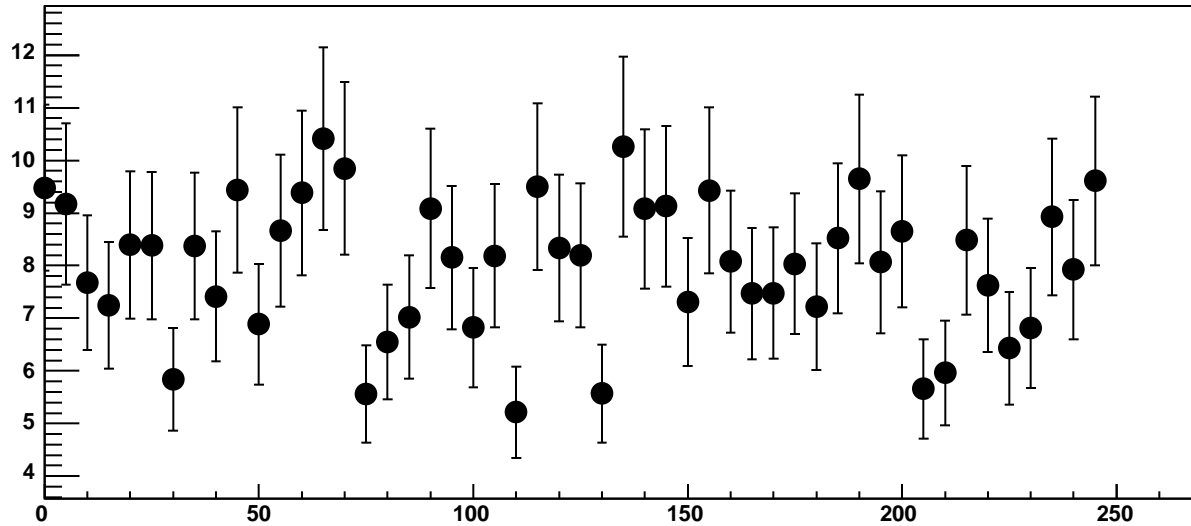


Chip 3, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold

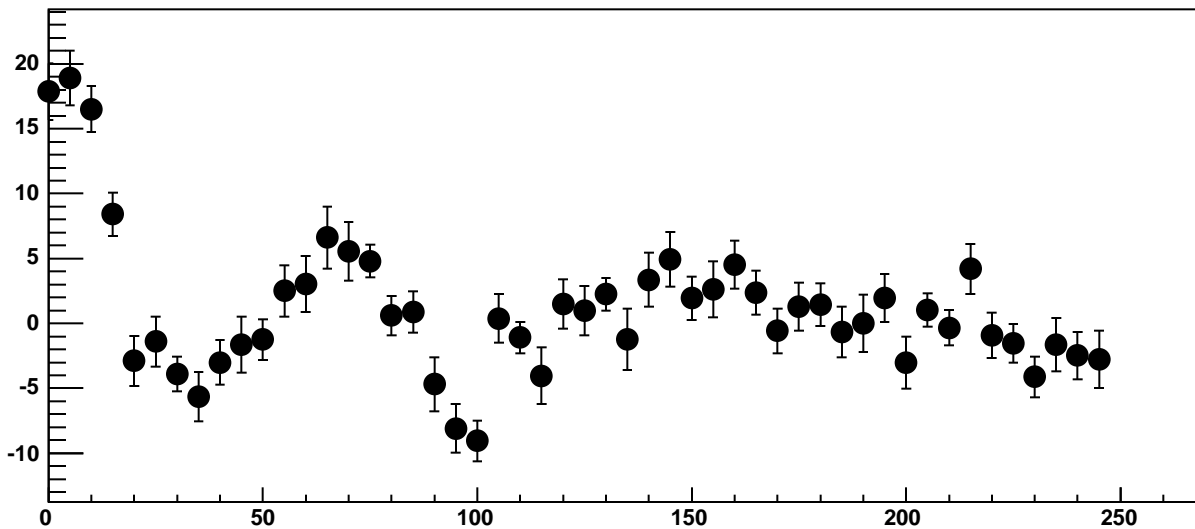


χ^2 / ndf	187.4 / 41
p0	-363.1 ± 0.6131
p1	95.89 ± 0.6754
p2	-4.744e+07 ± 6.382e+05
p3	3.089e+07 ± 1.823e+05
p4	1.482 ± 0.01715

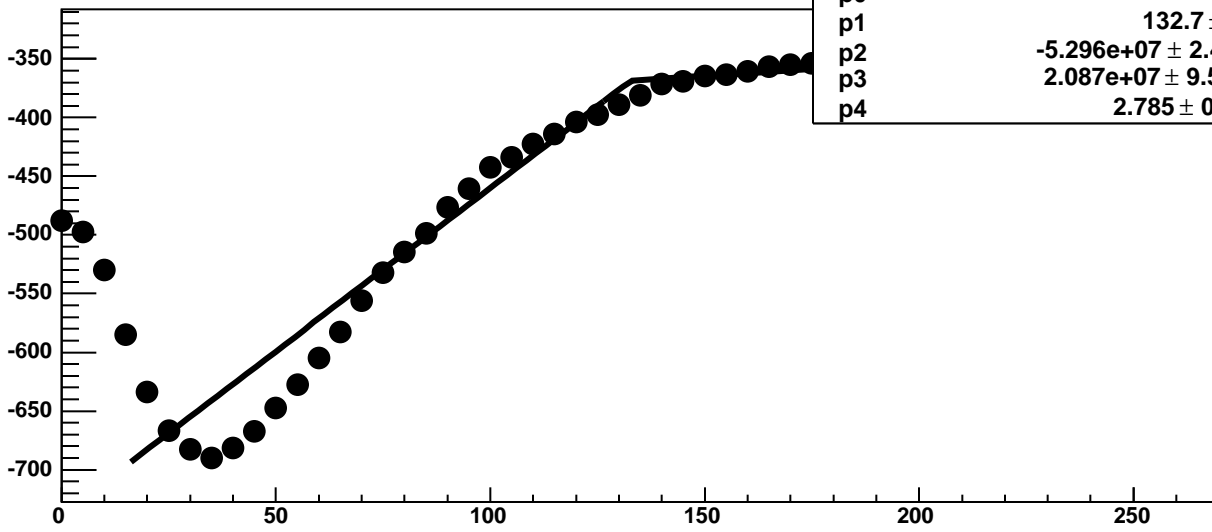
Chip 3, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold

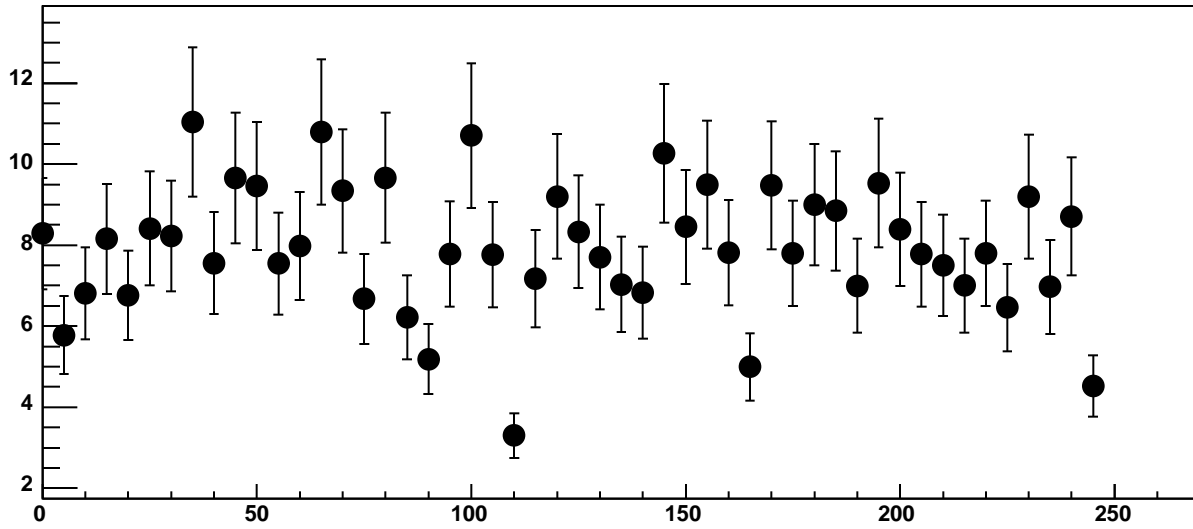


Chip 3, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

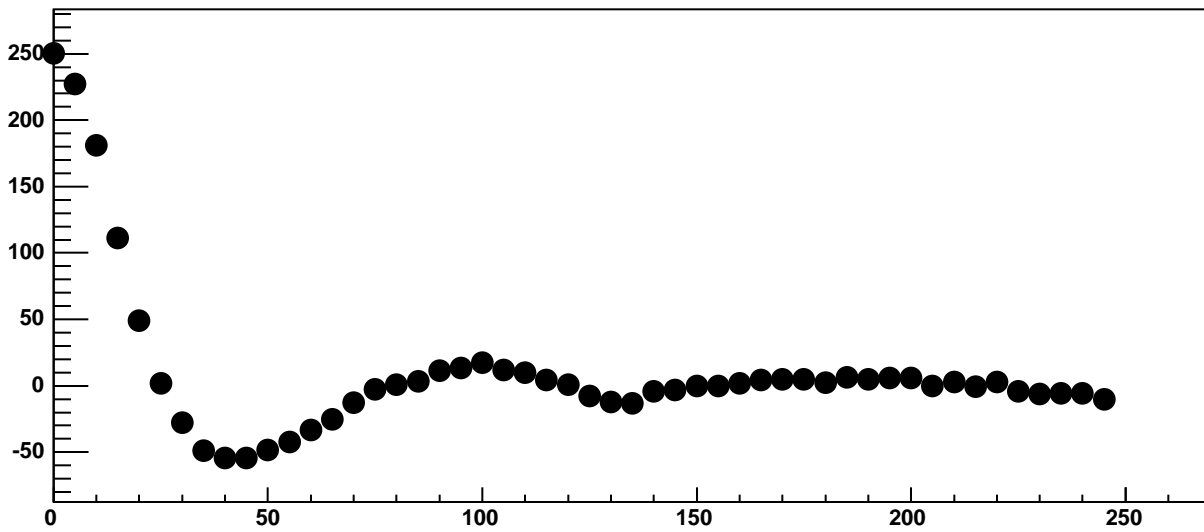


χ^2 / ndf	9026 / 41
p0	-368.9 ± 0.8199
p1	132.7 ± 0.3829
p2	$-5.296\text{e}+07 \pm 2.436\text{e}+06$
p3	$2.087\text{e}+07 \pm 9.516\text{e}+05$
p4	2.785 ± 0.009936

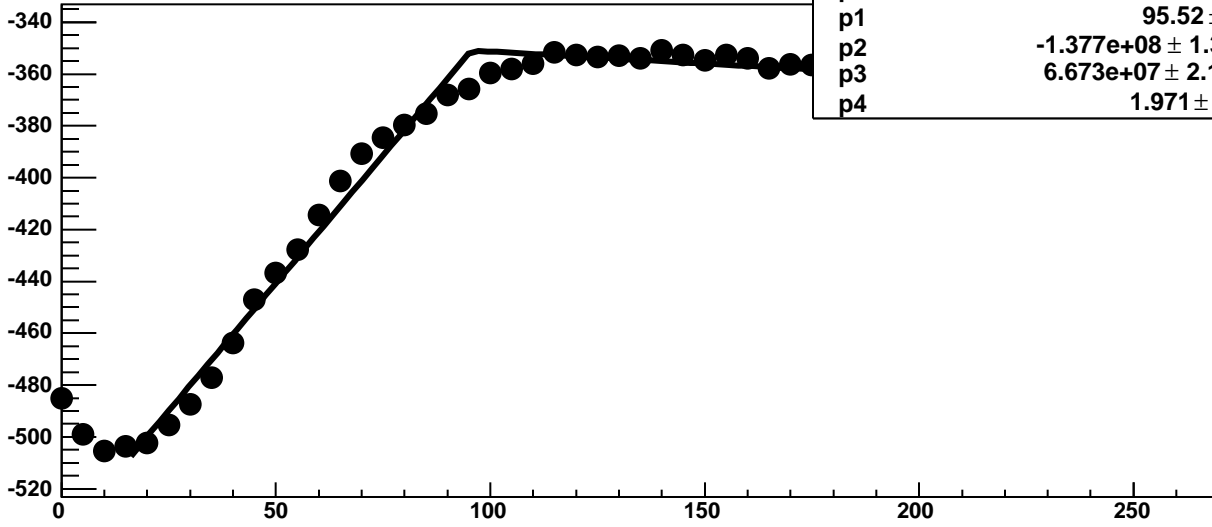
Chip 3, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

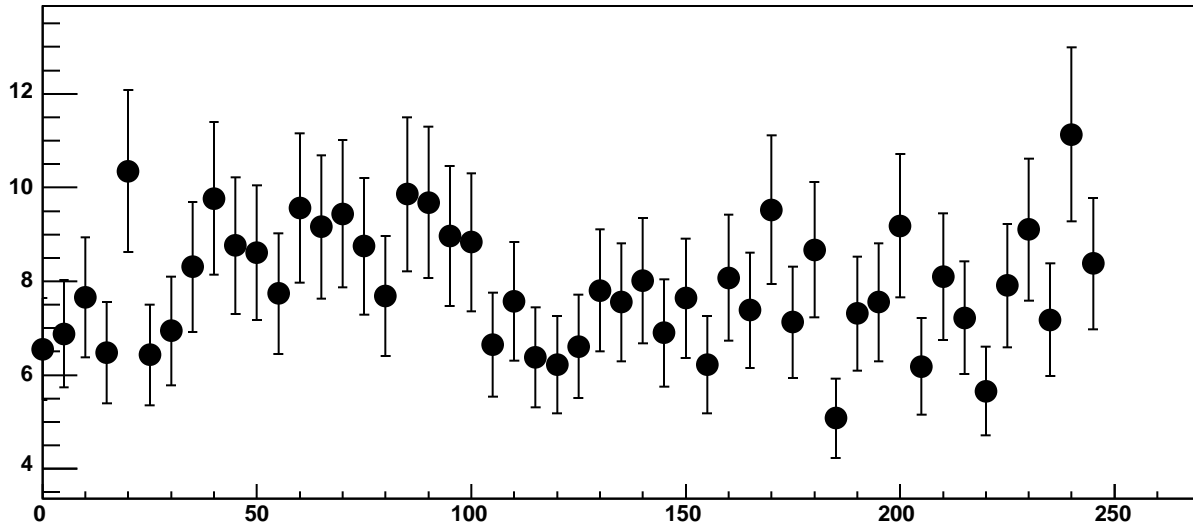


Chip 3, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

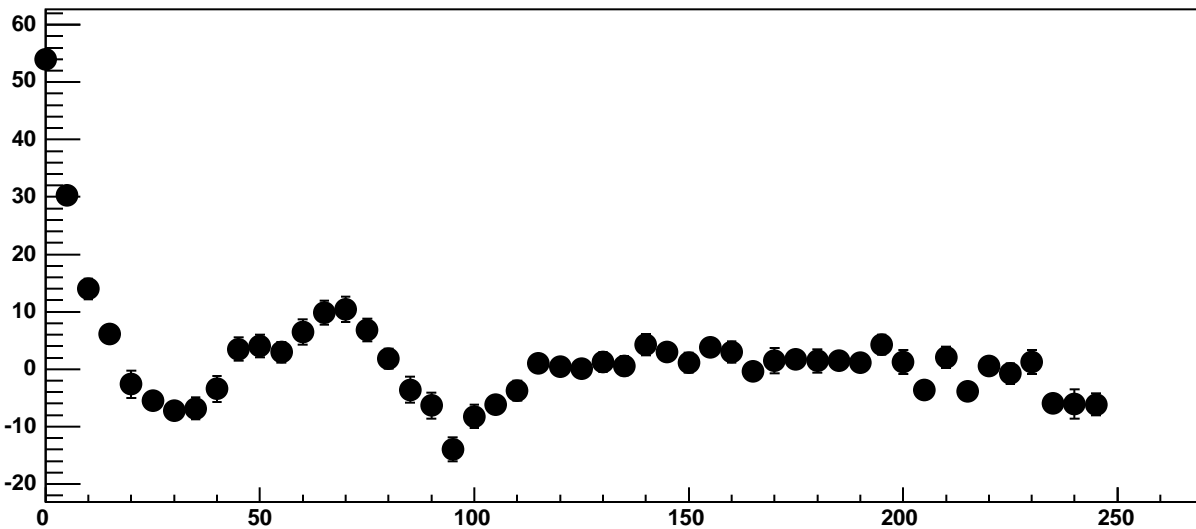


χ^2 / ndf	302.2 / 41
p0	-350.9 ± 0.604
p1	95.52 ± 0.5509
p2	$-1.377\text{e}+08 \pm 1.396\text{e}+06$
p3	$6.673\text{e}+07 \pm 2.104\text{e}+05$
p4	1.971 ± 0.01857

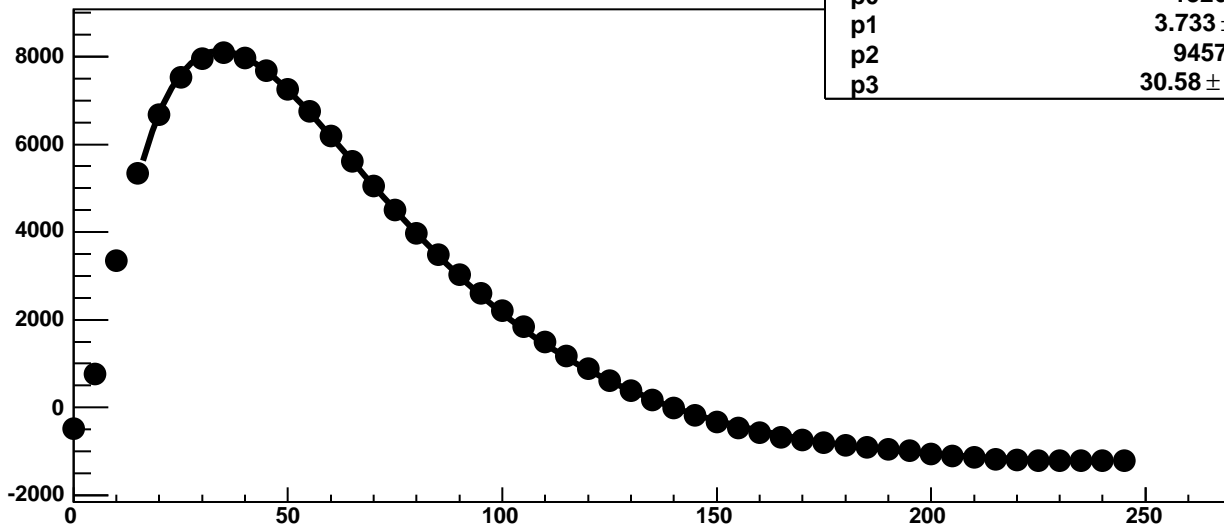
Chip 3, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

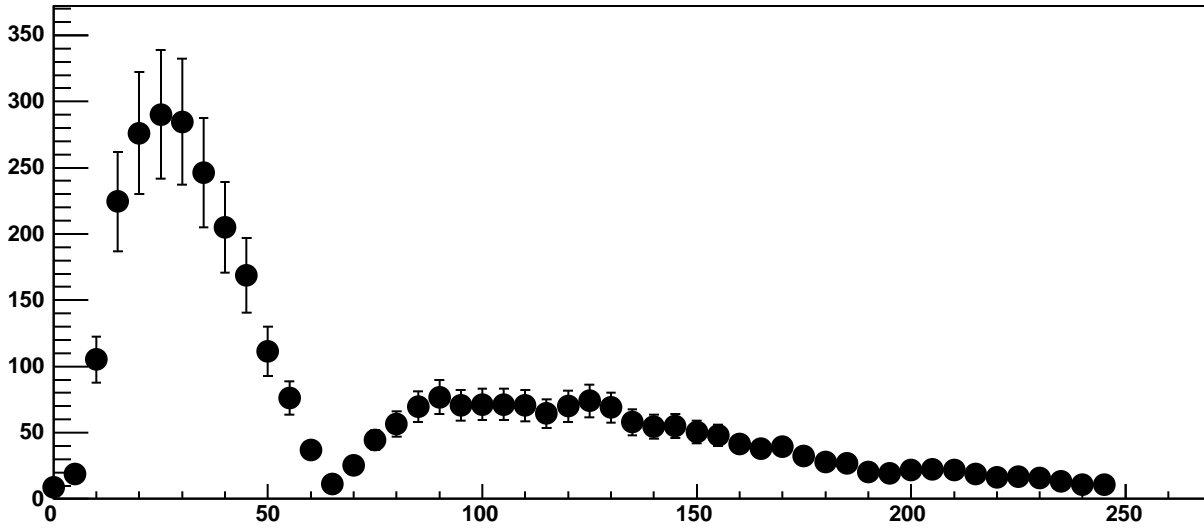


Chip 3, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold

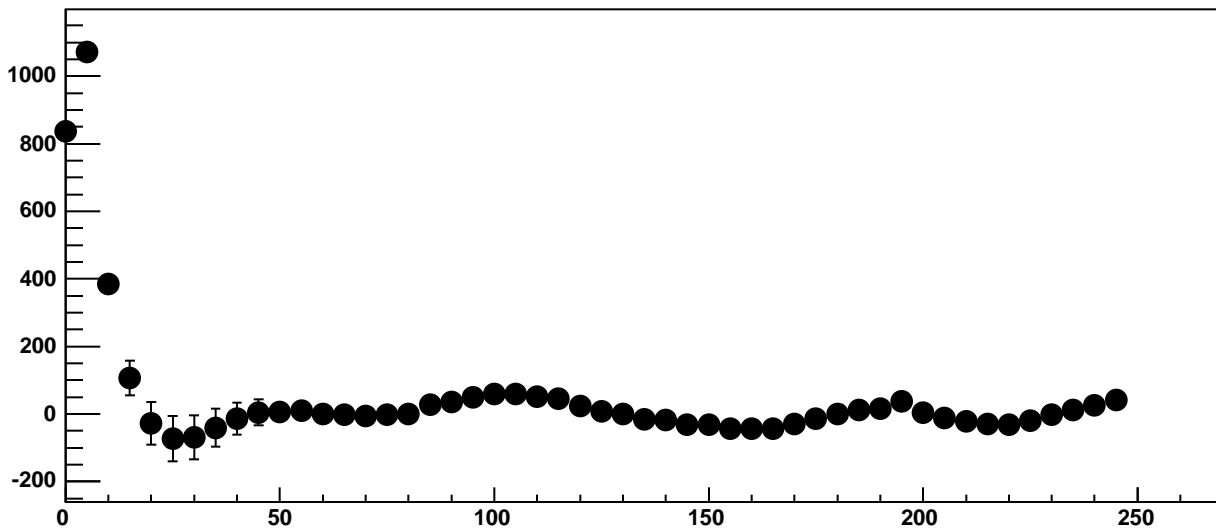


χ^2 / ndf	539.3 / 42
p0	-1326 ± 1.673
p1	3.733 ± 0.1013
p2	9457 ± 11.44
p3	30.58 ± 0.04015

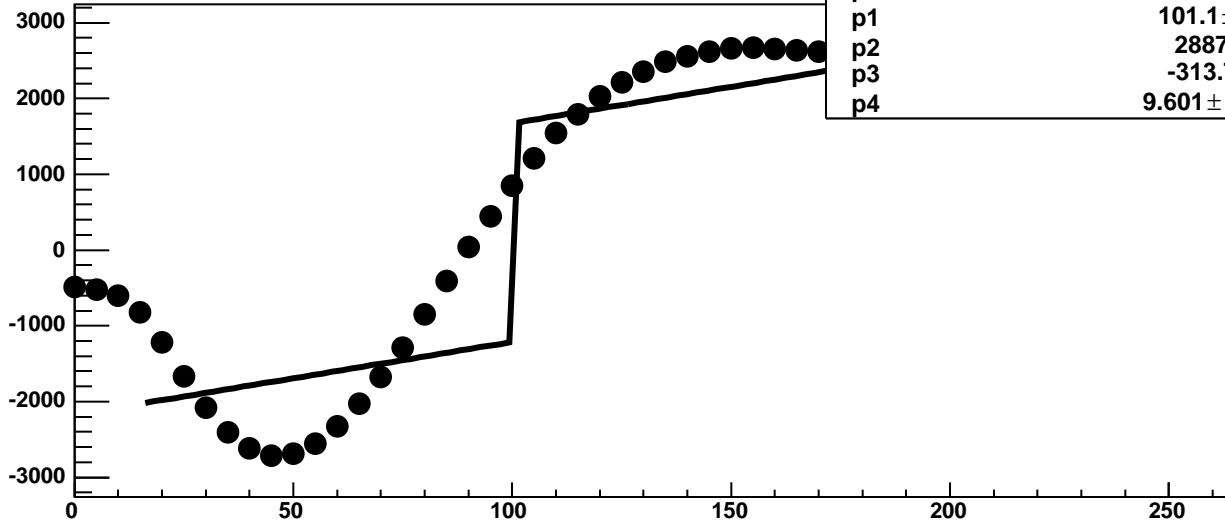
Chip 3, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold

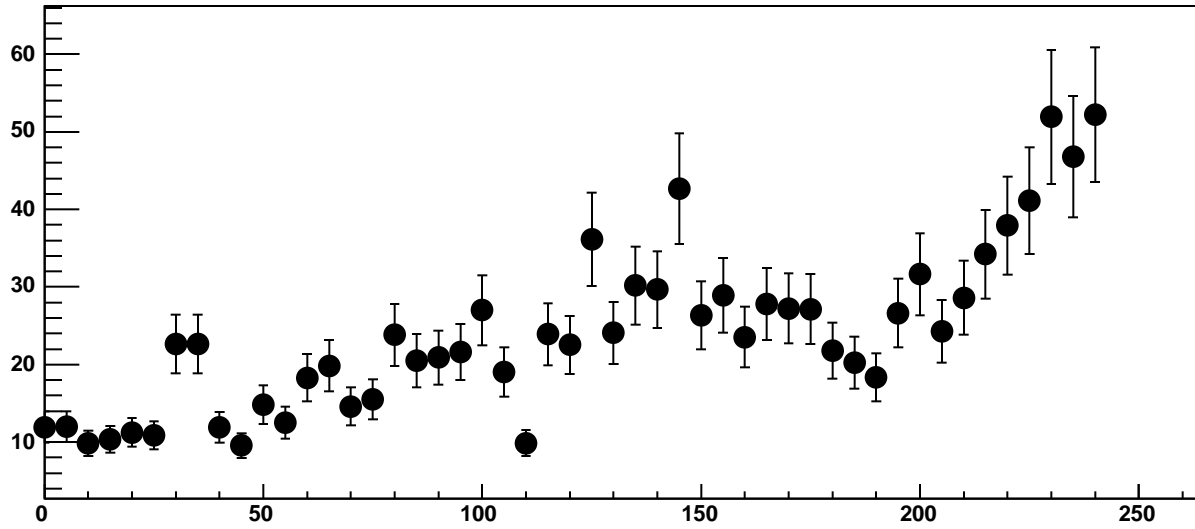


Chip 3, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

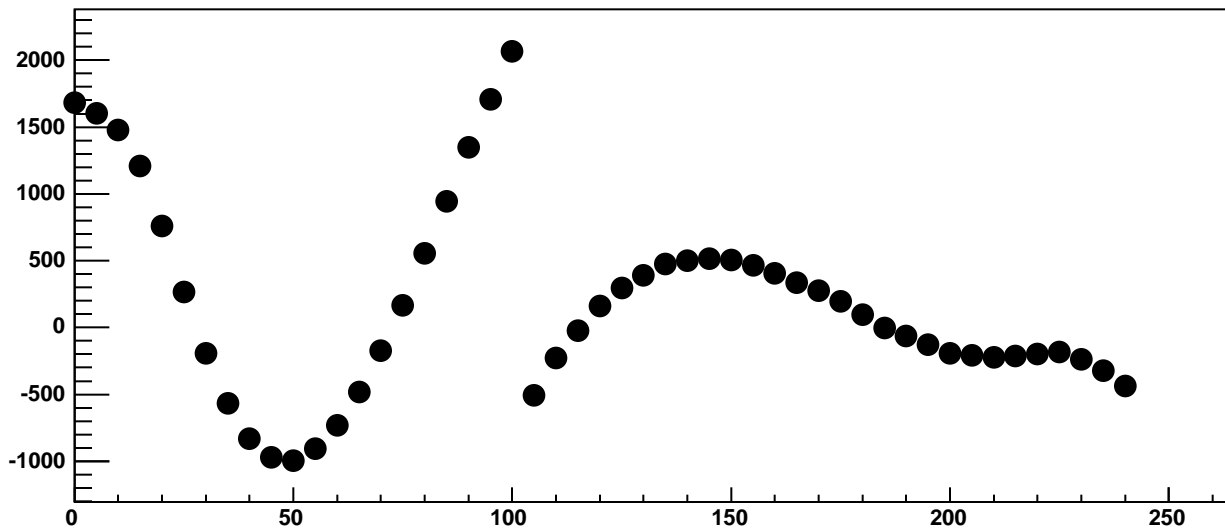


χ^2 / ndf	1.33e+06 / 41
p0	-1202 ± 2.737
p1	101.1 ± 0.5412
p2	2887 ± 6.975
p3	-313.7 ± 7.67
p4	9.601 ± 0.08679

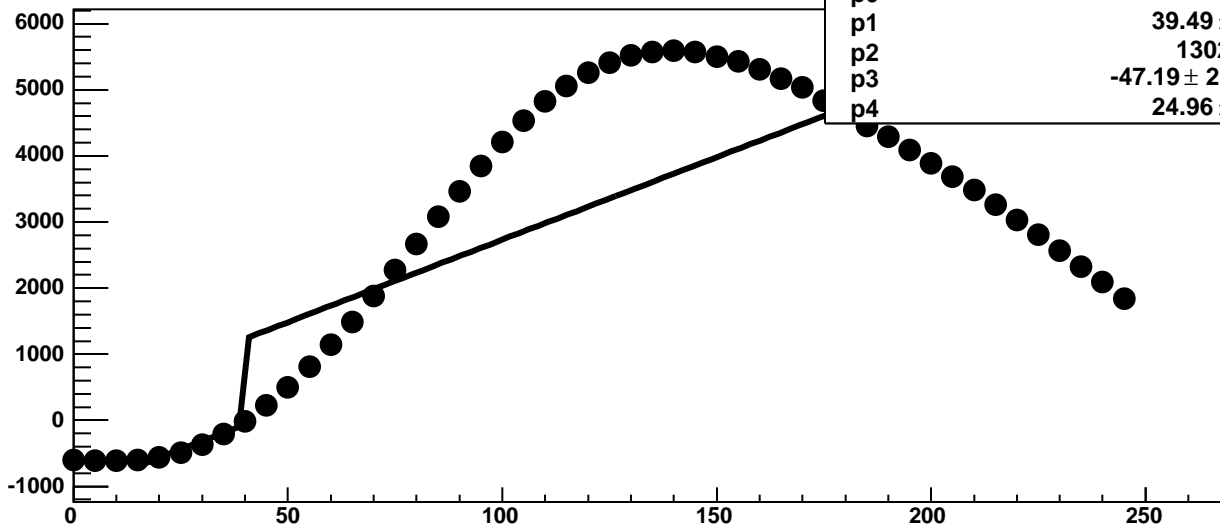
Chip 3, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

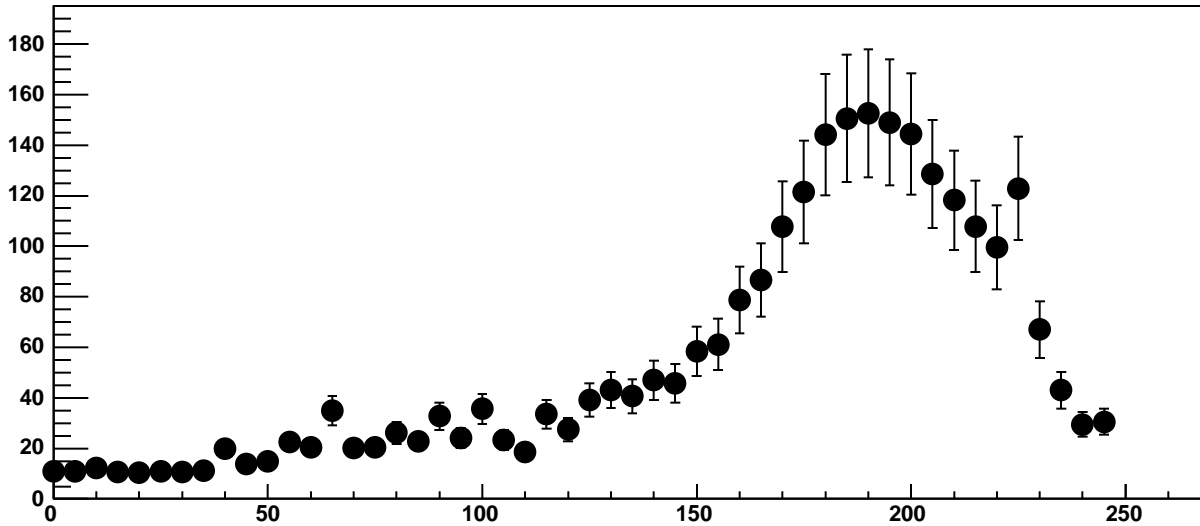


Chip 3, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

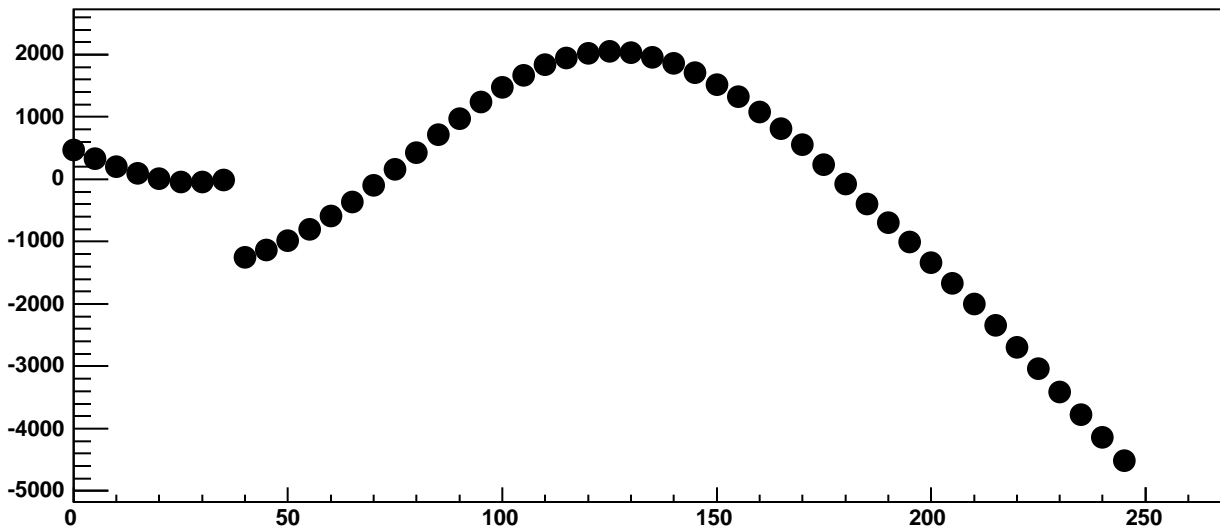


χ^2 / ndf	1.766e+06 / 41
p0	-79.28 ± 0.9348
p1	39.49 ± 0.0235
p2	1302 ± 2.321
p3	-47.19 ± 2.854e-06
p4	24.96 ± 0.0245

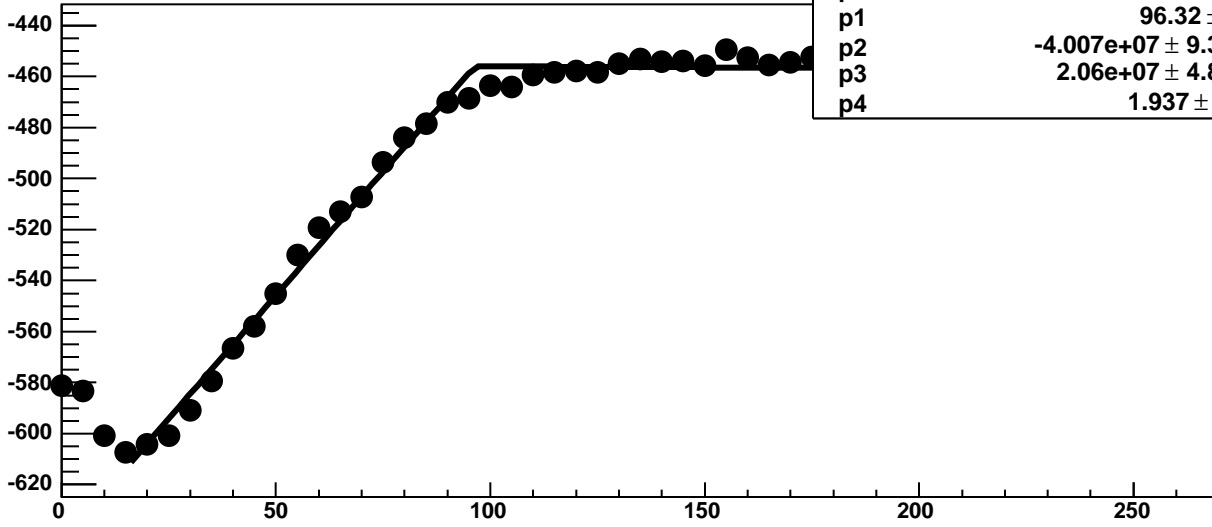
Chip 3, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold

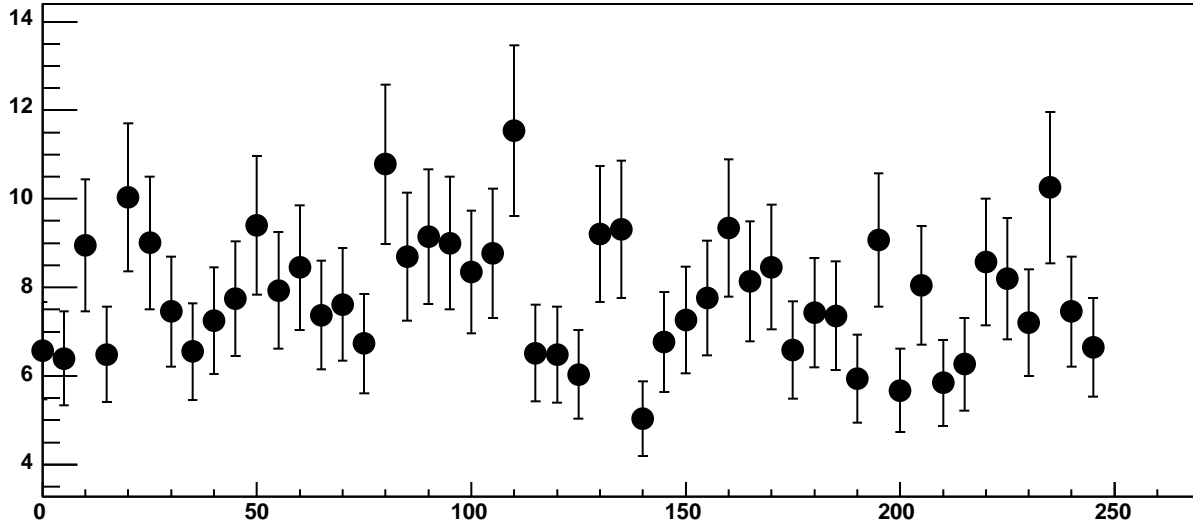


Chip 3, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

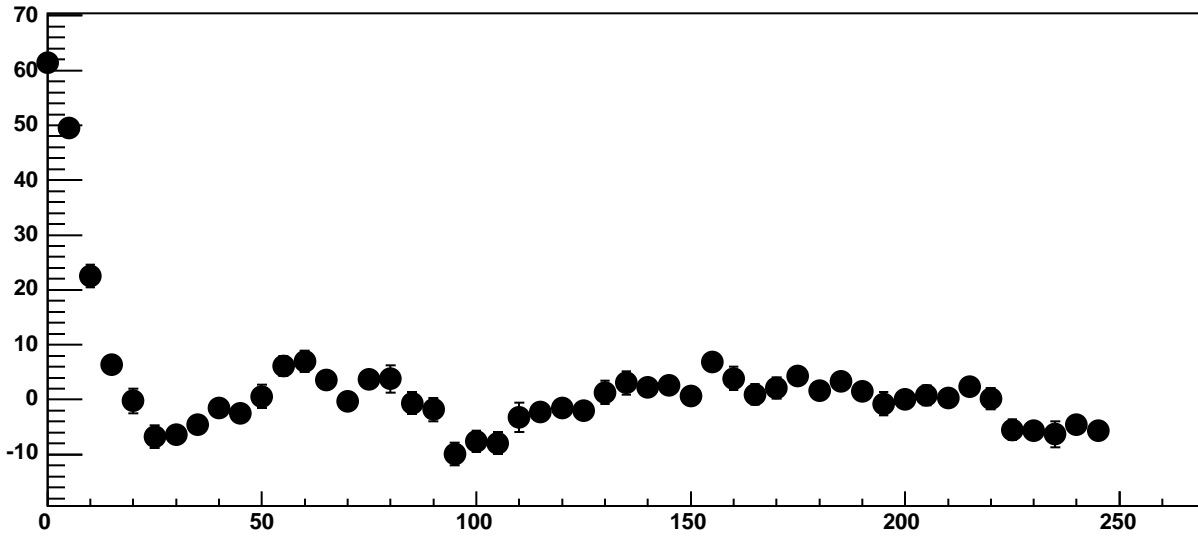


χ^2 / ndf	234.3 / 41
p0	-456 ± 0.6389
p1	96.32 ± 0.5554
p2	$-4.007\text{e}+07 \pm 9.351\text{e}+05$
p3	$2.06\text{e}+07 \pm 4.822\text{e}+05$
p4	1.937 ± 0.01716

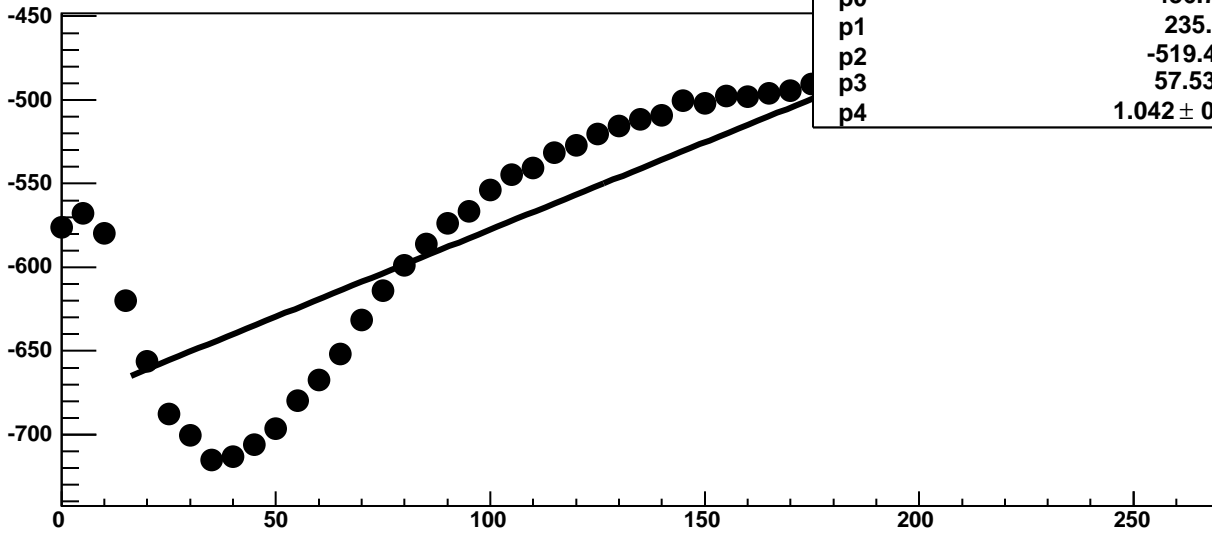
Chip 3, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

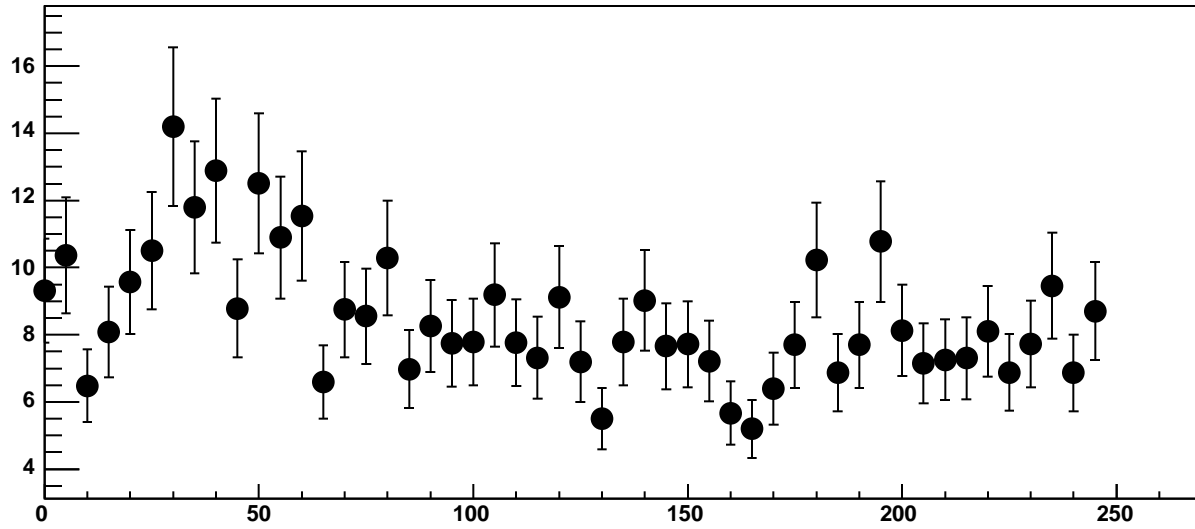


Chip 3, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold

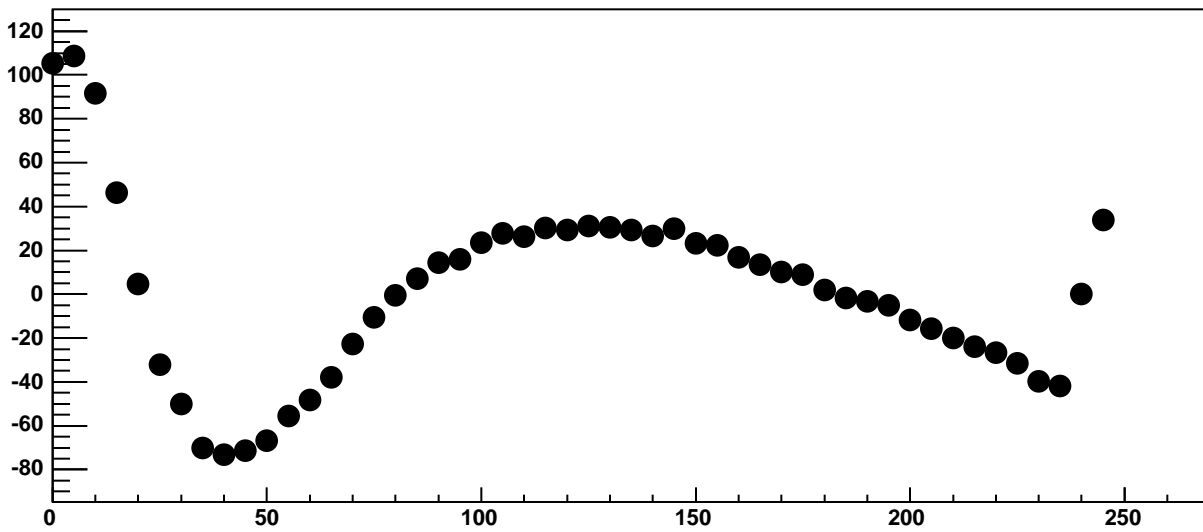


χ^2 / ndf	1.135e+04 / 41
p0	-436.7 ± 2.773
p1	235.1 ± 2.62
p2	-519.4 ± 248.5
p3	57.53 ± 23.25
p4	1.042 ± 0.004564

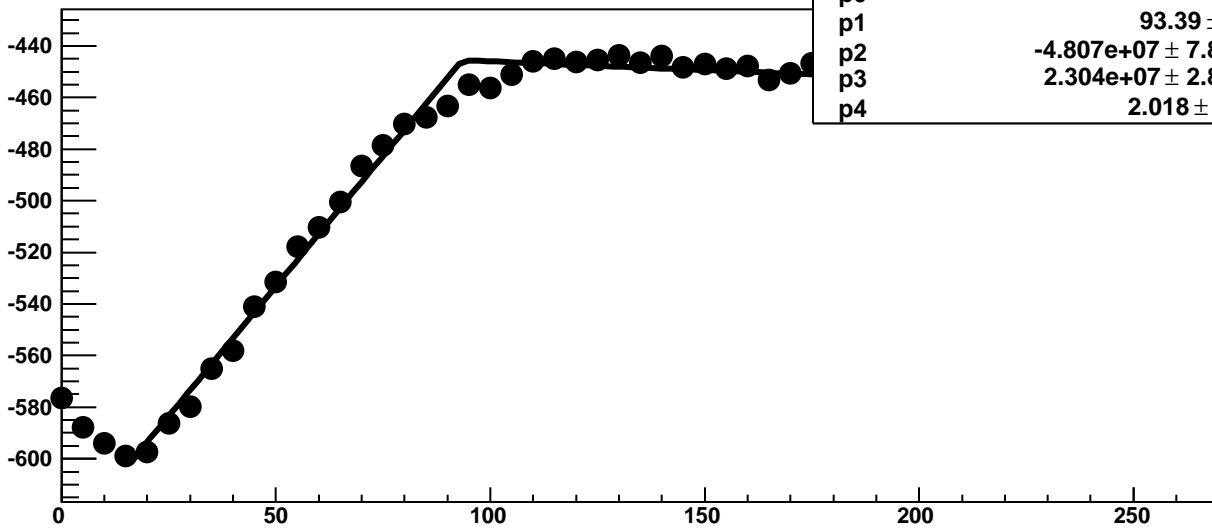
Chip 3, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold

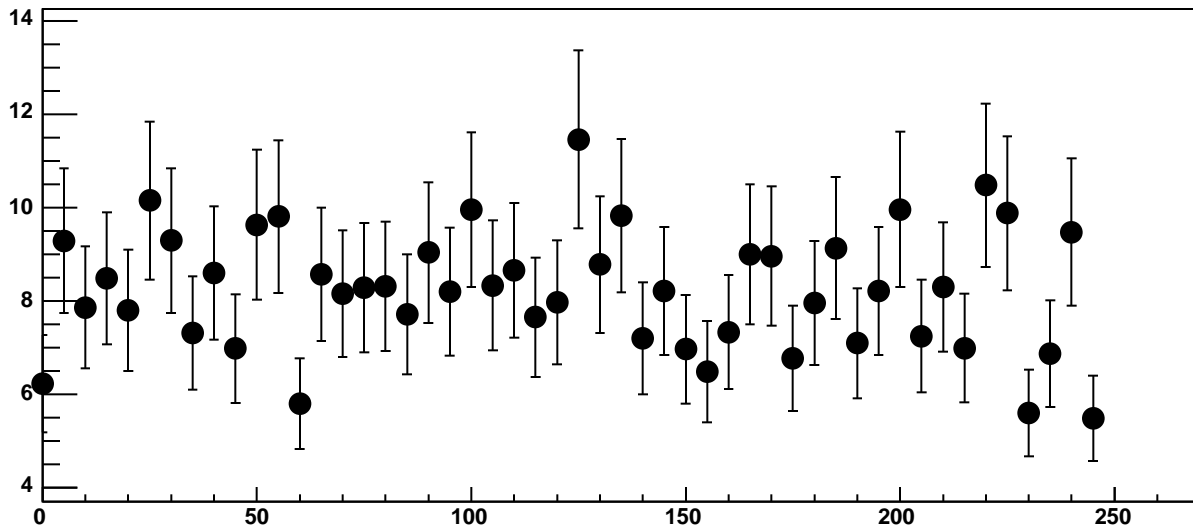


Chip 3, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

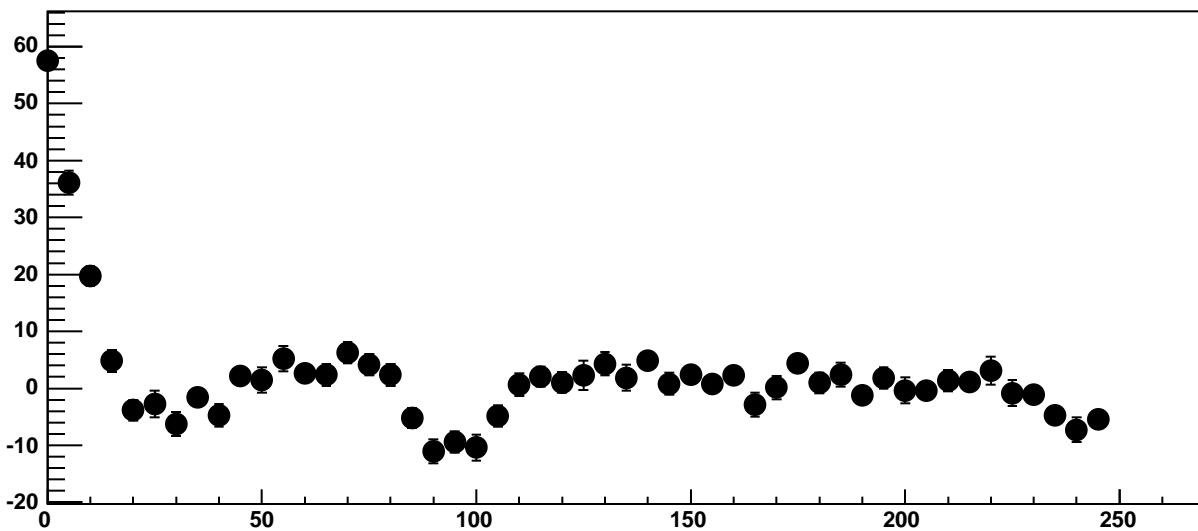


χ^2 / ndf	204.9 / 41
p0	-445.5 ± 0.6701
p1	93.39 ± 0.5674
p2	$-4.807\text{e}+07 \pm 7.876\text{e}+05$
p3	$2.304\text{e}+07 \pm 2.873\text{e}+05$
p4	2.018 ± 0.02087

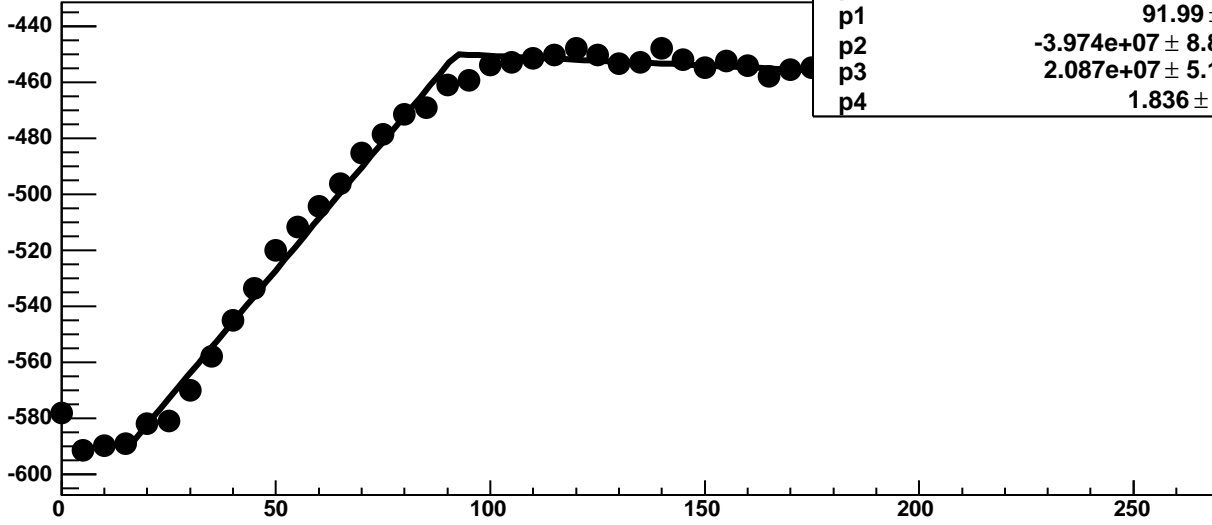
Chip 3, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

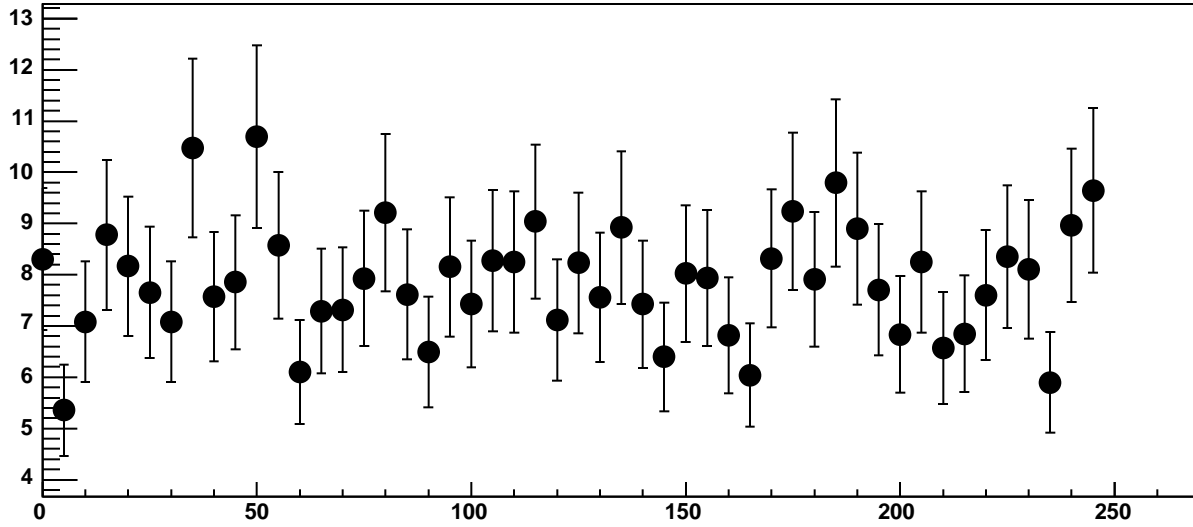


Chip 3, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

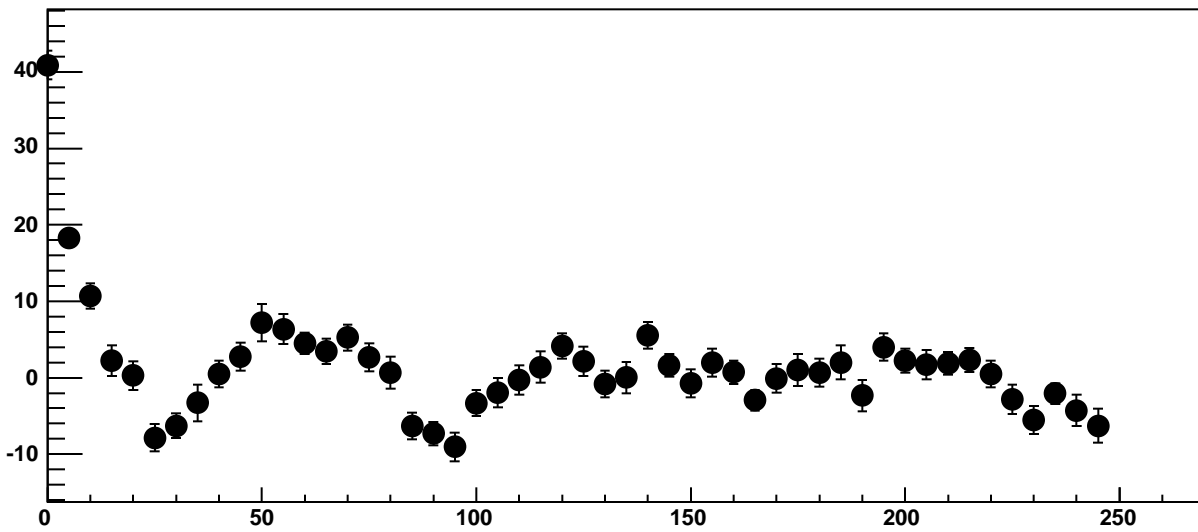


χ^2 / ndf	209.7 / 41
p0	-450 ± 0.6182
p1	91.99 ± 0.5228
p2	$-3.974\text{e}+07 \pm 8.802\text{e}+05$
p3	$2.087\text{e}+07 \pm 5.124\text{e}+05$
p4	1.836 ± 0.01714

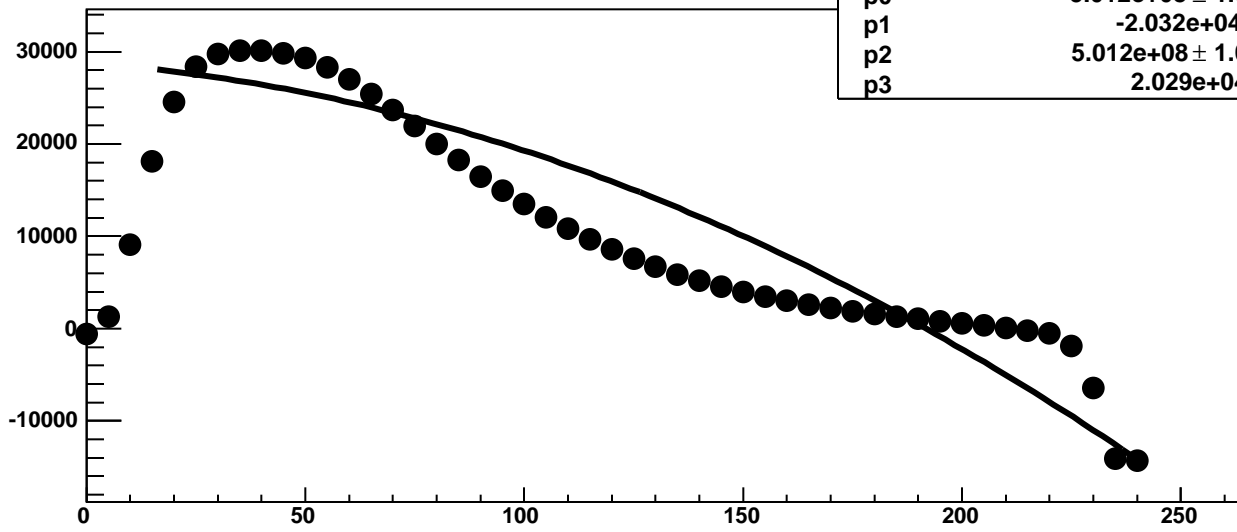
Chip 3, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

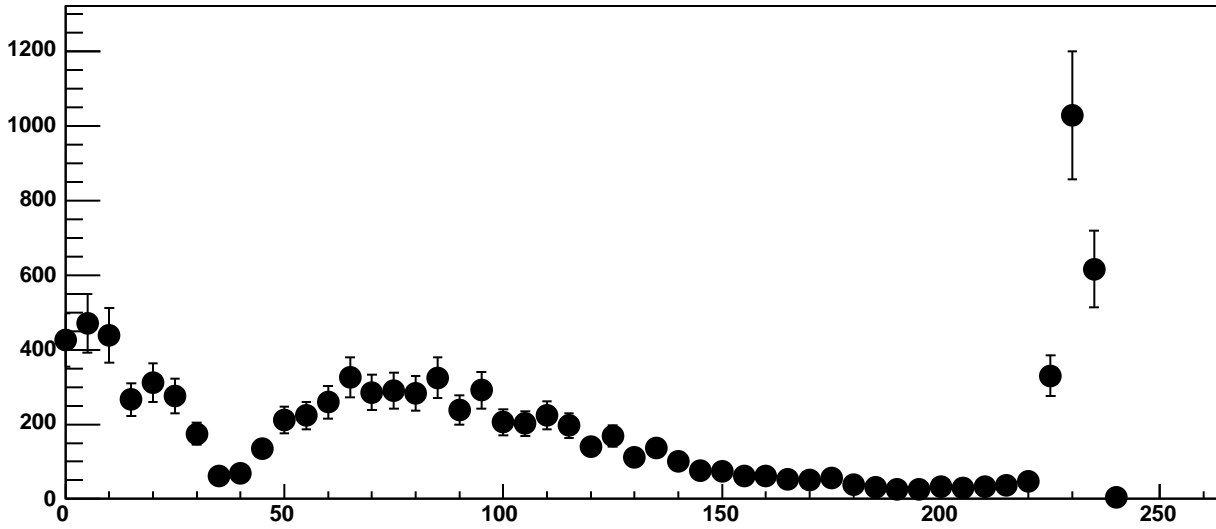


Chip 3, Channel 15, Enable 5!, DAC=4000, ADC Mean vs Hold

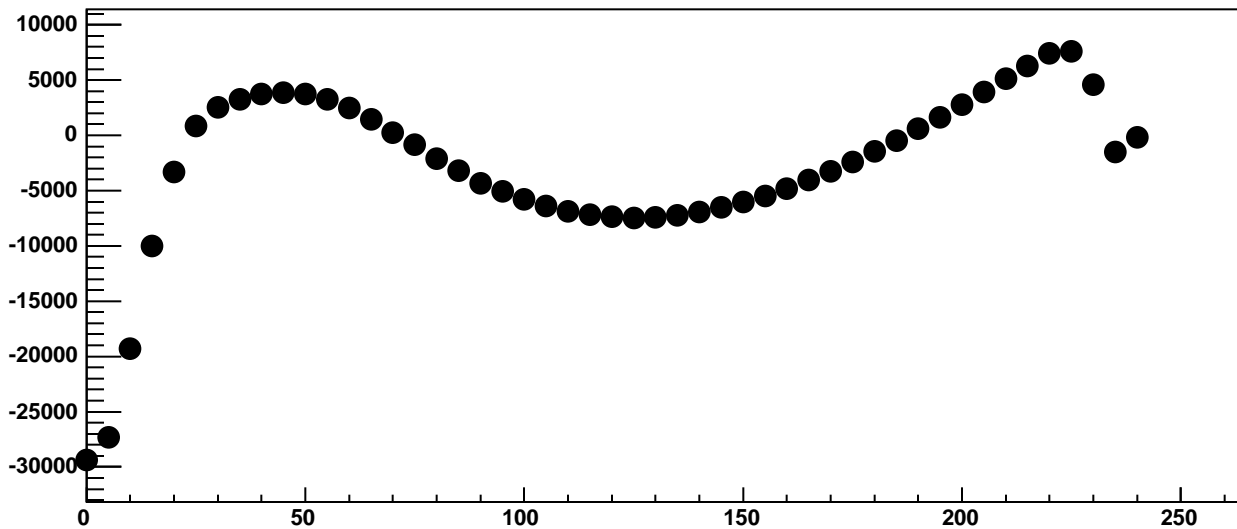


χ^2 / ndf	3.444e+06 / 42
p0	-5.012e+08 ± 1.018e+06
p1	-2.032e+04 ± 21.83
p2	5.012e+08 ± 1.018e+06
p3	2.029e+04 ± 21.8

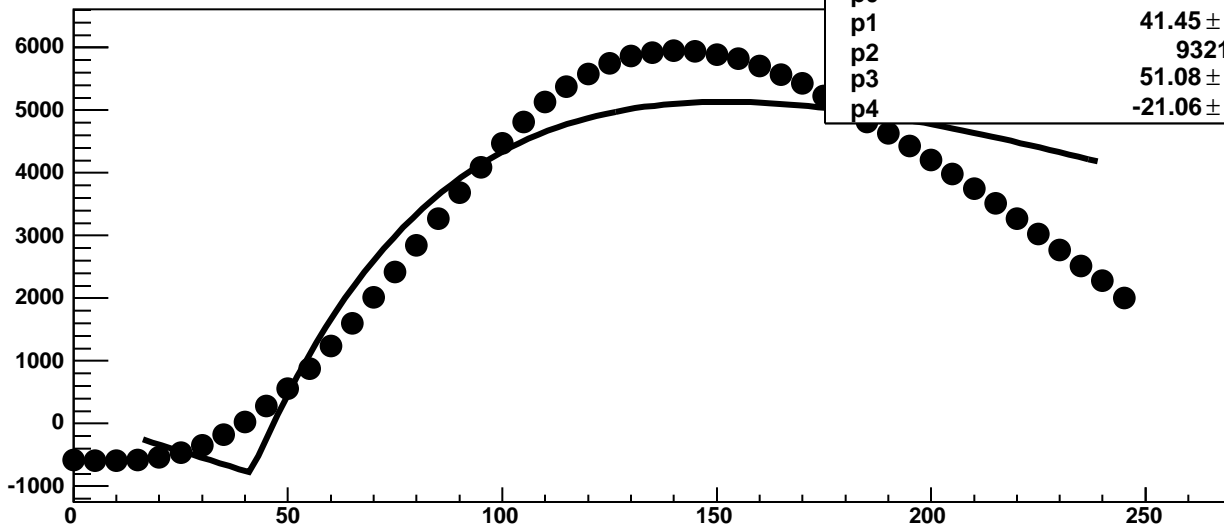
Chip 3, Channel 15, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 15, Enable 5!, DAC=4000, ADC Residuals vs Hold

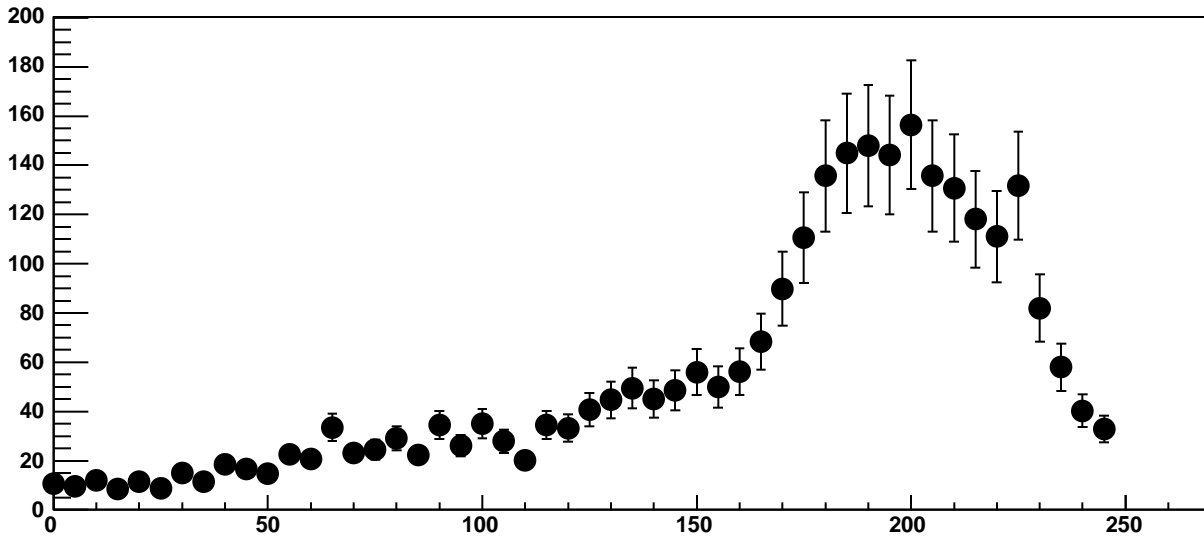


Chip 3, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

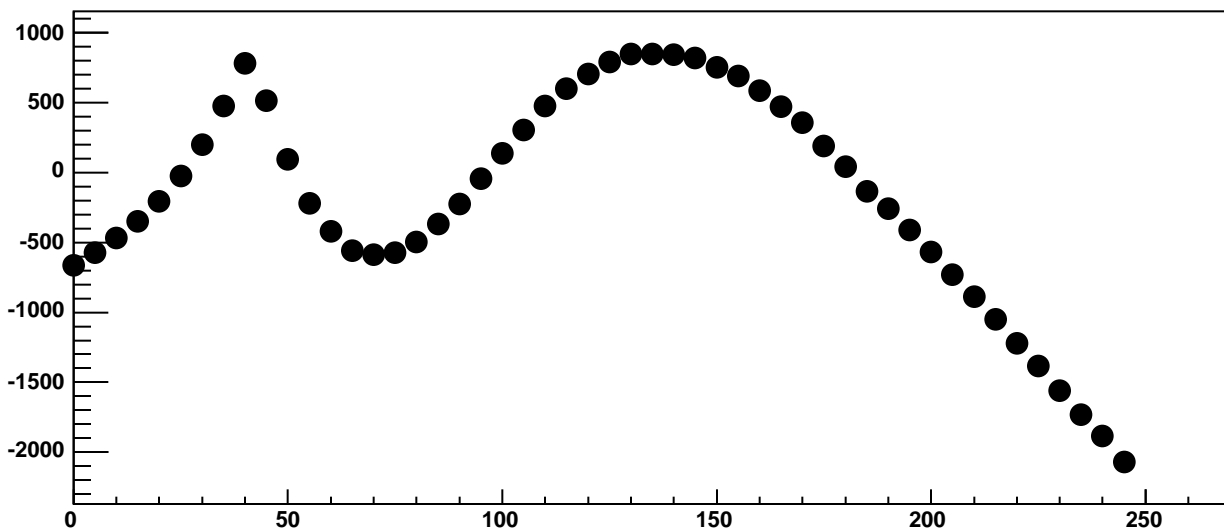


χ^2 / ndf	3.191e+05 / 41
p0	-786.8 ± 1.566
p1	41.45 ± 0.01954
p2	9321 ± 16.16
p3	51.08 ± 0.09234
p4	-21.06 ± 0.08126

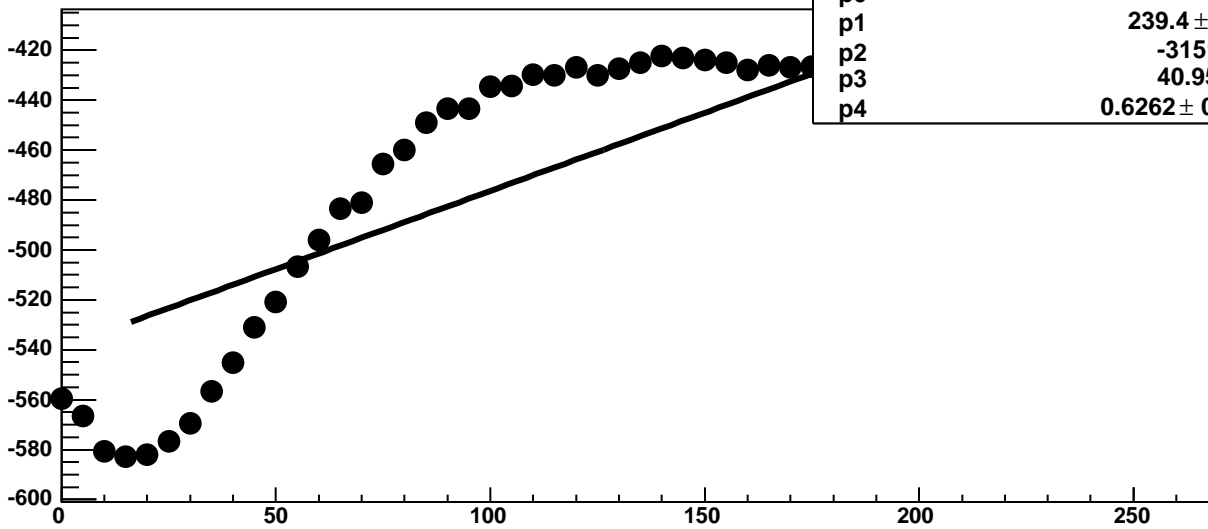
Chip 3, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



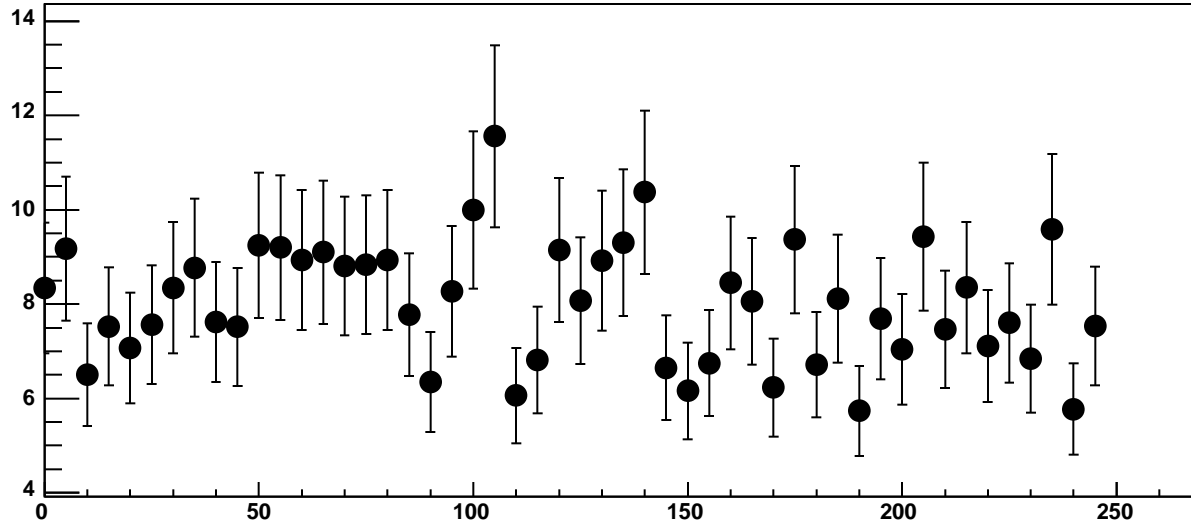
Chip 3, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold



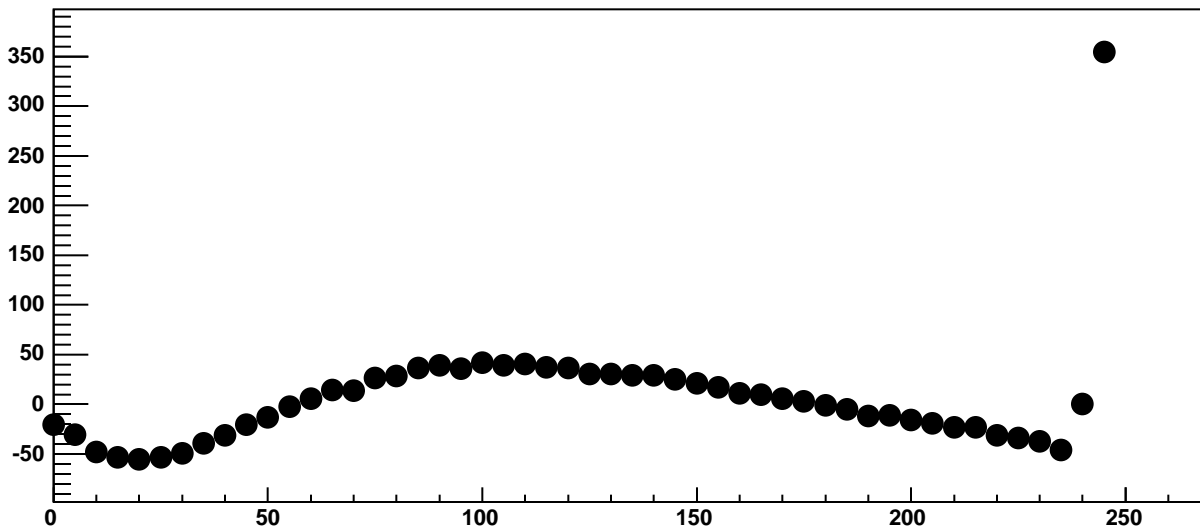
Chip 3, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold



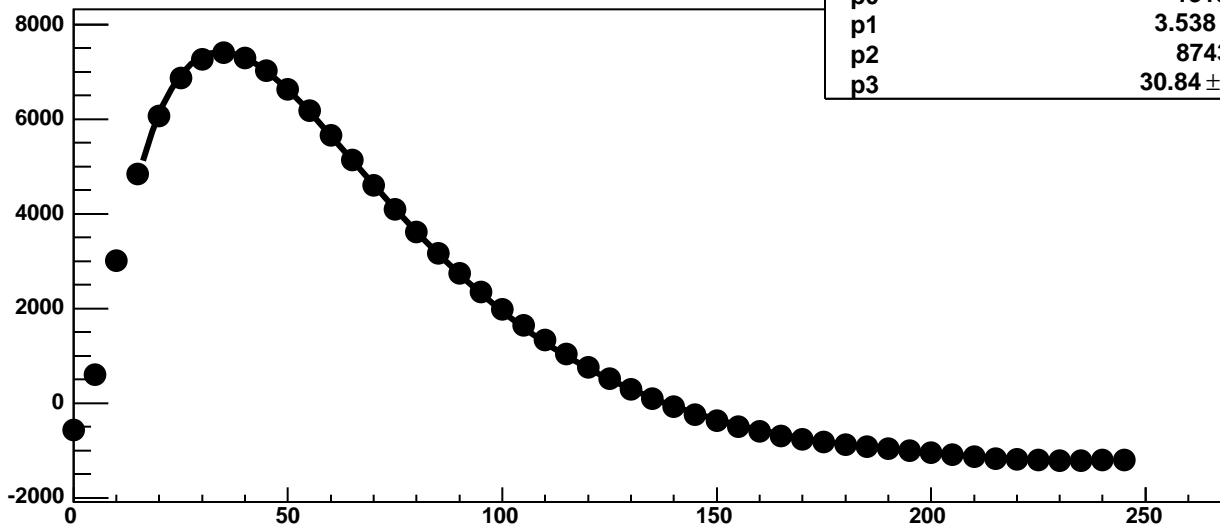
Chip 3, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold

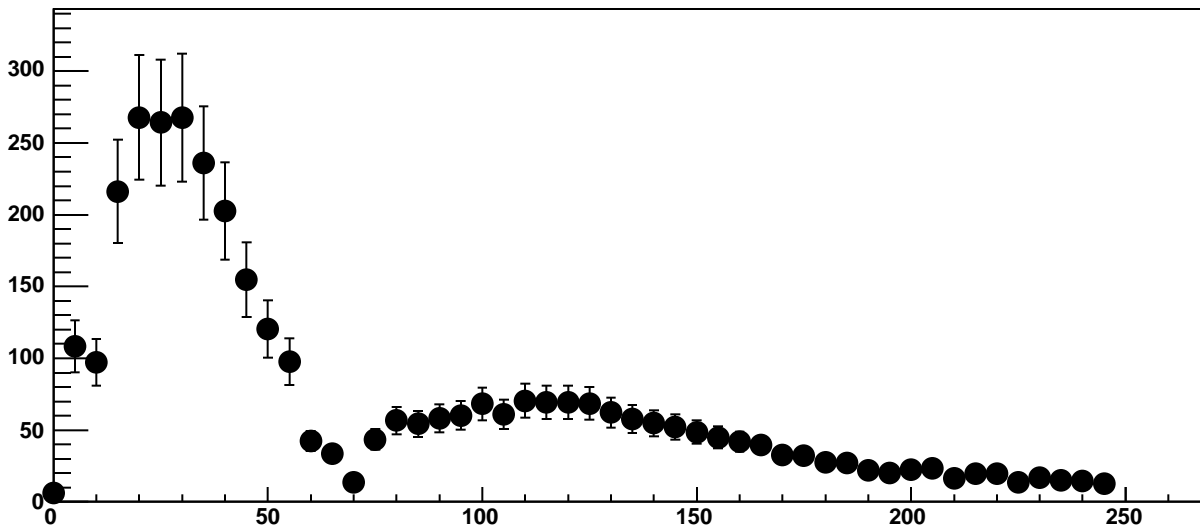


Chip 3, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold

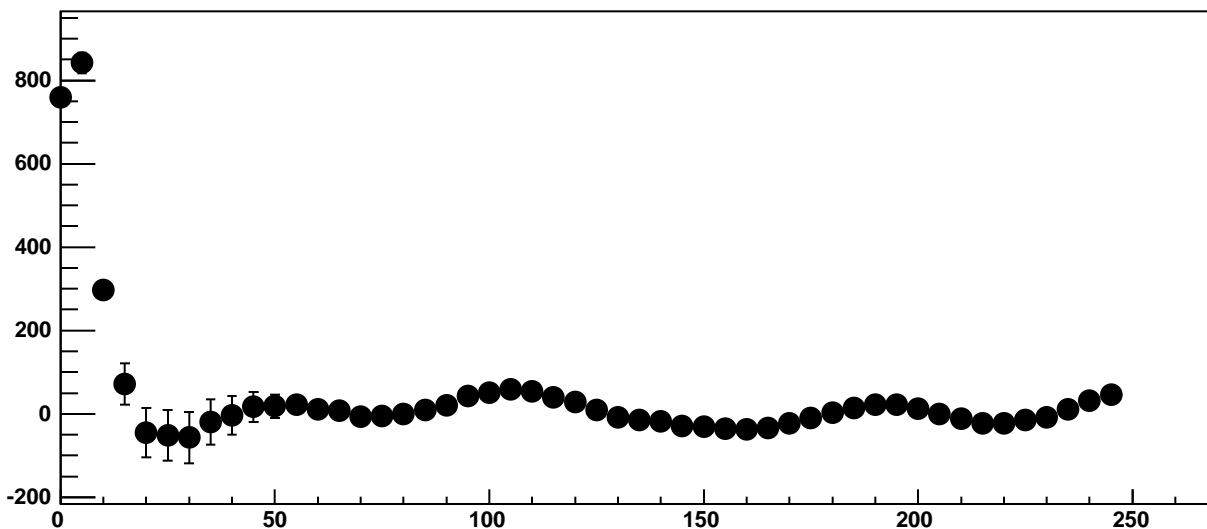


χ^2 / ndf	379.3 / 42
p0	-1319 ± 1.797
p1	3.538 ± 0.1083
p2	8743 ± 11.49
p3	30.84 ± 0.04487

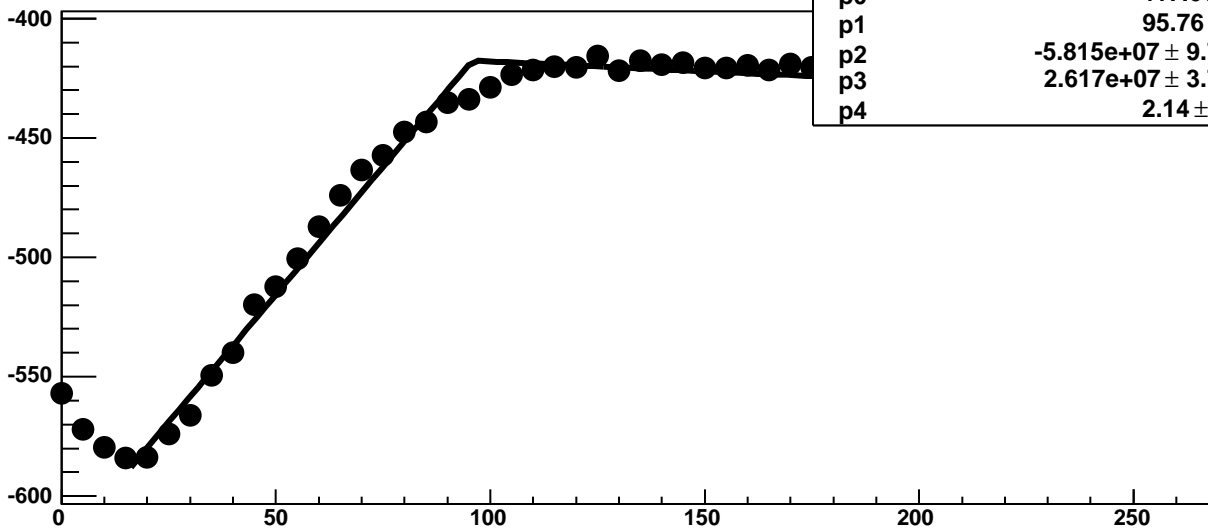
Chip 3, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold

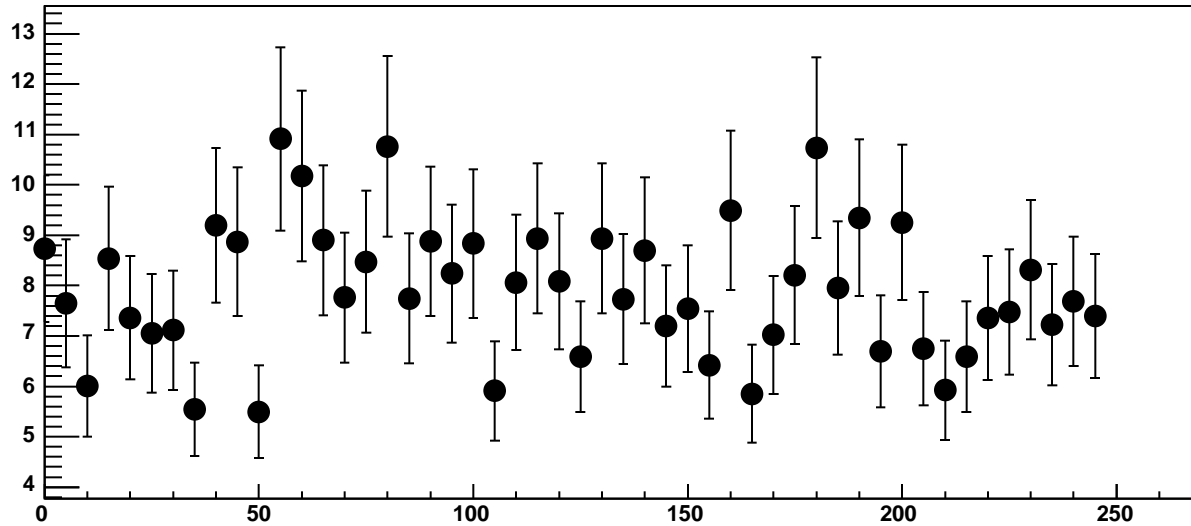


Chip 3, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

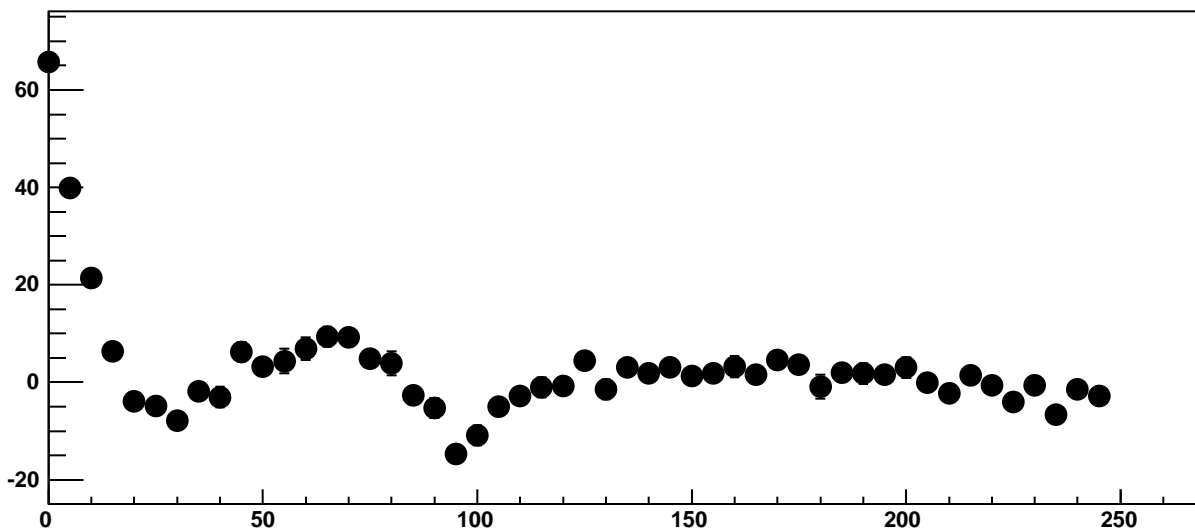


χ^2 / ndf	317.2 / 41
p0	-417.6 ± 0.6324
p1	95.76 ± 0.5053
p2	$-5.815\text{e}+07 \pm 9.717\text{e}+05$
p3	$2.617\text{e}+07 \pm 3.719\text{e}+05$
p4	2.14 ± 0.01813

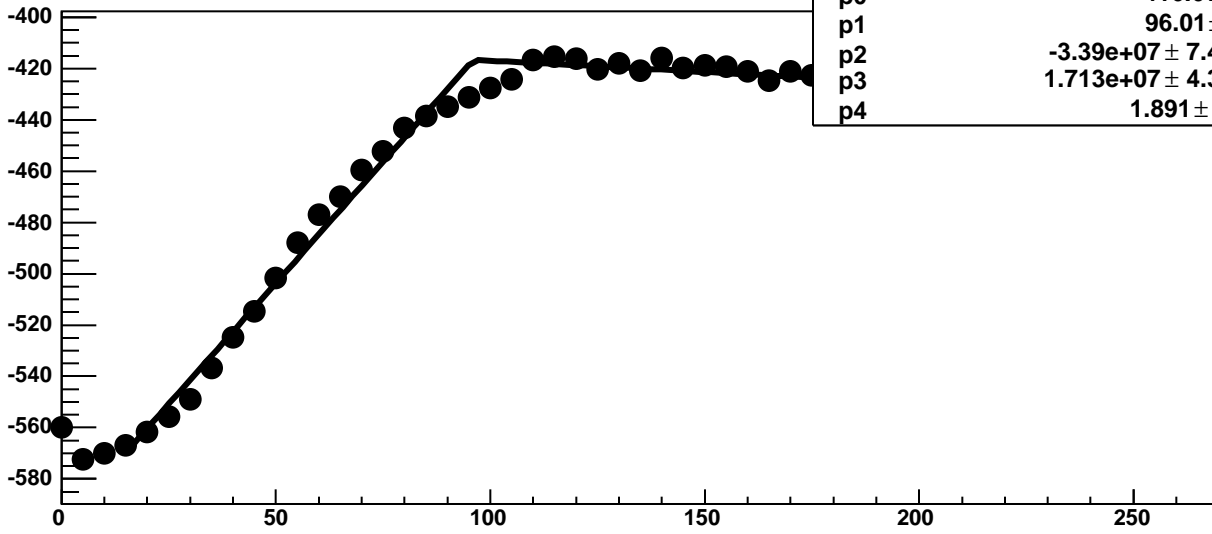
Chip 3, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

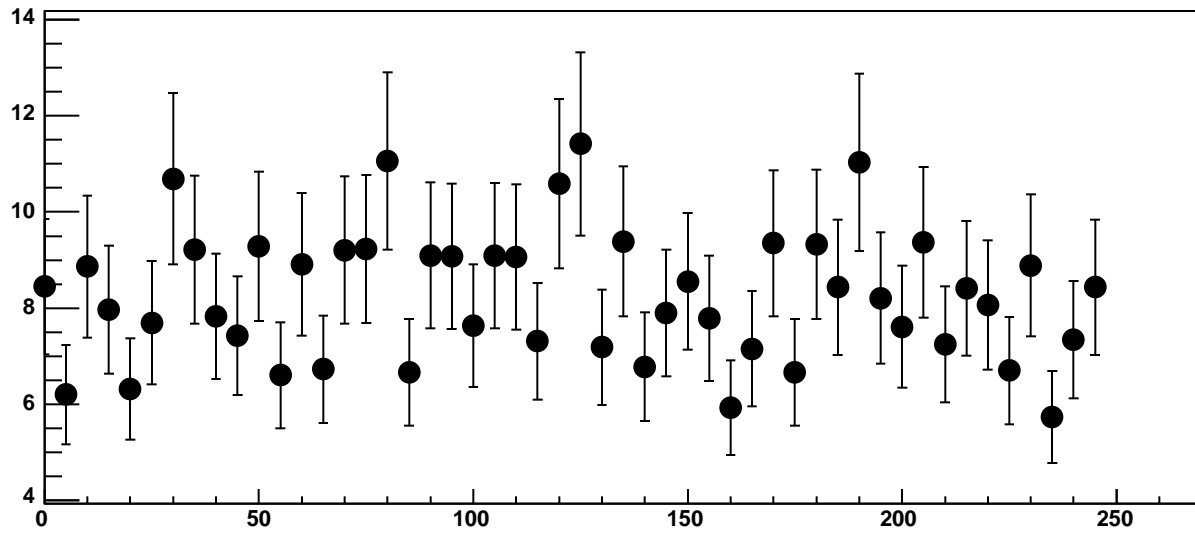


Chip 3, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

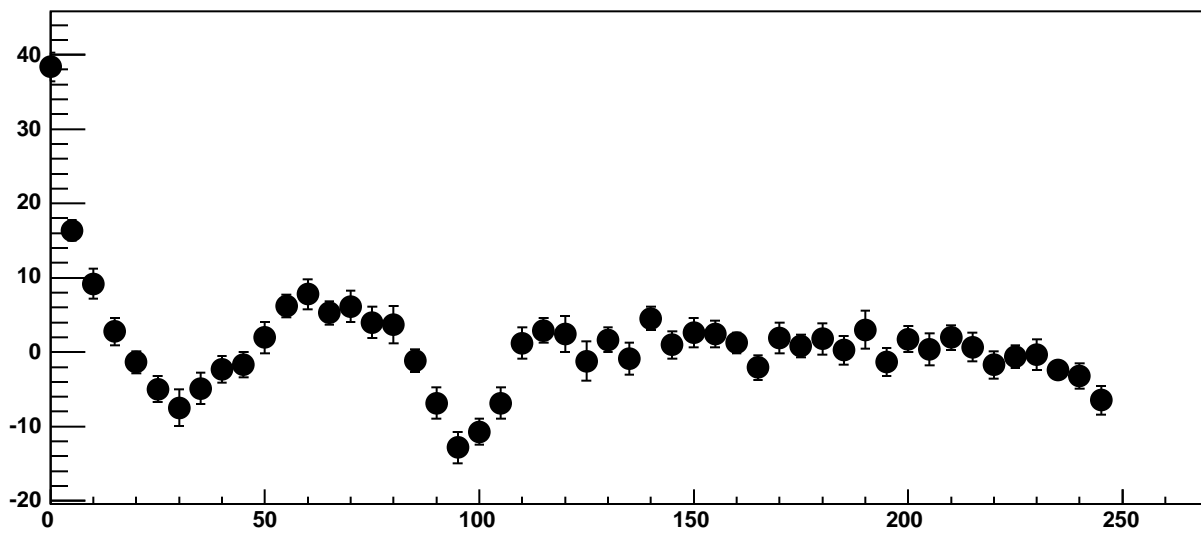


χ^2 / ndf	219 / 41
p0	-416.6 ± 0.6473
p1	96.01 ± 0.5357
p2	$-3.39\text{e}+07 \pm 7.488\text{e}+05$
p3	$1.713\text{e}+07 \pm 4.322\text{e}+05$
p4	1.891 ± 0.01678

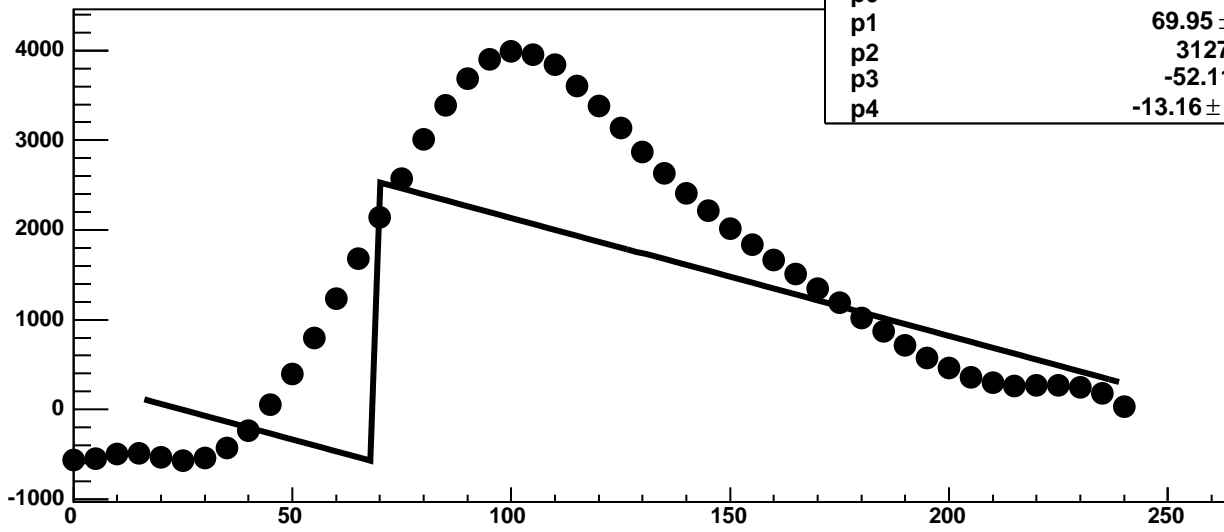
Chip 3, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

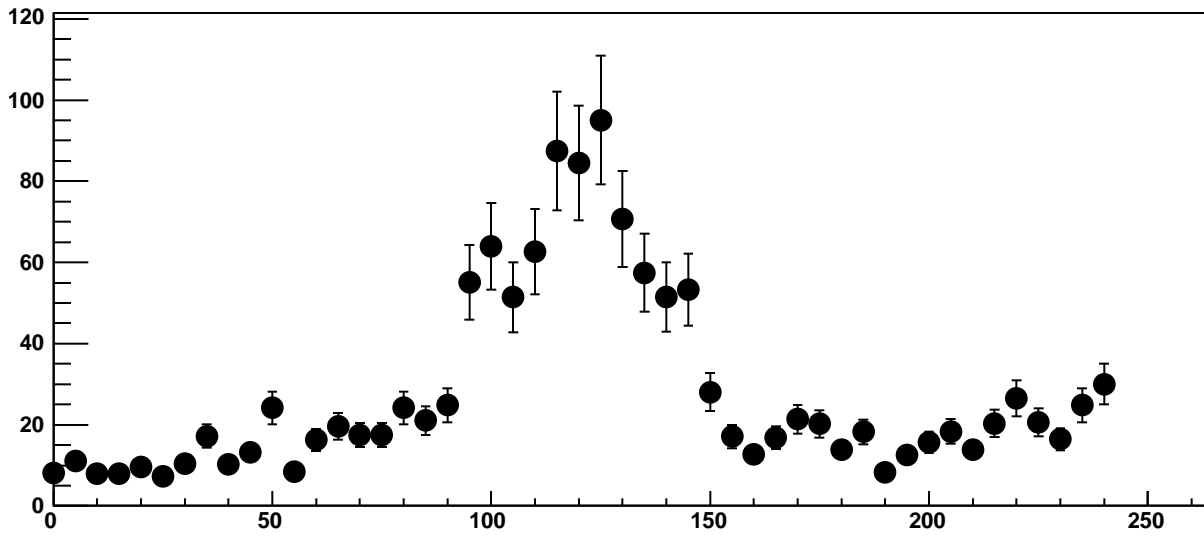


Chip 3, Channel 16, Enable 5, DAC=4000, ADC Mean vs Hold

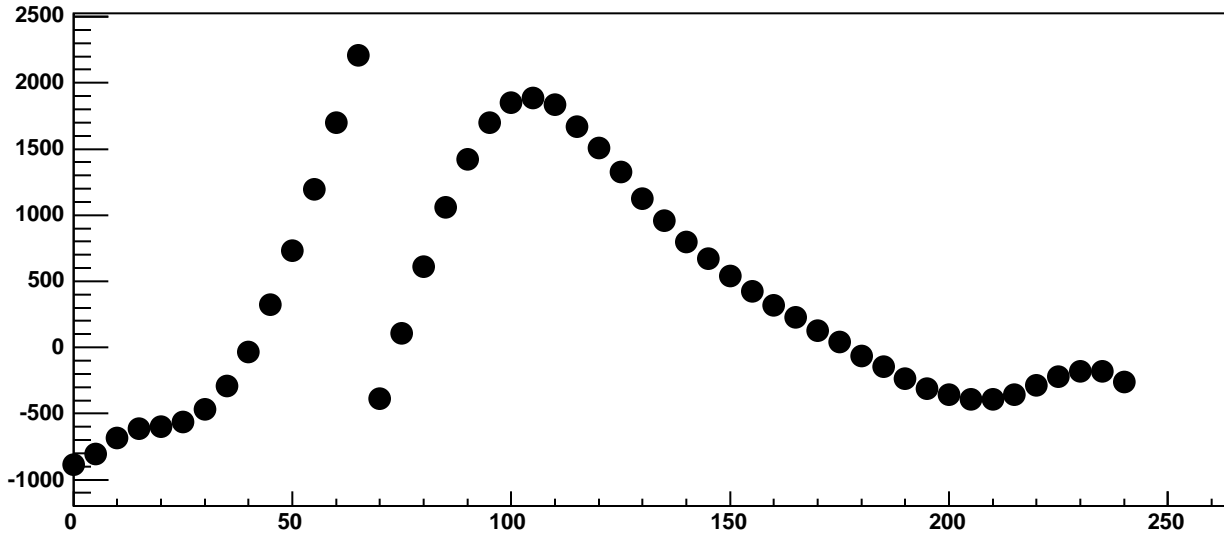


χ^2 / ndf	1.557e+06 / 41
p0	-595.9 ± 4.459
p1	69.95 ± 0.1274
p2	3127 ± 4.061
p3	-52.11 ± 1.552
p4	-13.16 ± 0.05568

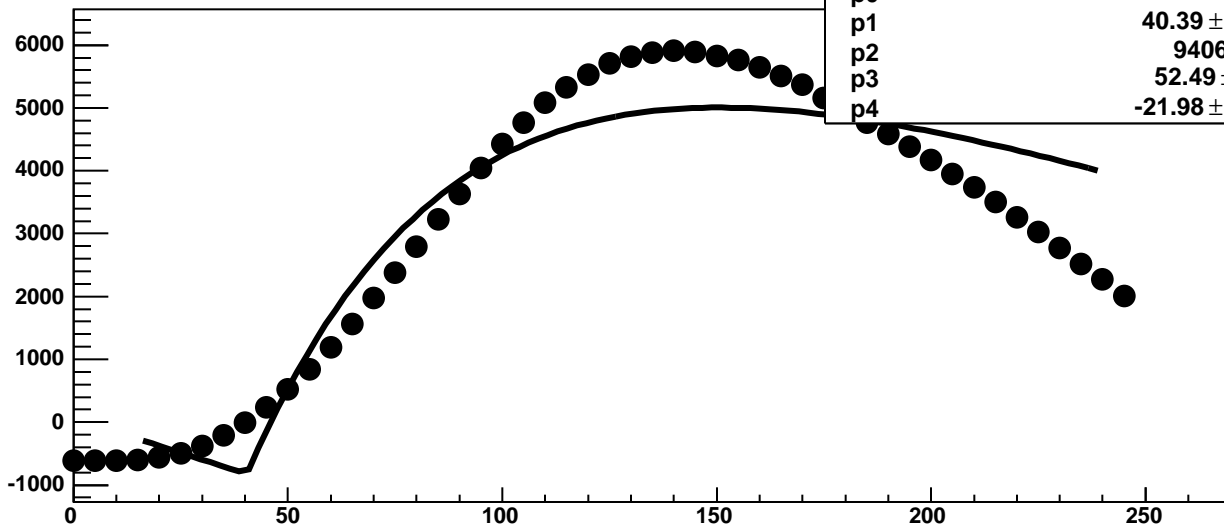
Chip 3, Channel 16, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 16, Enable 5, DAC=4000, ADC Residuals vs Hold

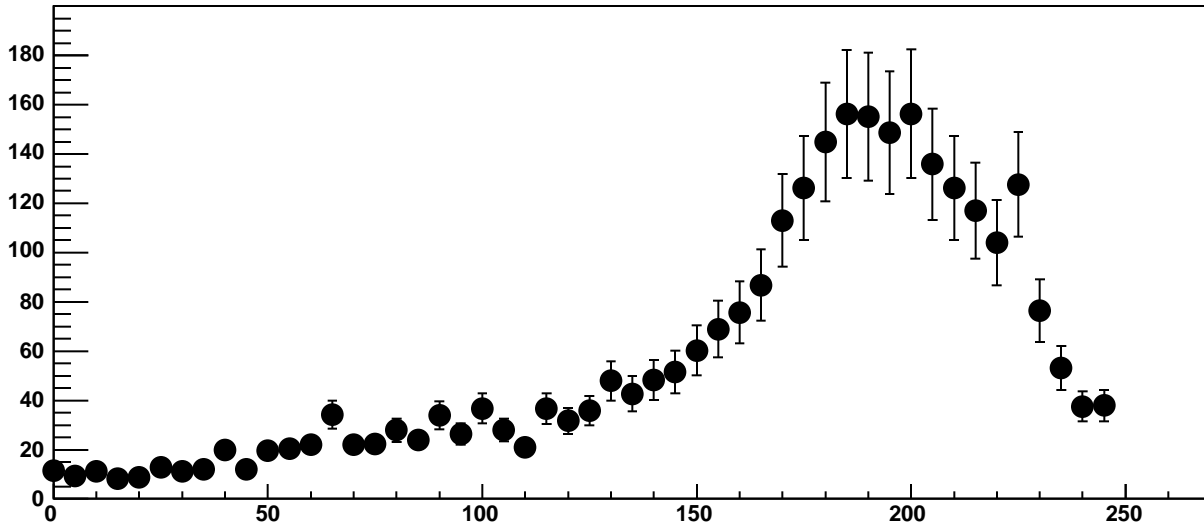


Chip 3, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

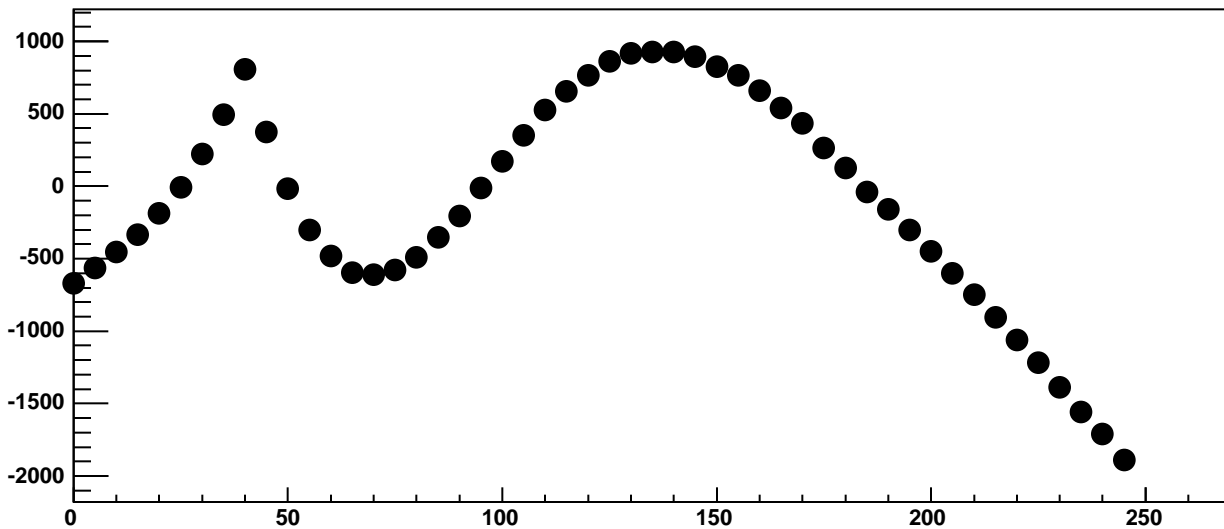


χ^2 / ndf	3.354e+05 / 41
p0	-825.5 ± 1.493
p1	40.39 ± 0.01861
p2	9406 ± 16.18
p3	52.49 ± 0.0932
p4	-21.98 ± 0.07931

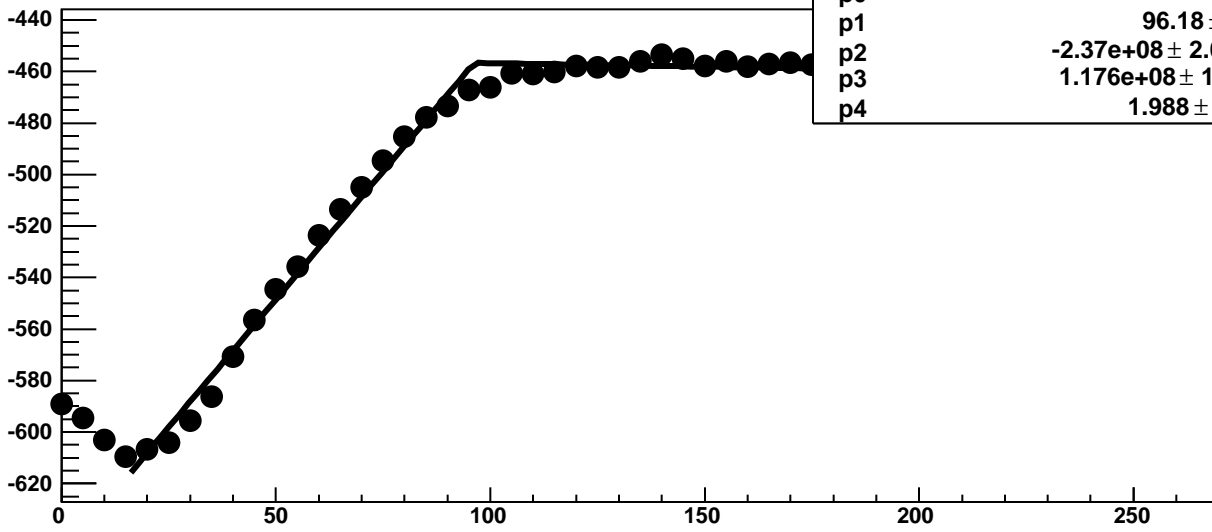
Chip 3, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

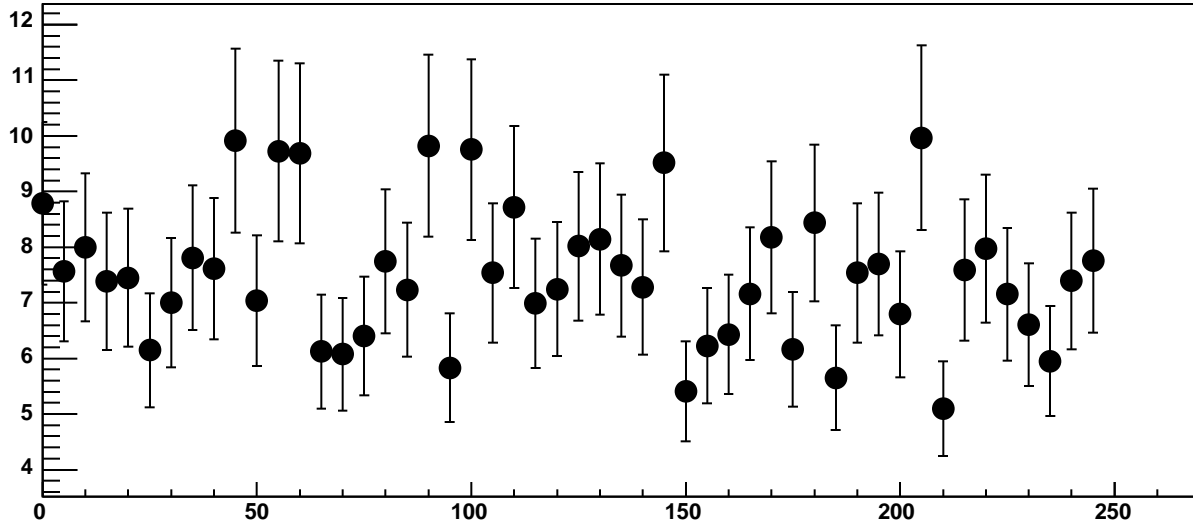


Chip 3, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

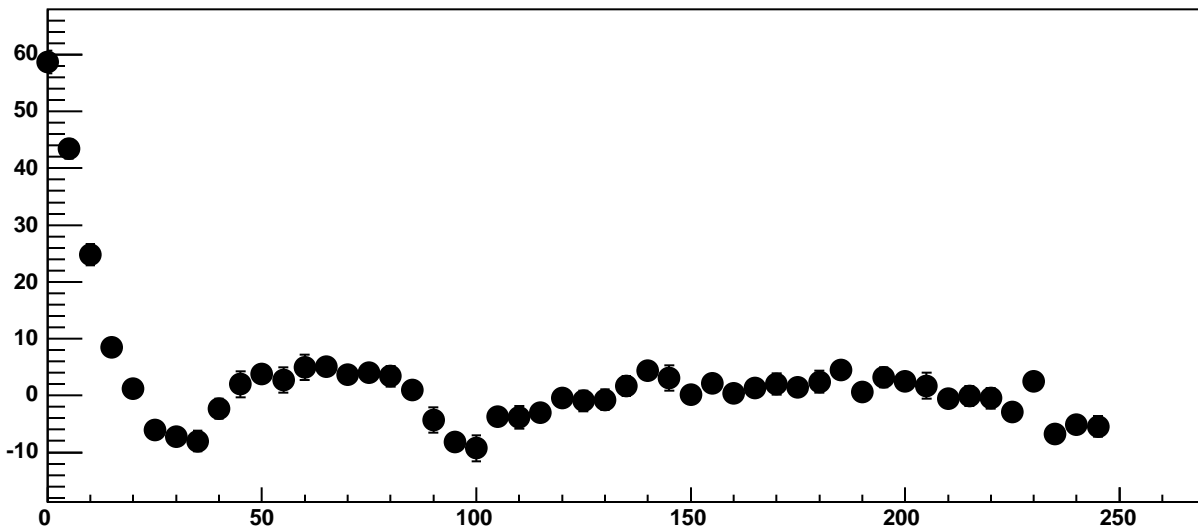


χ^2 / ndf	276.4 / 41
p0	-456.7 ± 0.6461
p1	96.18 ± 0.4953
p2	$-2.37\text{e}+08 \pm 2.098\text{e}+06$
p3	$1.176\text{e}+08 \pm 1.58\text{e}+05$
p4	1.988 ± 0.01597

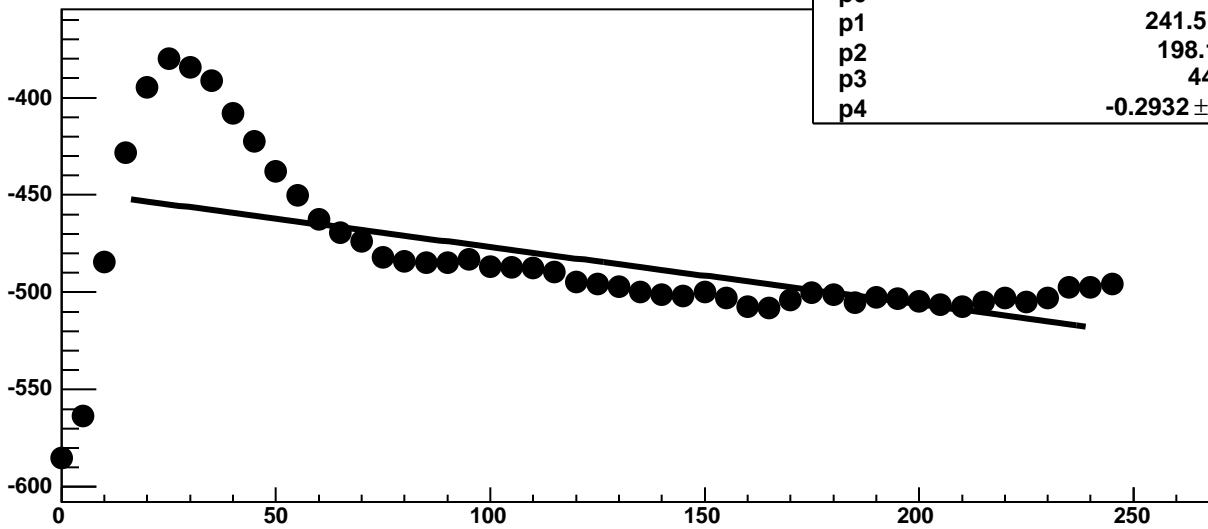
Chip 3, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

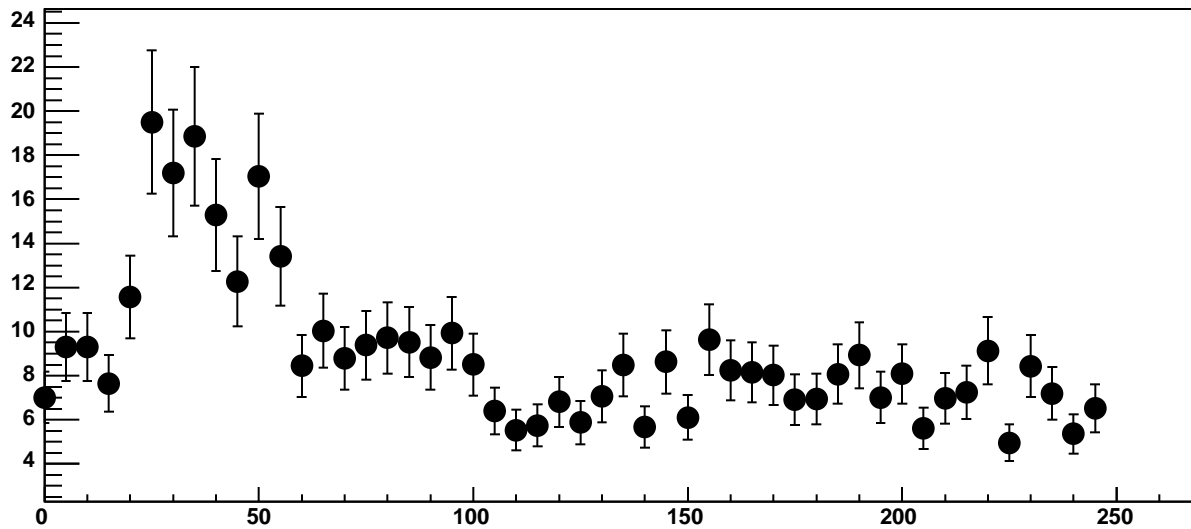


Chip 3, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold

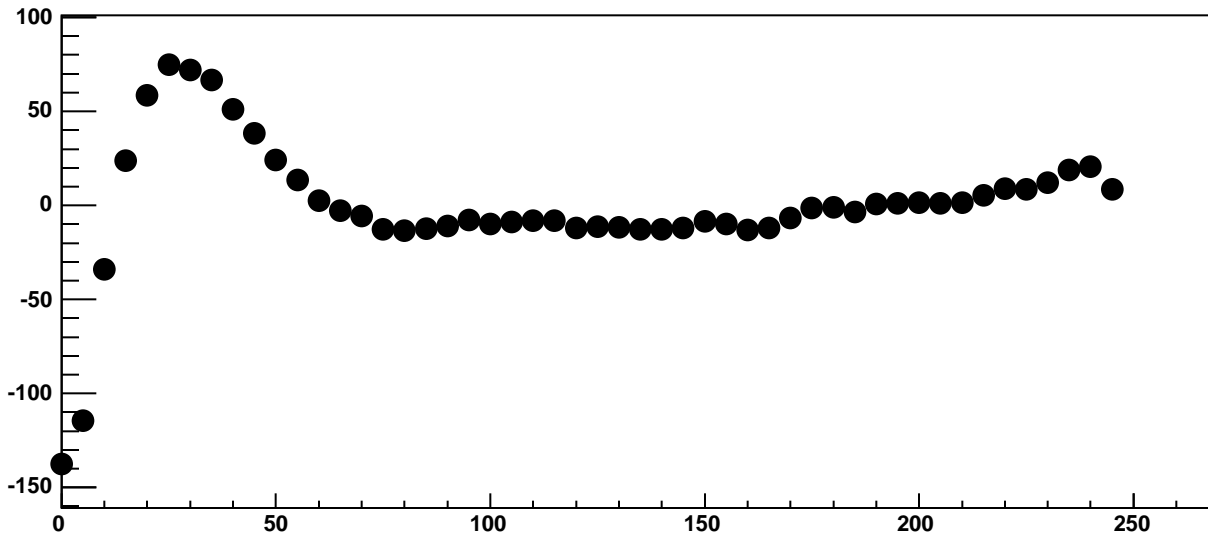


χ^2 / ndf	3348 / 41
p0	-518.3 ± 0.2636
p1	241.5 ± 0.8991
p2	198.1 ± 84.85
p3	44 ± 1.414
p4	-0.2932 ± 0.00238

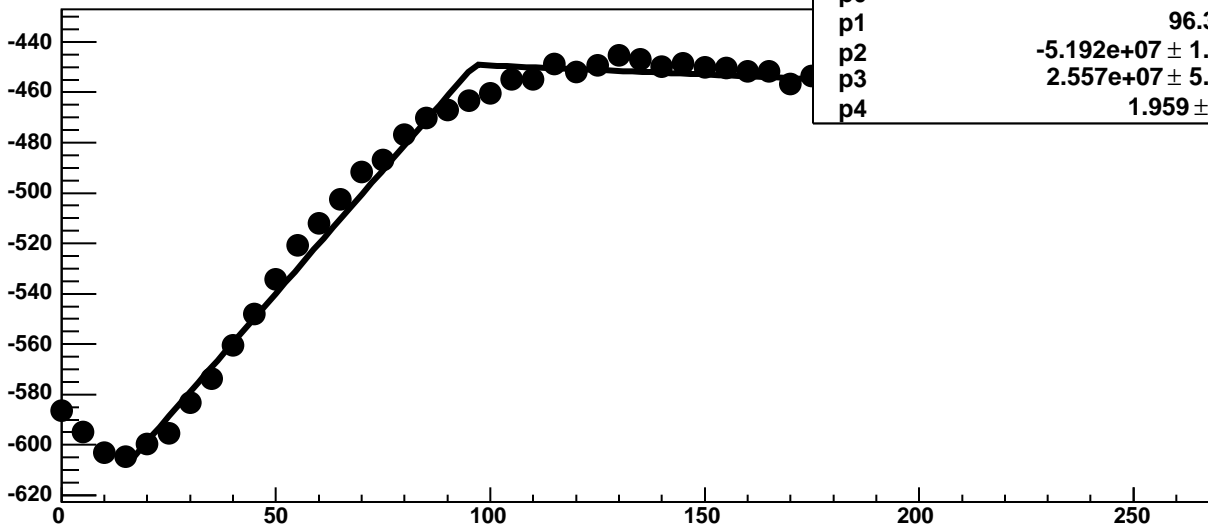
Chip 3, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold



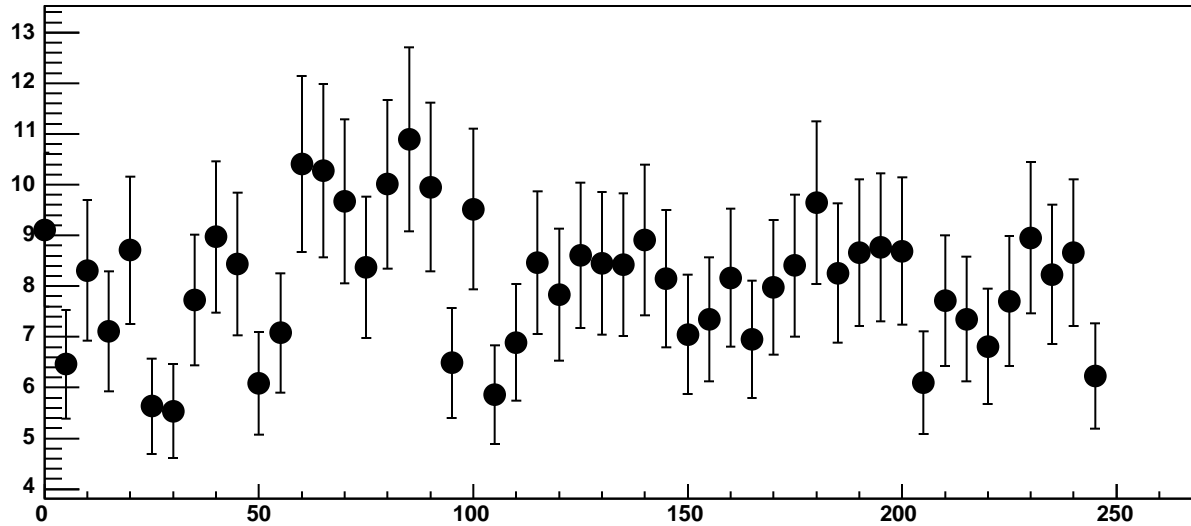
Chip 3, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold



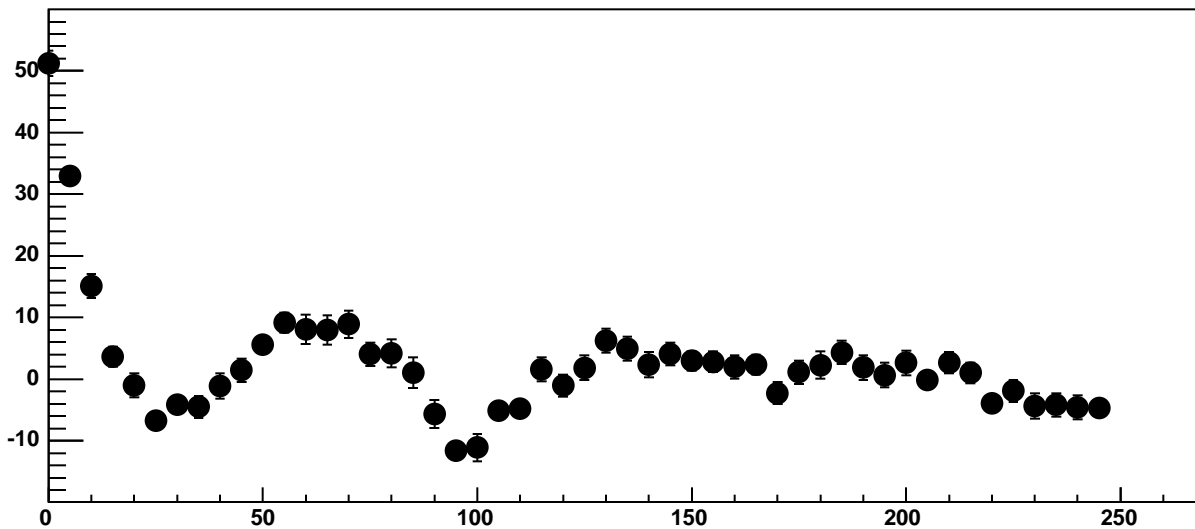
Chip 3, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



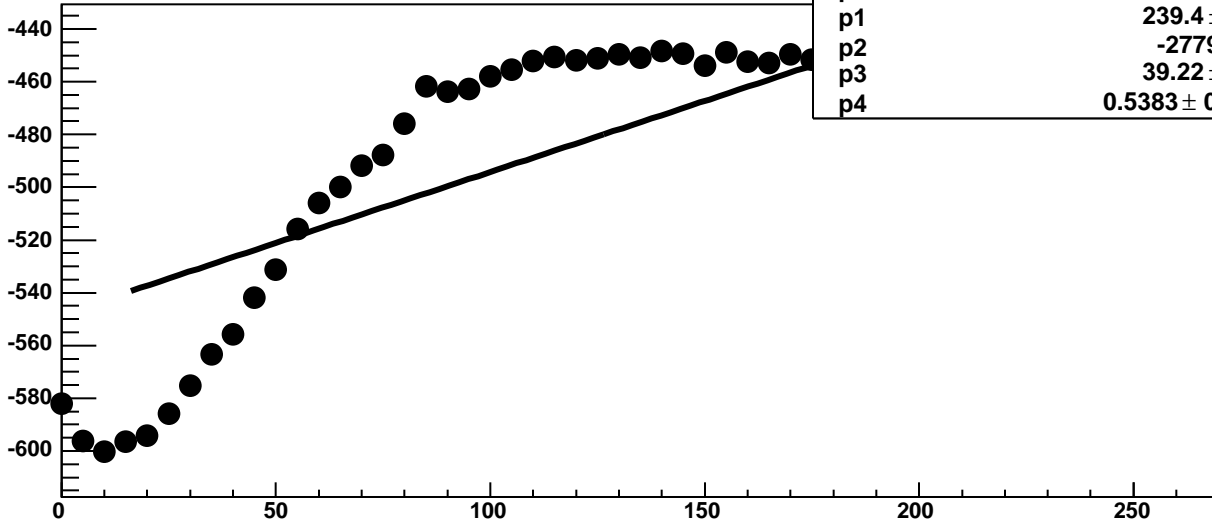
Chip 3, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold

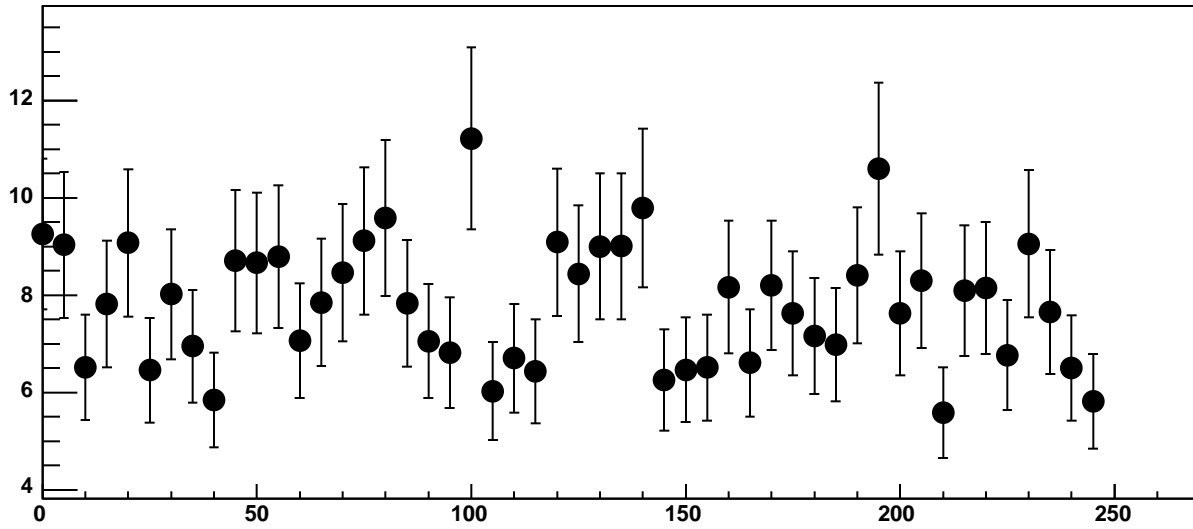


Chip 3, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold

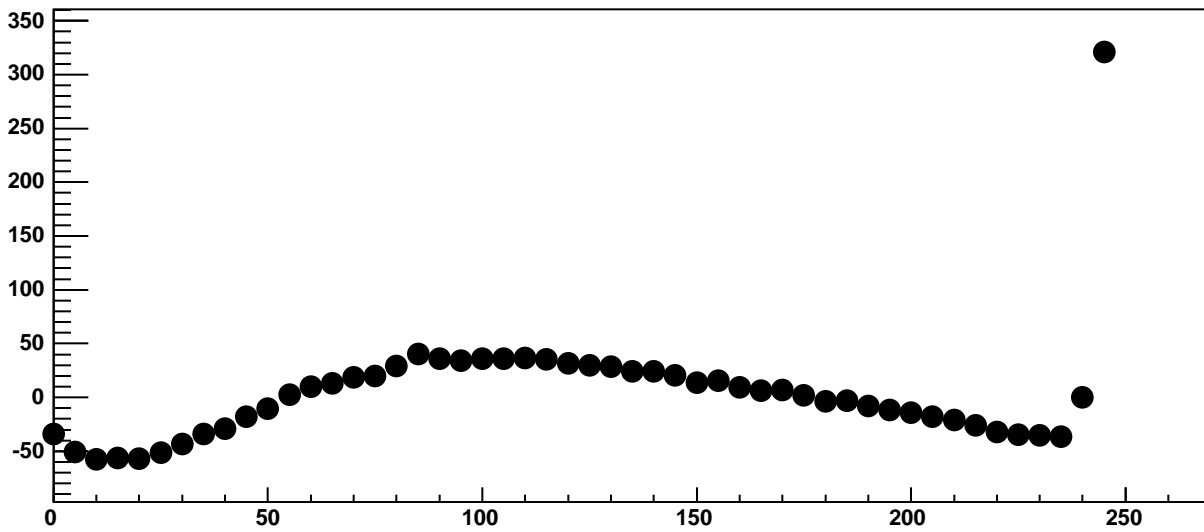


χ^2 / ndf	1.204e+04 / 41
p0	-419.1 ± 0.5189
p1	239.4 ± 0.0229
p2	-2779 ± 10.81
p3	39.22 ± 0.1174
p4	0.5383 ± 0.003992

Chip 3, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold

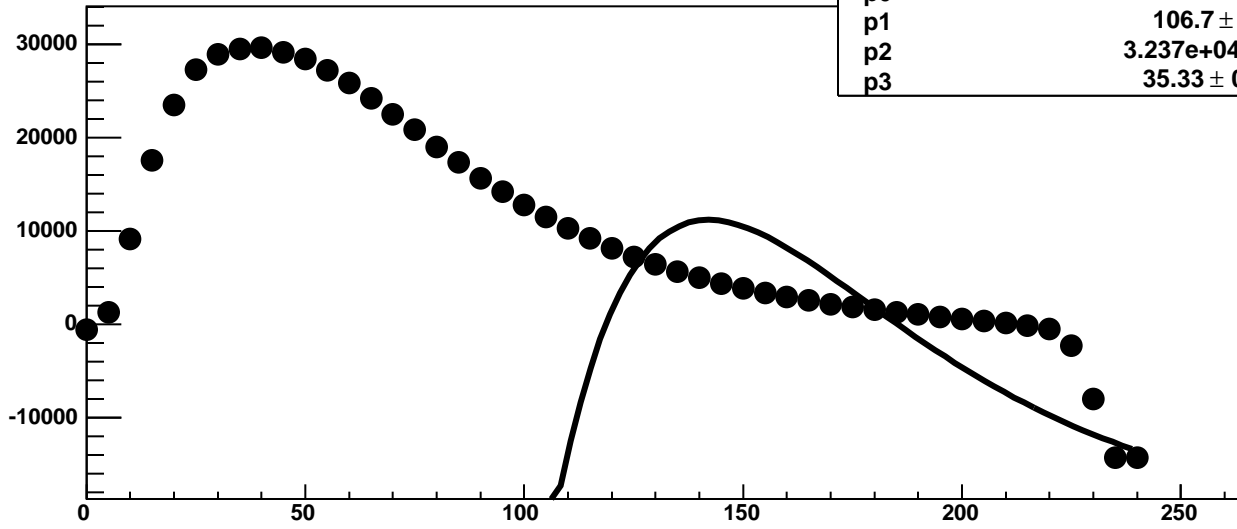


Chip 3, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

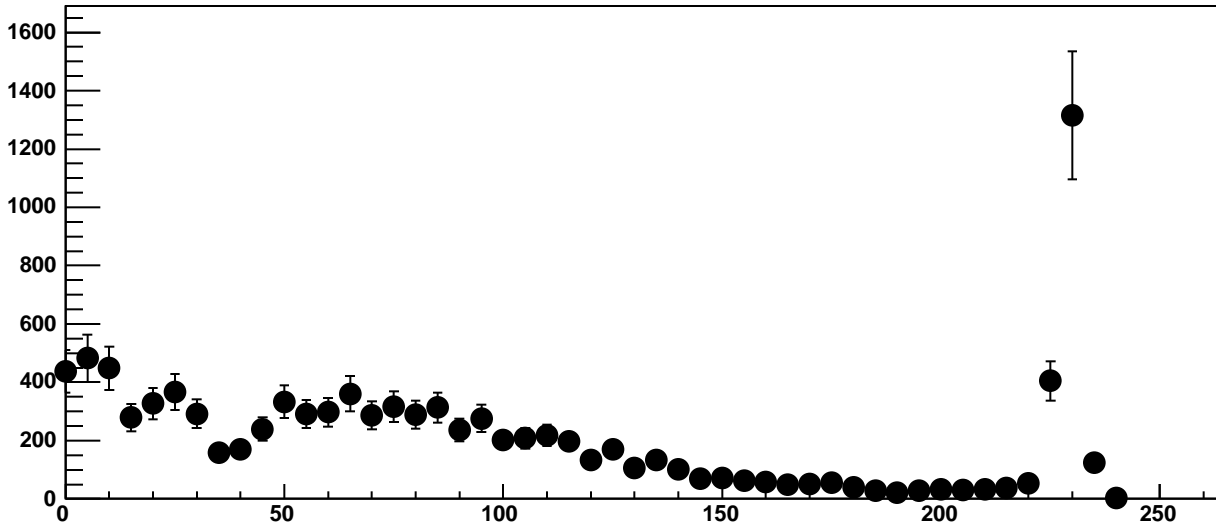


Chip 3, Channel 17, Enable 5!, DAC=4000, ADC Mean vs Hold

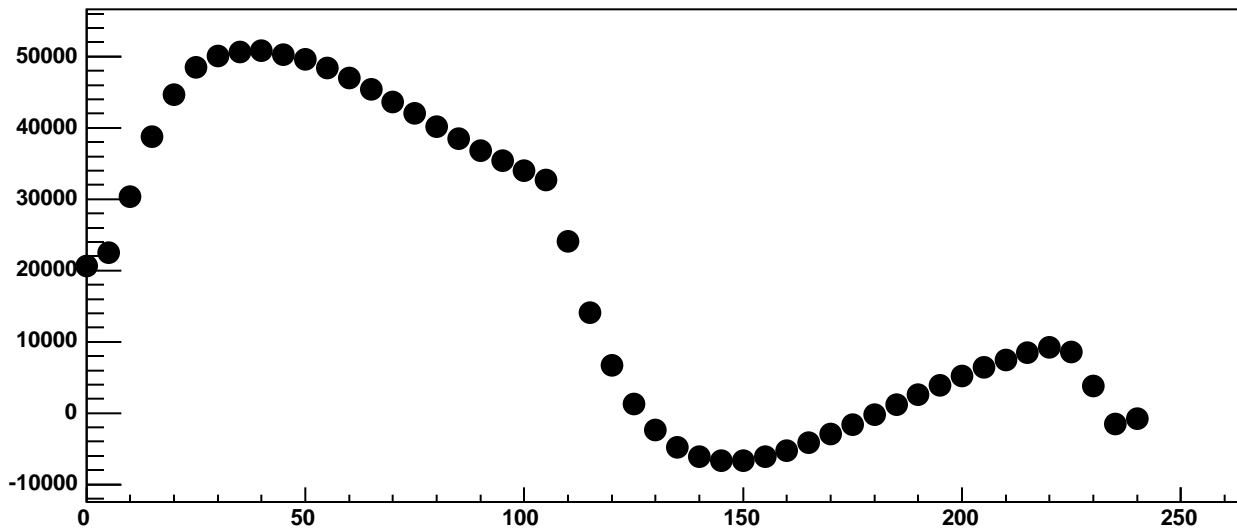
χ^2 / ndf	1.743e+07 / 42
p0	-2.118e+04 ± 4.42
p1	106.7 ± 0.01298
p2	3.237e+04 ± 6.125
p3	35.33 ± 0.007691



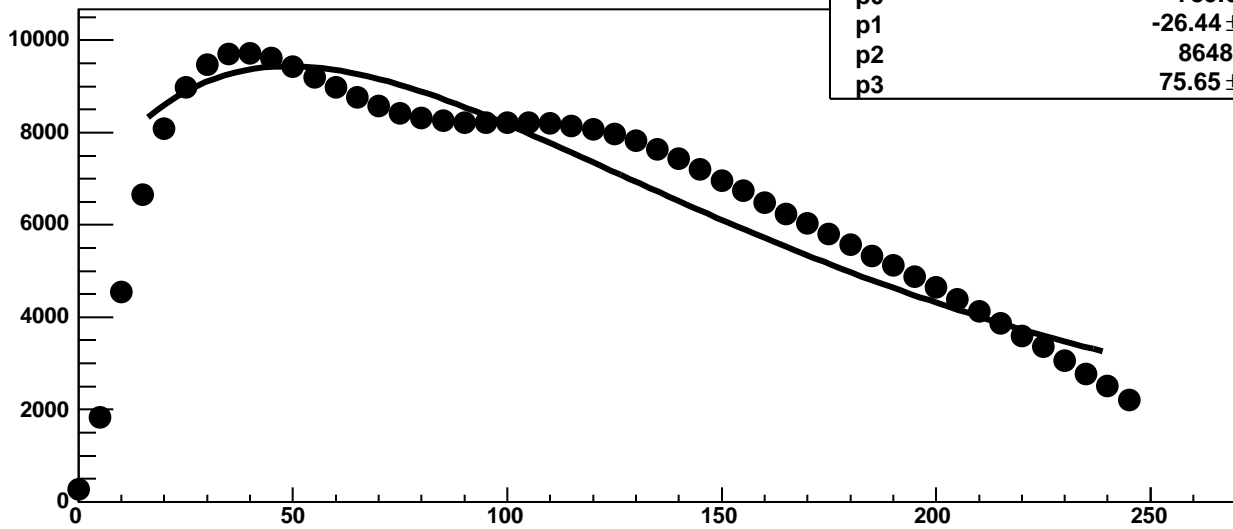
Chip 3, Channel 17, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 3, Channel 17, Enable 5!, DAC=4000, ADC Residuals vs Hold

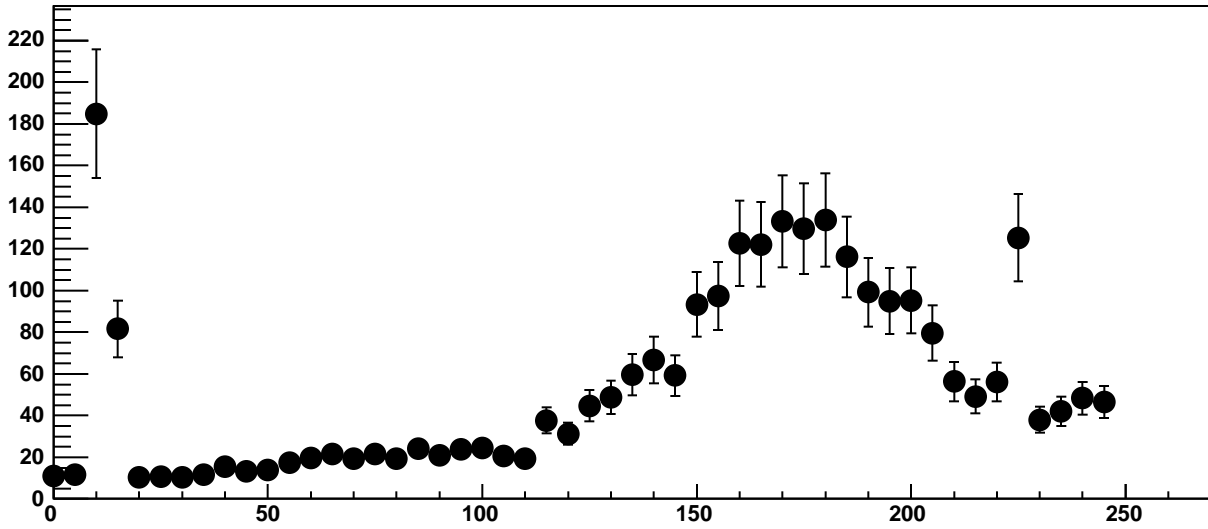


Chip 4, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold

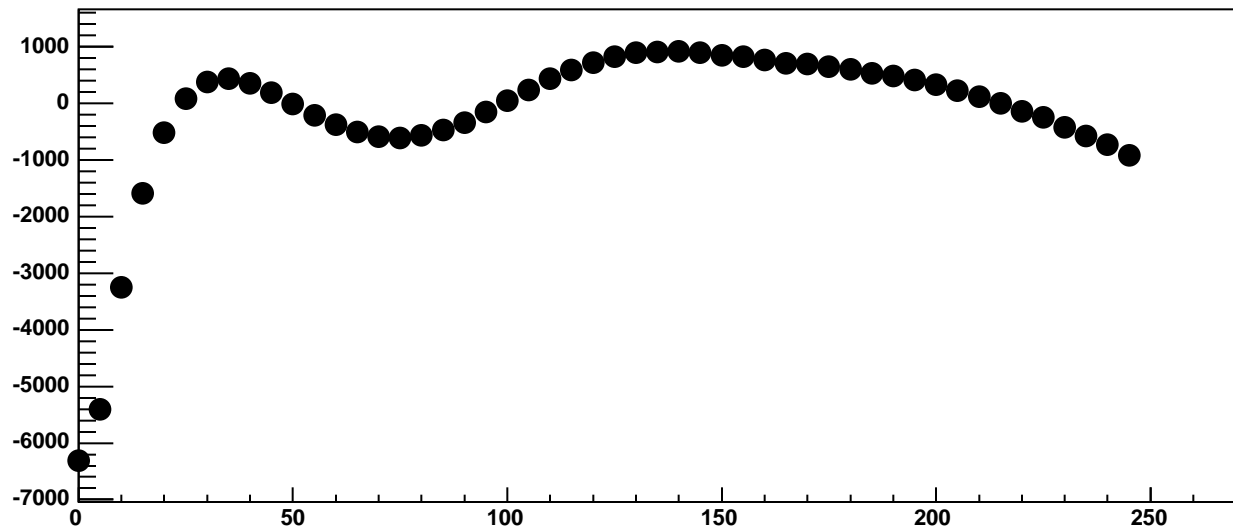


χ^2 / ndf	2.739e+05 / 42
p0	789.6 ± 19.11
p1	-26.44 ± 0.1629
p2	8648 ± 18.37
p3	75.65 ± 0.1677

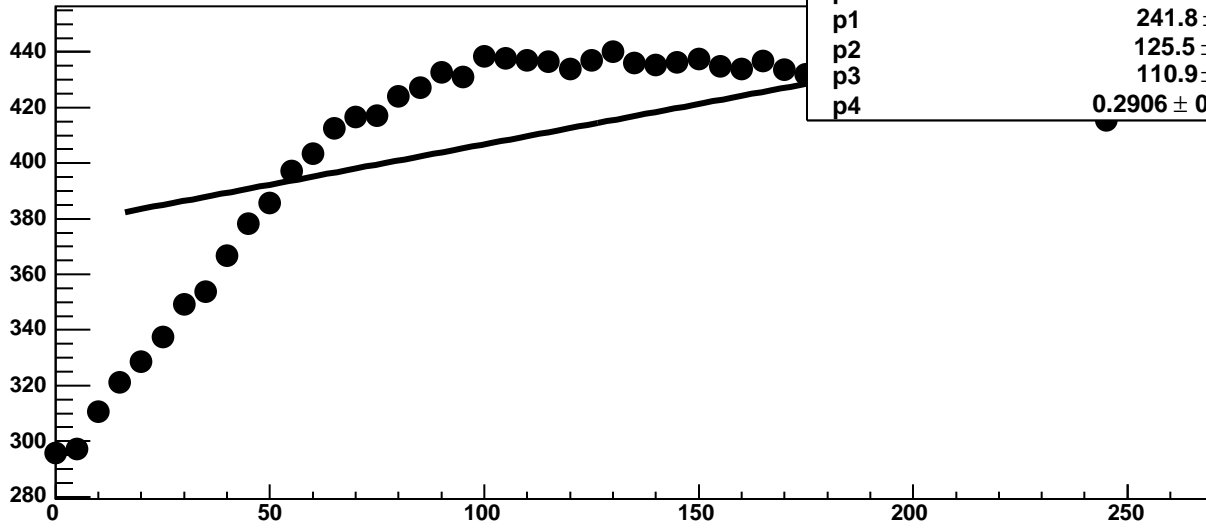
Chip 4, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold

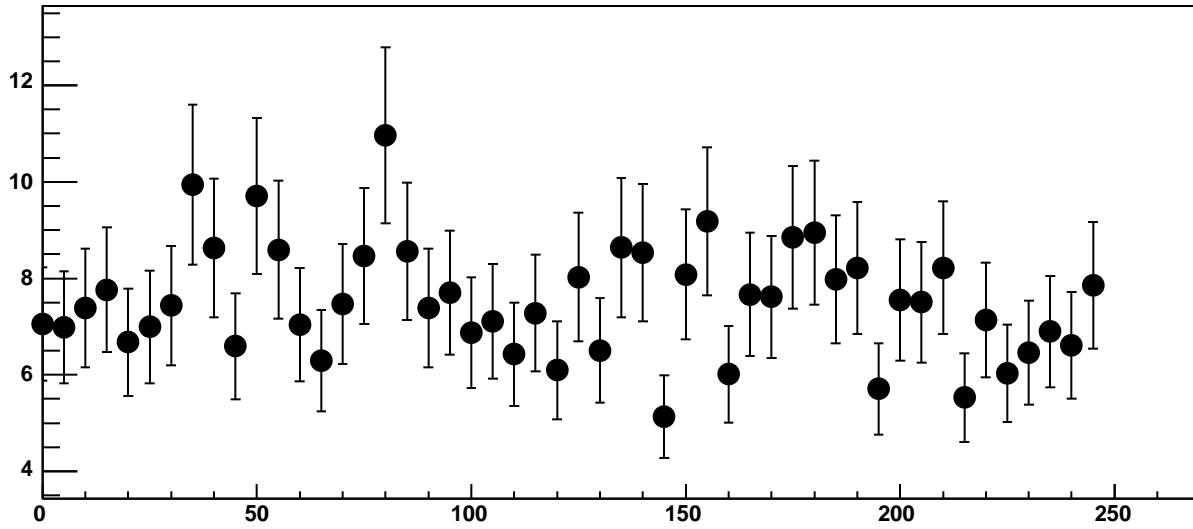


Chip 4, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

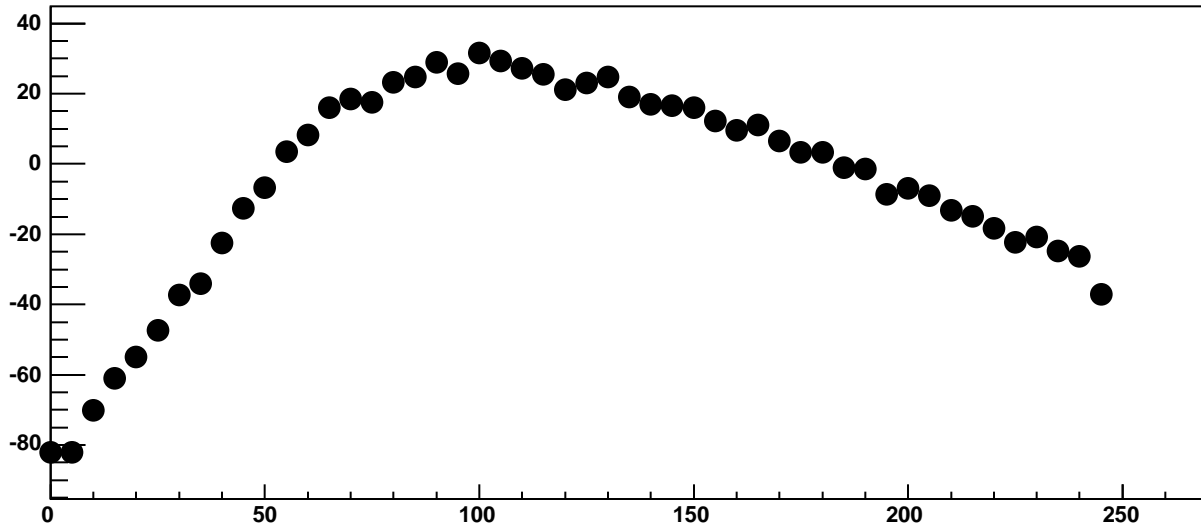


χ^2 / ndf	9284 / 41
p0	448 ± 0.4986
p1	241.8 ± 0.4612
p2	125.5 ± 0.2905
p3	110.9 ± 0.3095
p4	0.2906 ± 0.003667

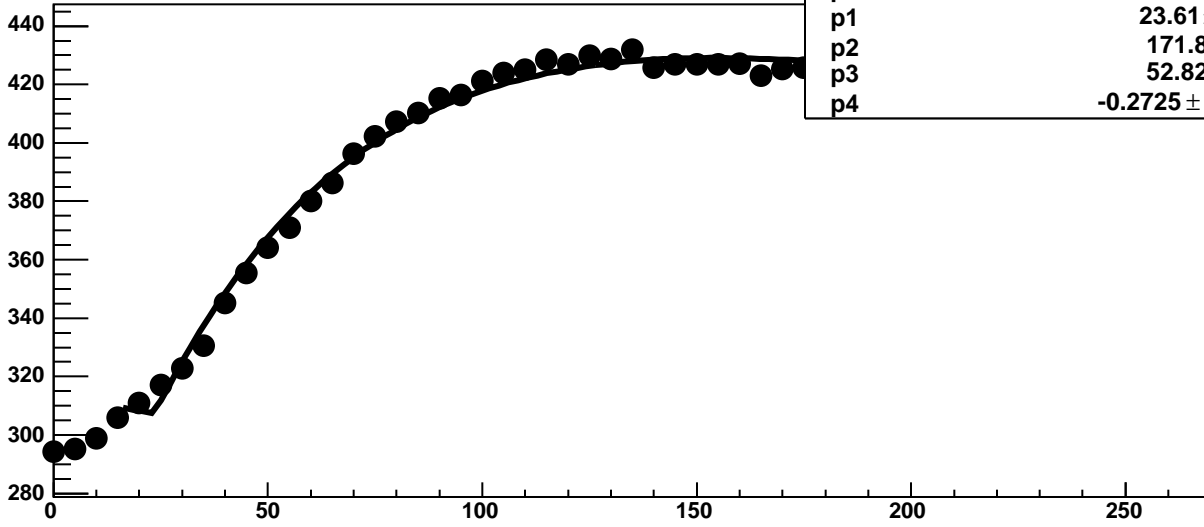
Chip 4, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold

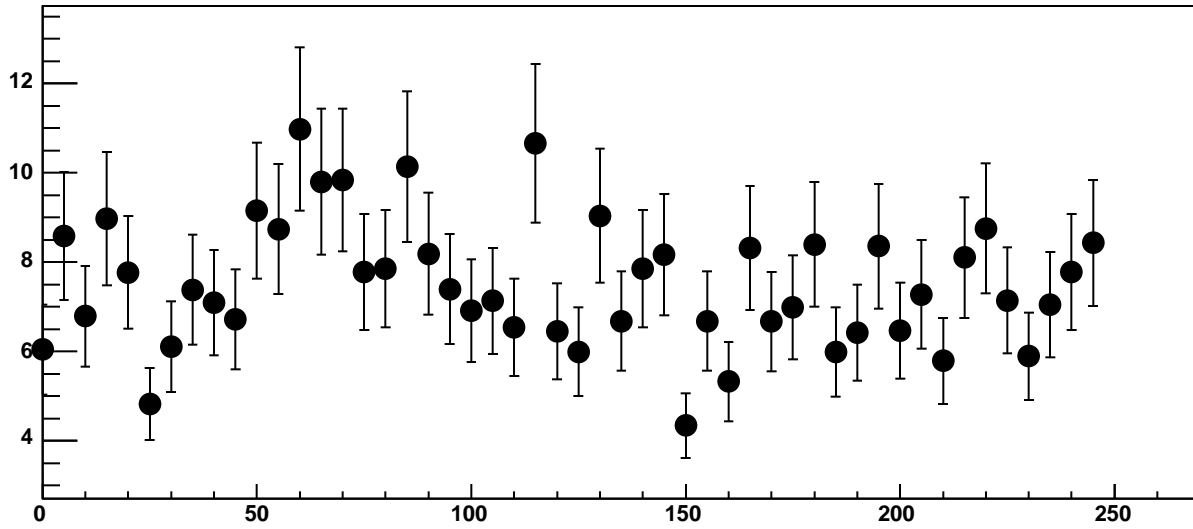


Chip 4, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

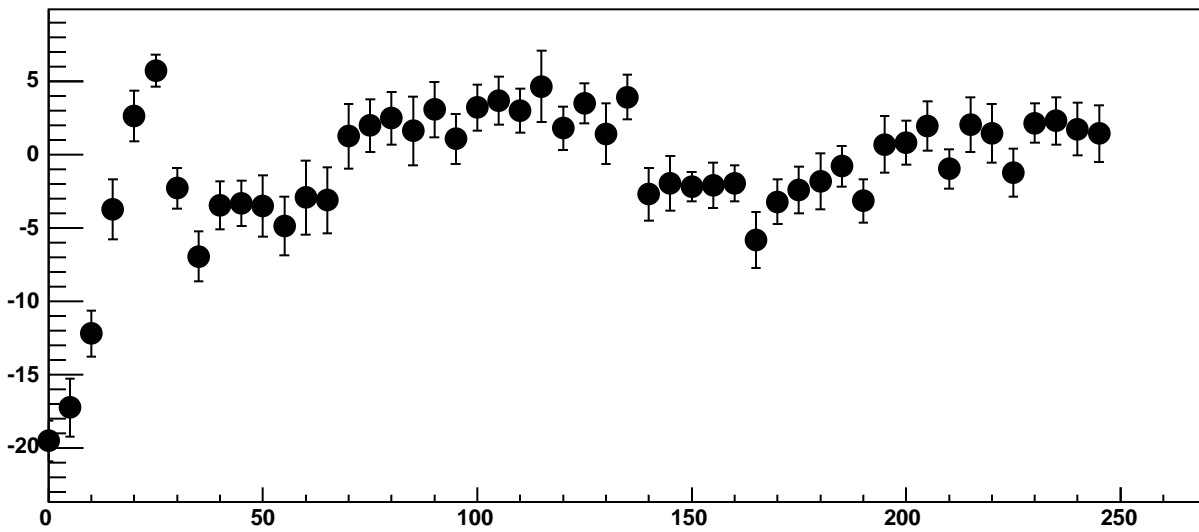


χ^2 / ndf	156.5 / 41
p0	307.3 ± 1.215
p1	23.61 ± 0.4961
p2	171.8 ± 4.988
p3	52.82 ± 1.925
p4	-0.2725 ± 0.02203

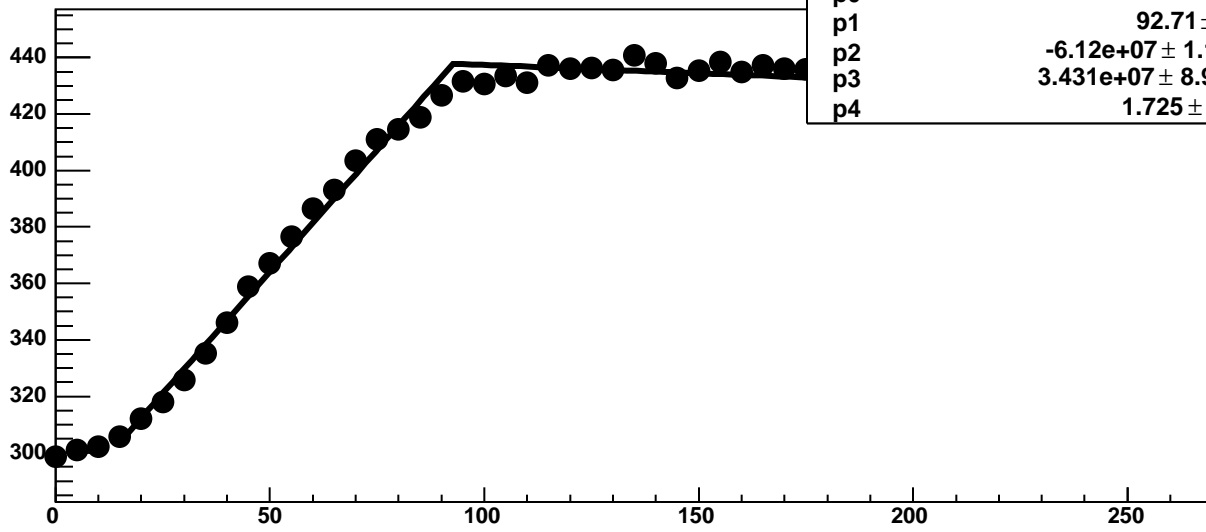
Chip 4, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

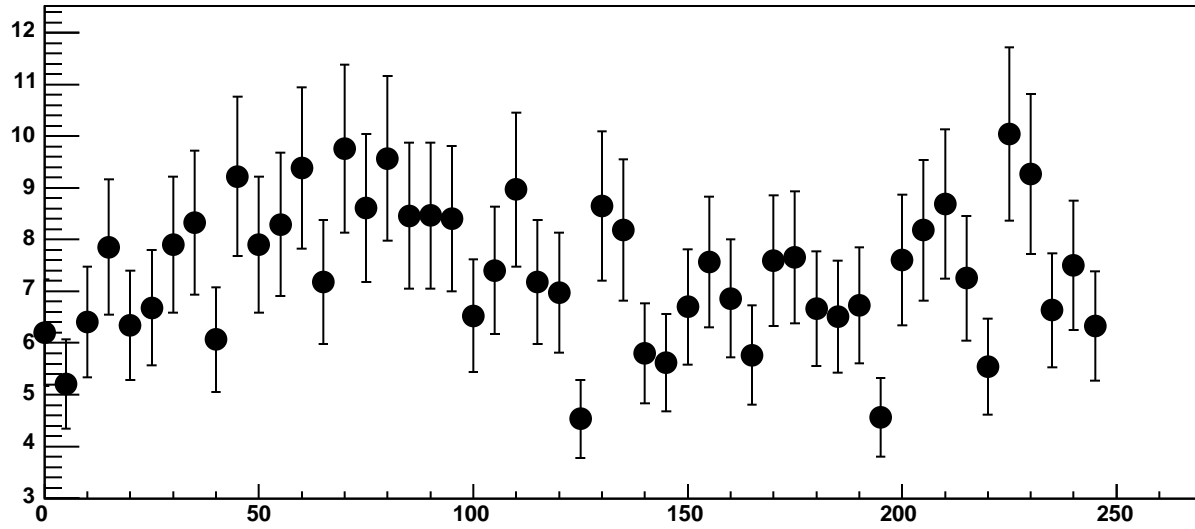


Chip 4, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold

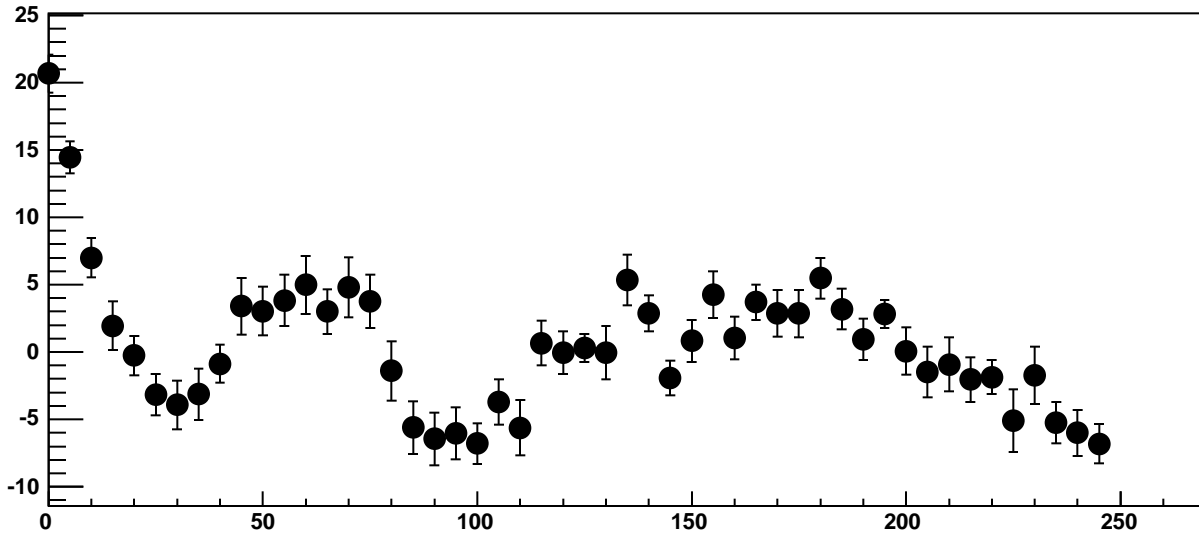


χ^2 / ndf	196.6 / 41
p0	437.9 ± 0.5884
p1	92.71 ± 0.5015
p2	$-6.12\text{e}+07 \pm 1.175\text{e}+06$
p3	$3.431\text{e}+07 \pm 8.941\text{e}+05$
p4	1.725 ± 0.01608

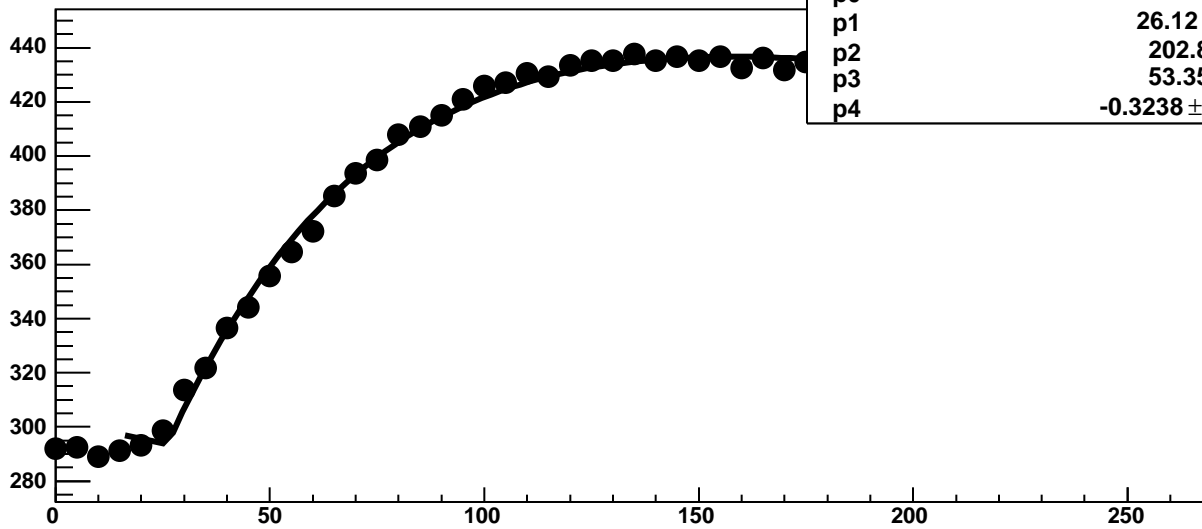
Chip 4, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold

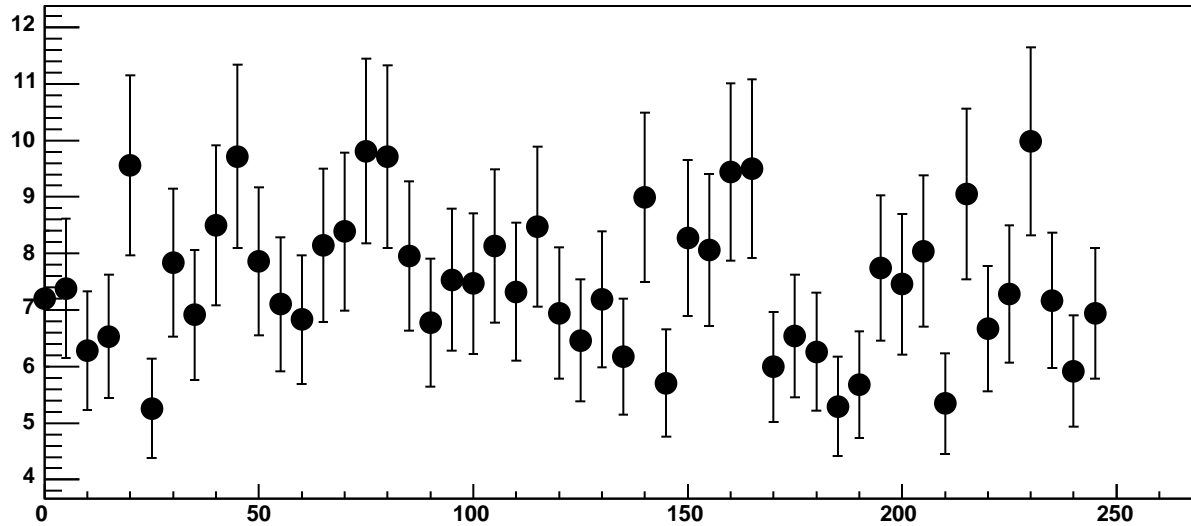


Chip 4, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

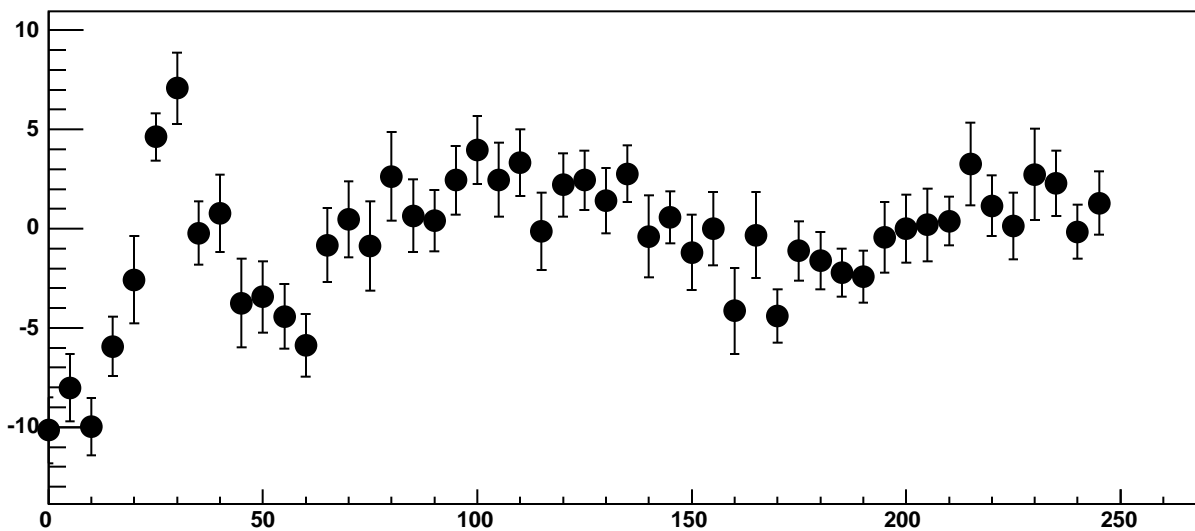


χ^2 / ndf	129.7 / 41
p0	293.7 ± 0.7943
p1	26.12 ± 0.4468
p2	202.8 ± 5.598
p3	53.35 ± 1.939
p4	-0.3238 ± 0.02494

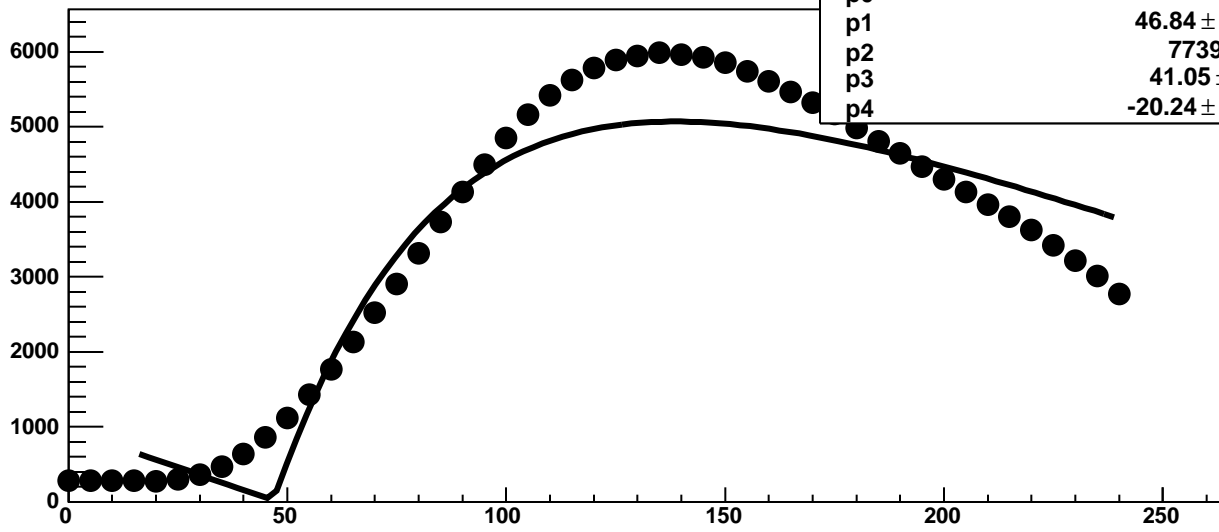
Chip 4, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

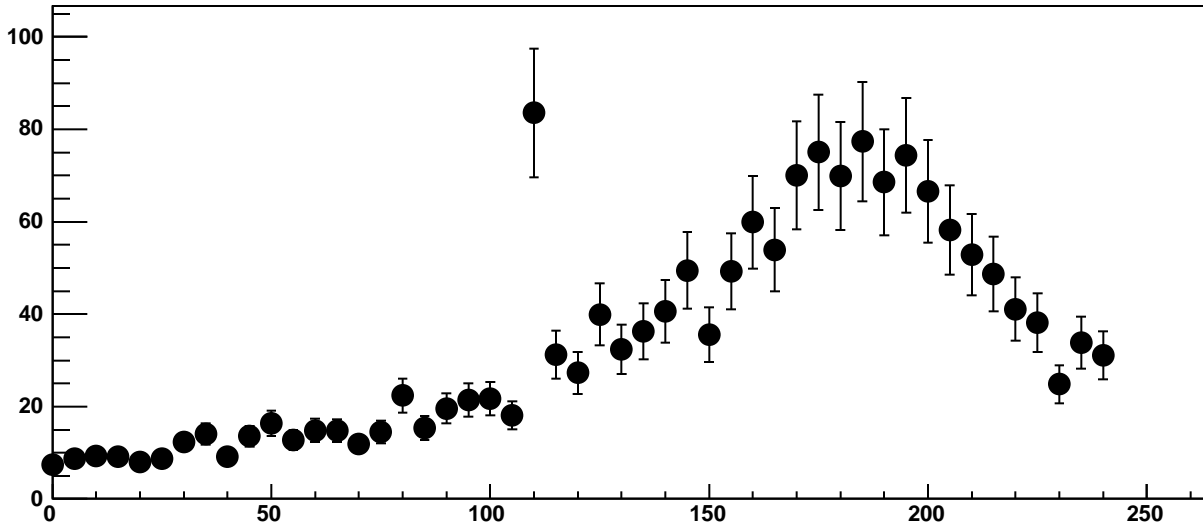


Chip 4, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold

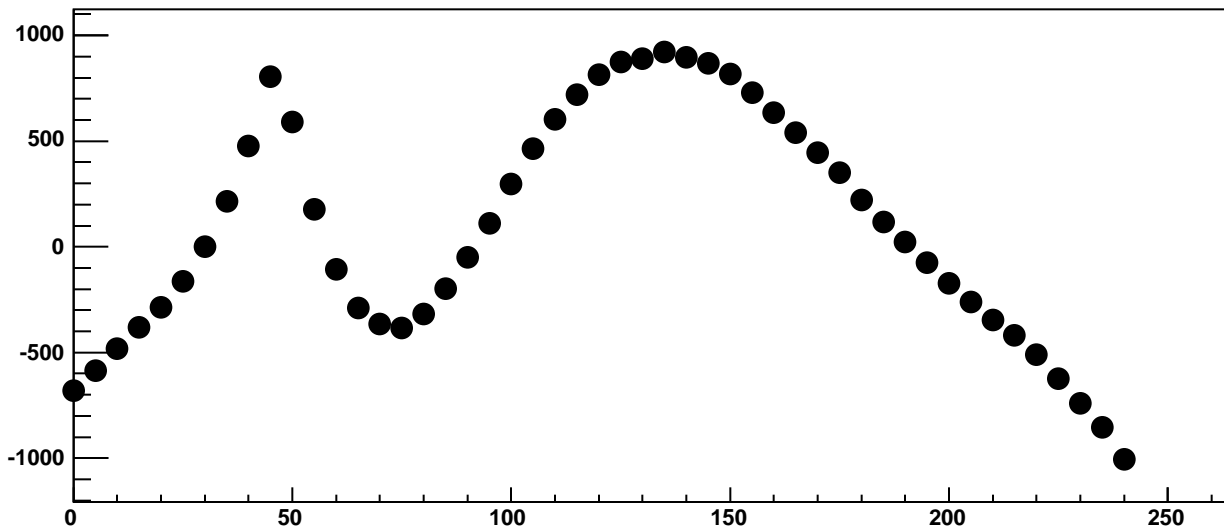


χ^2 / ndf	4.366e+05 / 41
p0	18.23 ± 1.186
p1	46.84 ± 0.01657
p2	7739 ± 9.664
p3	41.05 ± 0.0589
p4	-20.24 ± 0.05026

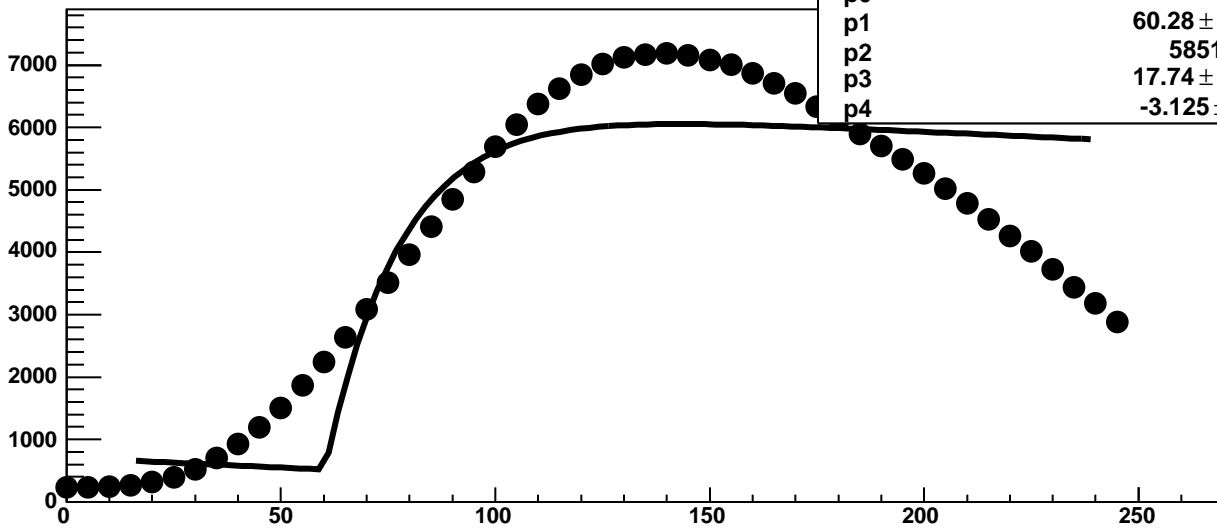
Chip 4, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold

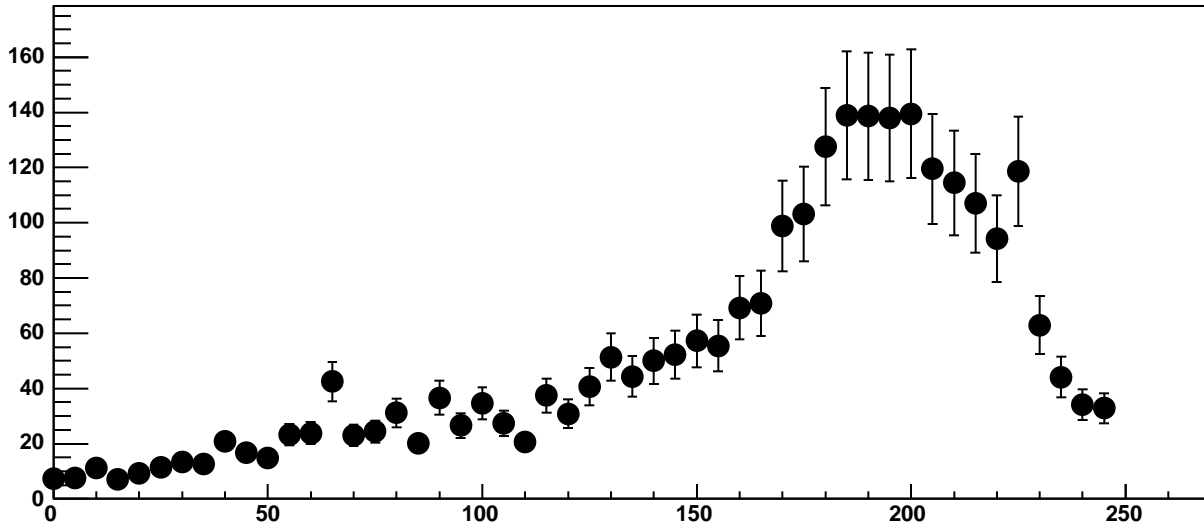


Chip 4, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold

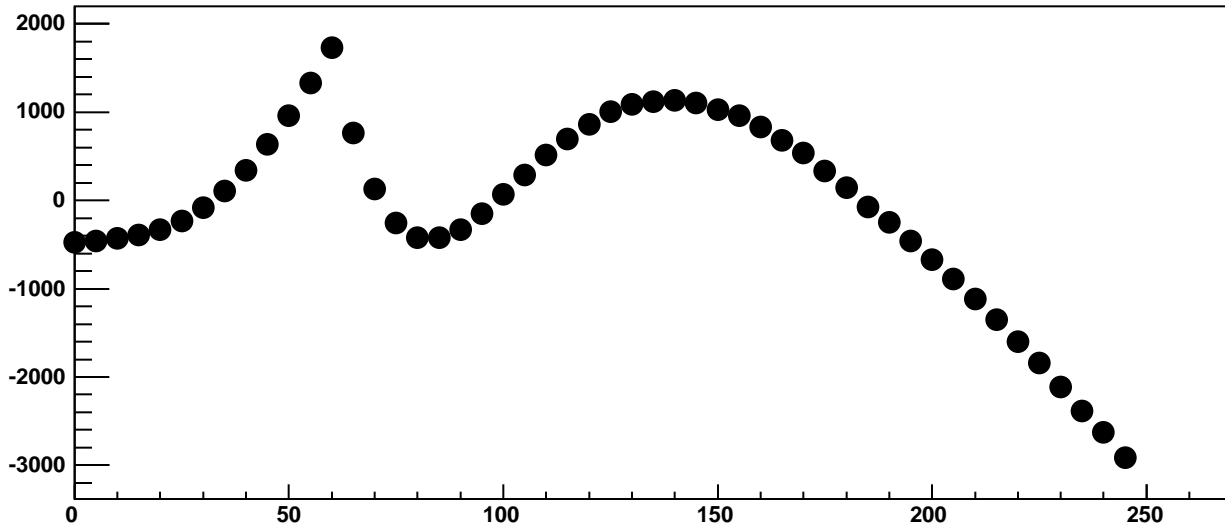


χ^2 / ndf	7e+05 / 41
p0	517.4 ± 1.621
p1	60.28 ± 0.02889
p2	5851 ± 6.128
p3	17.74 ± 0.03995
p4	-3.125 ± 0.0419

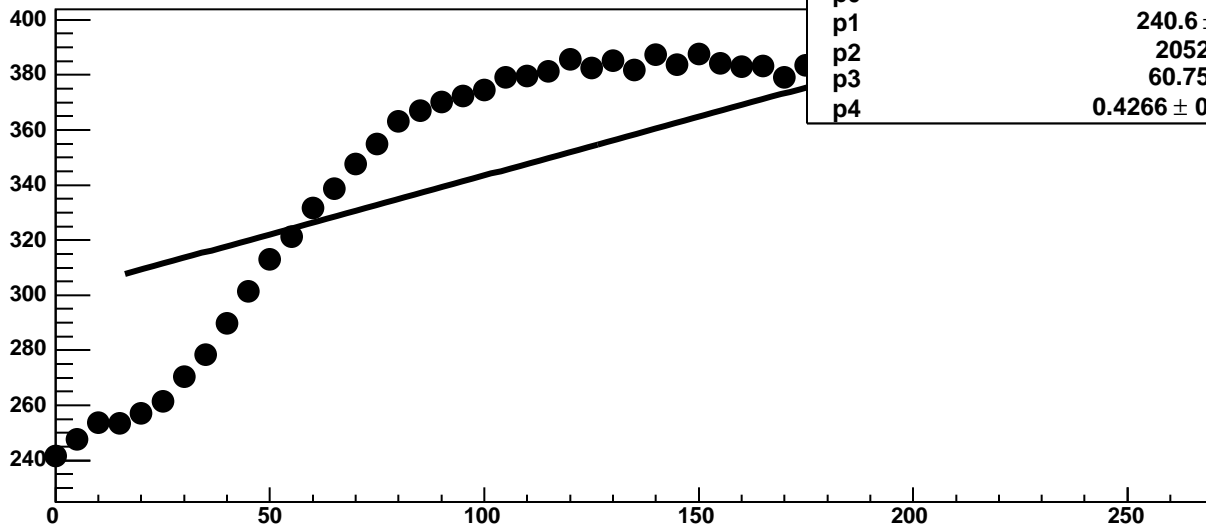
Chip 4, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold

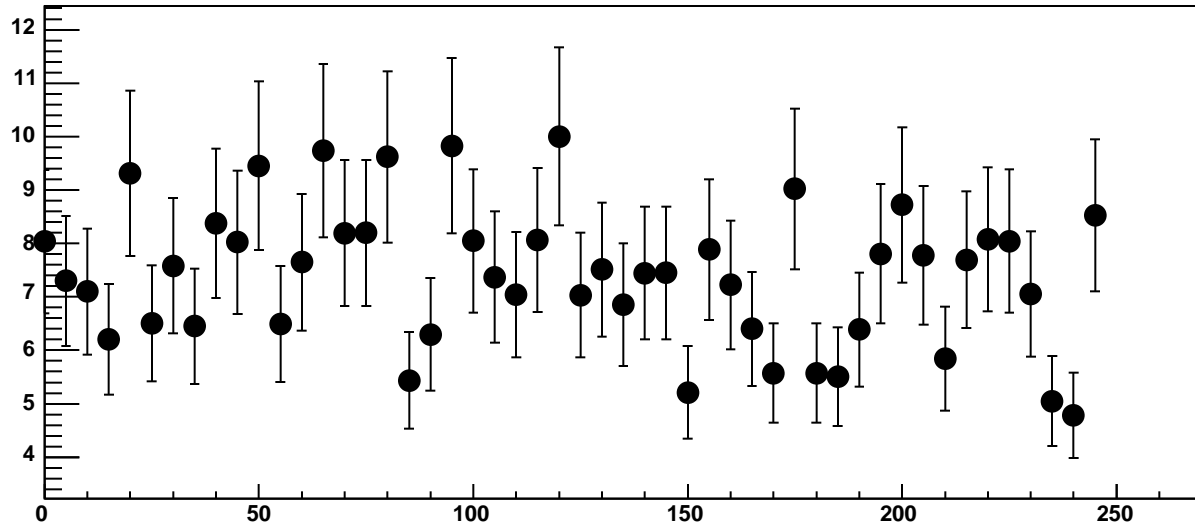


Chip 4, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

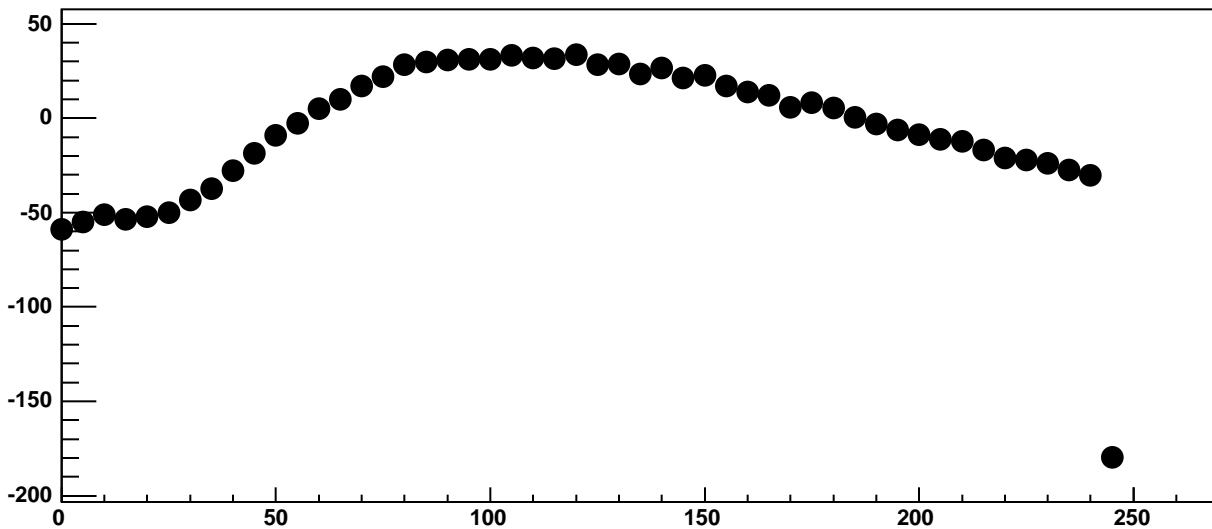


χ^2 / ndf	1.185e+04 / 41
p0	403.4 ± 0.2371
p1	240.6 ± 0.5557
p2	2052 ± 84.85
p3	60.75 ± 1.414
p4	0.4266 ± 0.001904

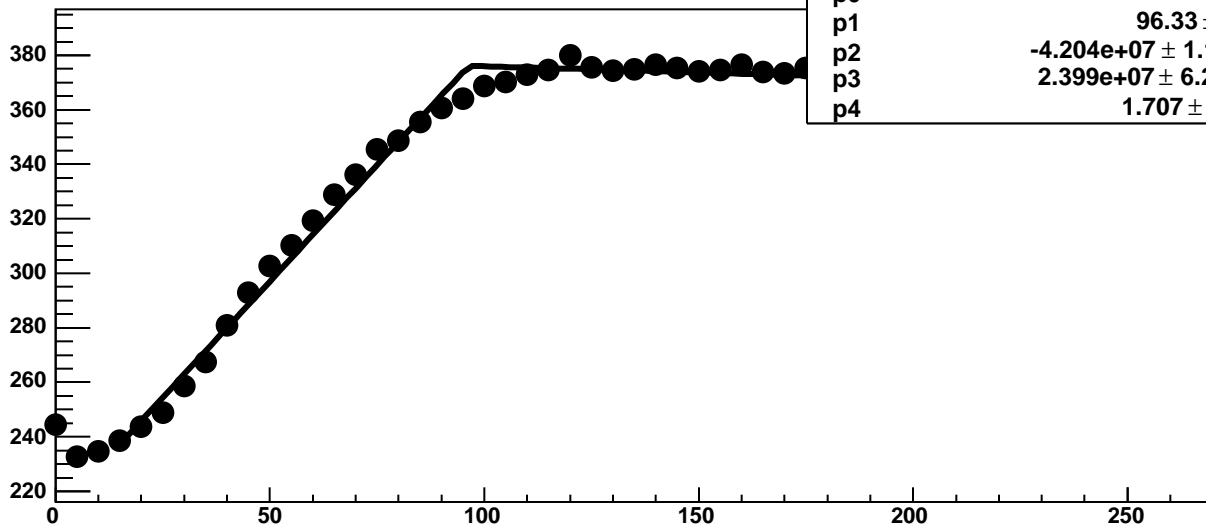
Chip 4, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

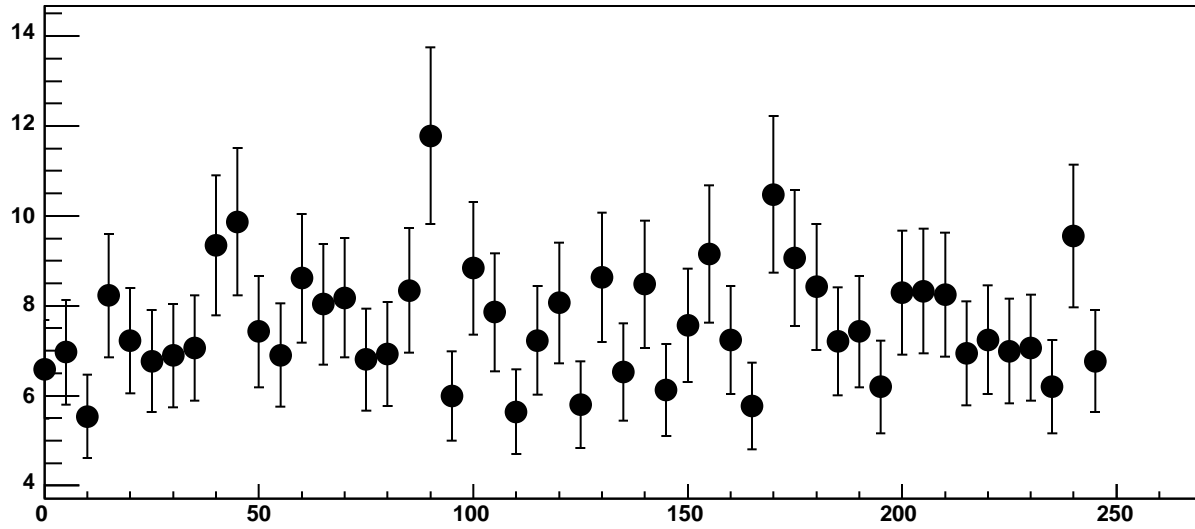


Chip 4, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

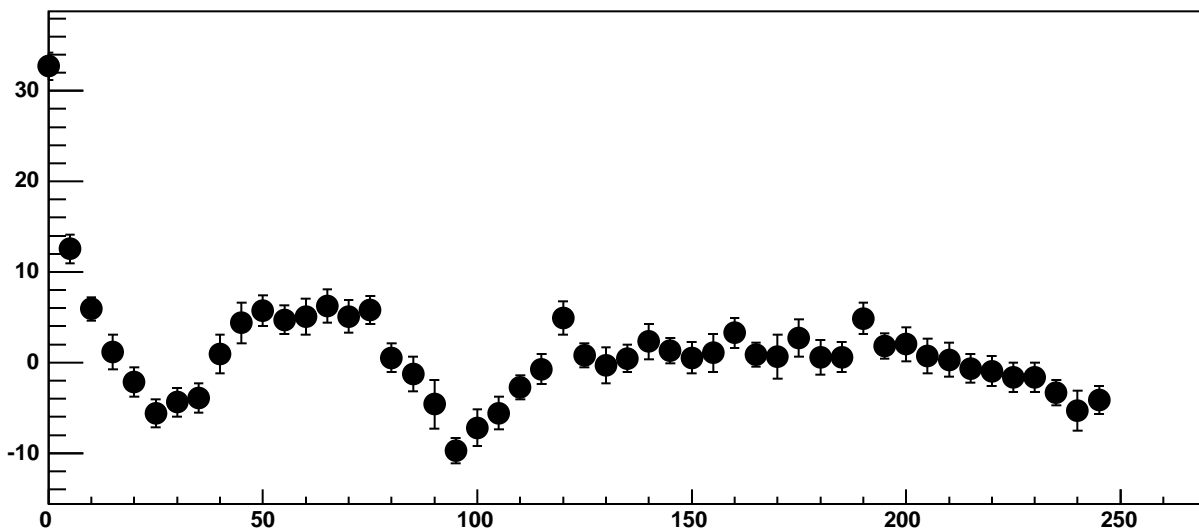


χ^2 / ndf	214.3 / 41
p0	376.2 ± 0.6057
p1	96.33 ± 0.5868
p2	$-4.204e+07 \pm 1.103e+06$
p3	$2.399e+07 \pm 6.205e+05$
p4	1.707 ± 0.01688

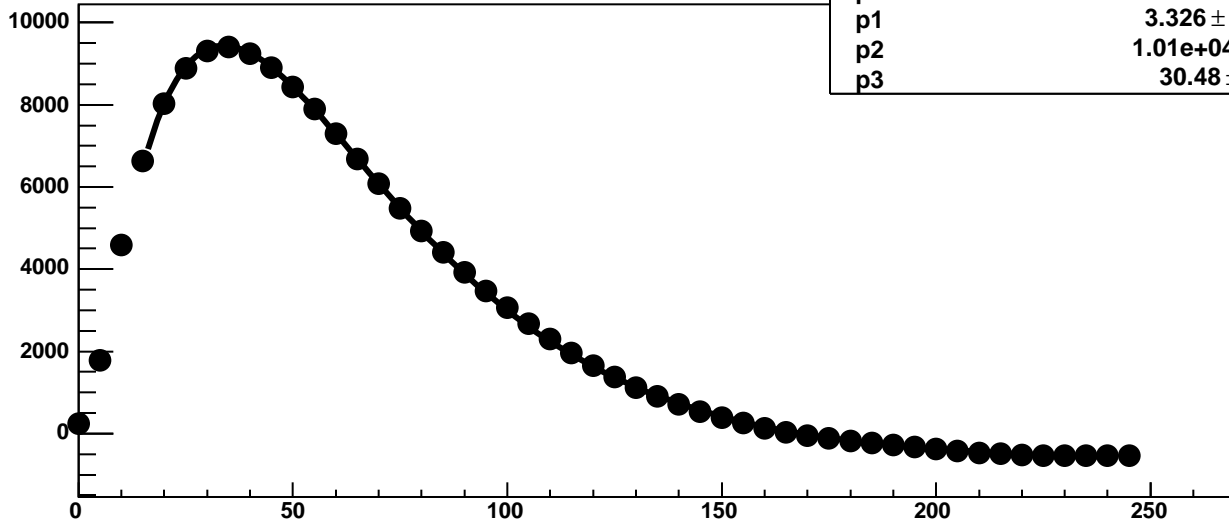
Chip 4, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

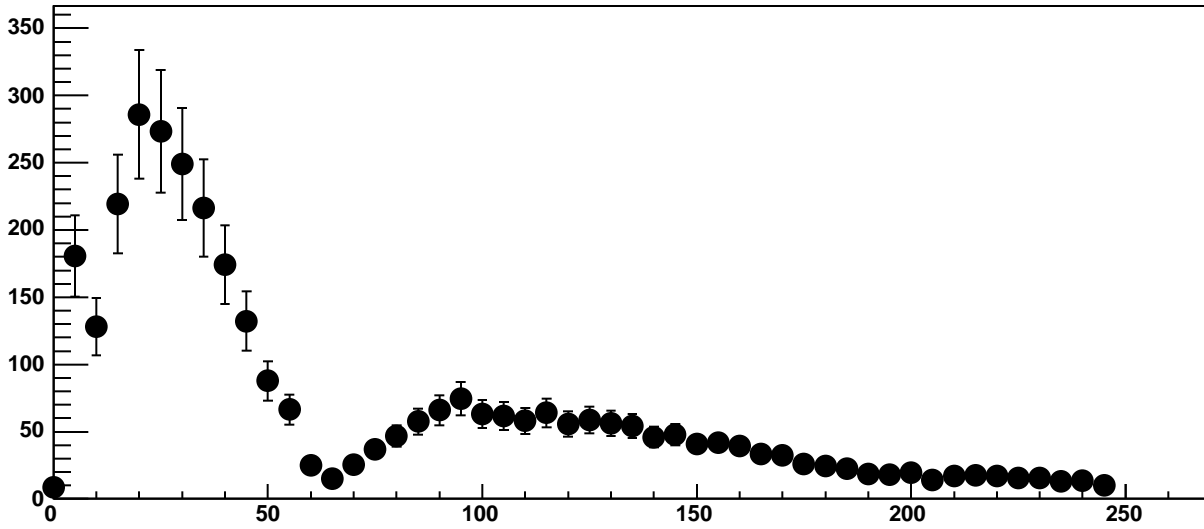


Chip 4, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold

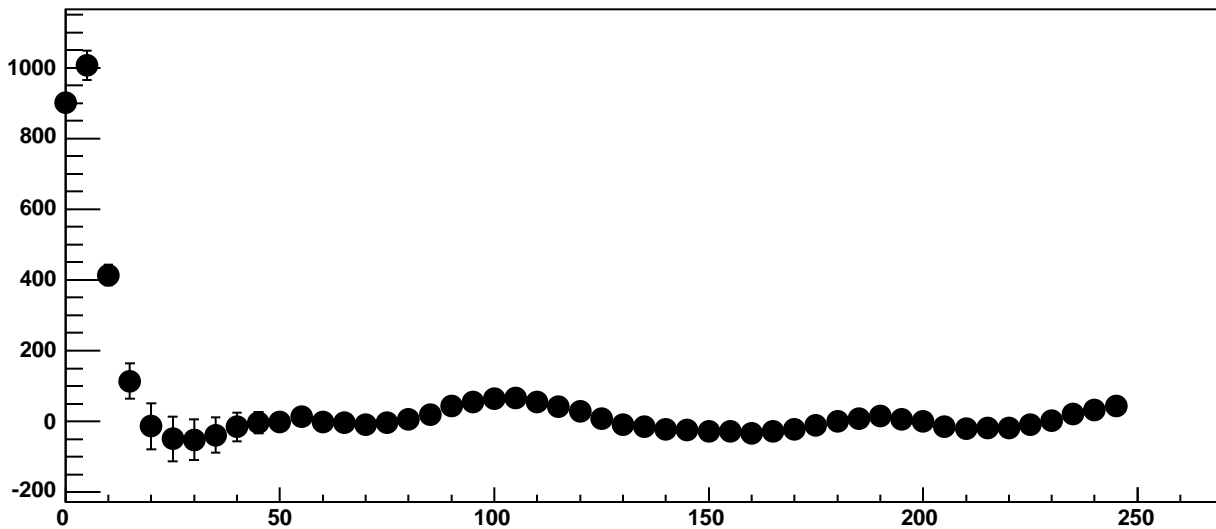


χ^2 / ndf	443.5 / 42
p0	-659.5 ± 1.605
p1	3.326 ± 0.09459
p2	$1.01\text{e}+04 \pm 11.13$
p3	30.48 ± 0.0348

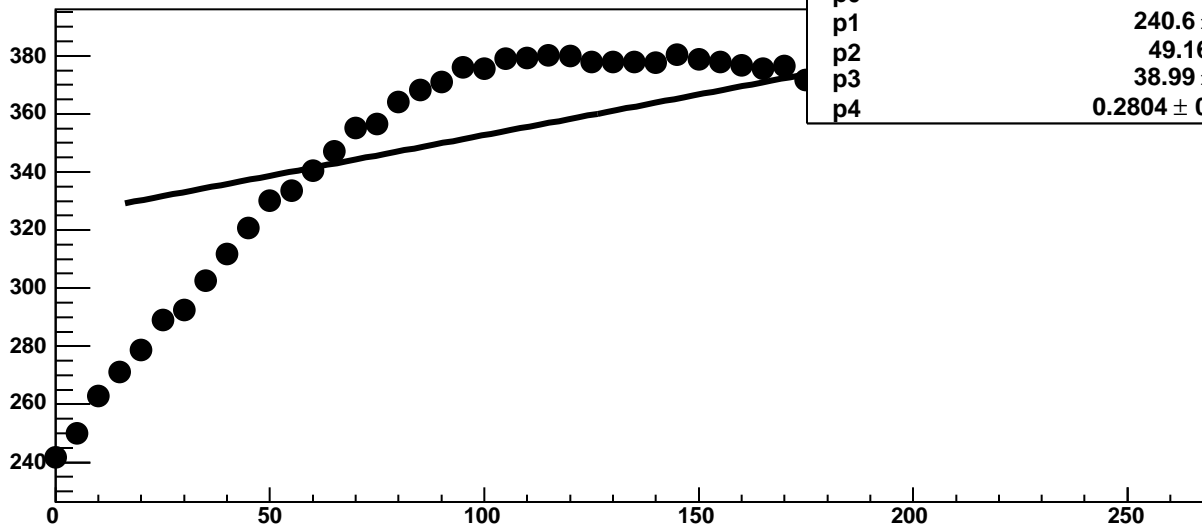
Chip 4, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold

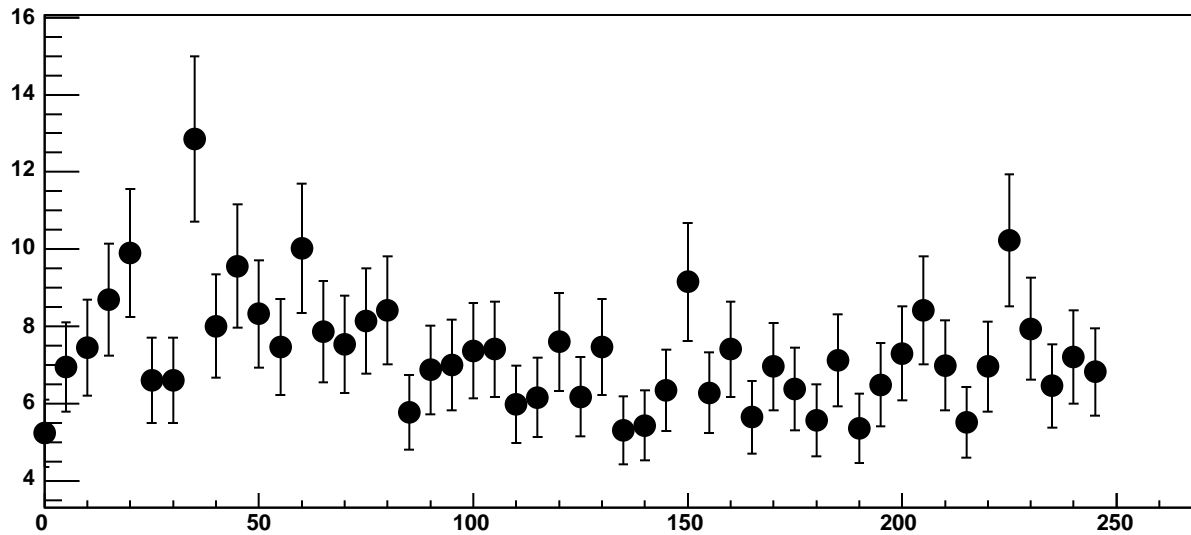


Chip 4, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

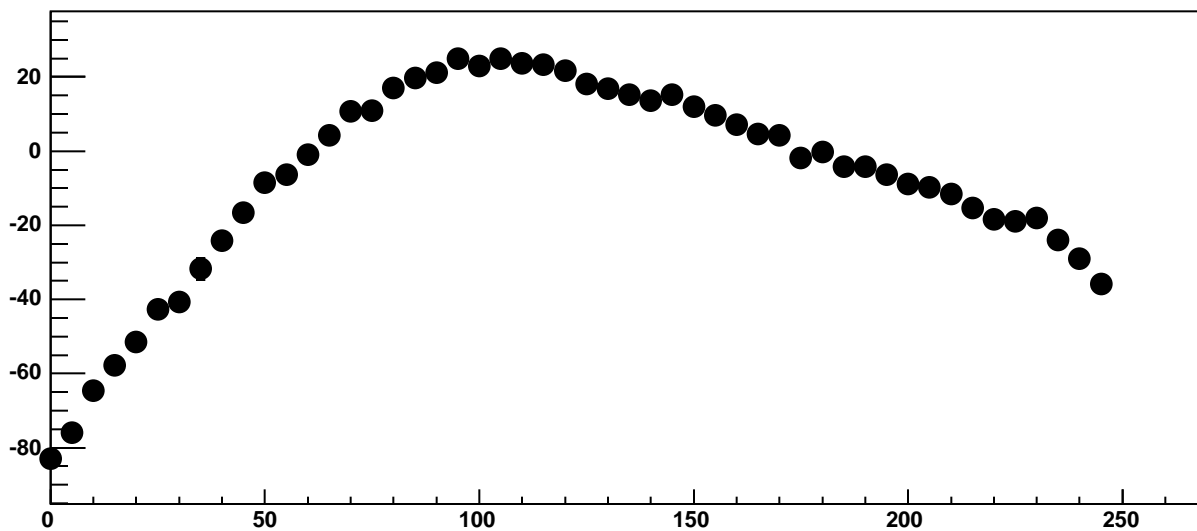


χ^2 / ndf	7081 / 41
p0	392.1 ± 0.4868
p1	240.6 ± 0.4555
p2	49.16 ± 1.639
p3	38.99 ± 0.1865
p4	0.2804 ± 0.003815

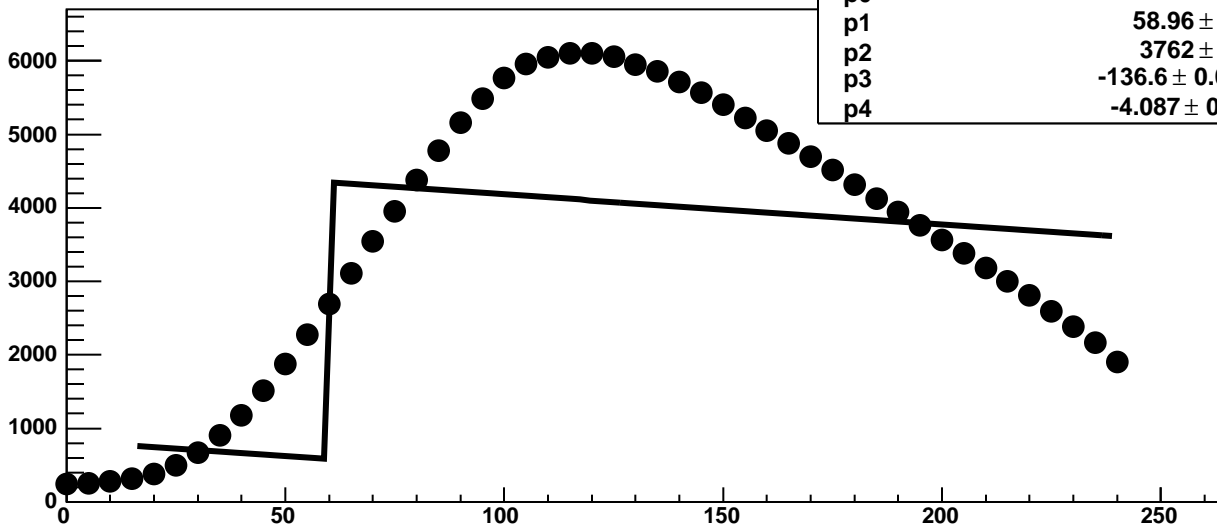
Chip 4, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



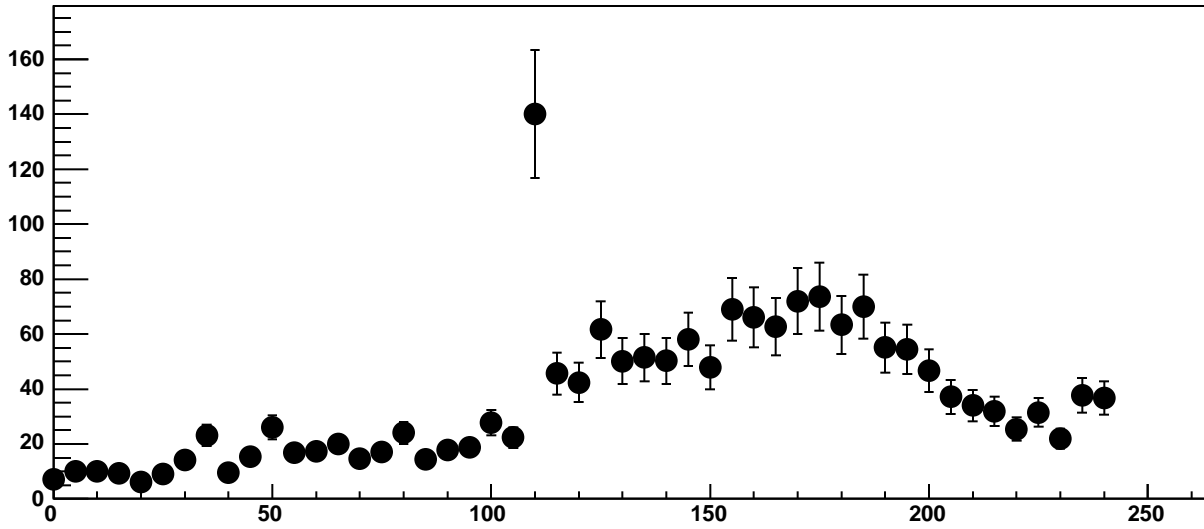
Chip 4, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold



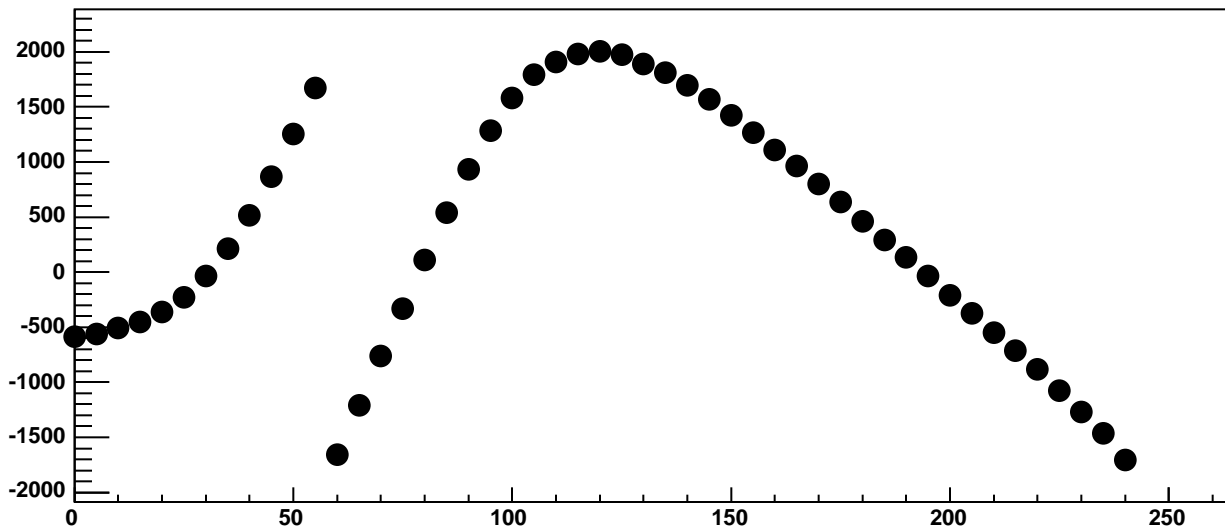
Chip 4, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold



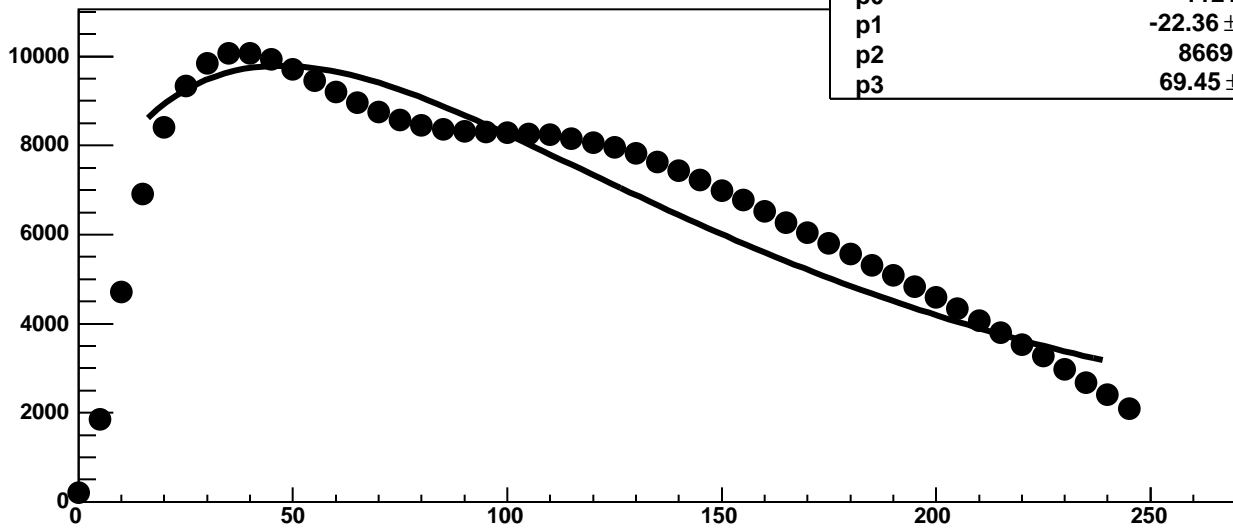
Chip 4, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold

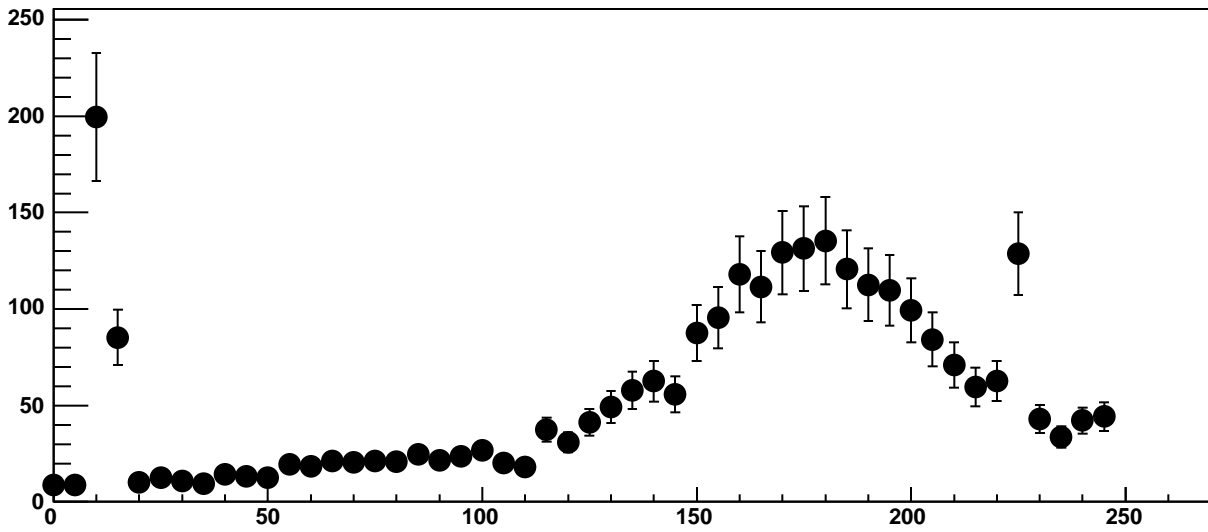


Chip 4, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold

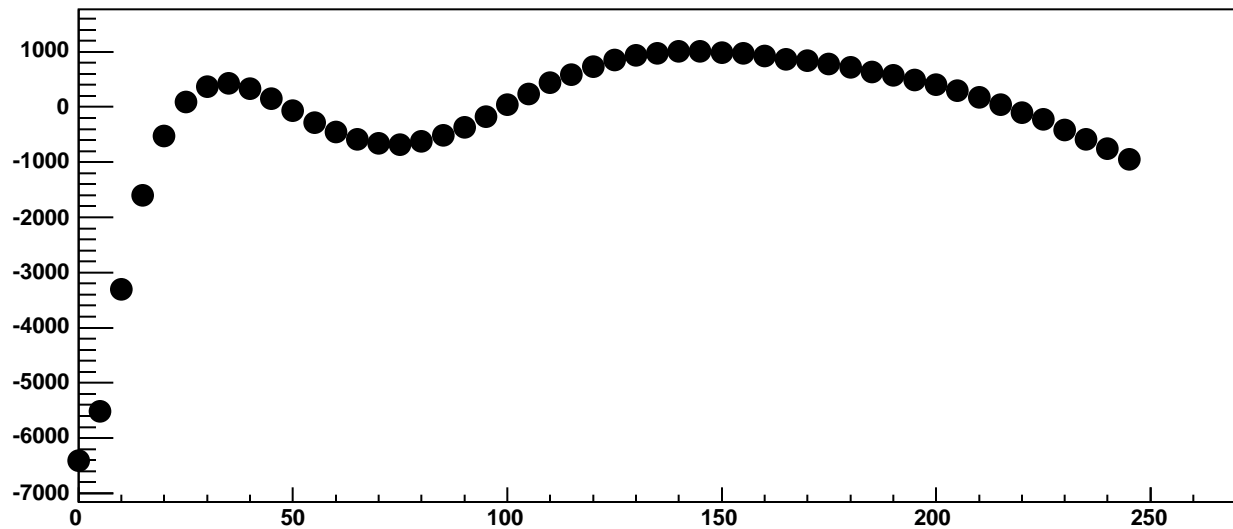


χ^2 / ndf	3.166e+05 / 42
p0	1121 ± 14.86
p1	-22.36 ± 0.1254
p2	8669 ± 14.23
p3	69.45 ± 0.1262

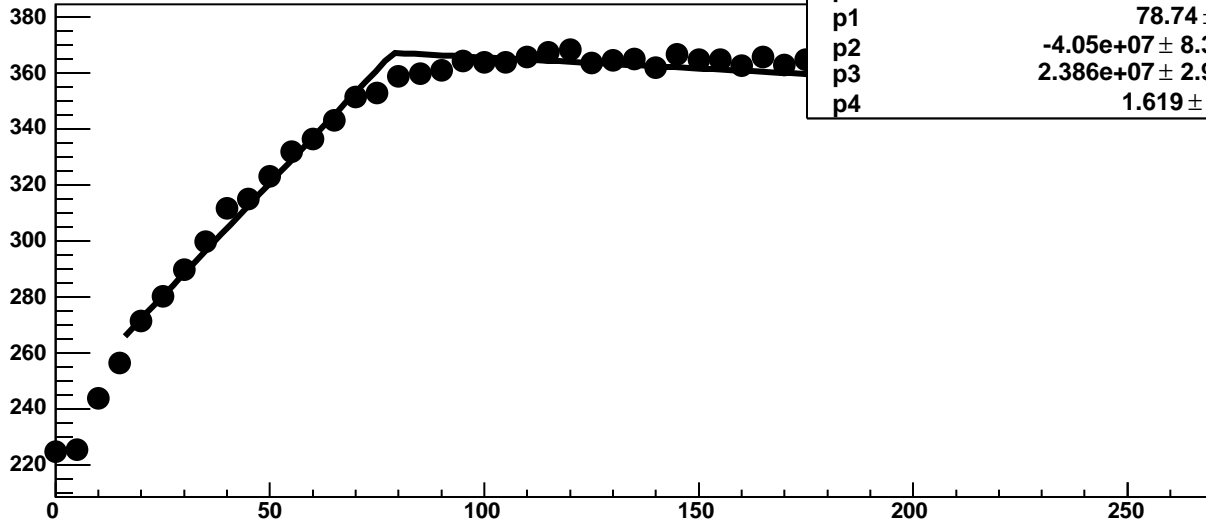
Chip 4, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold

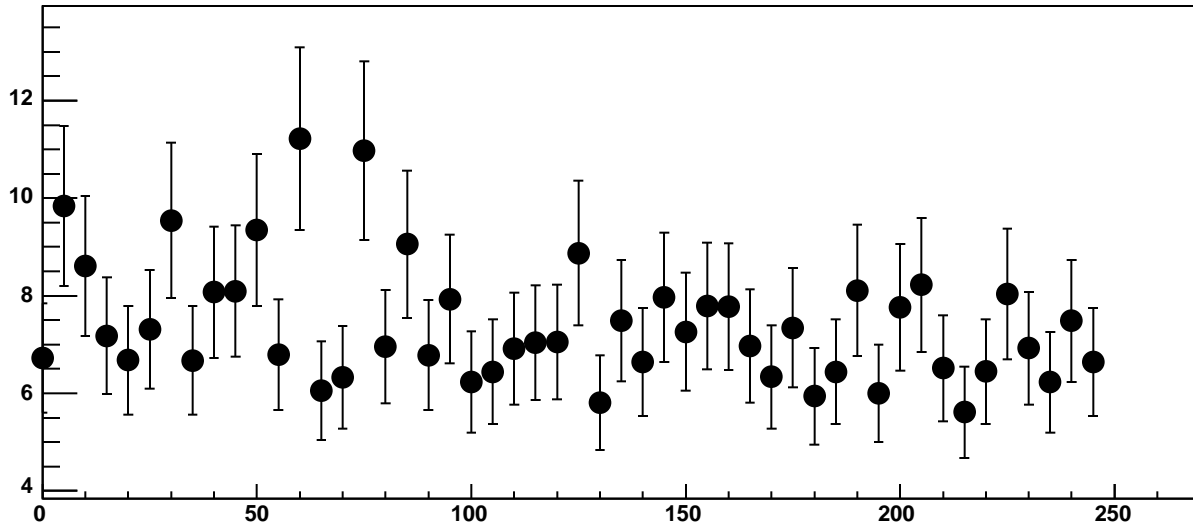


Chip 4, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

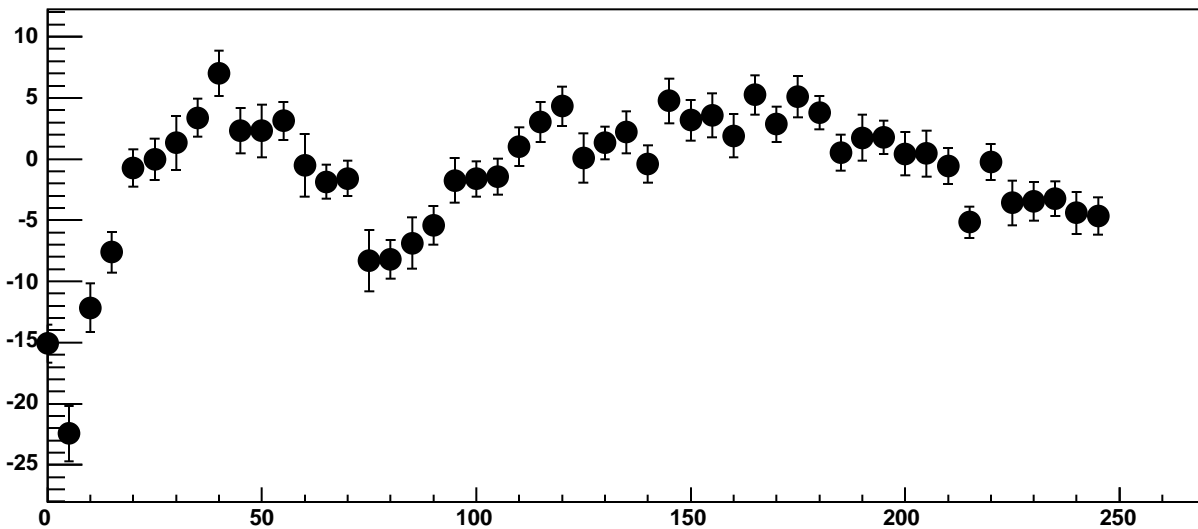


χ^2 / ndf	214.6 / 41
p0	367.3 ± 0.5353
p1	78.74 ± 0.6737
p2	$-4.05\text{e}+07 \pm 8.321\text{e}+05$
p3	$2.386\text{e}+07 \pm 2.989\text{e}+05$
p4	1.619 ± 0.02534

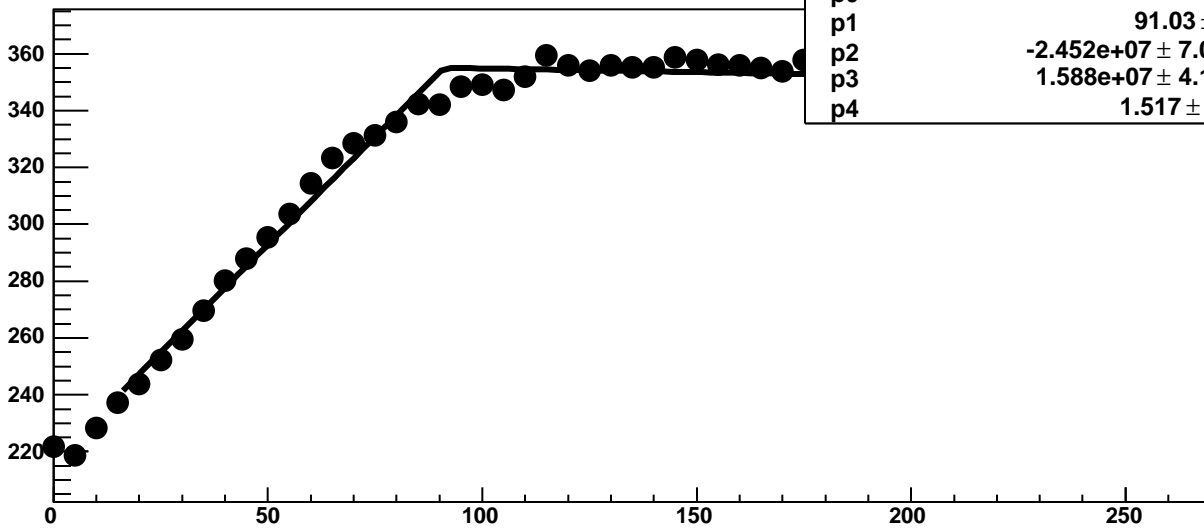
Chip 4, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold

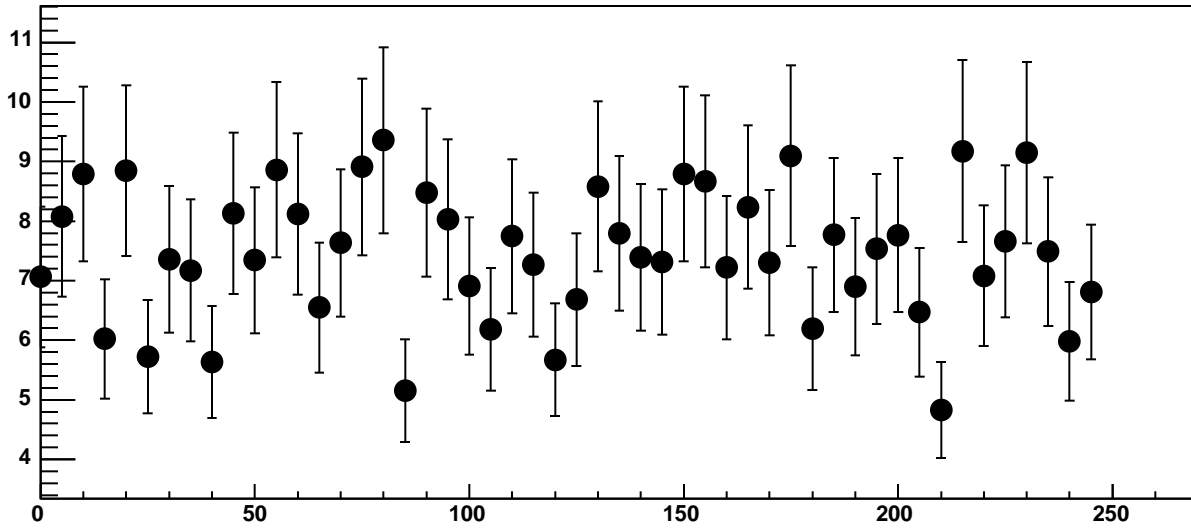


Chip 4, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

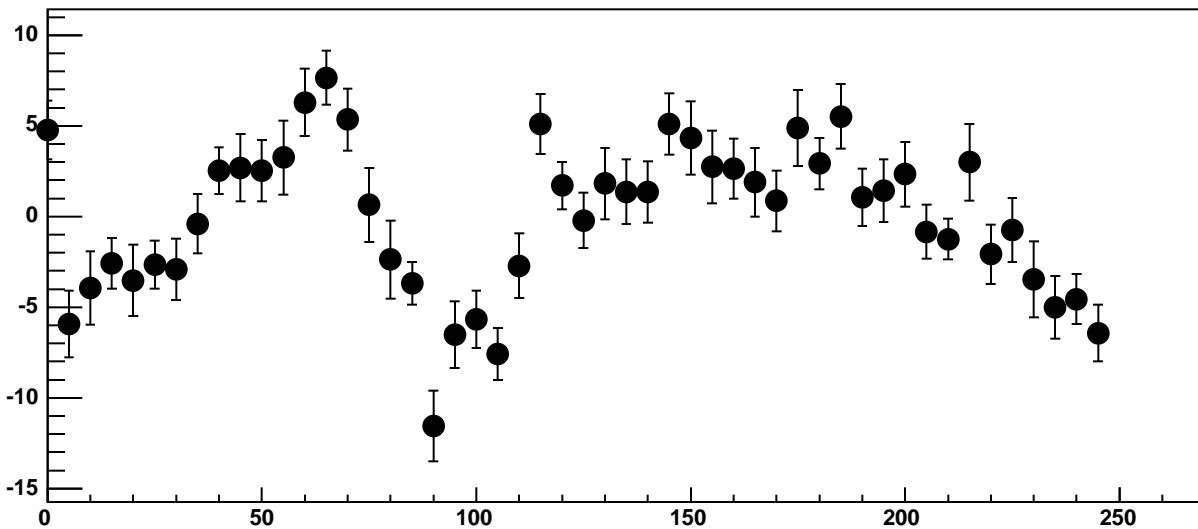


χ^2 / ndf	256.8 / 41
p0	355.1 ± 0.5949
p1	91.03 ± 0.6478
p2	$-2.452e+07 \pm 7.063e+05$
p3	$1.588e+07 \pm 4.172e+05$
p4	1.517 ± 0.01706

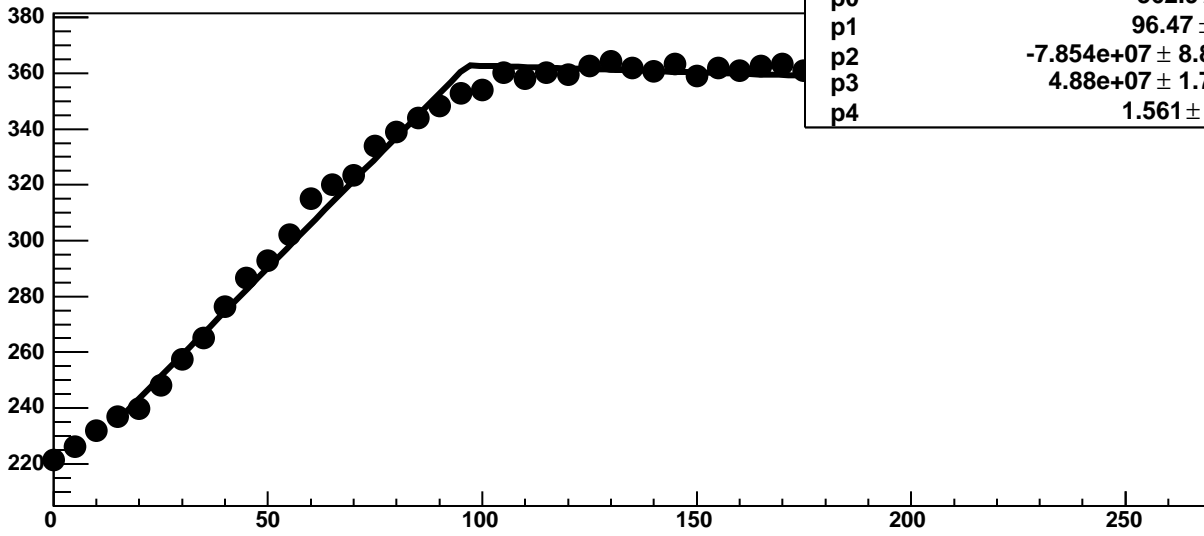
Chip 4, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

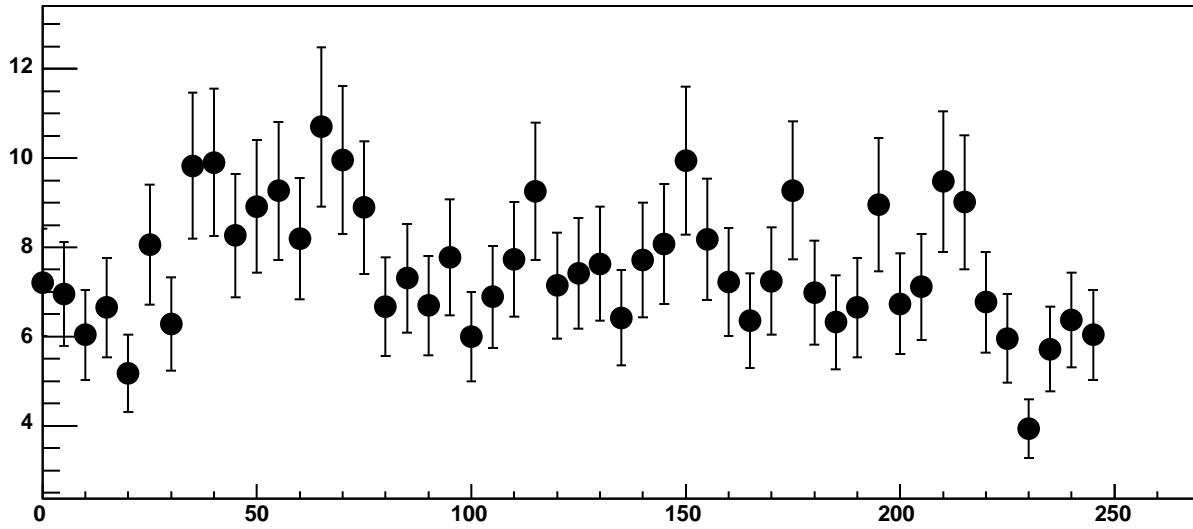


Chip 4, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold

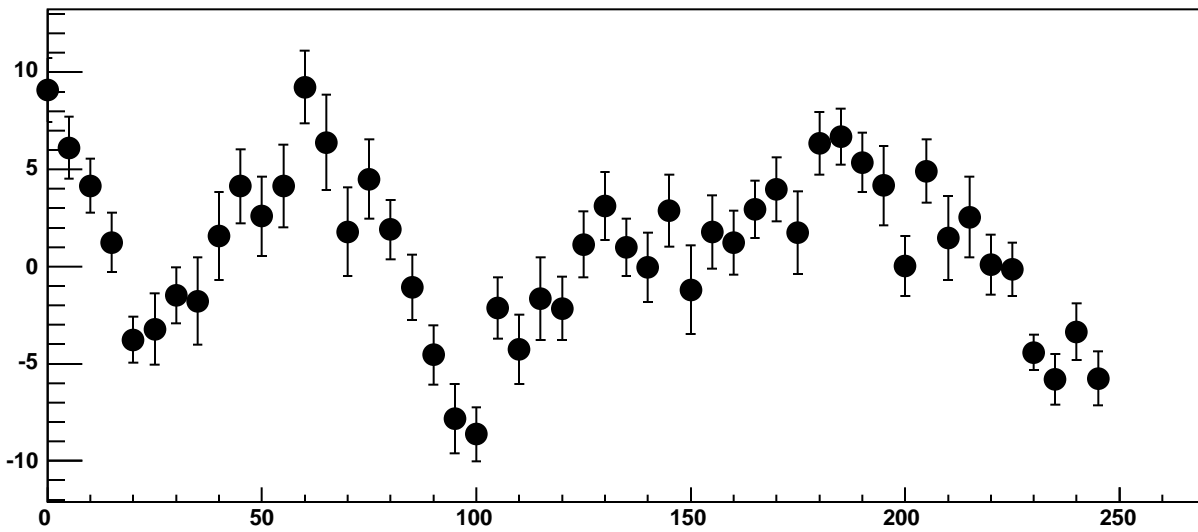


χ^2 / ndf	273.9 / 41
p0	362.9 ± 0.5901
p1	96.47 ± 0.6325
p2	$-7.854e+07 \pm 8.833e+05$
p3	$4.88e+07 \pm 1.702e+05$
p4	1.561 ± 0.01577

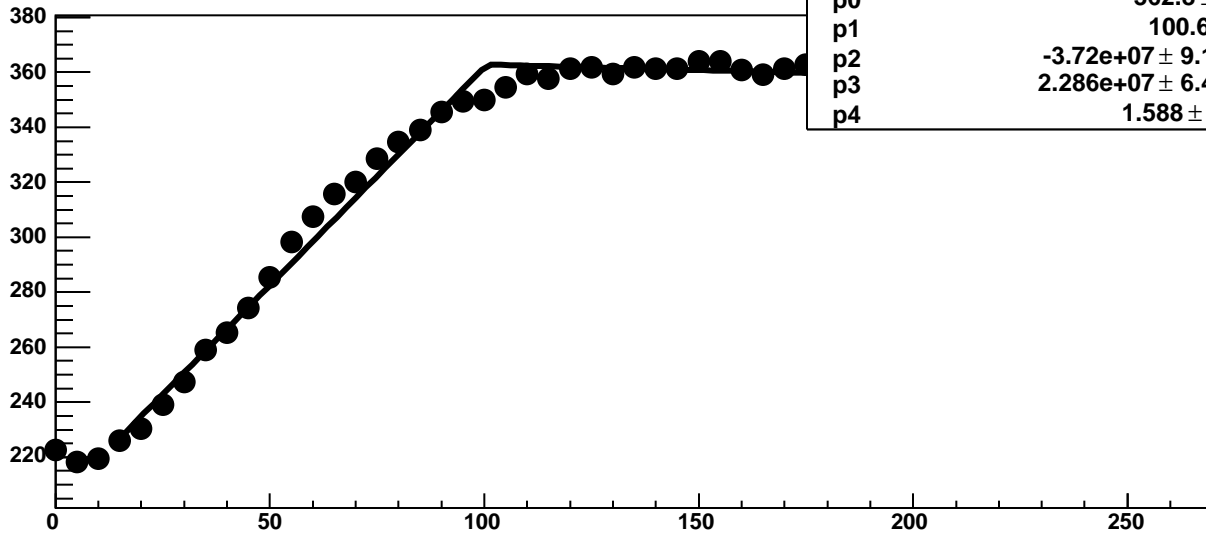
Chip 4, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold

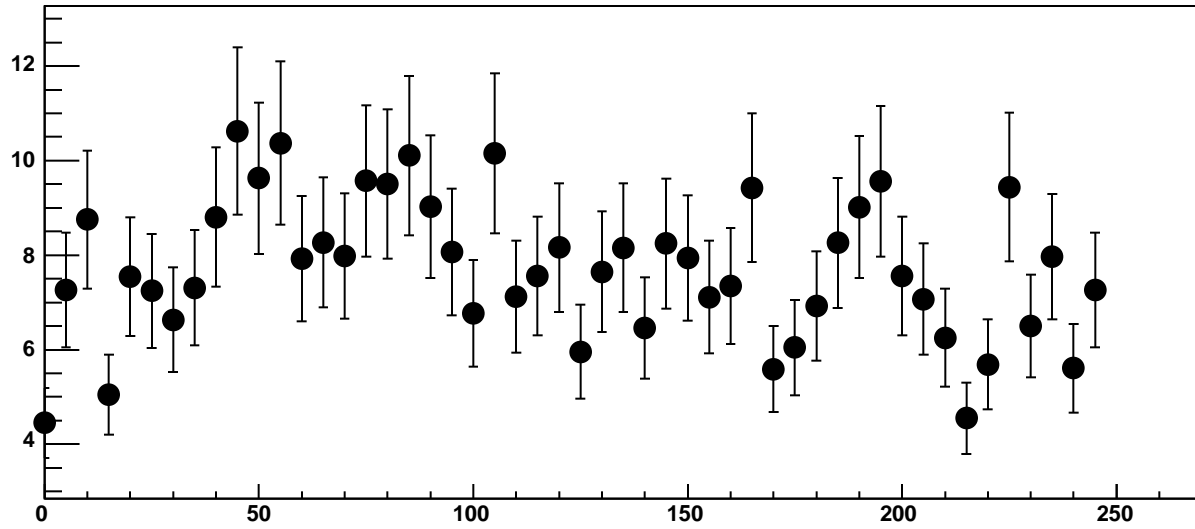


Chip 4, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold

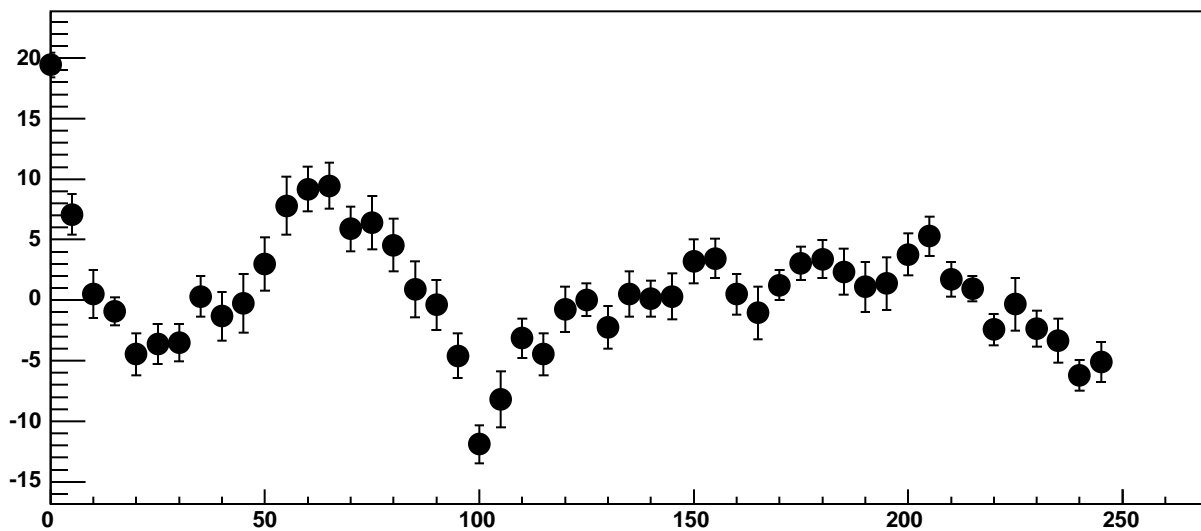


χ^2 / ndf	264.1 / 41
p0	362.8 ± 0.6204
p1	100.6 ± 0.595
p2	$-3.72\text{e}+07 \pm 9.129\text{e}+05$
p3	$2.286\text{e}+07 \pm 6.428\text{e}+05$
p4	1.588 ± 0.01363

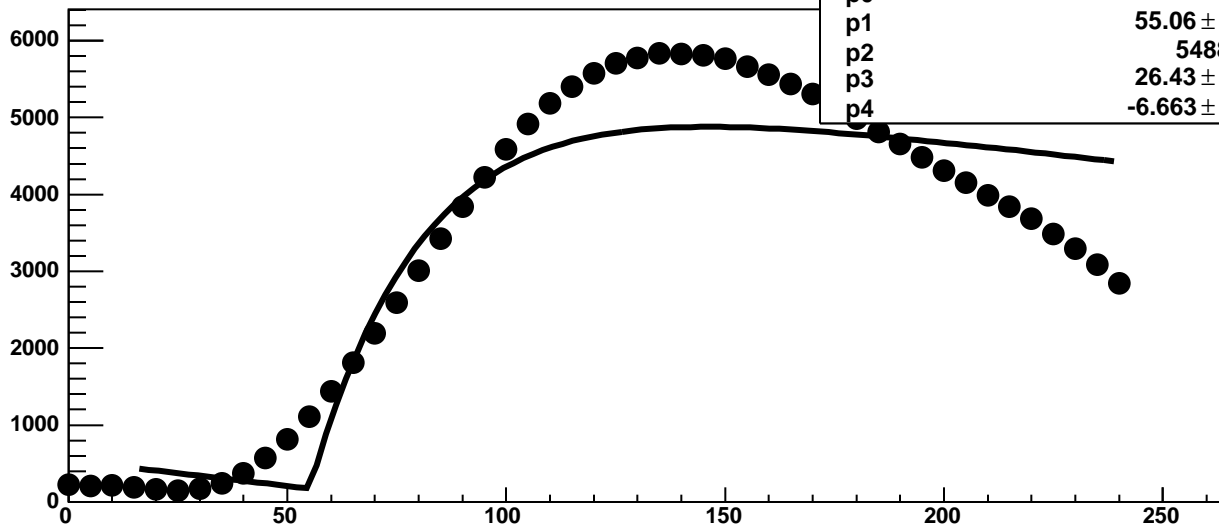
Chip 4, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

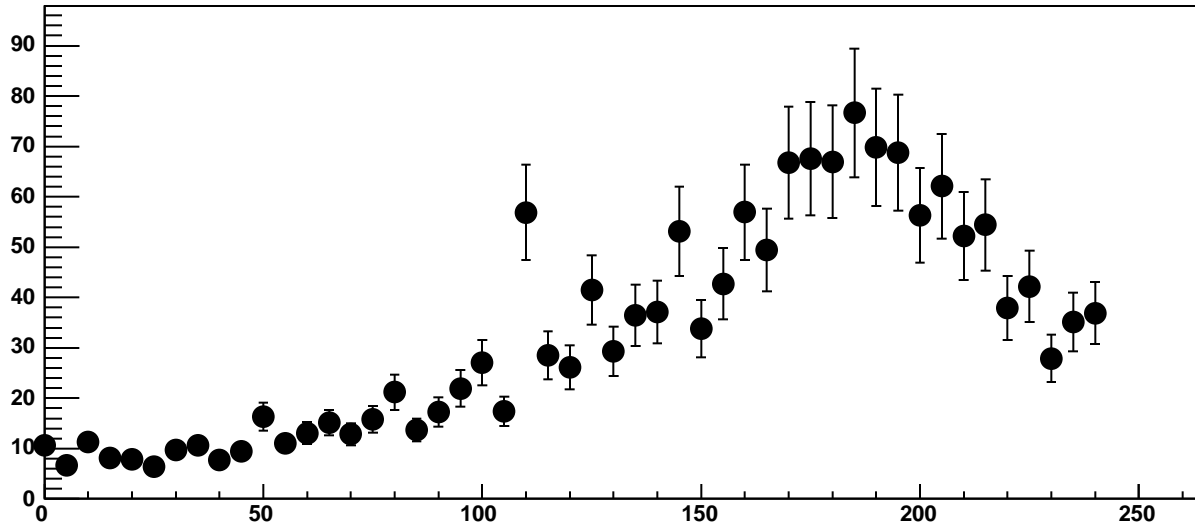


Chip 4, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

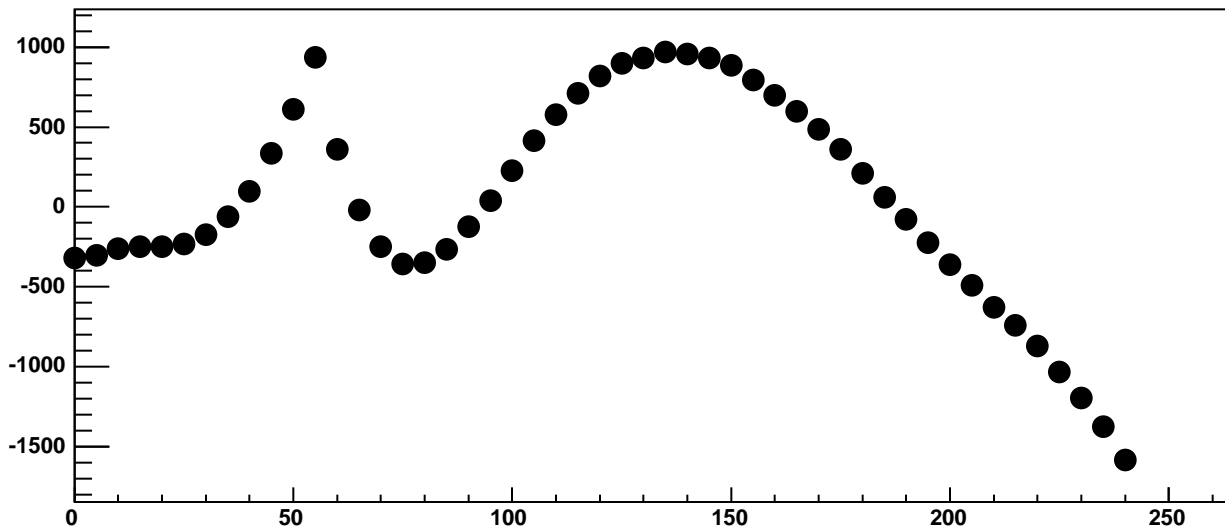


χ^2 / ndf	5.626e+05 / 41
p0	172.9 ± 1.048
p1	55.06 ± 0.01767
p2	5488 ± 5.73
p3	26.43 ± 0.04145
p4	-6.663 ± 0.03503

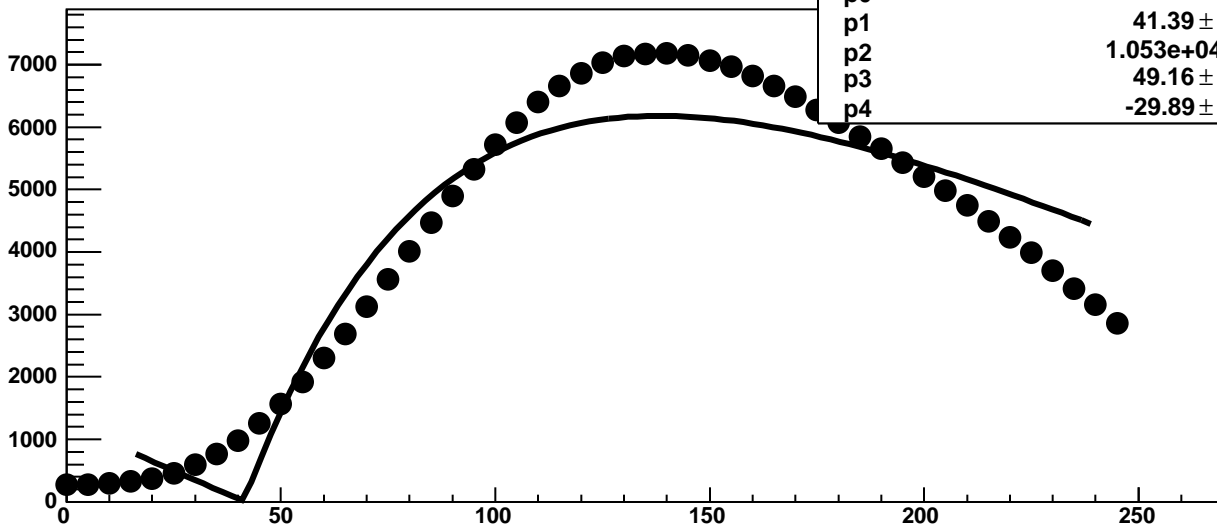
Chip 4, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold



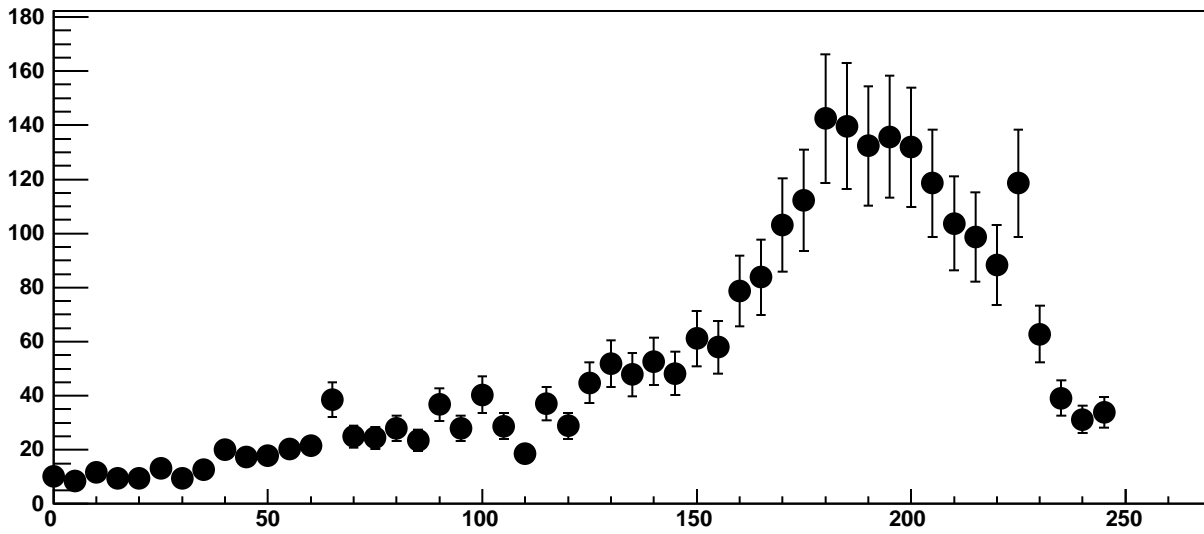
Chip 4, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold



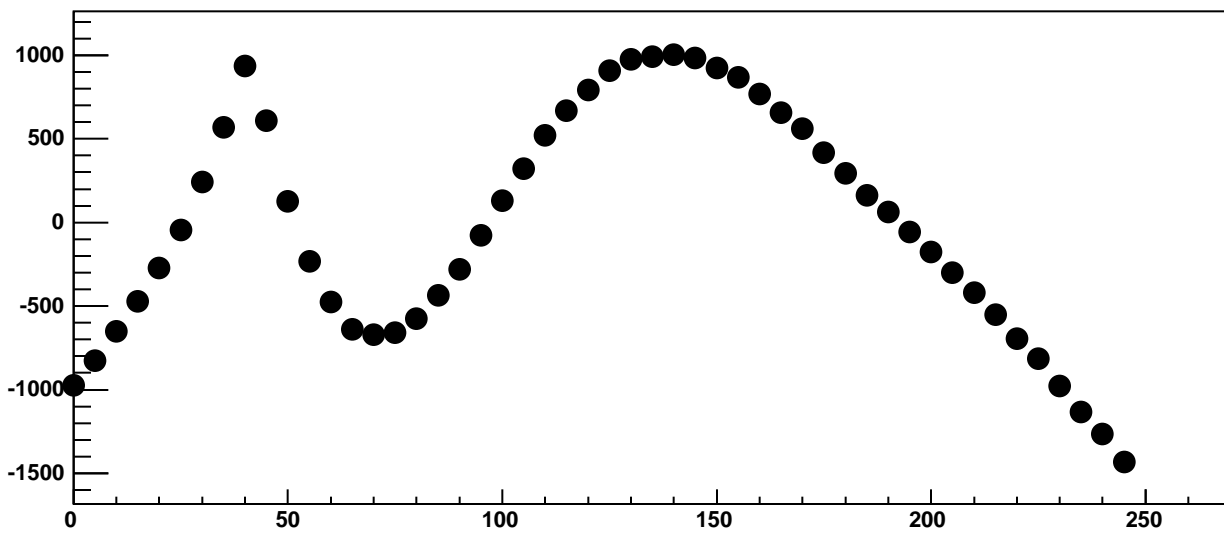
Chip 4, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold



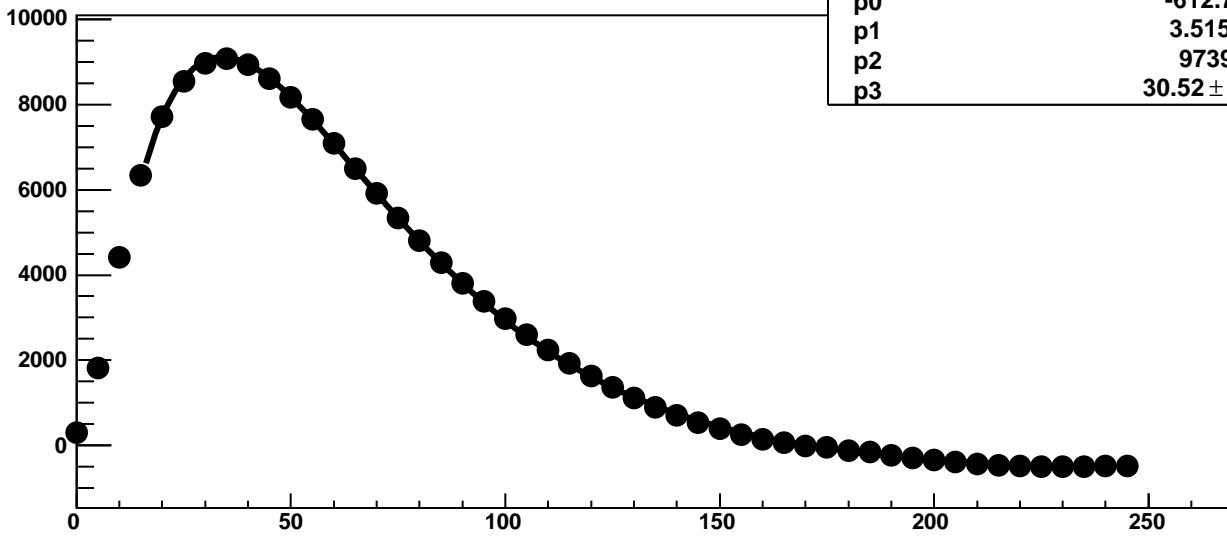
Chip 4, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

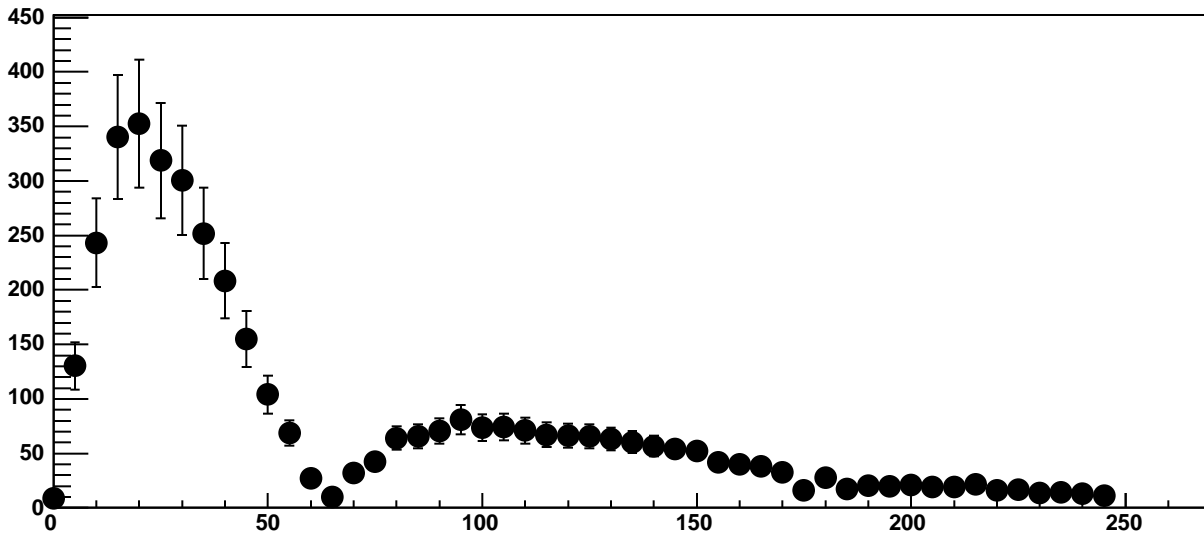


Chip 4, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold

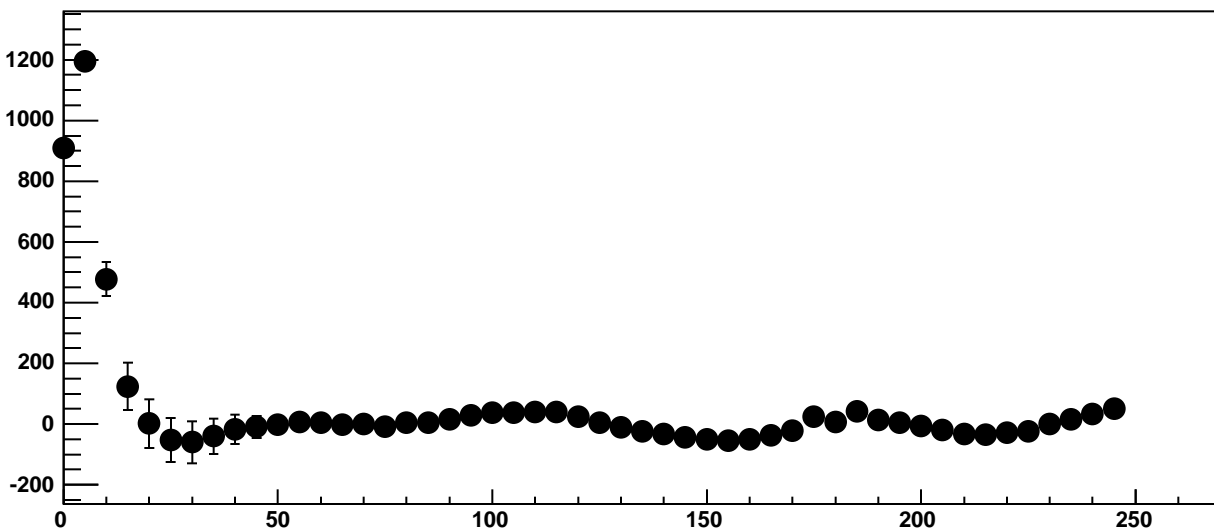


χ^2 / ndf	707.3 / 42
p0	-612.7 ± 1.731
p1	3.515 ± 0.128
p2	9739 ± 14.31
p3	30.52 ± 0.04082

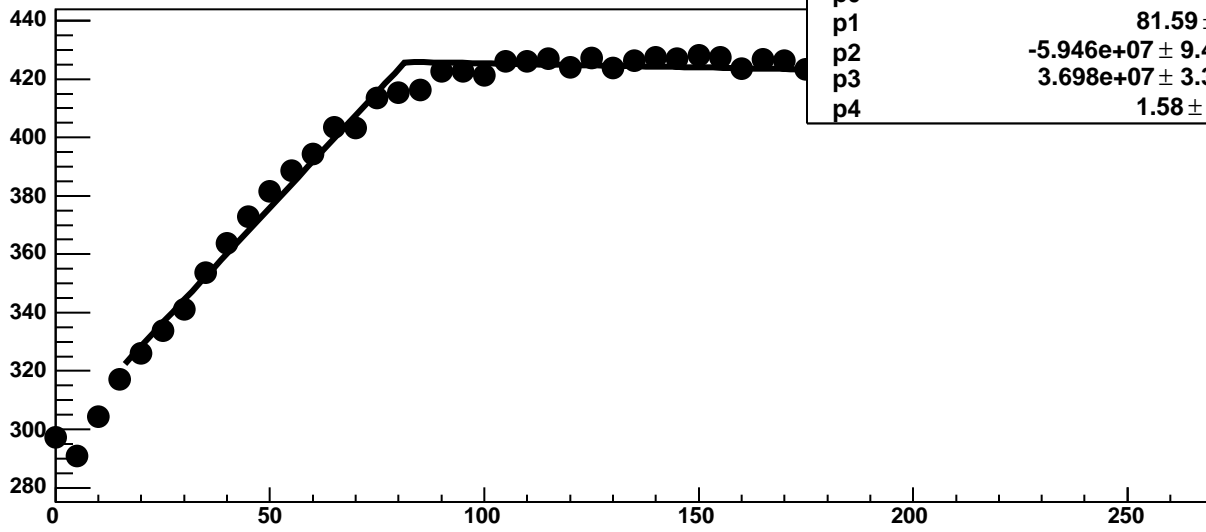
Chip 4, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold

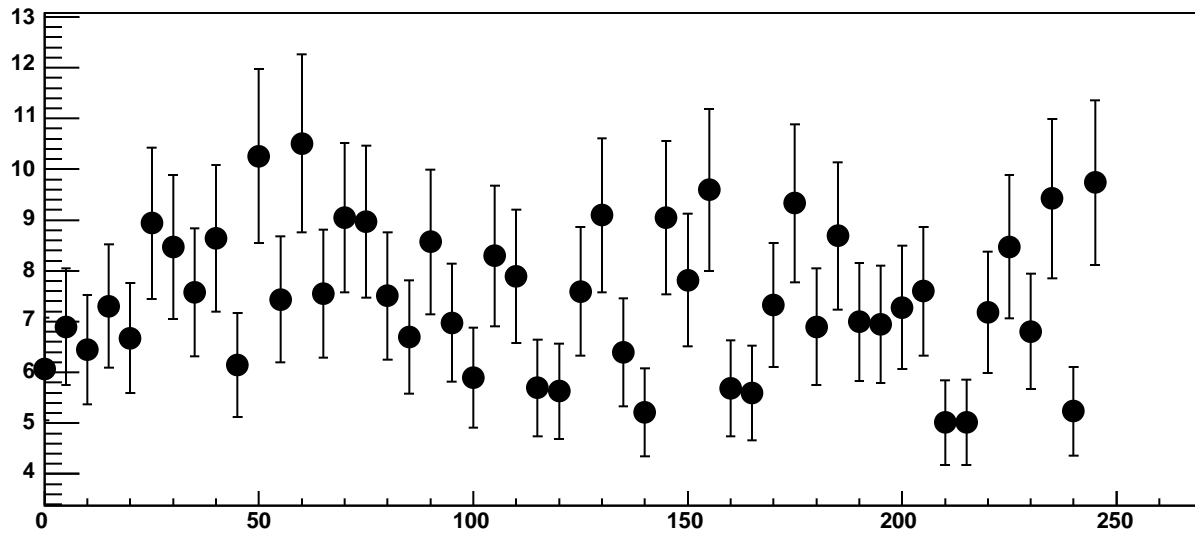


Chip 4, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

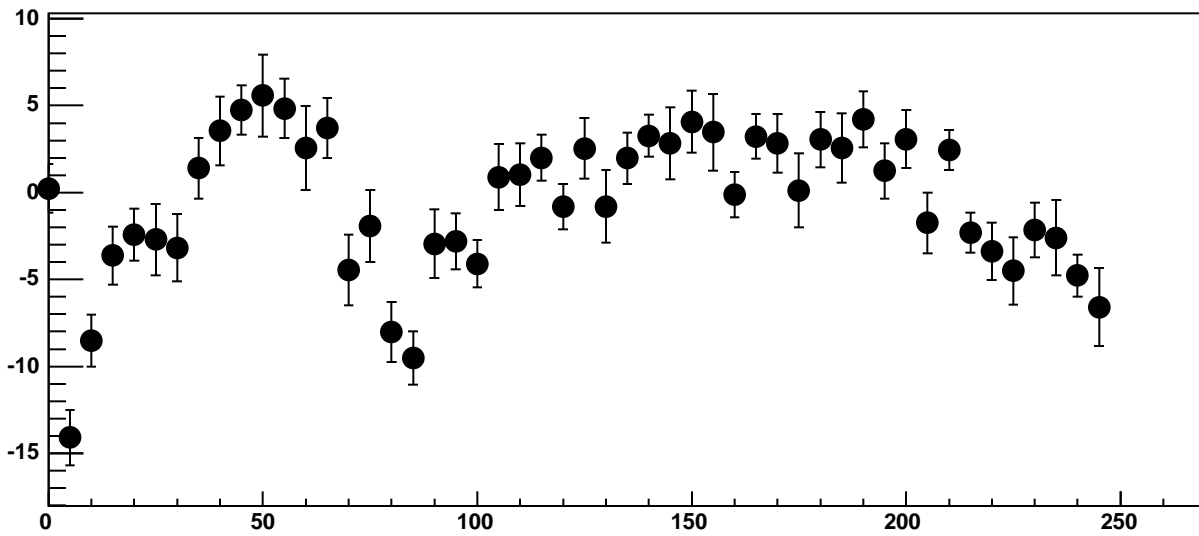


χ^2 / ndf	214.6 / 41
p0	425.9 ± 0.4938
p1	81.59 ± 0.6988
p2	$-5.946\text{e}+07 \pm 9.438\text{e}+05$
p3	$3.698\text{e}+07 \pm 3.398\text{e}+05$
p4	1.58 ± 0.02243

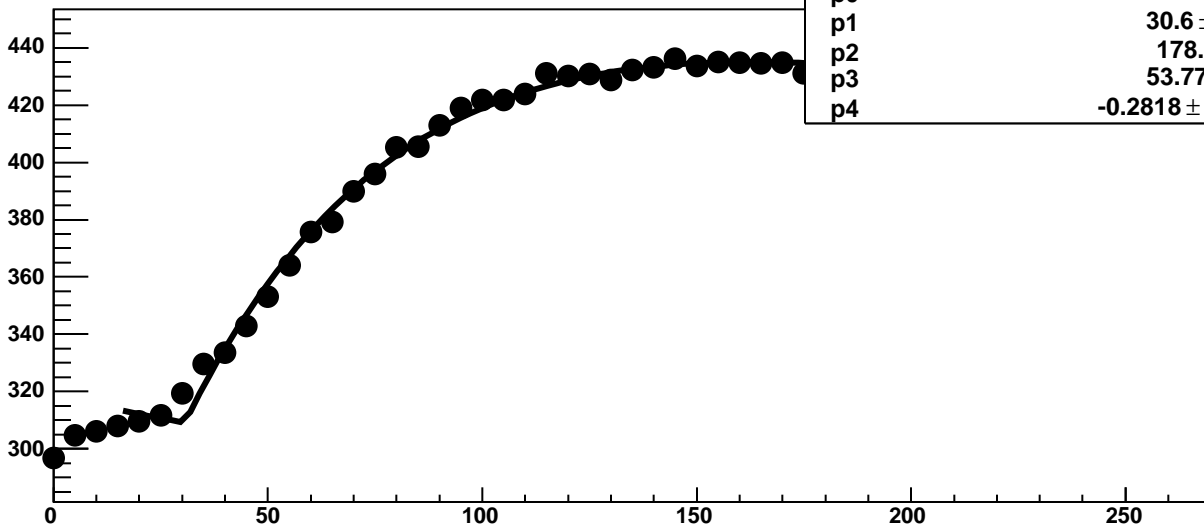
Chip 4, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

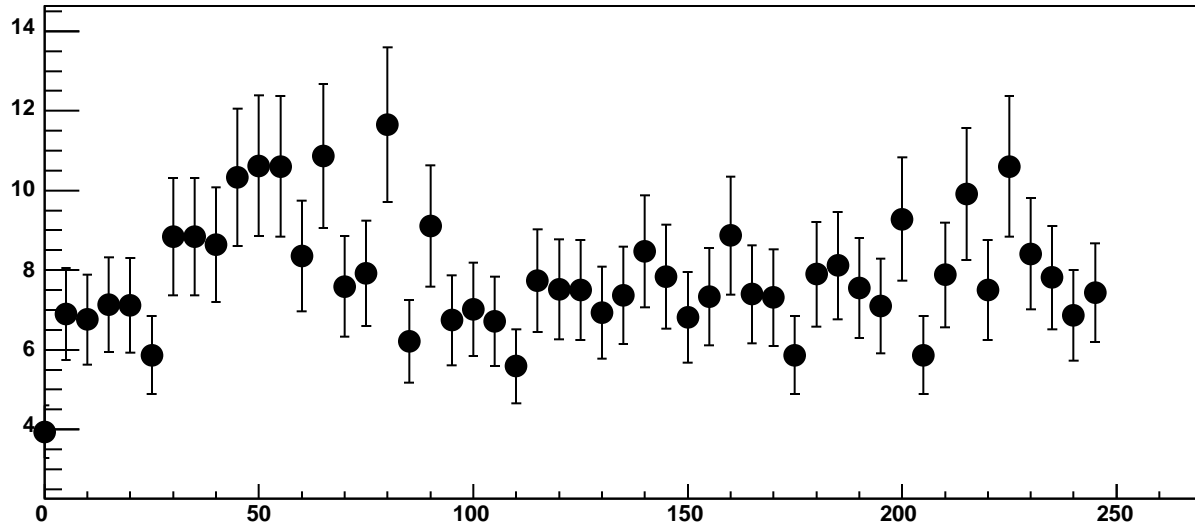


Chip 4, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

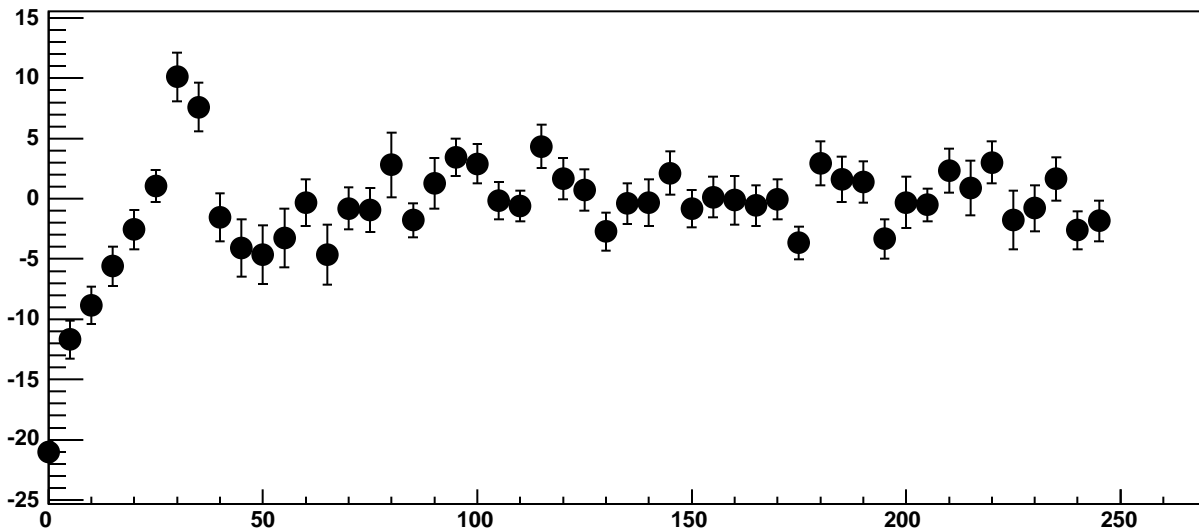


χ^2 / ndf	115.1 / 41
p0	309.1 ± 0.7633
p1	30.6 ± 0.5966
p2	178.4 ± 6.61
p3	53.77 ± 2.533
p4	-0.2818 ± 0.02933

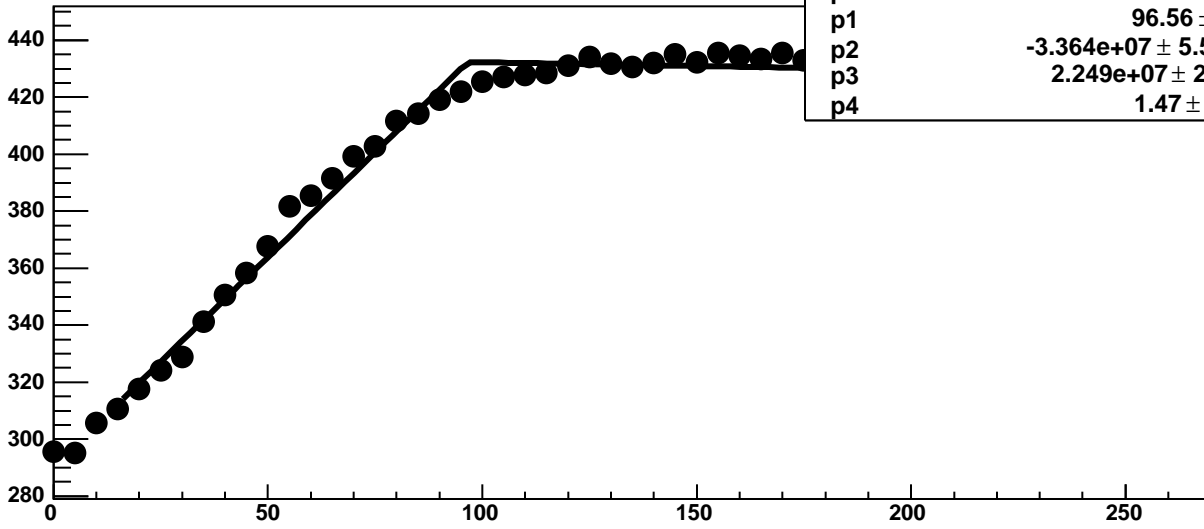
Chip 4, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

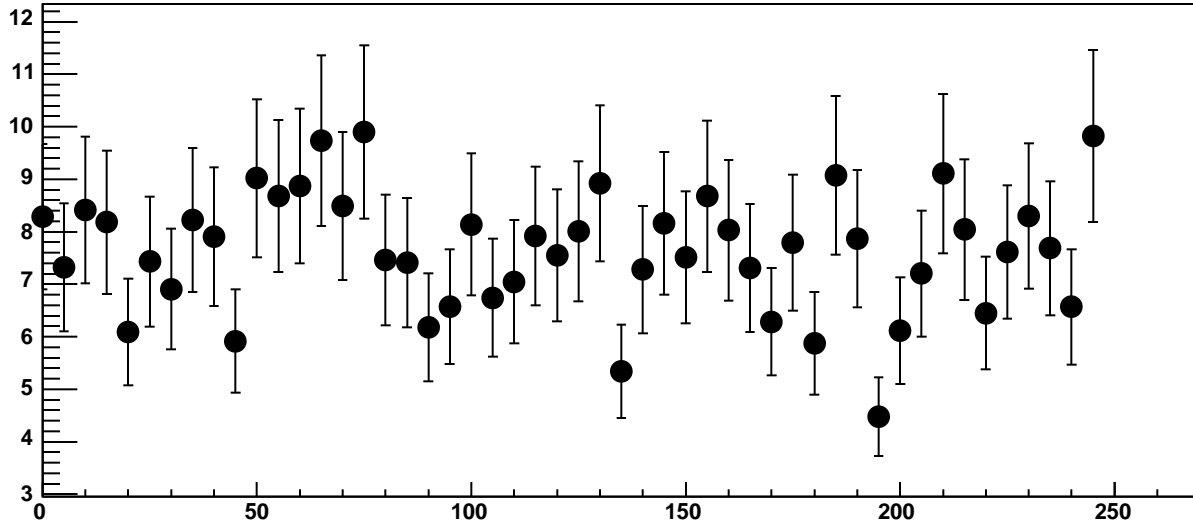


Chip 4, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold

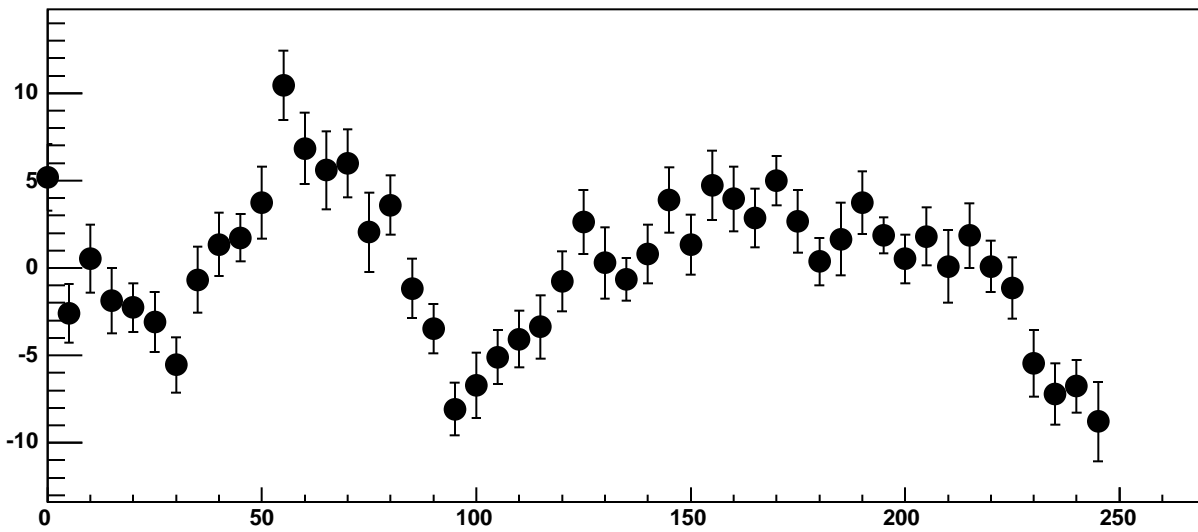


χ^2 / ndf	244.9 / 41
p0	432.3 ± 0.6228
p1	96.56 ± 0.6786
p2	$-3.364\text{e}+07 \pm 5.522\text{e}+05$
p3	$2.249\text{e}+07 \pm 2.57\text{e}+05$
p4	1.47 ± 0.01605

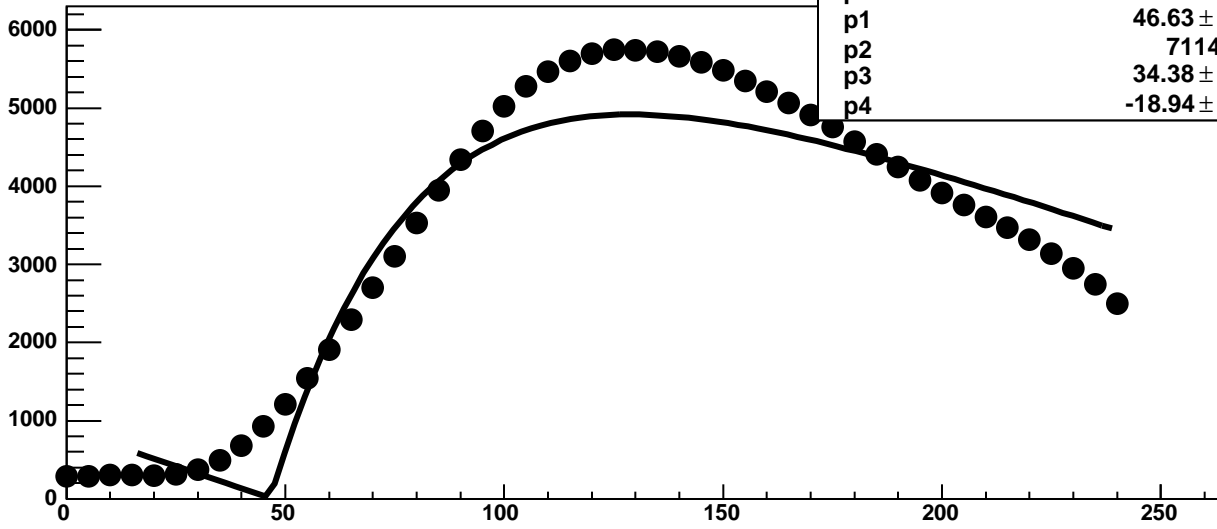
Chip 4, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold

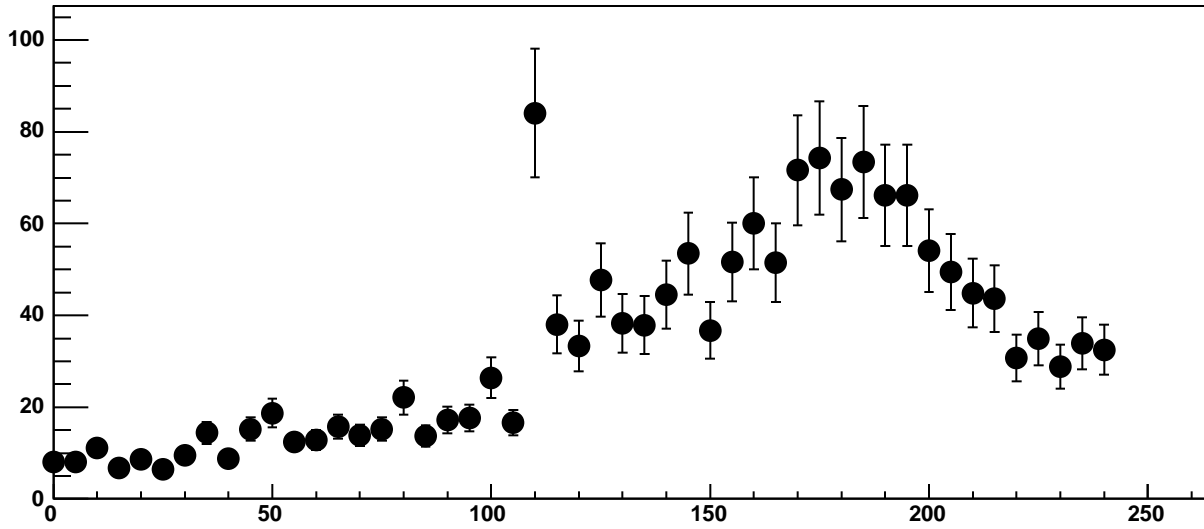


Chip 4, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold

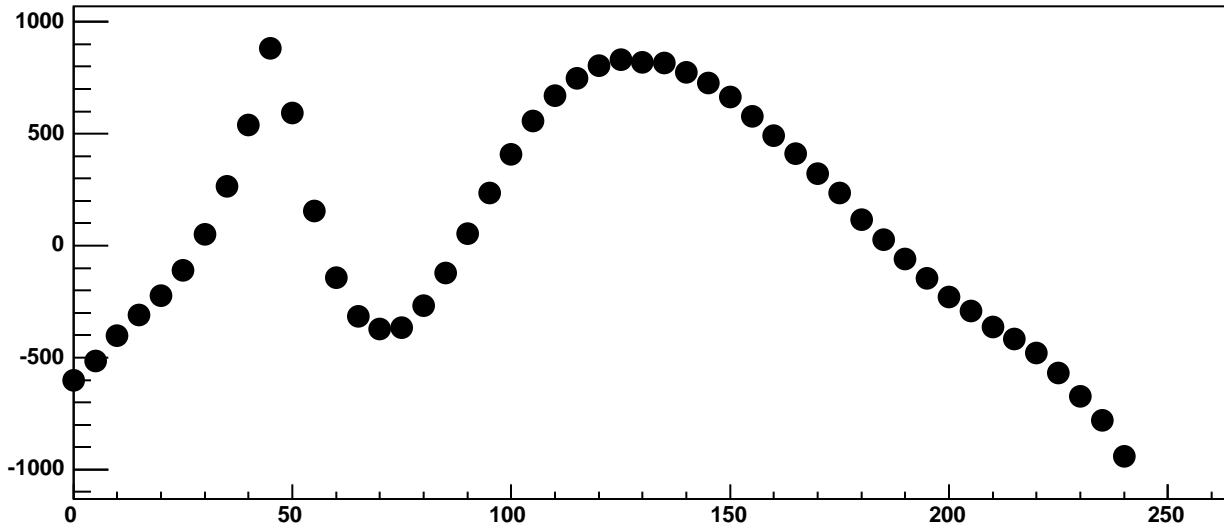


χ^2 / ndf	4.047e+05 / 41
p0	11.76 ± 1.096
p1	46.63 ± 0.01657
p2	7114 ± 8.186
p3	34.38 ± 0.05107
p4	-18.94 ± 0.04463

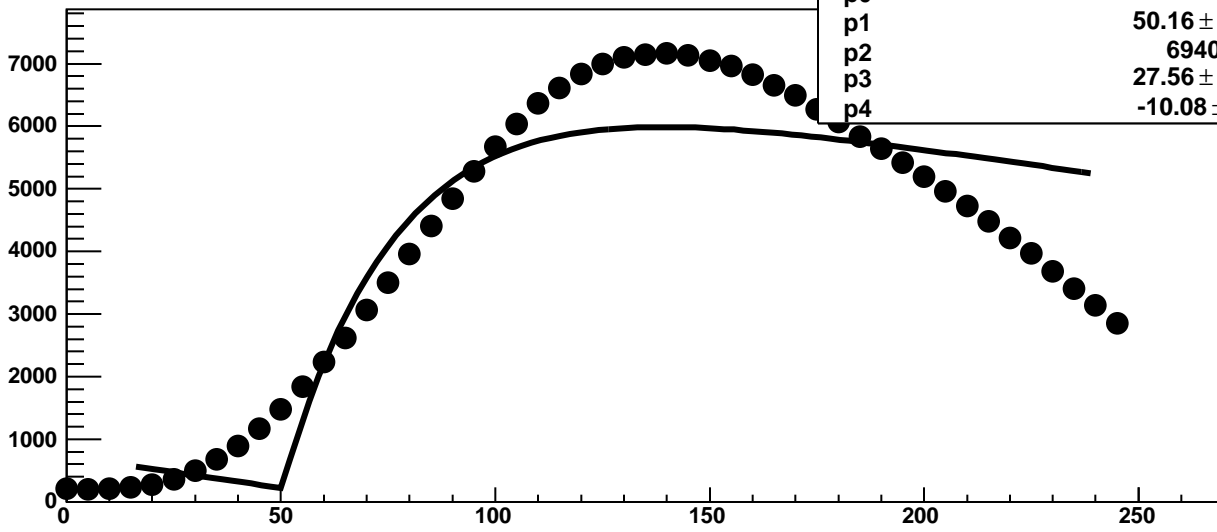
Chip 4, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold

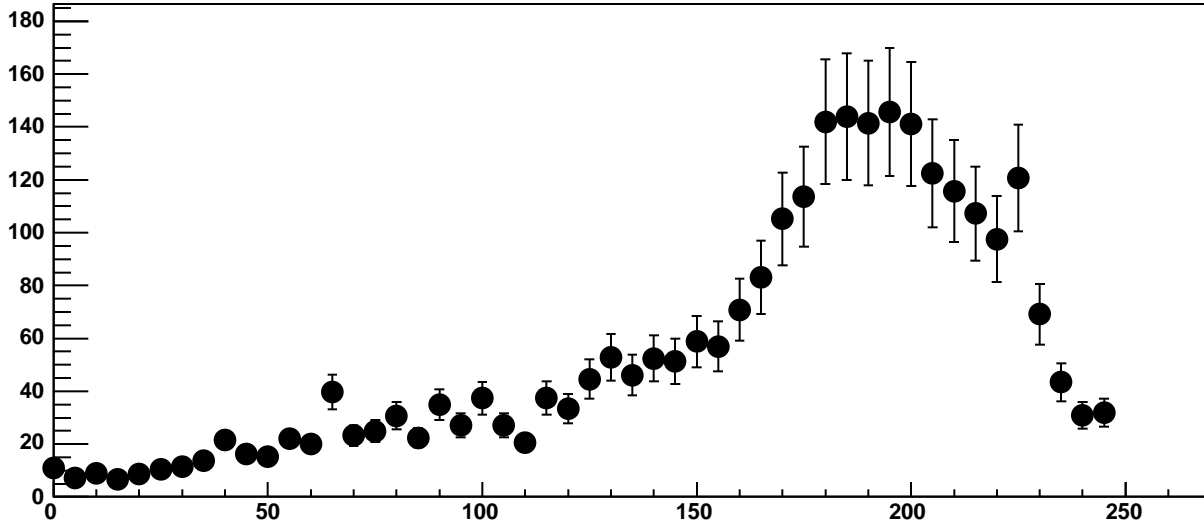


Chip 4, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

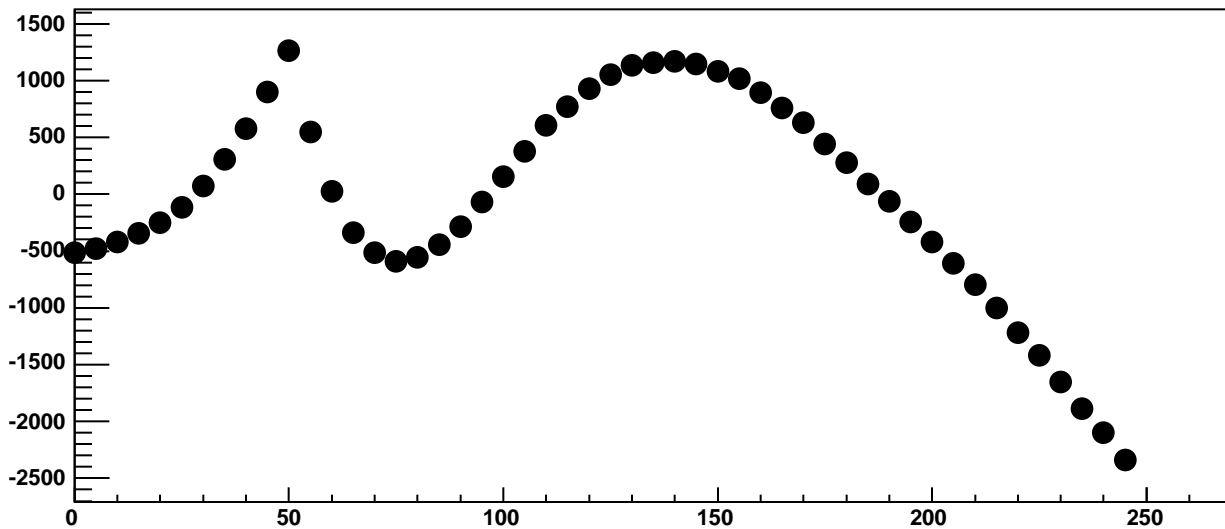


χ^2 / ndf	5.899e+05 / 41
p0	220.4 ± 1.429
p1	50.16 ± 0.02172
p2	6940 ± 7.785
p3	27.56 ± 0.04813
p4	-10.08 ± 0.0475

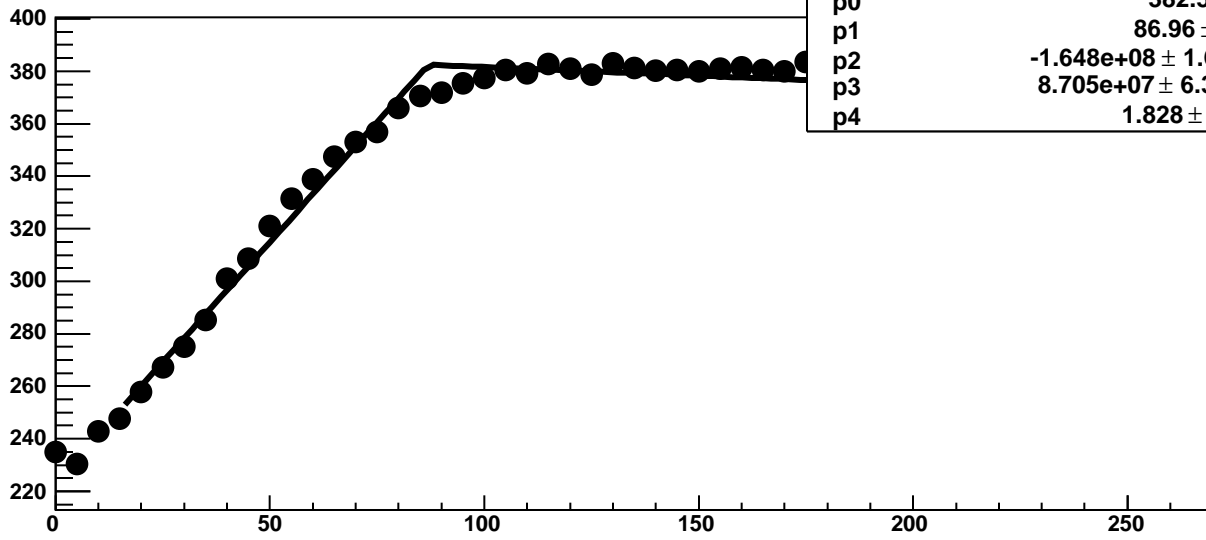
Chip 4, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

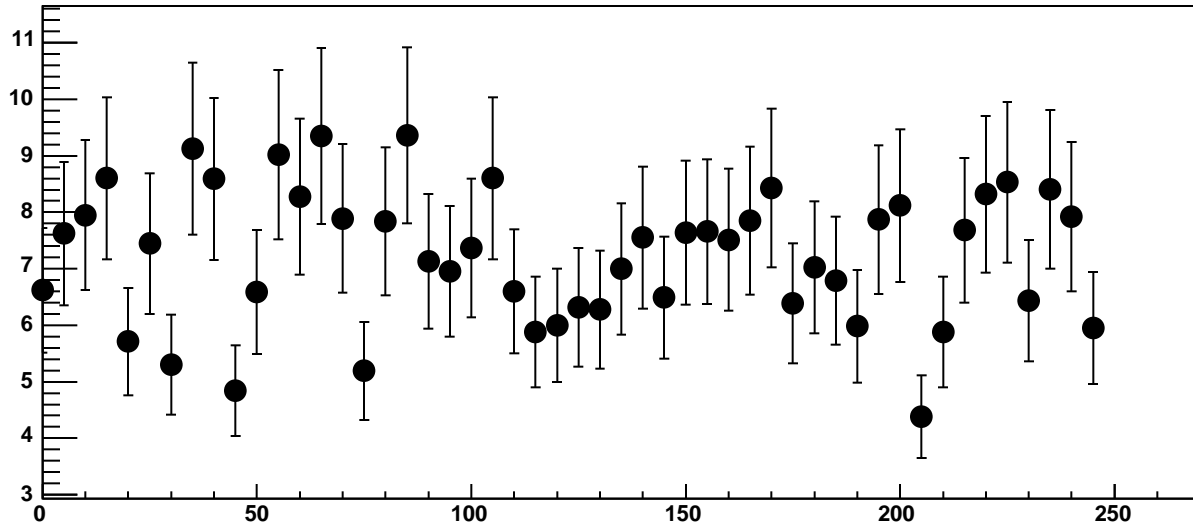


Chip 4, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold

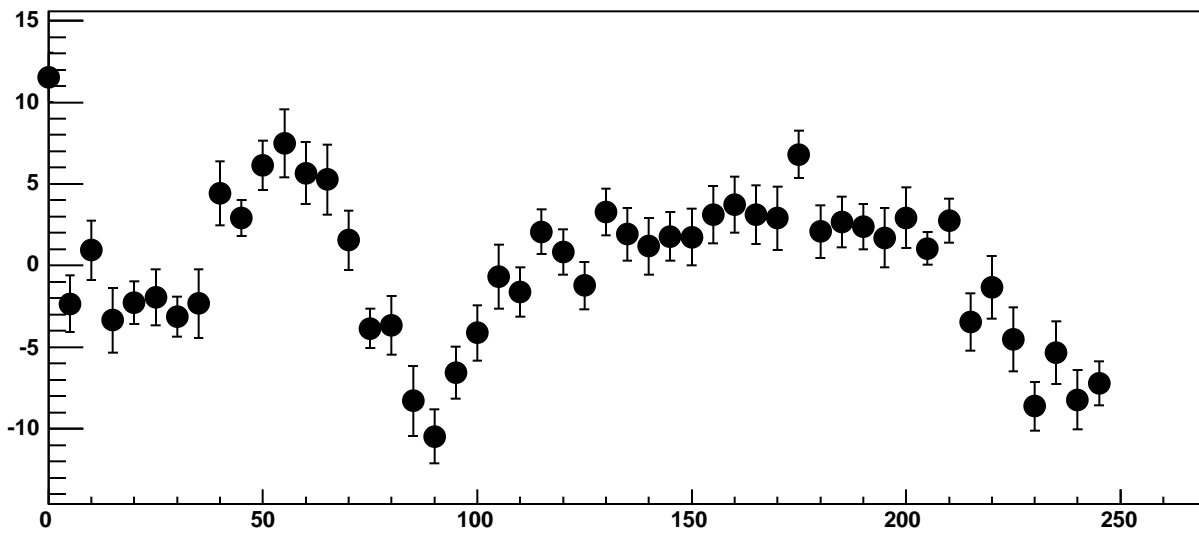


χ^2 / ndf	302.7 / 41
p0	382.5 ± 0.632
p1	86.96 ± 0.6905
p2	$-1.648\text{e}+08 \pm 1.685\text{e}+06$
p3	$8.705\text{e}+07 \pm 6.313\text{e}+05$
p4	1.828 ± 0.02136

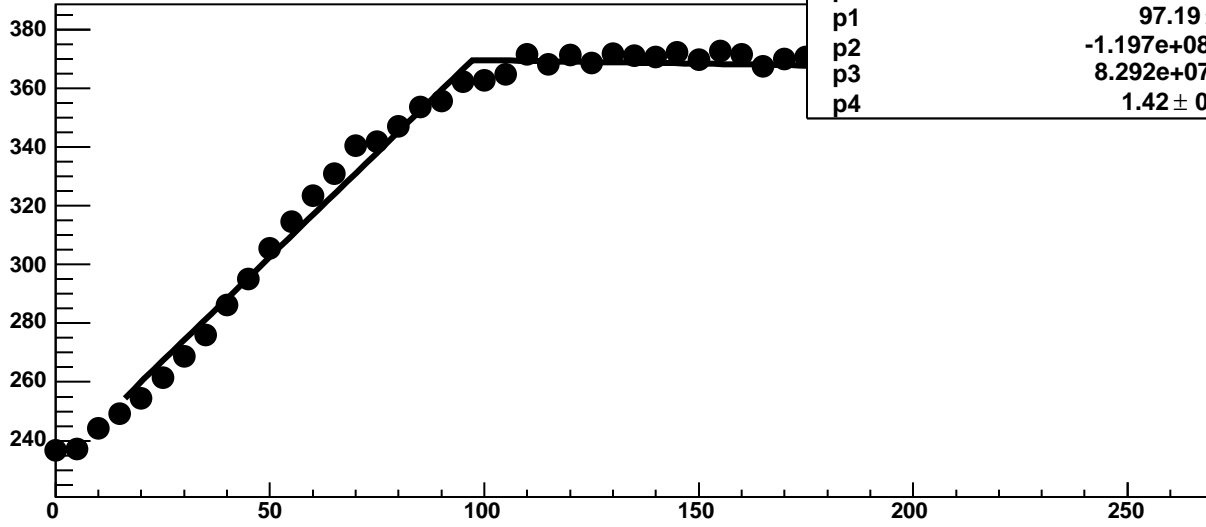
Chip 4, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold

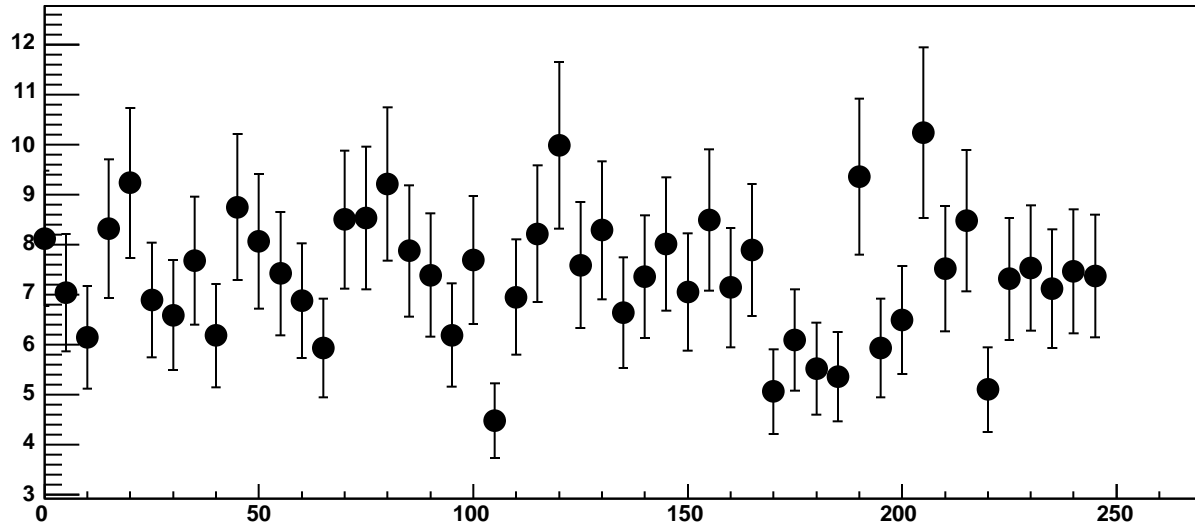


Chip 4, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

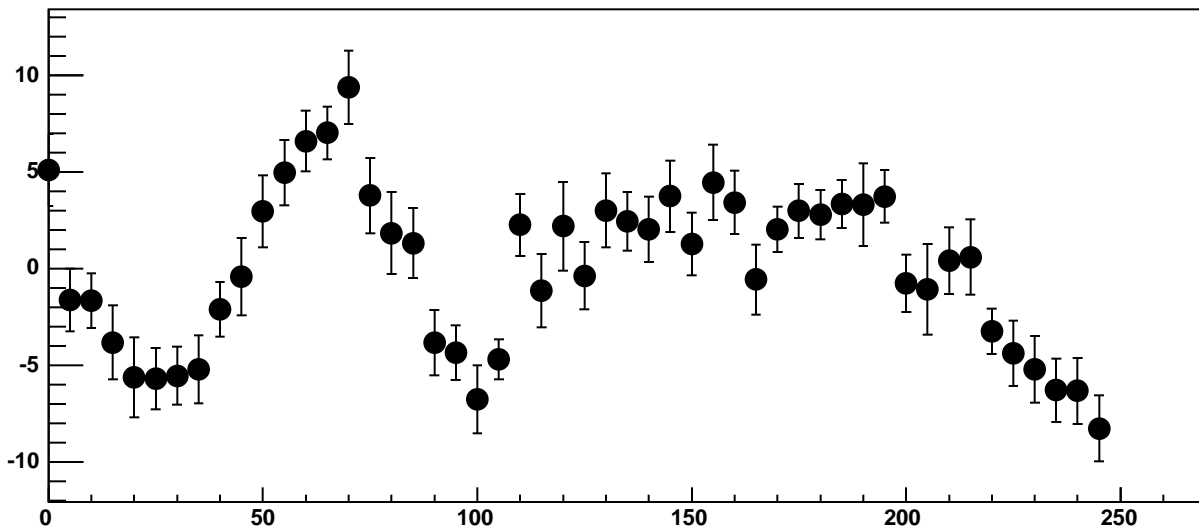


χ^2 / ndf	290.1 / 41
p0	369.6 ± 0.5393
p1	97.19 ± 0.6191
p2	$-1.197\text{e}+08 \pm 1.414$
p3	$8.292\text{e}+07 \pm 1.414$
p4	1.42 ± 0.006468

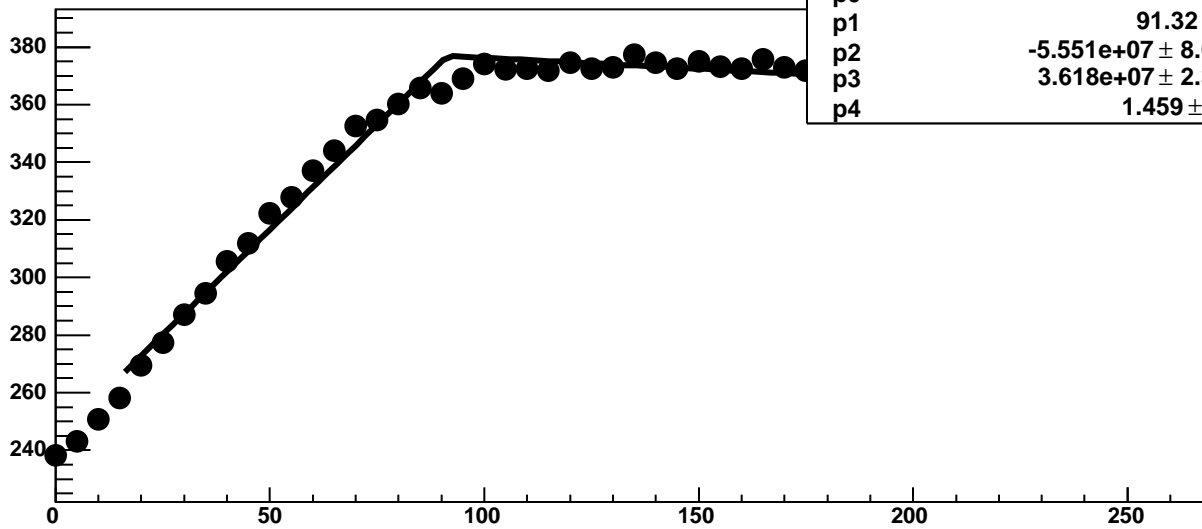
Chip 4, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

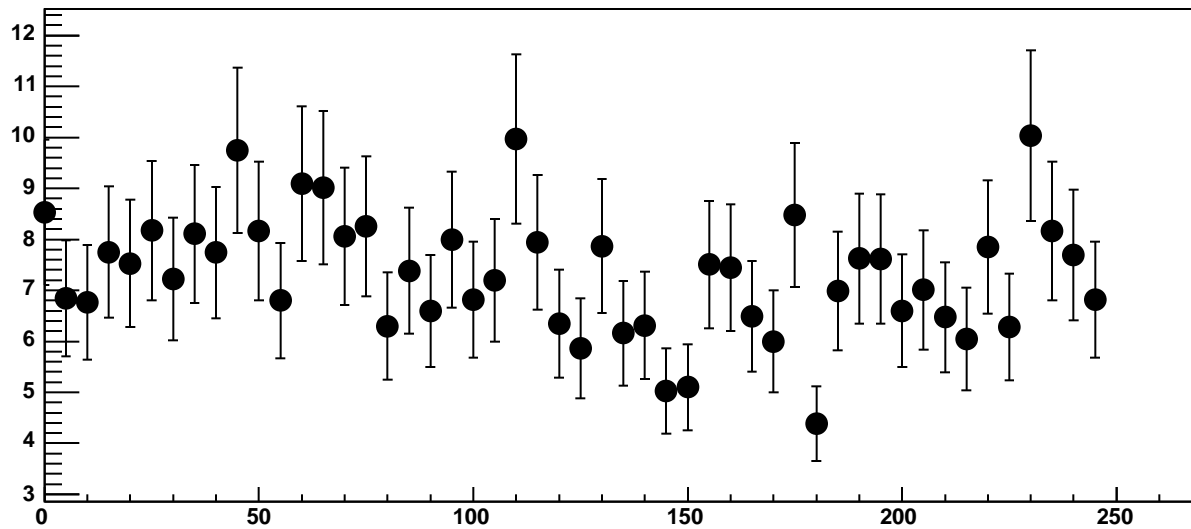


Chip 4, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

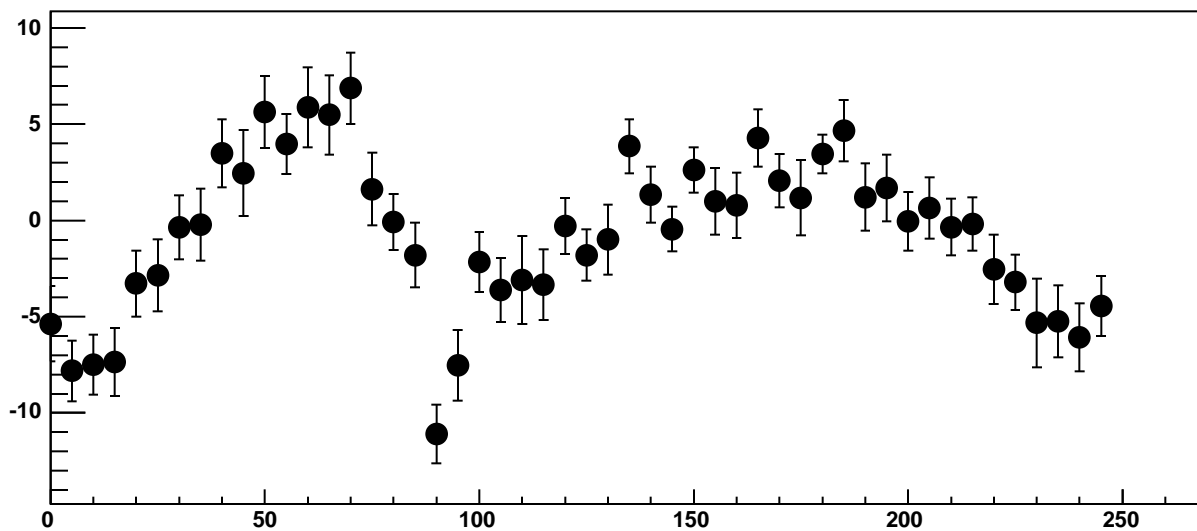


χ^2 / ndf	237.7 / 41
p0	376.9 ± 0.5698
p1	91.32 ± 0.6579
p2	$-5.551\text{e}+07 \pm 8.015\text{e}+05$
p3	$3.618\text{e}+07 \pm 2.386\text{e}+05$
p4	1.459 ± 0.01835

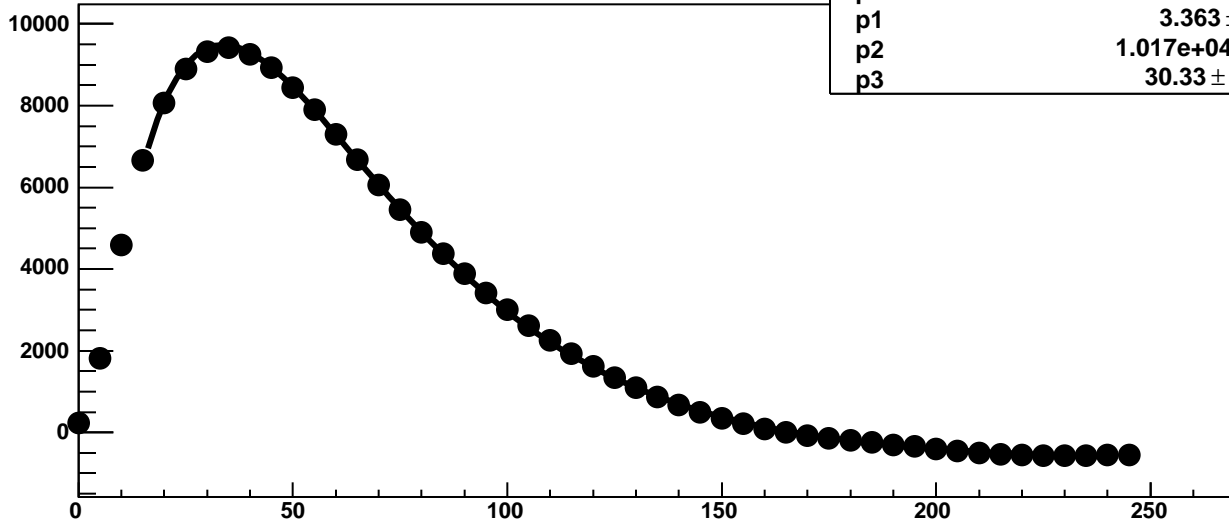
Chip 4, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

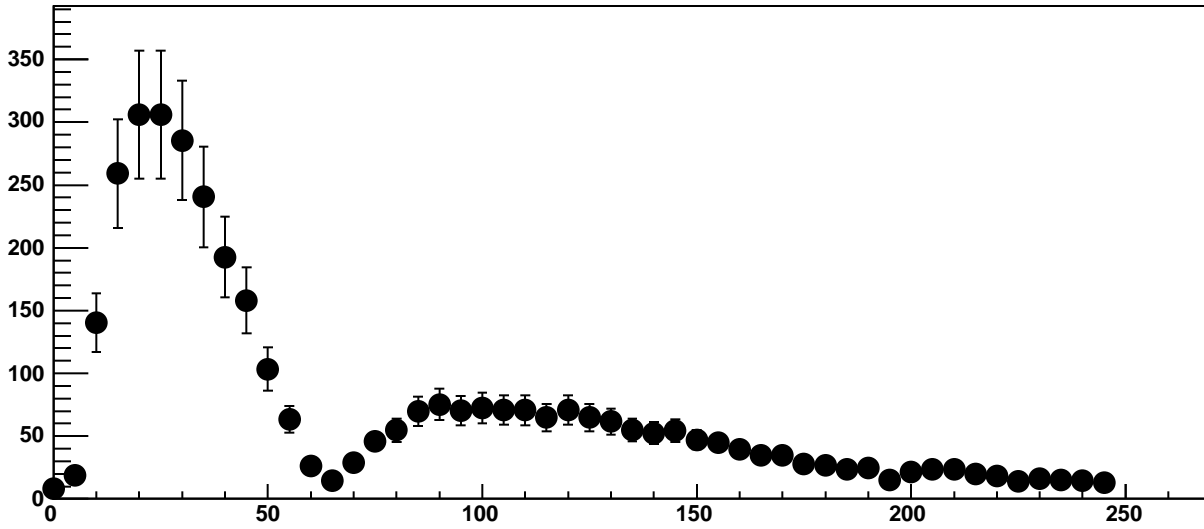


Chip 4, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold

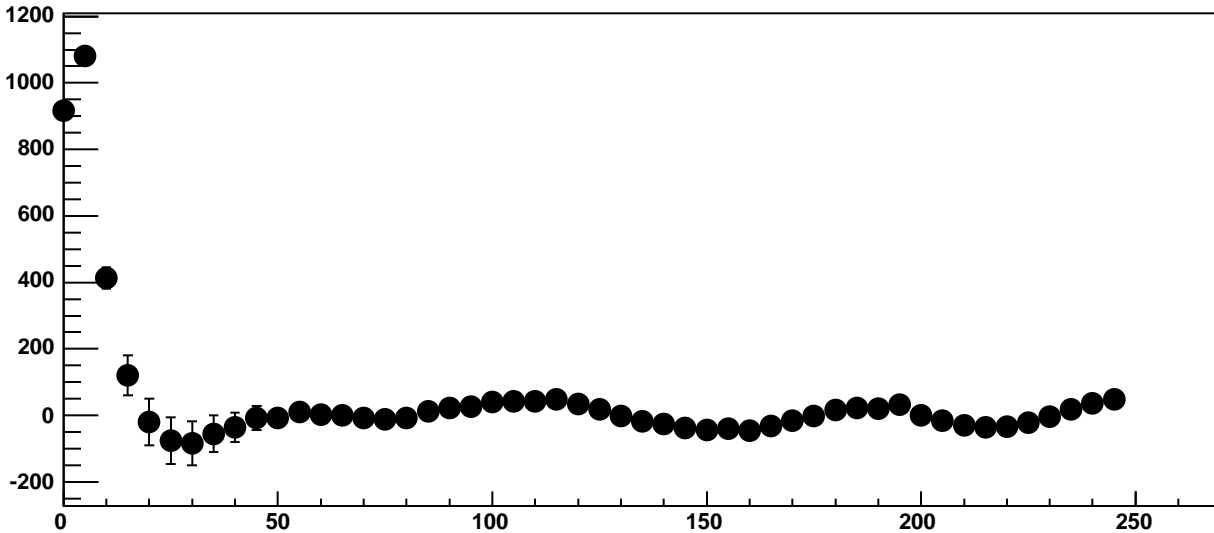


χ^2 / ndf	625.7 / 42
p0	-682.4 \pm 1.752
p1	3.363 \pm 0.1067
p2	1.017e+04 \pm 12.67
p3	30.33 \pm 0.03828

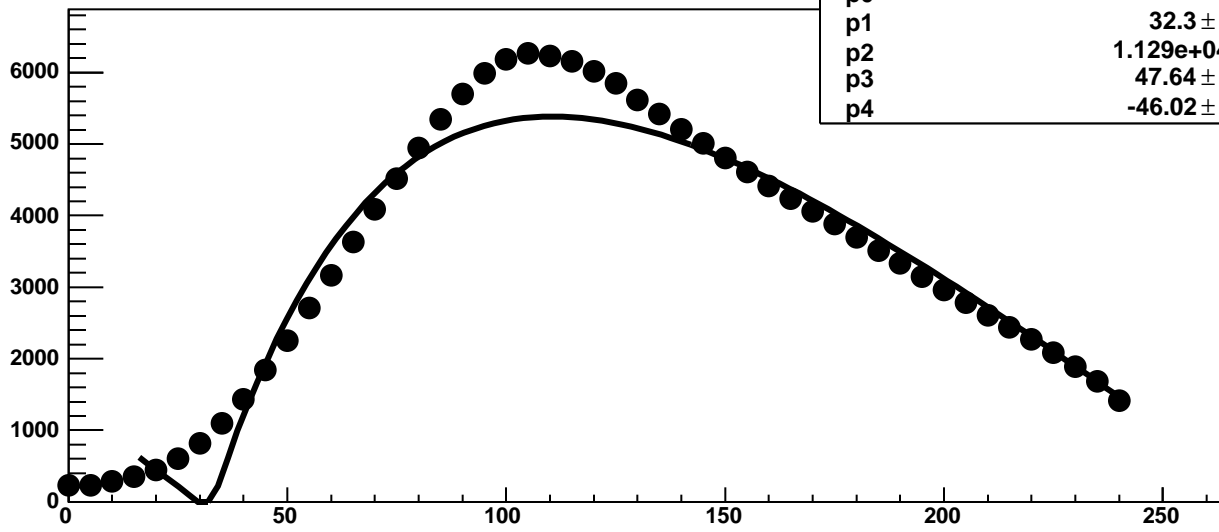
Chip 4, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold

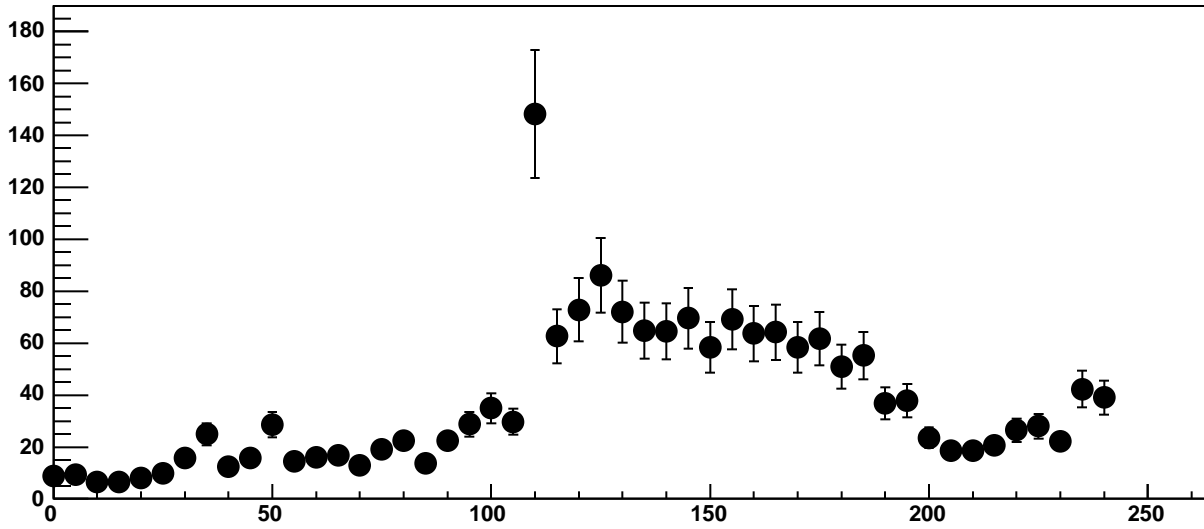


Chip 4, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold

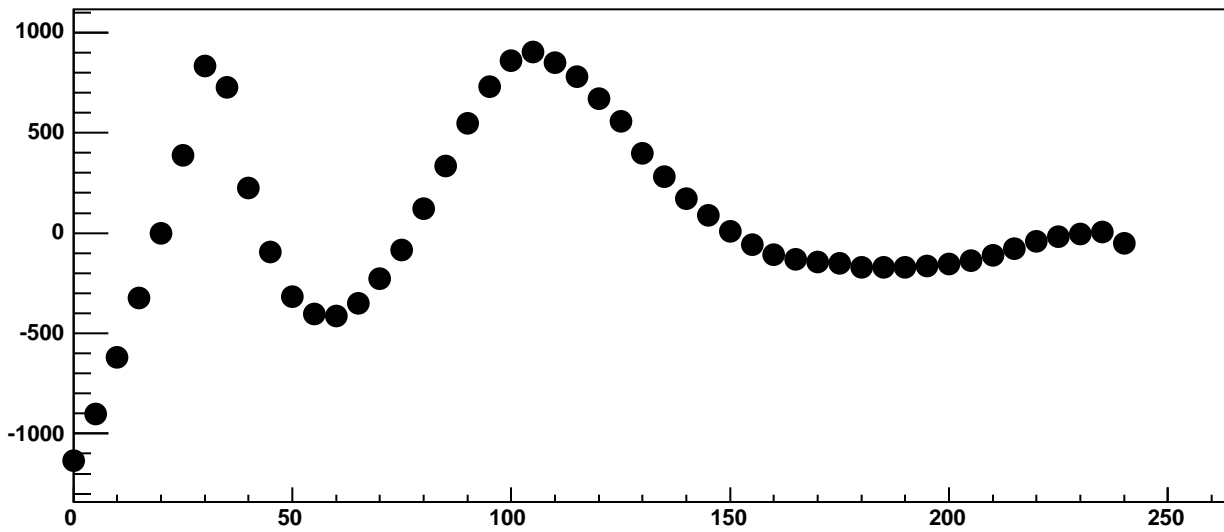


χ^2 / ndf	2.709e+05 / 41
p0	-121.6 ± 1.186
p1	32.3 ± 0.01538
p2	1.129e+04 ± 16.6
p3	47.64 ± 0.07737
p4	-46.02 ± 0.07766

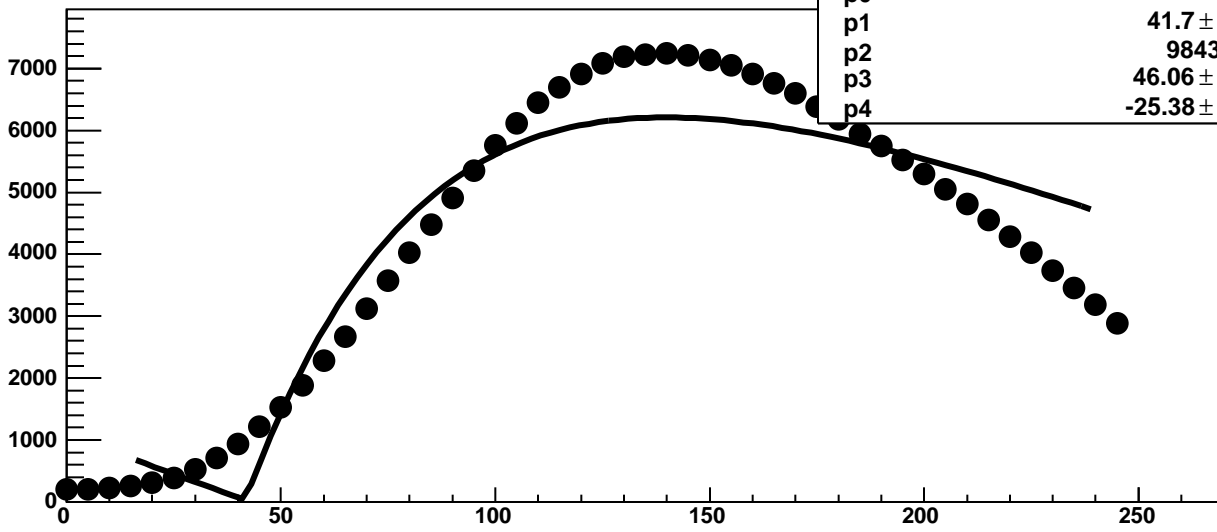
Chip 4, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold

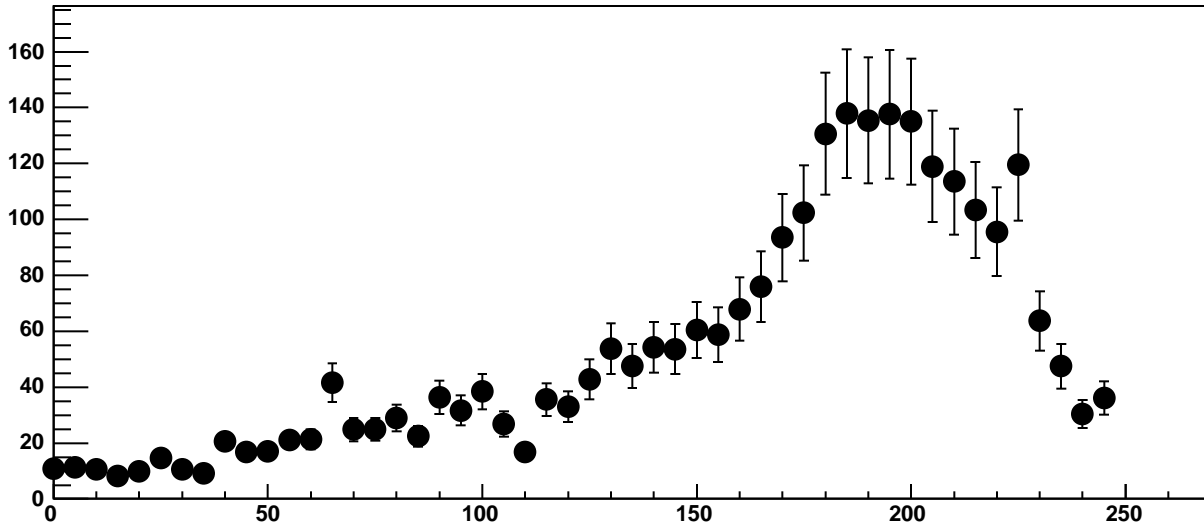


Chip 4, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold

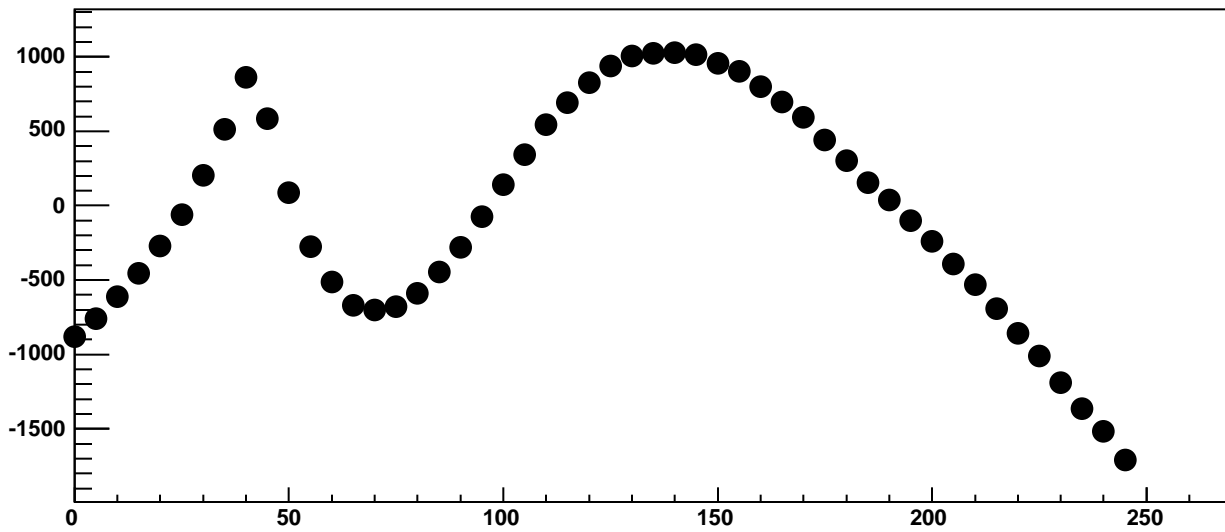


χ^2 / ndf	4.336e+05 / 41
p0	27.15 ± 1.345
p1	41.7 ± 0.01629
p2	9843 ± 13.43
p3	46.06 ± 0.07025
p4	-25.38 ± 0.06828

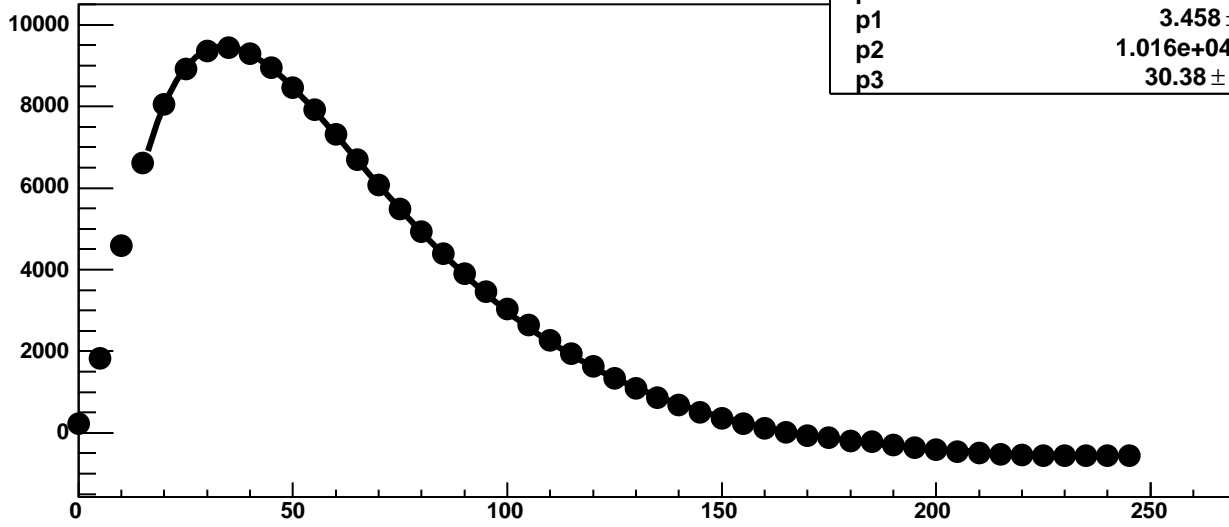
Chip 4, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

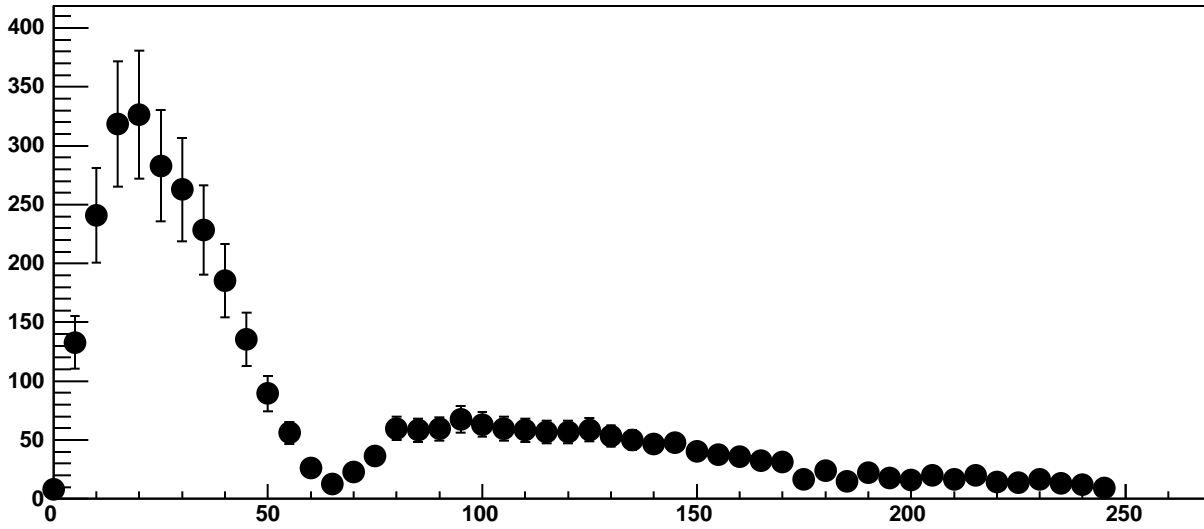


Chip 4, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold

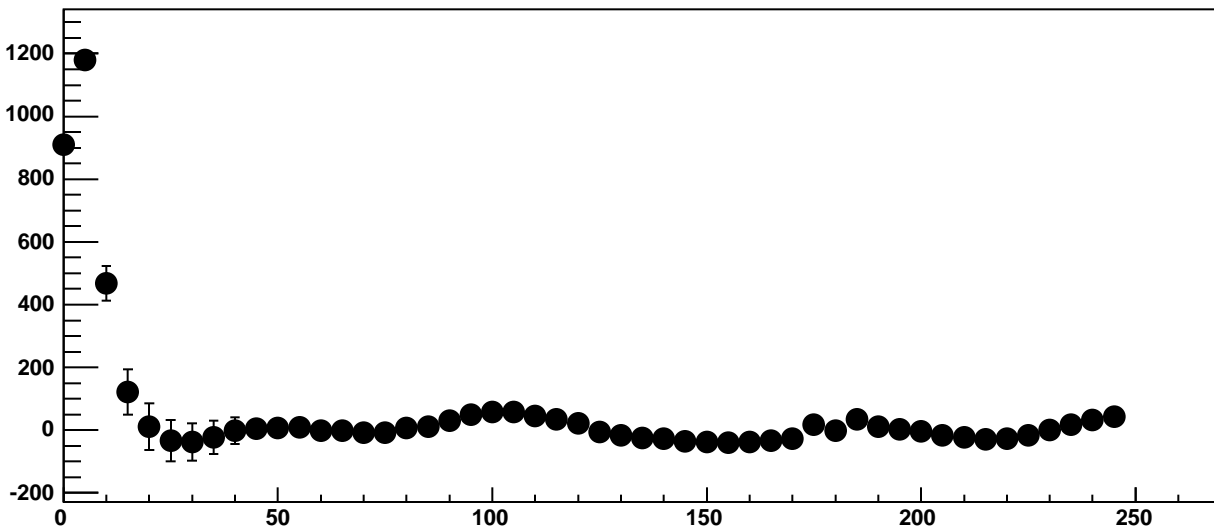


χ^2 / ndf	676.9 / 42
p0	-678.3 \pm 1.579
p1	3.458 \pm 0.1135
p2	1.016e+04 \pm 13.22
p3	30.38 \pm 0.03535

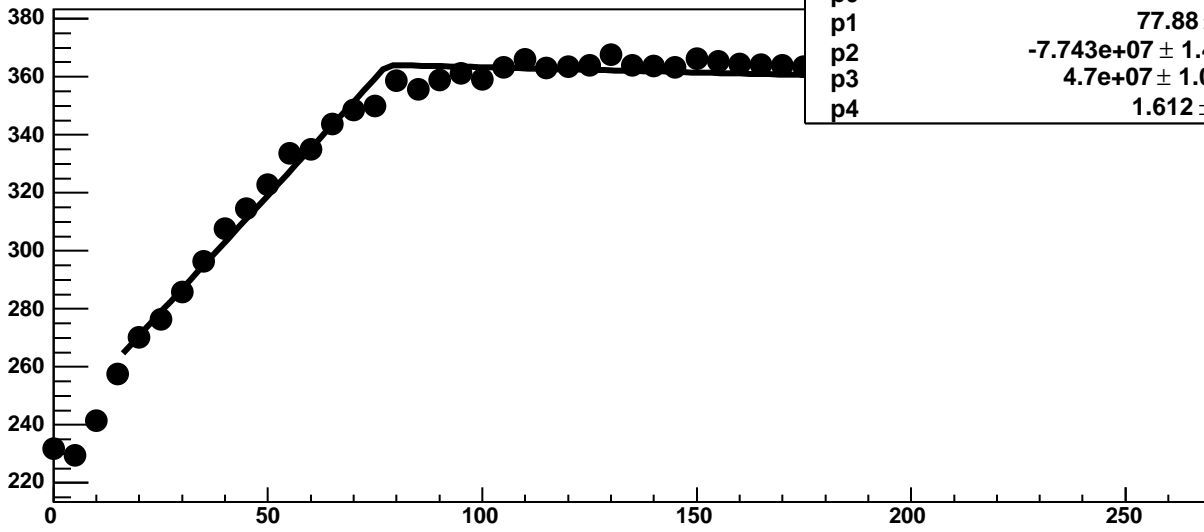
Chip 4, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold



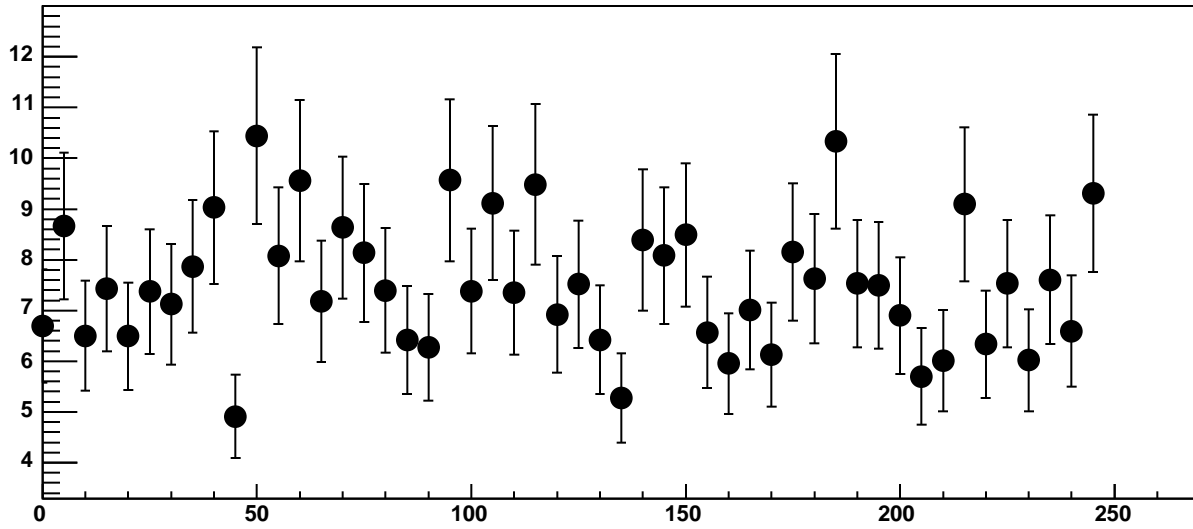
Chip 4, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold



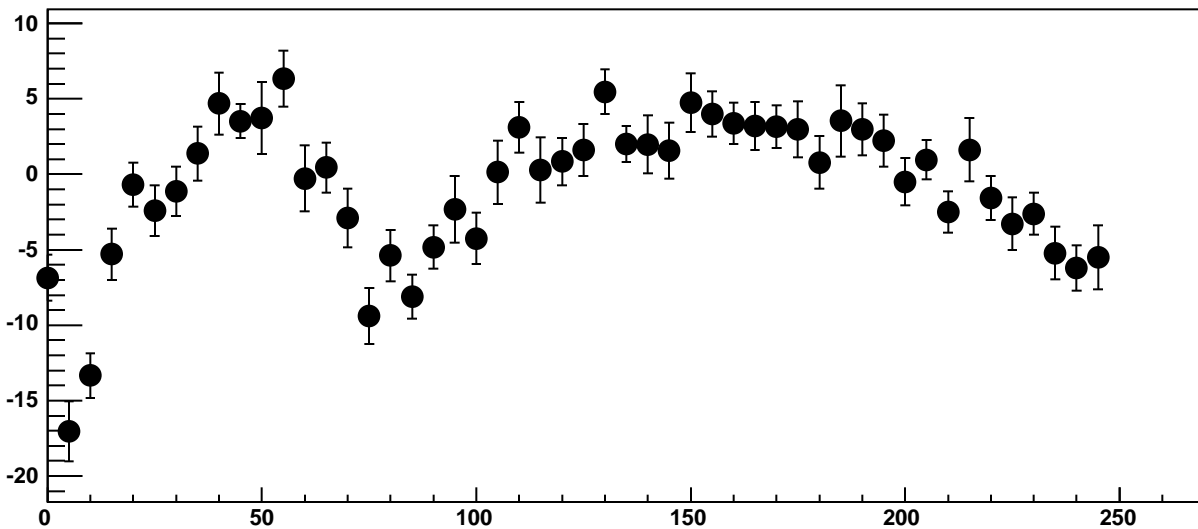
Chip 4, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold



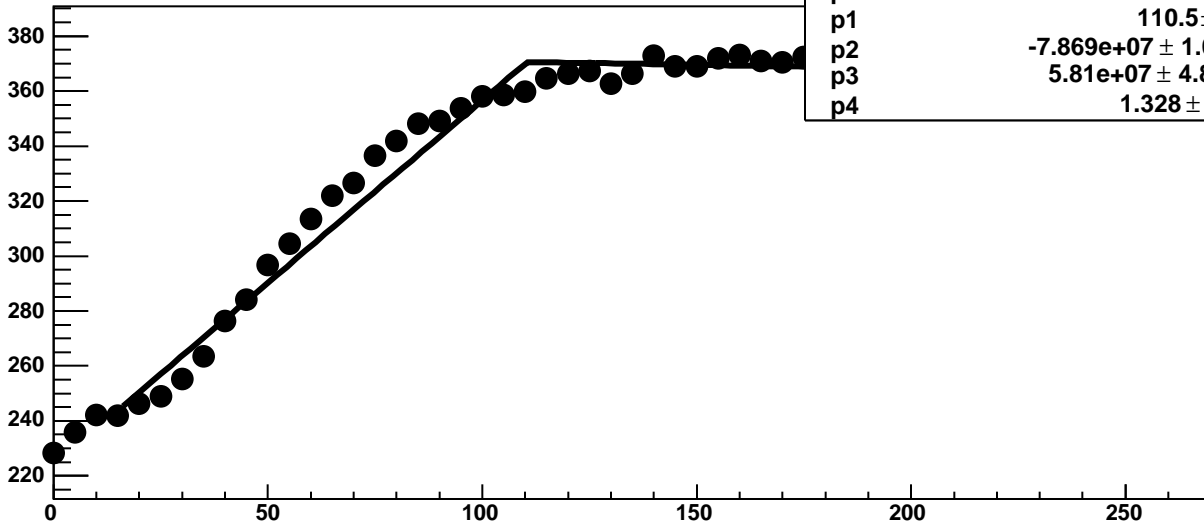
Chip 4, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

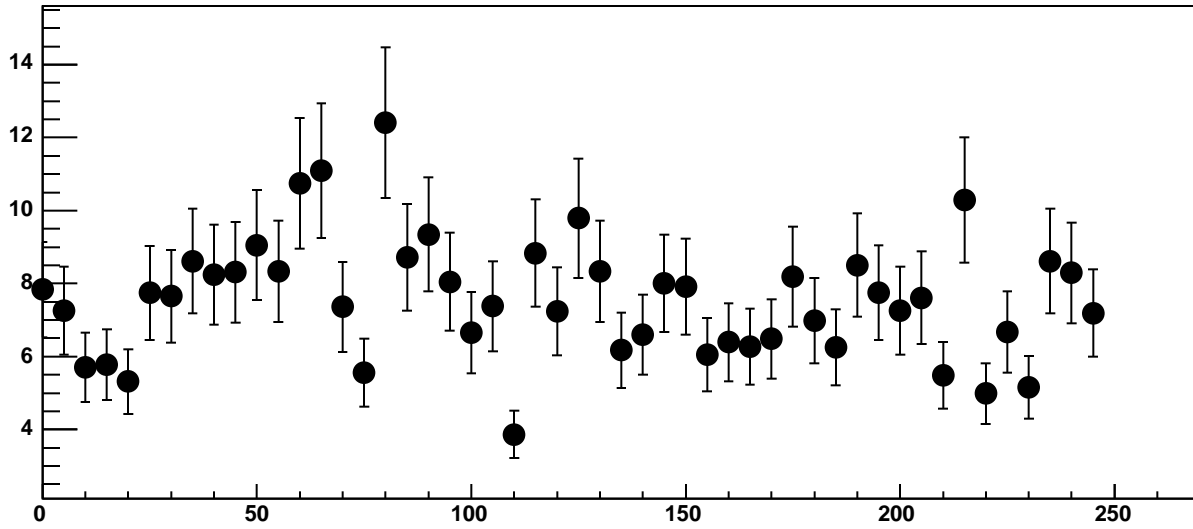


Chip 4, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

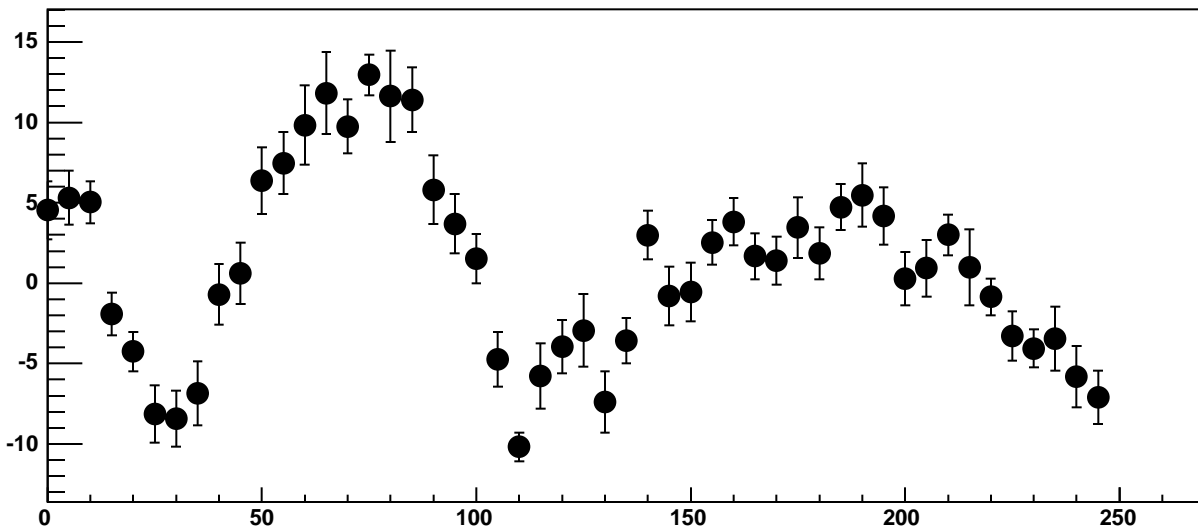


χ^2 / ndf	587.8 / 41
p0	370.6 ± 0.6726
p1	110.5 ± 0.6843
p2	$-7.869\text{e}+07 \pm 1.052\text{e}+06$
p3	$5.81\text{e}+07 \pm 4.825\text{e}+05$
p4	1.328 ± 0.01084

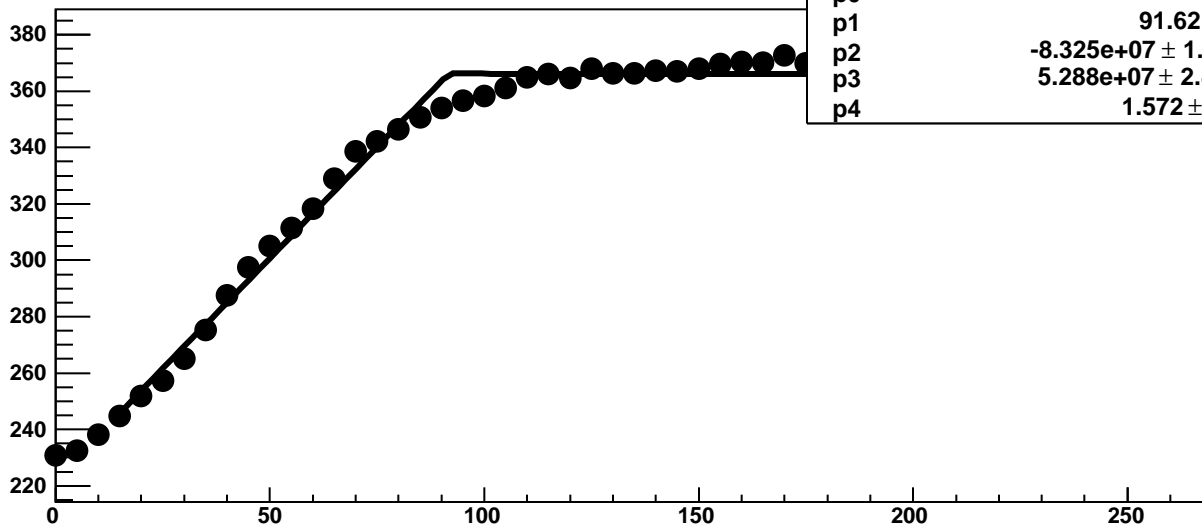
Chip 4, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

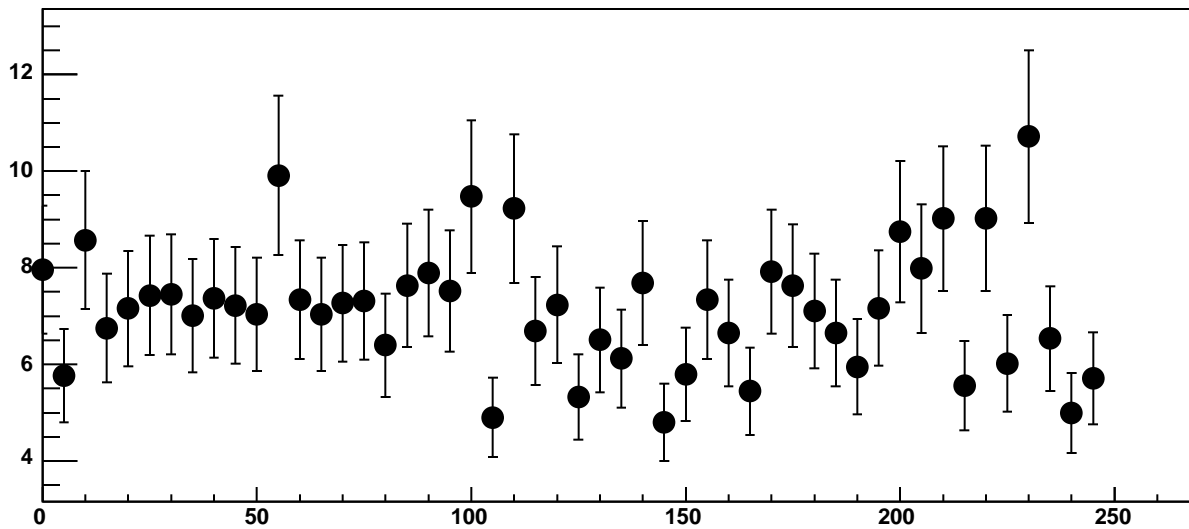


Chip 4, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold

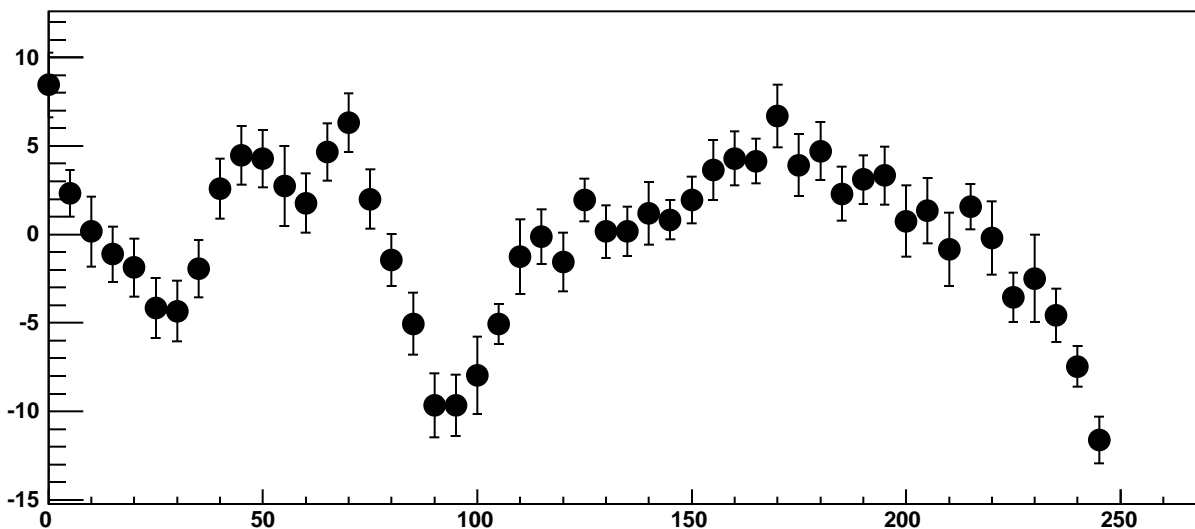


χ^2 / ndf	293 / 41
p0	366.2 ± 0.557
p1	91.62 ± 0.6431
p2	$-8.325e+07 \pm 1.126e+06$
p3	$5.288e+07 \pm 2.879e+05$
p4	1.572 ± 0.01805

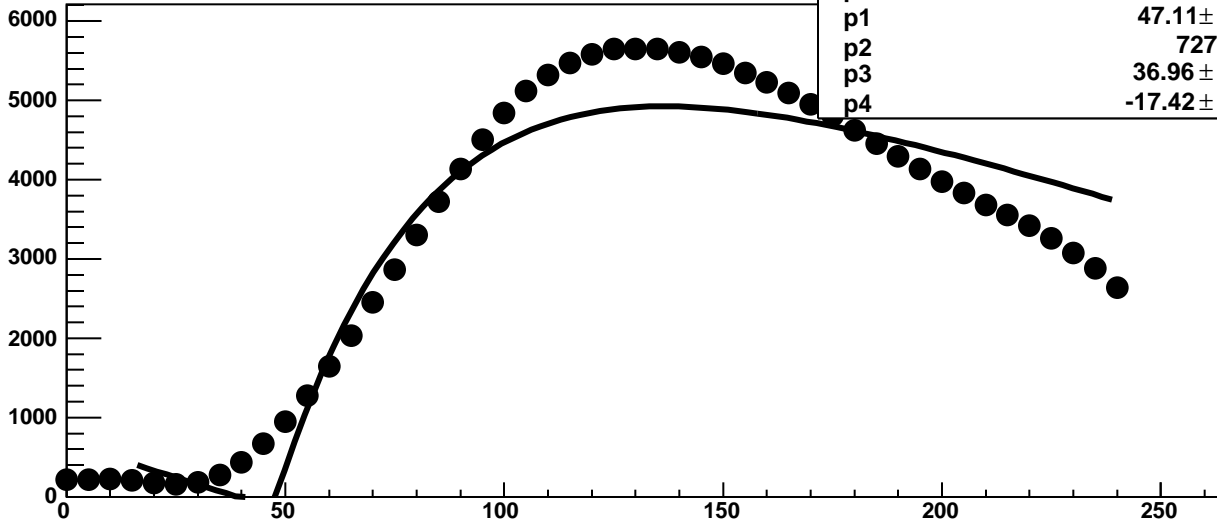
Chip 4, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold

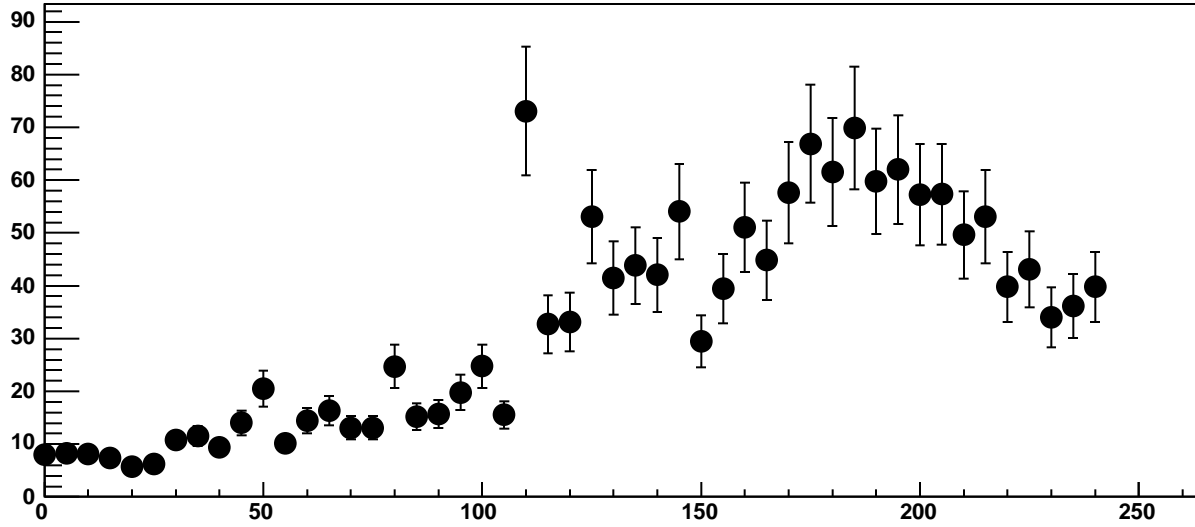


Chip 4, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

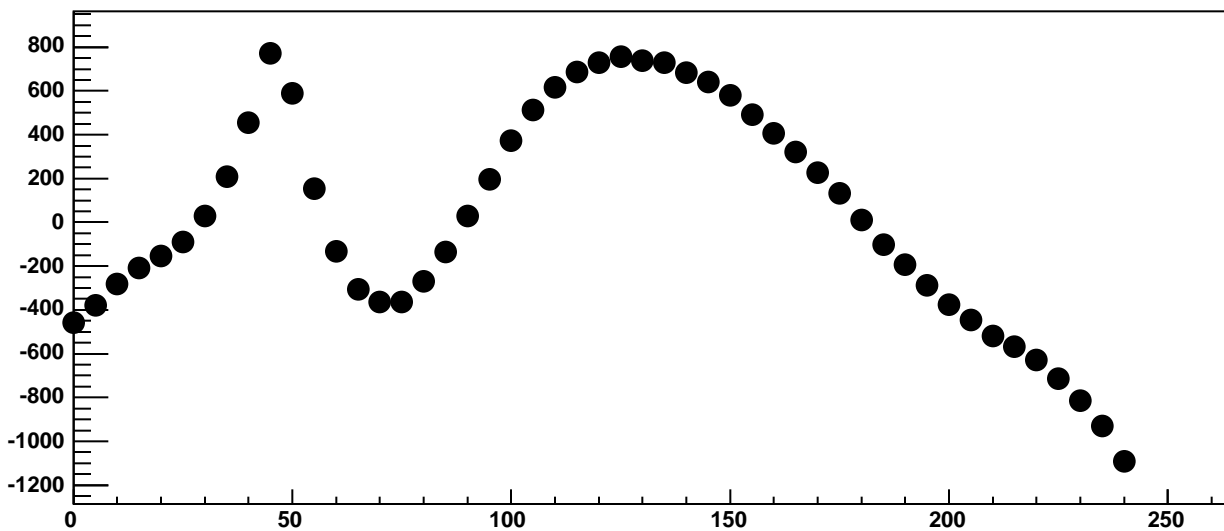


χ^2 / ndf	3.397e+05 / 41
p0	-140.3 ± 1.157
p1	47.11 ± 0.01626
p2	7270 ± 8.91
p3	36.96 ± 0.05505
p4	-17.42 ± 0.04844

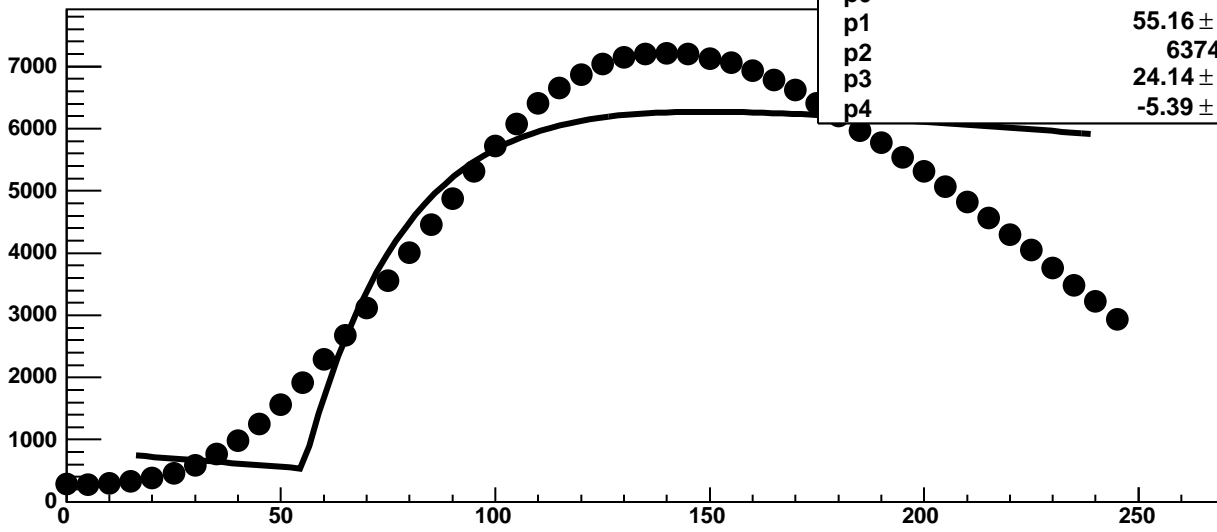
Chip 4, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

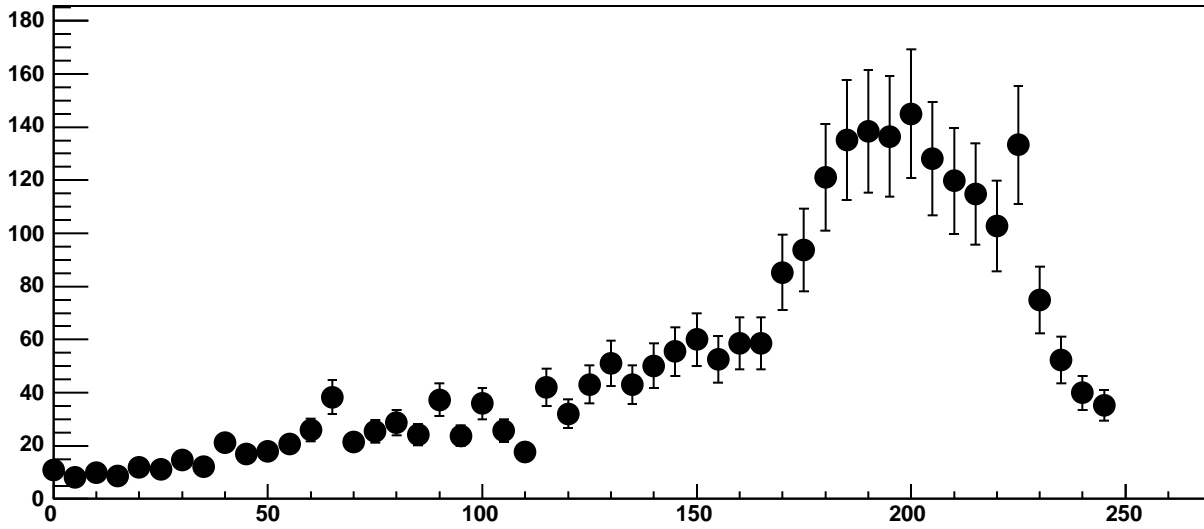


Chip 4, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

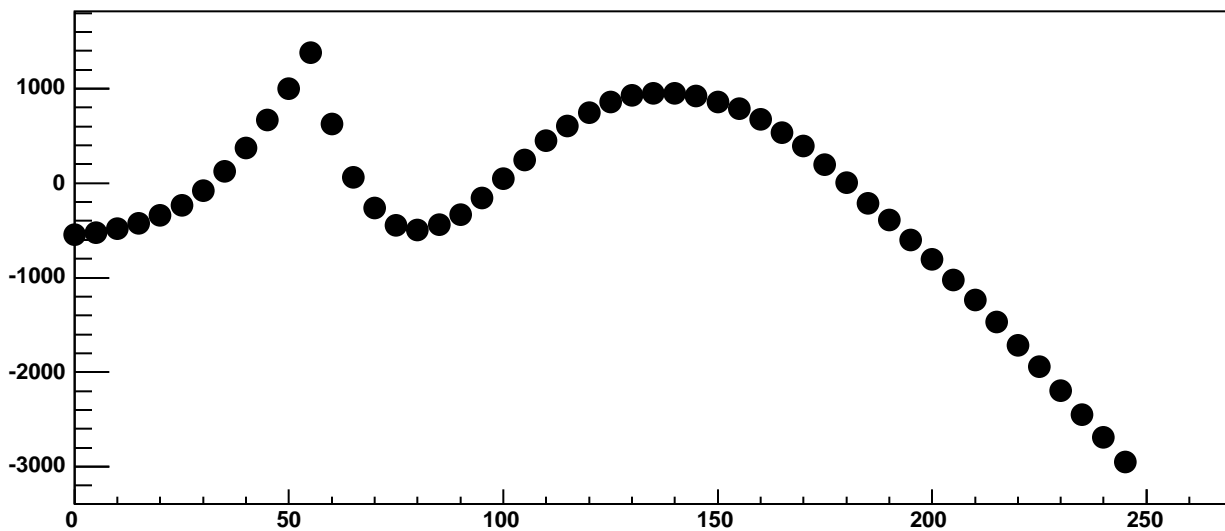


χ^2 / ndf	5.277e+05 / 41
p0	534.7 ± 1.662
p1	55.16 ± 0.02608
p2	6374 ± 7.507
p3	24.14 ± 0.04746
p4	-5.39 ± 0.05012

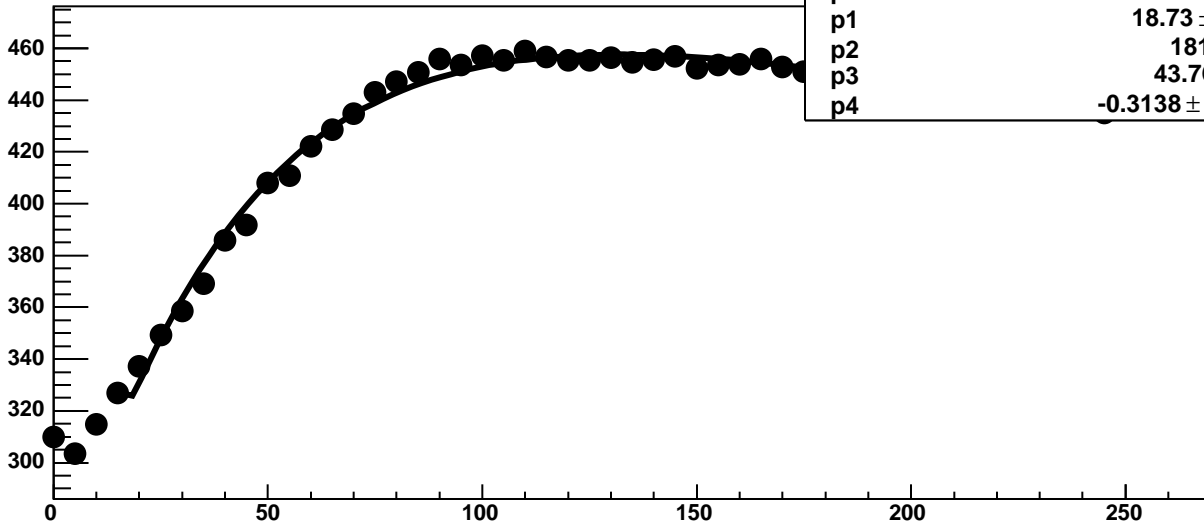
Chip 4, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold

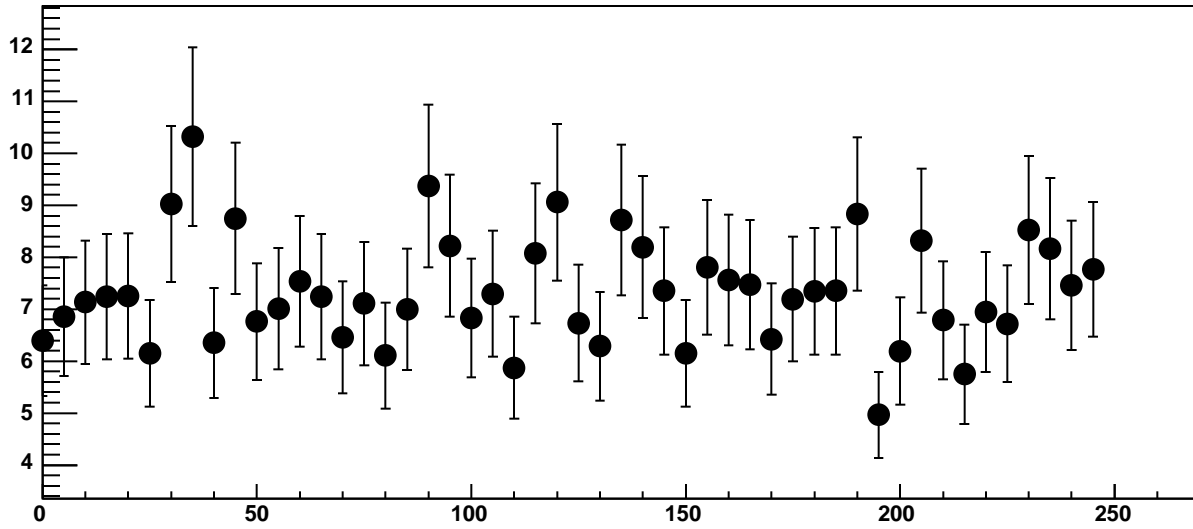


Chip 4, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold

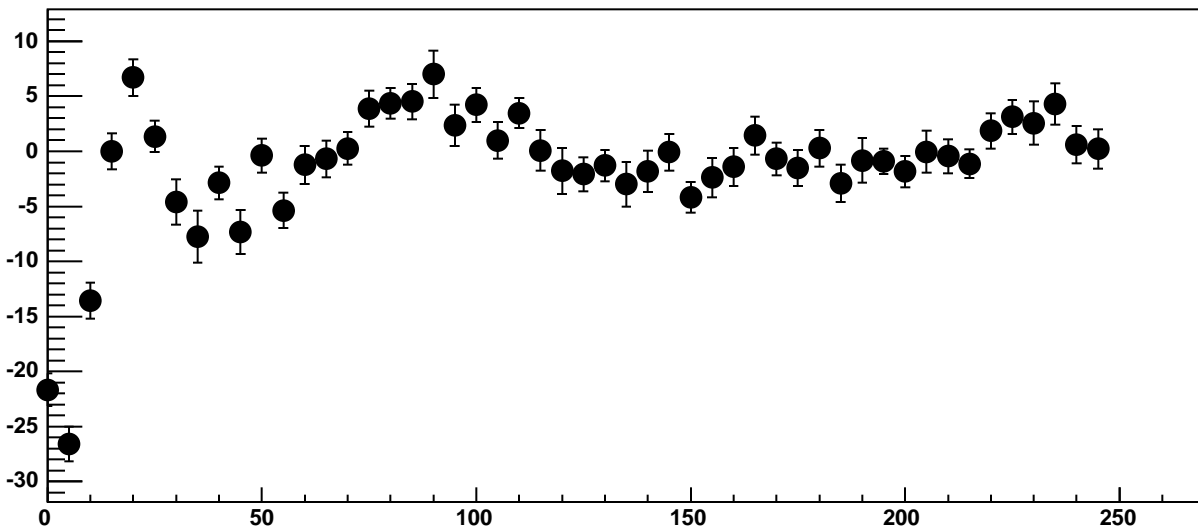


χ^2 / ndf	149.9 / 41
p0	325.7 ± 1.538
p1	18.73 ± 0.4984
p2	181 ± 3.754
p3	43.76 ± 1.24
p4	-0.3138 ± 0.01657

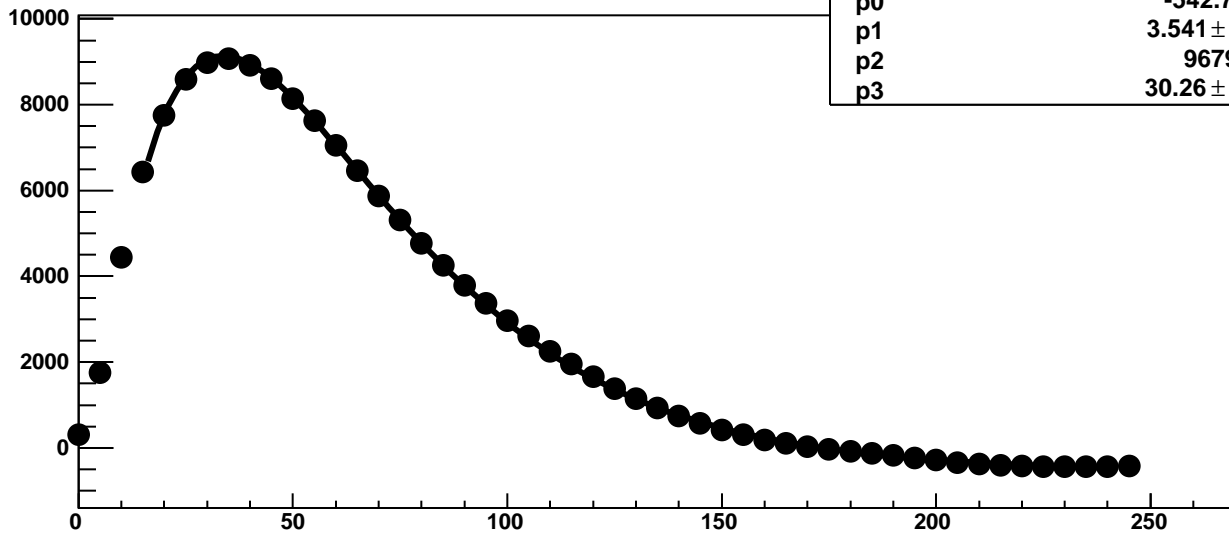
Chip 4, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold

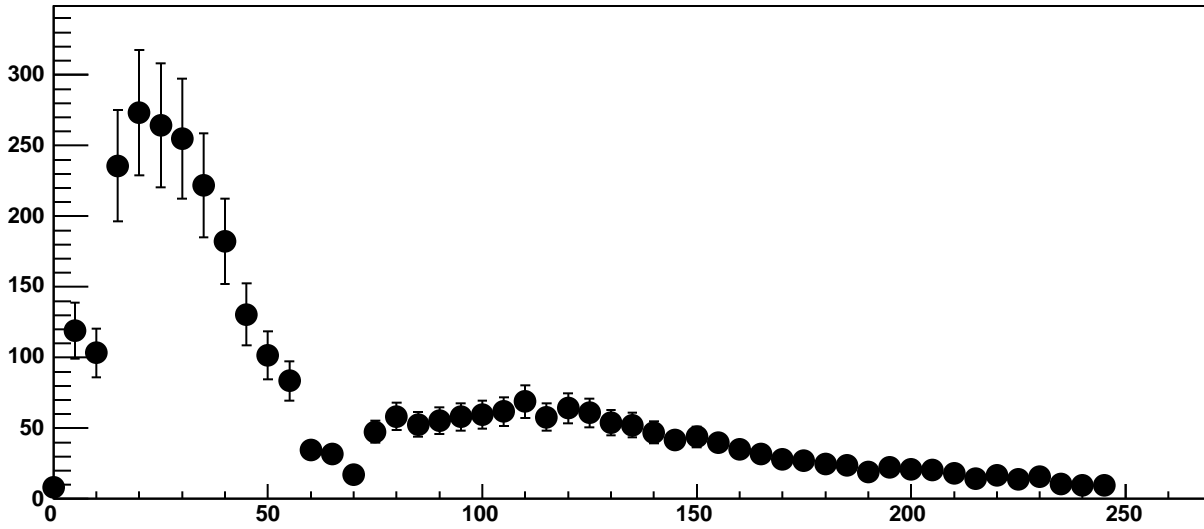


Chip 4, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold

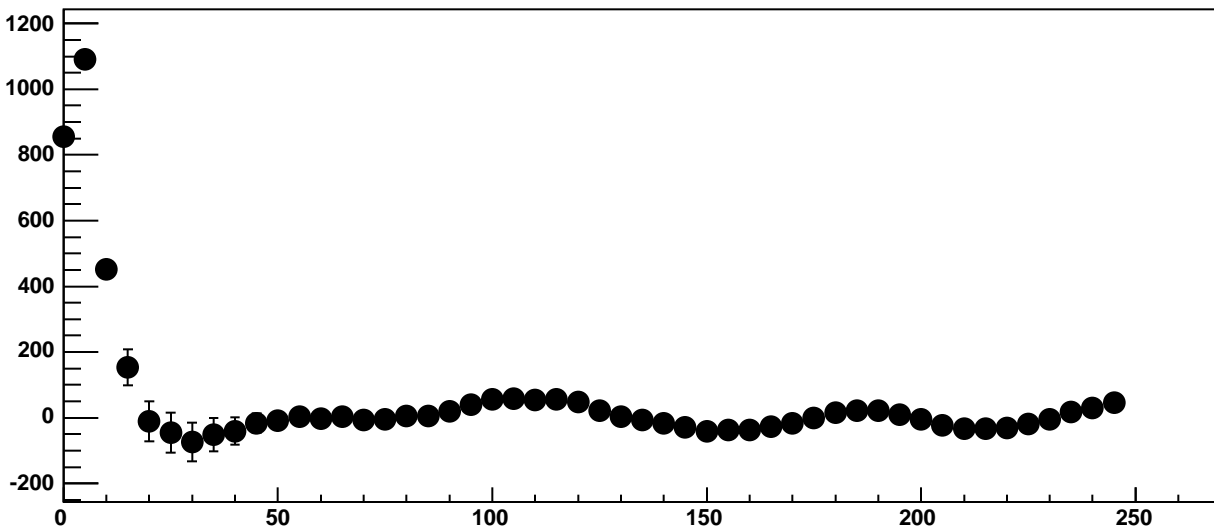


χ^2 / ndf	780.2 / 42
p0	-542.7 ± 1.434
p1	3.541 ± 0.09974
p2	9679 ± 11.81
p3	30.26 ± 0.03613

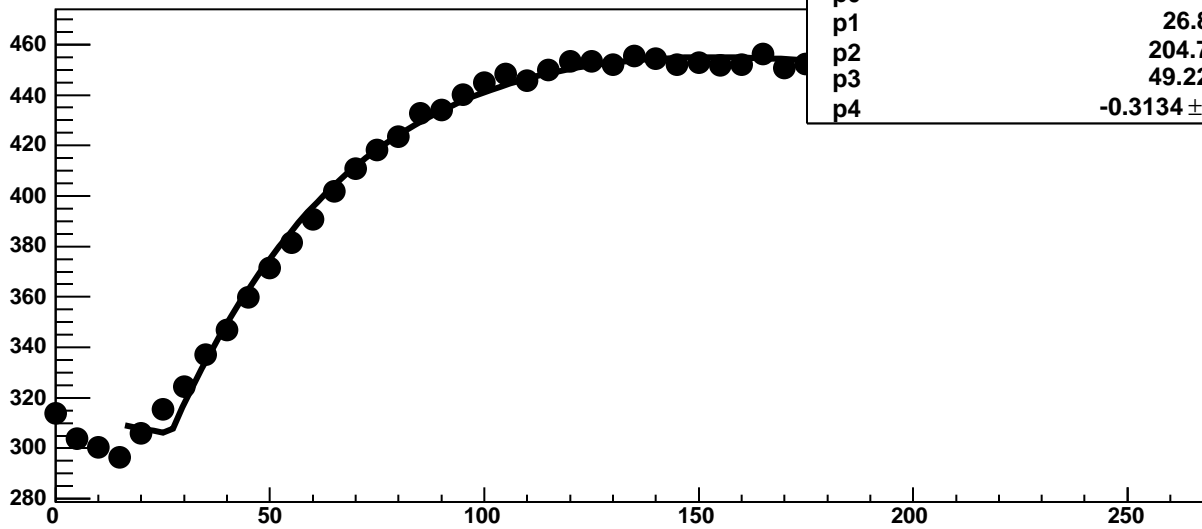
Chip 4, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold



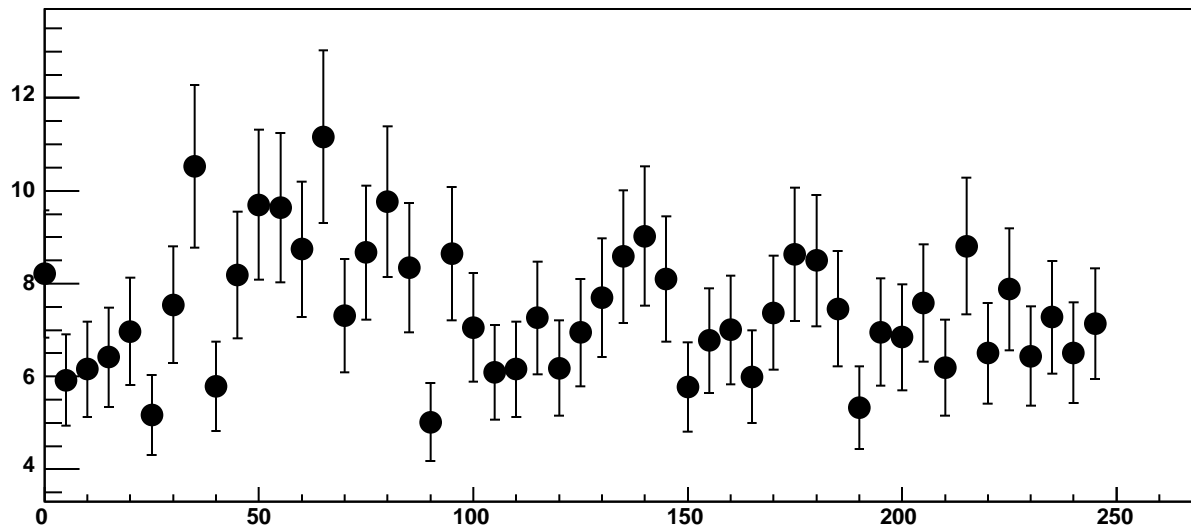
Chip 4, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold



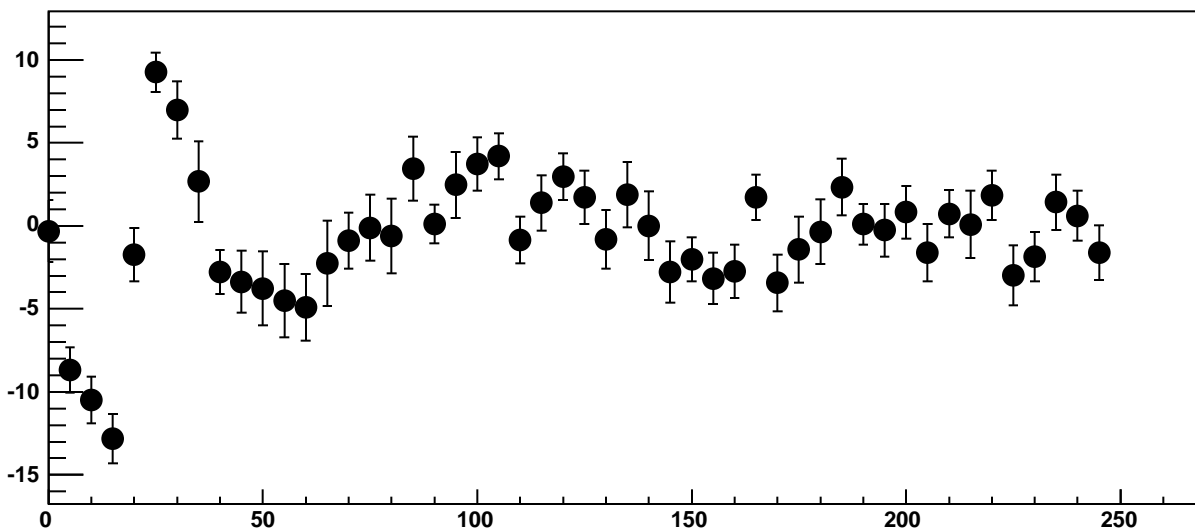
Chip 4, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold



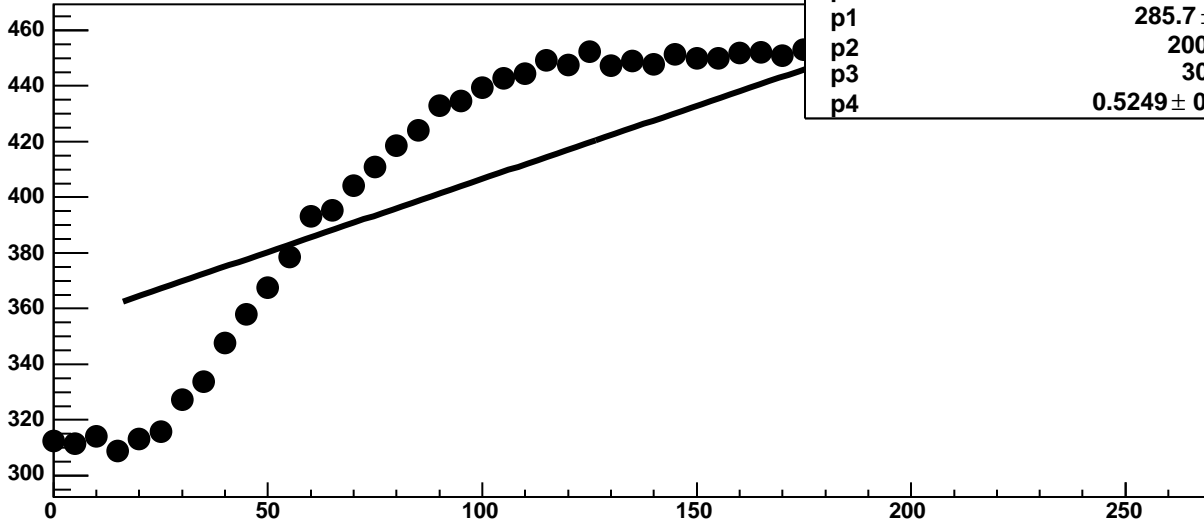
Chip 4, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

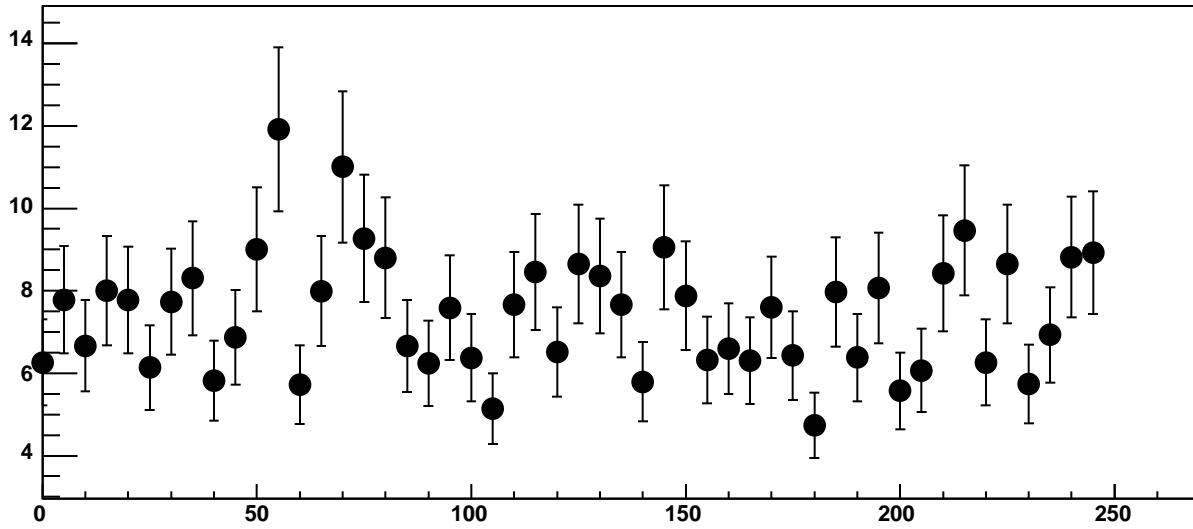


Chip 4, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

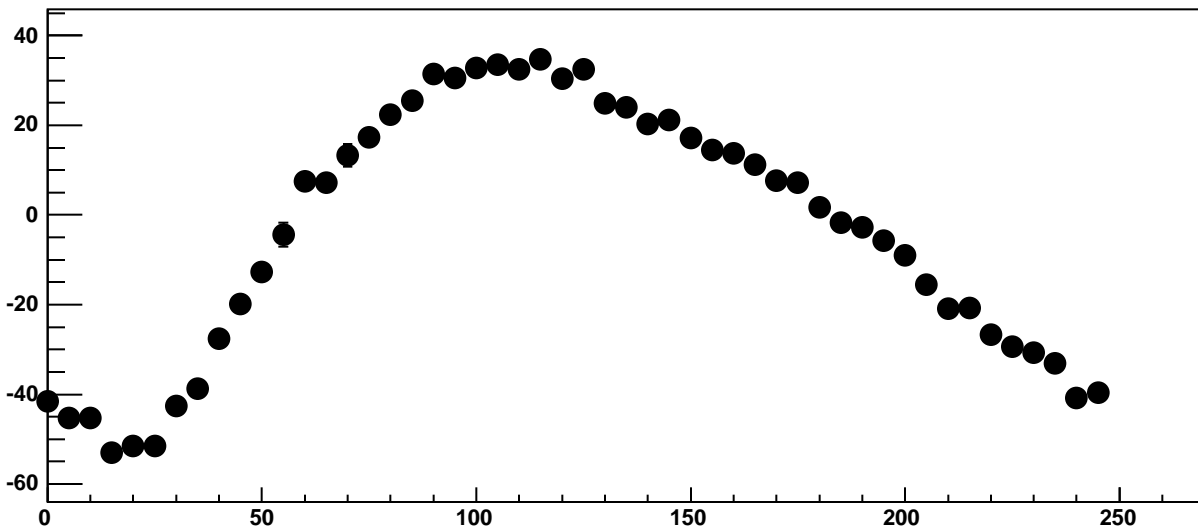


χ^2 / ndf	1.203e+04 / 41
p0	504.1 ± 0.6236
p1	285.7 ± 0.5002
p2	200 ± 1.414
p3	30 ± 1.414
p4	0.5249 ± 0.003645

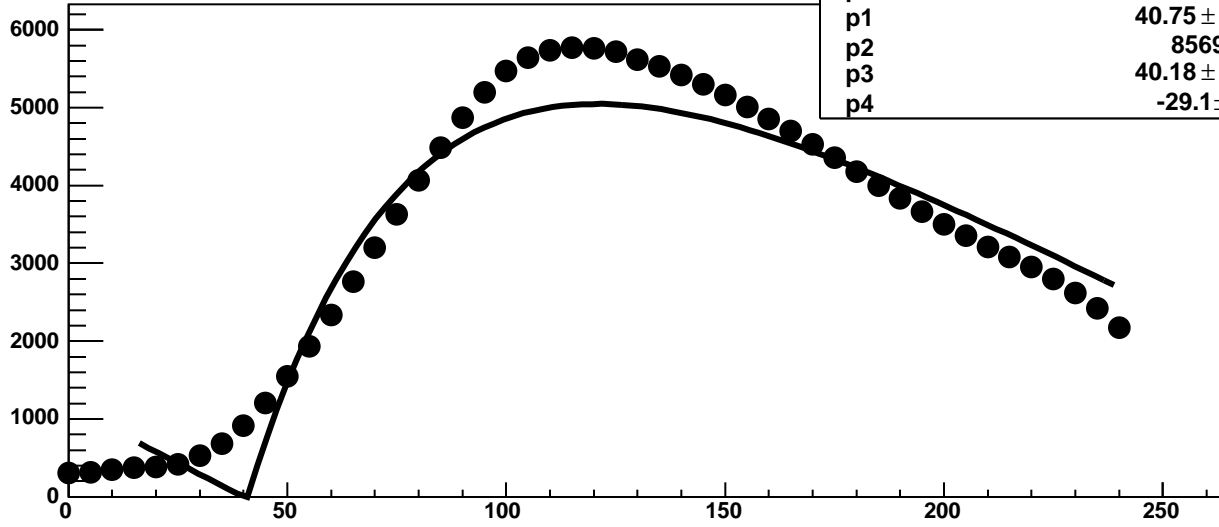
Chip 4, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

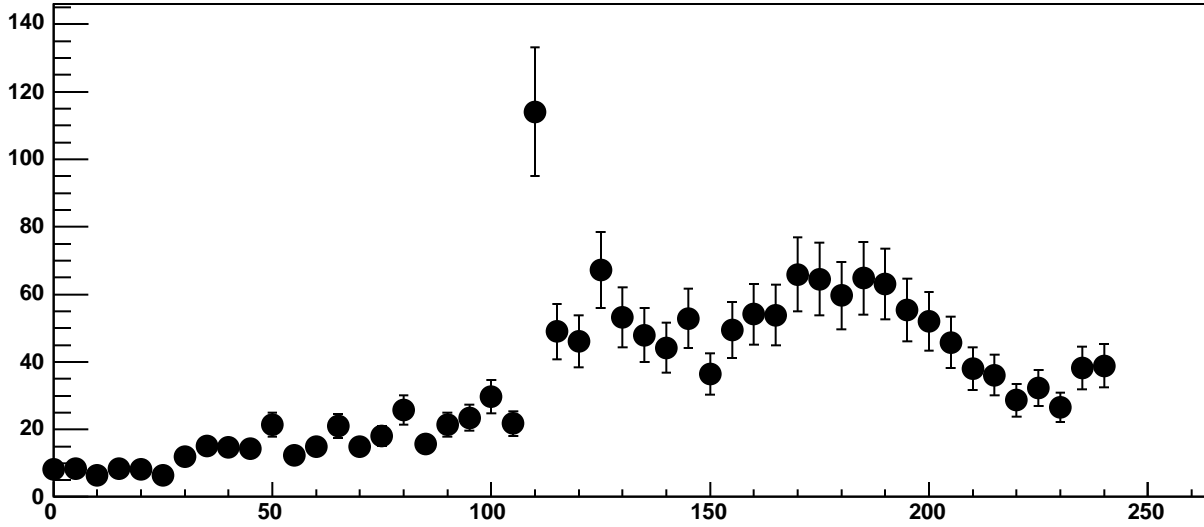


Chip 4, Channel 6, Enable 5, DAC=4000, ADC Mean vs Hold

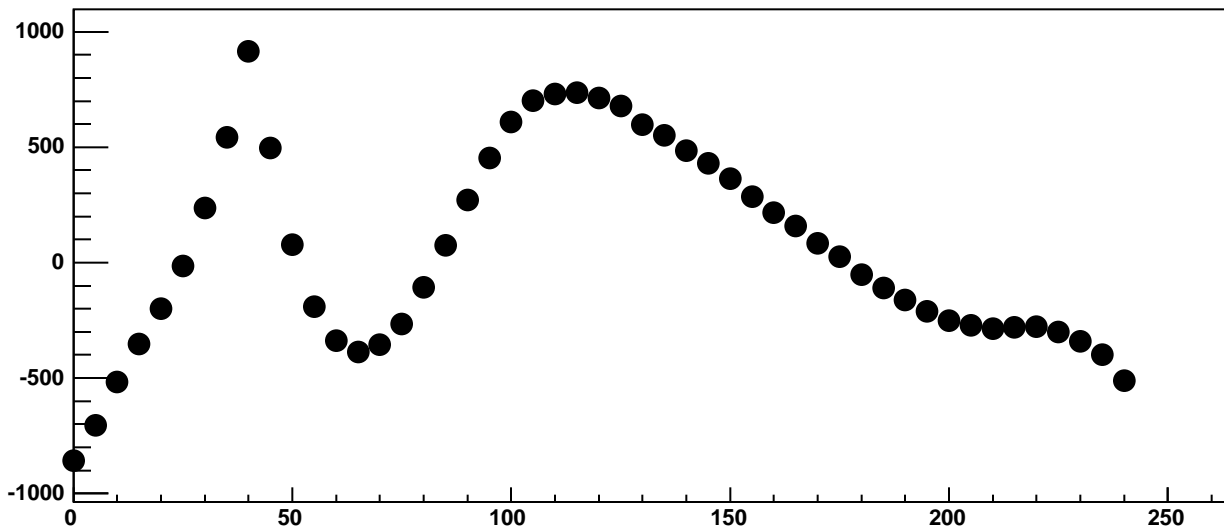


χ^2 / ndf	2.871e+05 / 41
p0	-23.43 ± 1.167
p1	40.75 ± 0.01676
p2	8569 ± 11.46
p3	40.18 ± 0.06386
p4	-29.1 ± 0.0586

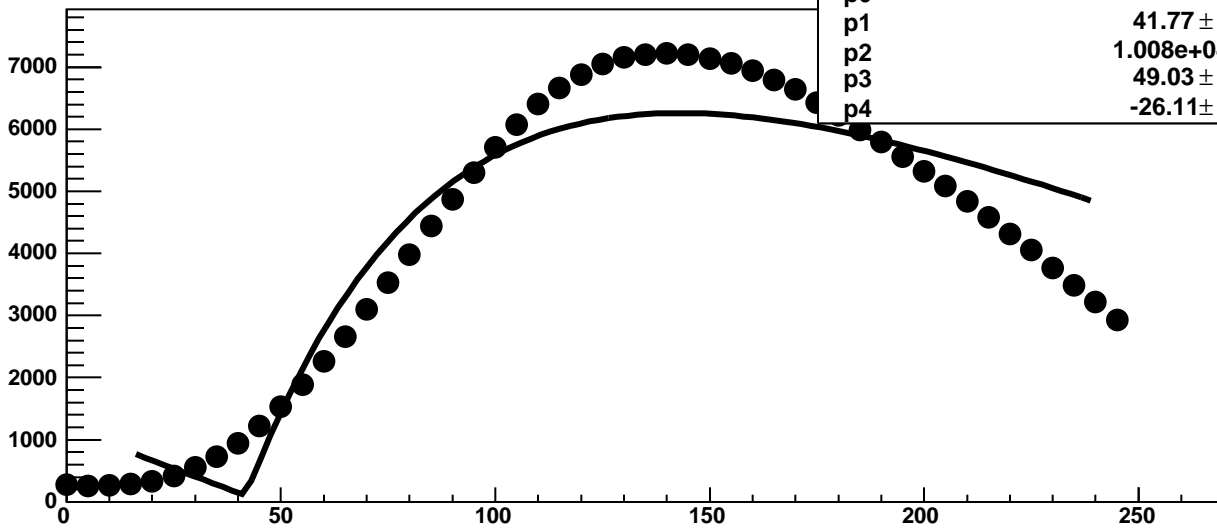
Chip 4, Channel 6, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 6, Enable 5, DAC=4000, ADC Residuals vs Hold

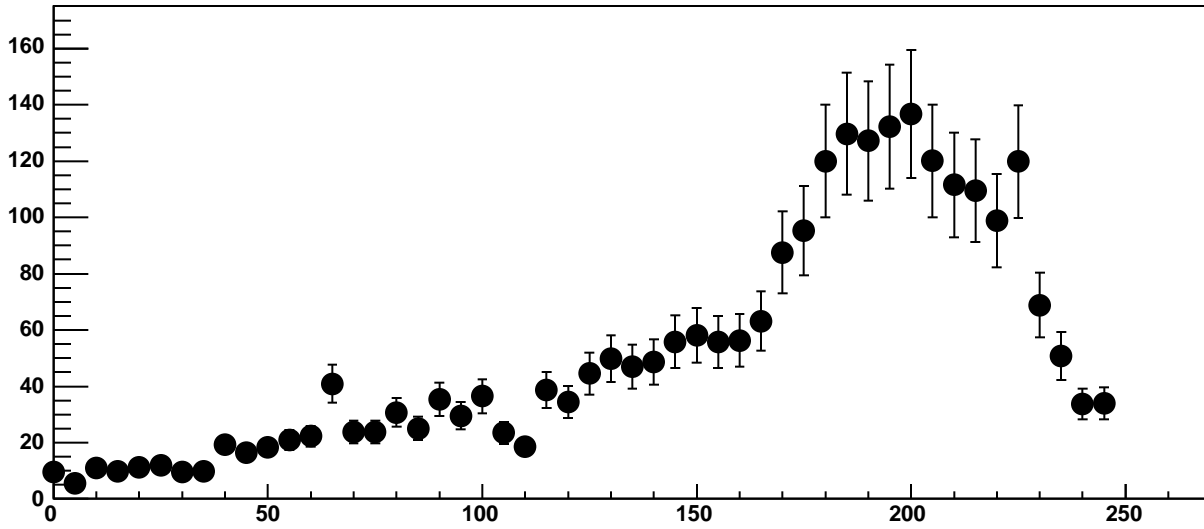


Chip 4, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

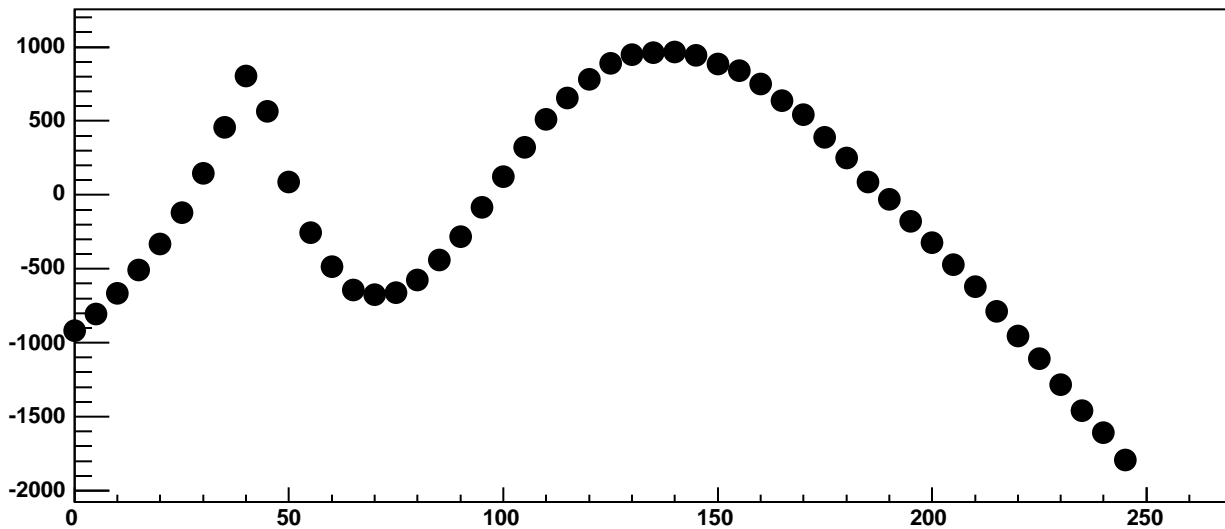


χ^2 / ndf	3.872e+05 / 41
p0	99.14 ± 1.415
p1	41.77 ± 0.01749
p2	1.008e+04 ± 15.1
p3	49.03 ± 0.07974
p4	-26.11 ± 0.07596

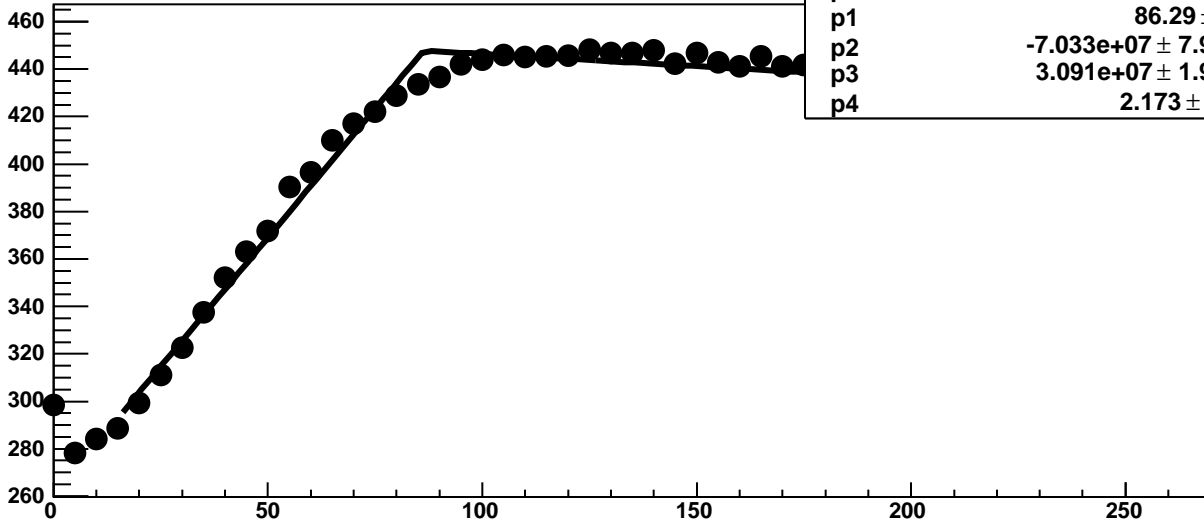
Chip 4, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

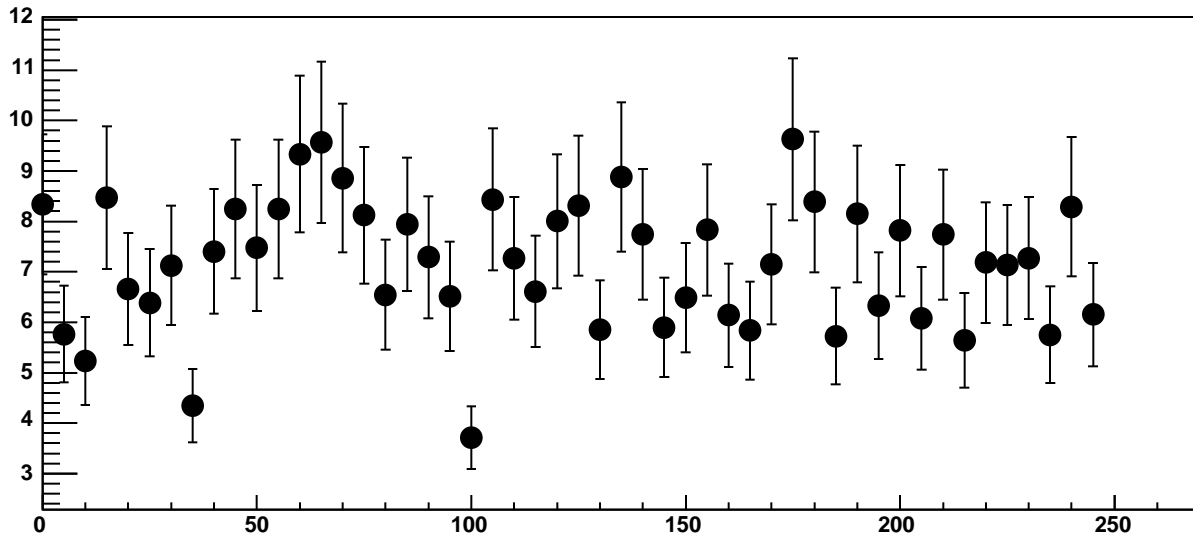


Chip 4, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold

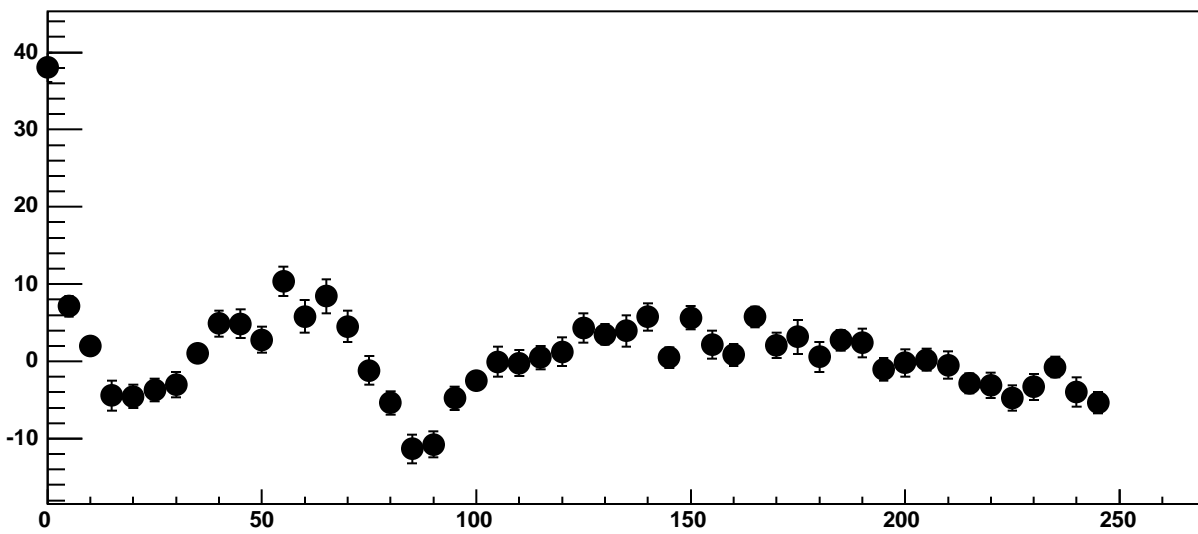


χ^2 / ndf	308.9 / 41
p0	447.8 ± 0.5047
p1	86.29 ± 0.4597
p2	$-7.033\text{e}+07 \pm 7.906\text{e}+05$
p3	$3.091\text{e}+07 \pm 1.928\text{e}+05$
p4	2.173 ± 0.02009

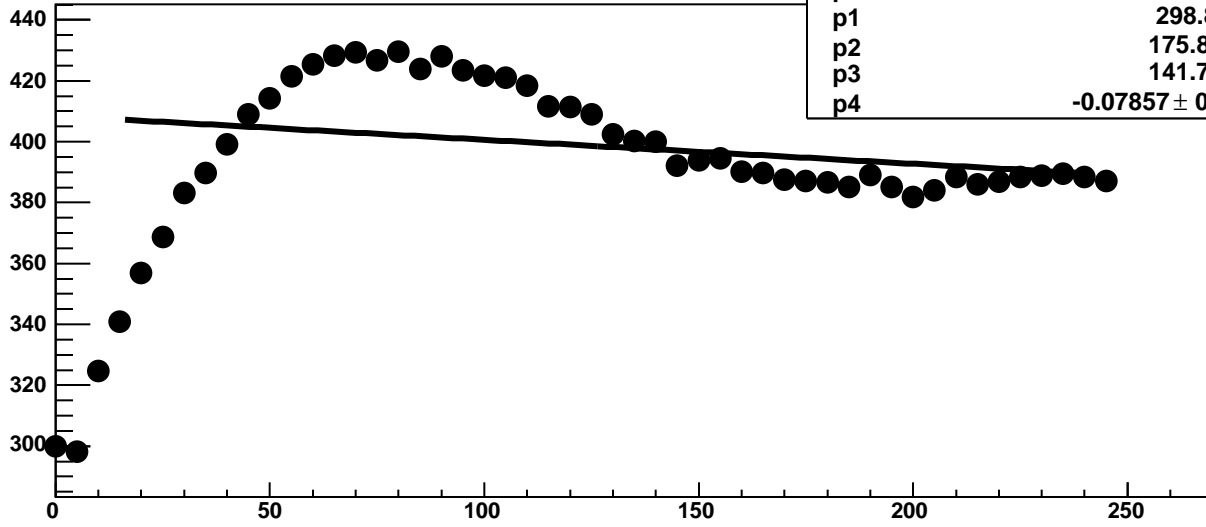
Chip 4, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

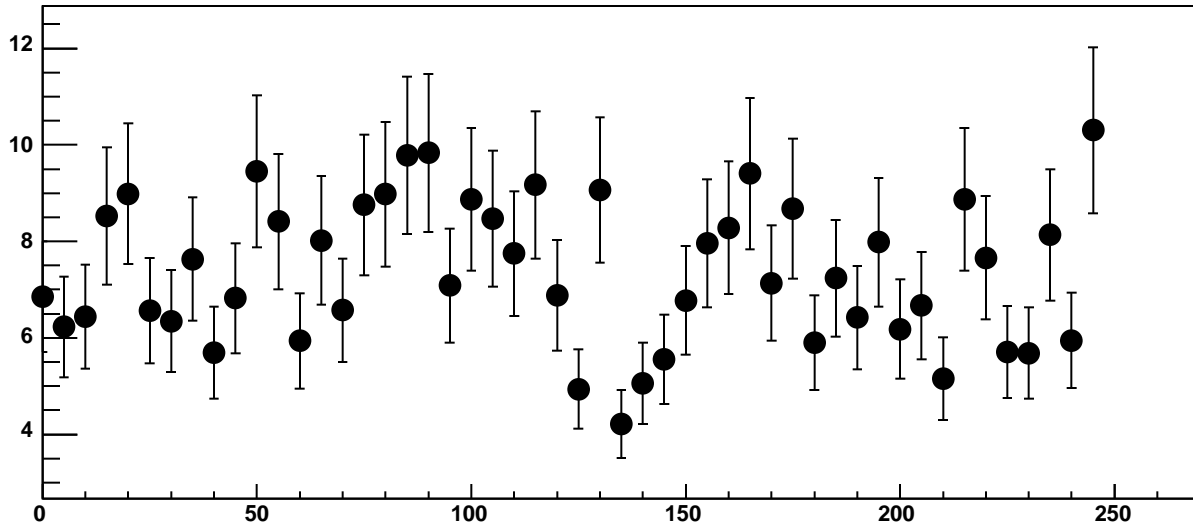


Chip 4, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold

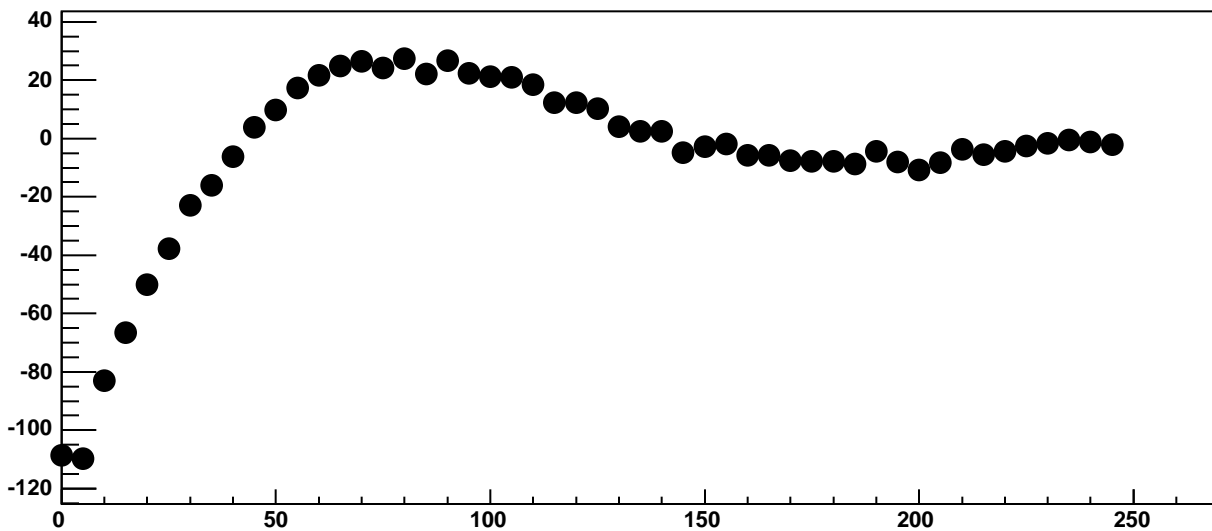


χ^2 / ndf	5175 / 41
p0	385 ± 0.2334
p1	298.8 ± 2.97
p2	175.8 ± 84.85
p3	141.7 ± 1.414
p4	-0.07857 ± 0.001314

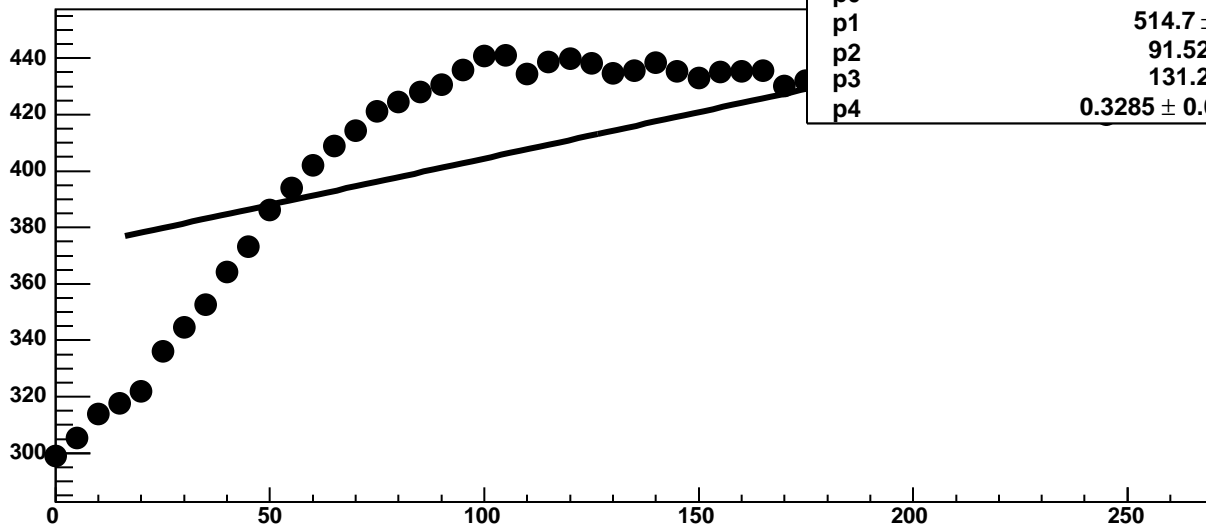
Chip 4, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold

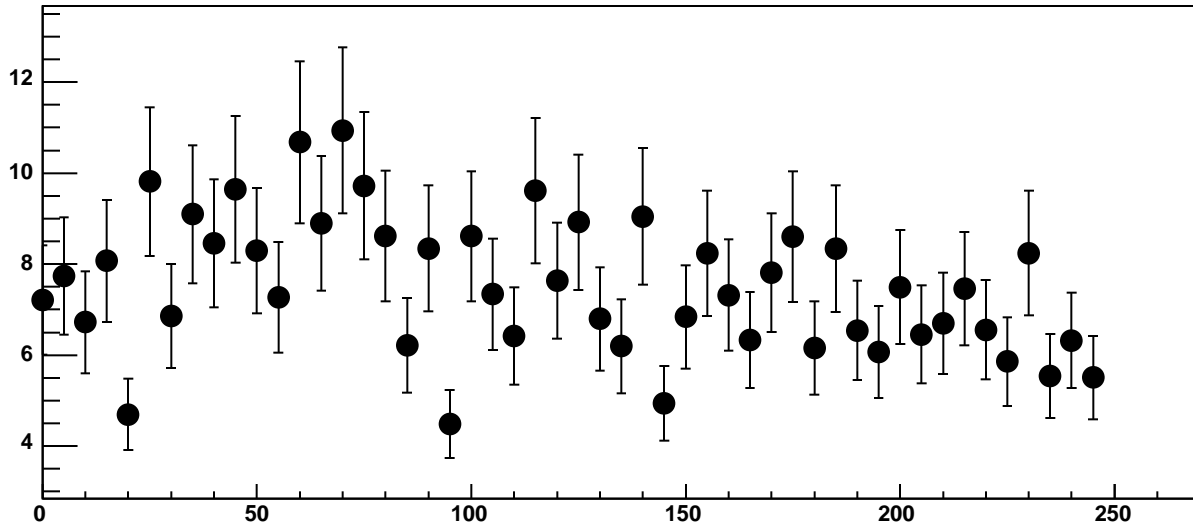


Chip 4, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

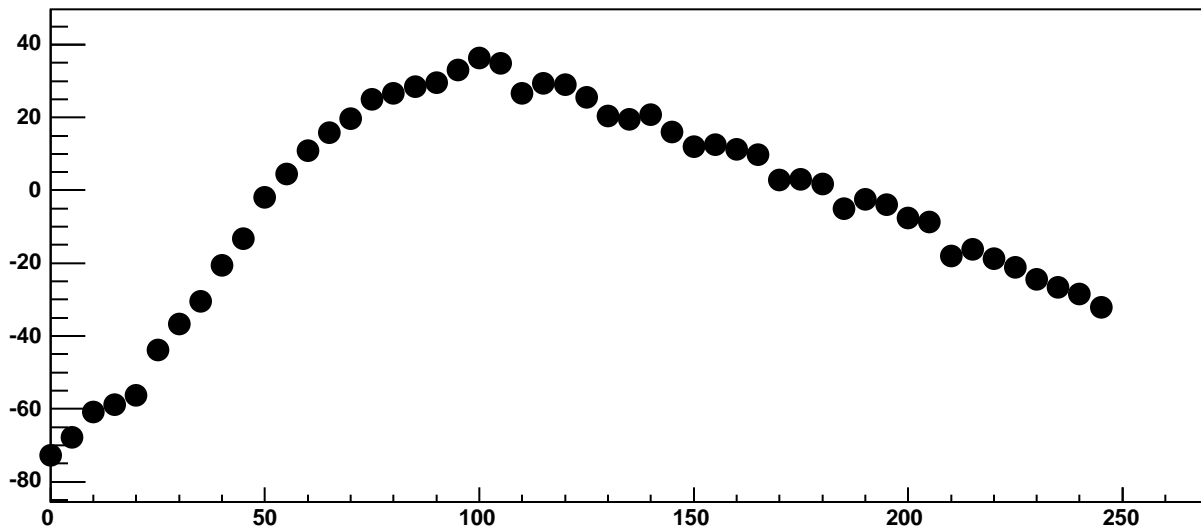


χ^2 / ndf	1.14e+04 / 41
p0	540.7 ± 0.2403
p1	514.7 ± 0.7314
p2	91.52 ± 84.85
p3	131.2 ± 1.414
p4	0.3285 ± 0.0006225

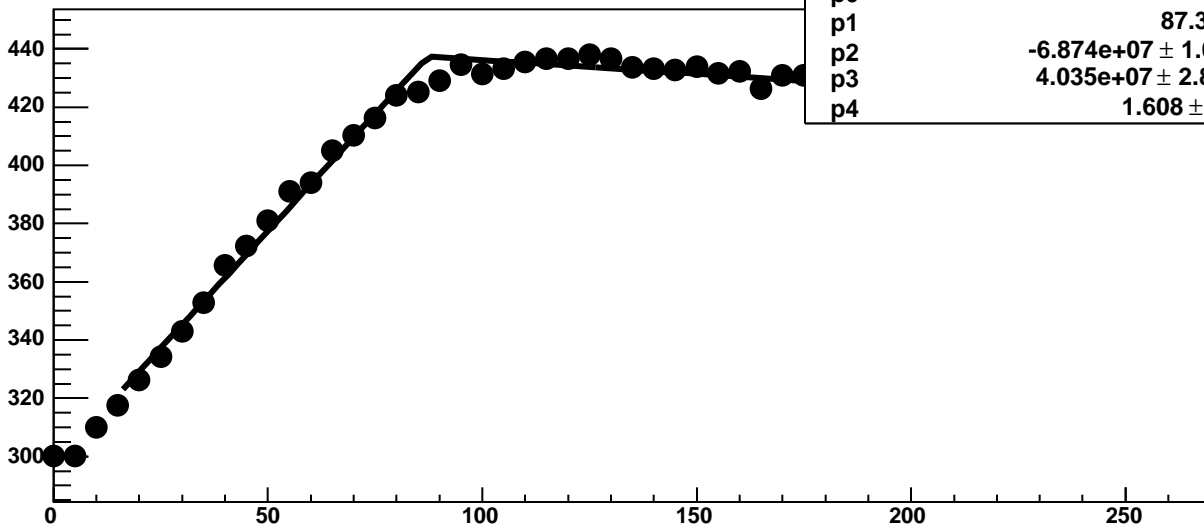
Chip 4, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

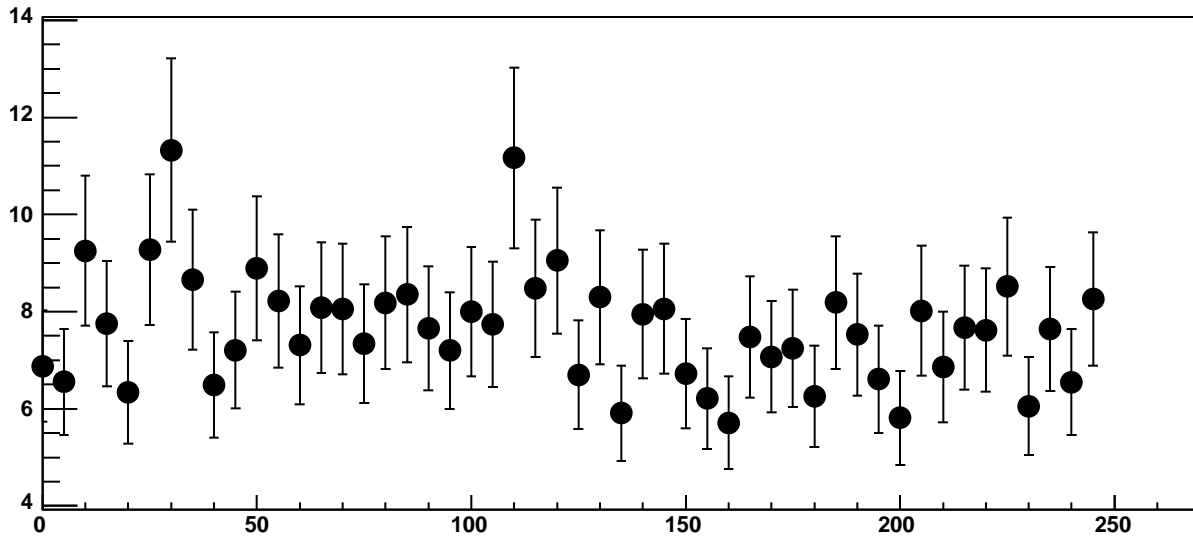


Chip 4, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

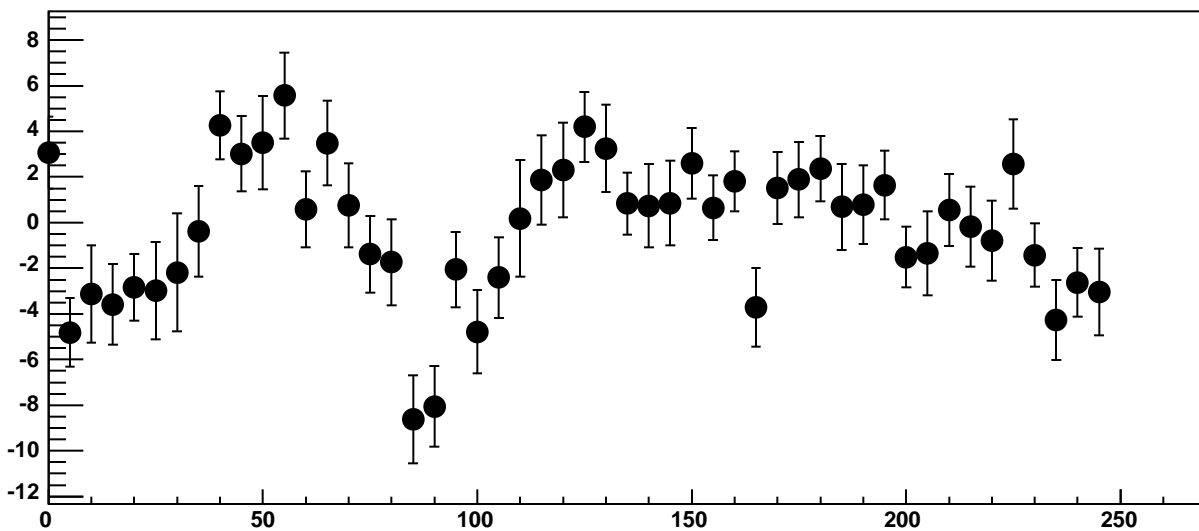


χ^2 / ndf	133.7 / 41
p0	437.4 ± 0.5928
p1	87.3 ± 0.655
p2	$-6.874\text{e}+07 \pm 1.018\text{e}+06$
p3	$4.035\text{e}+07 \pm 2.847\text{e}+05$
p4	1.608 ± 0.02141

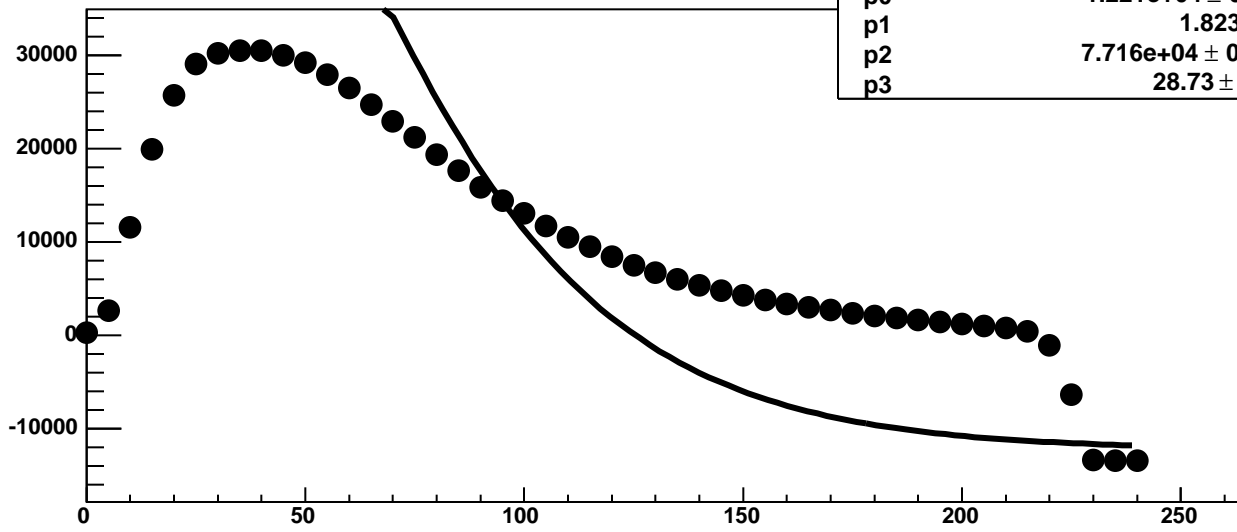
Chip 4, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

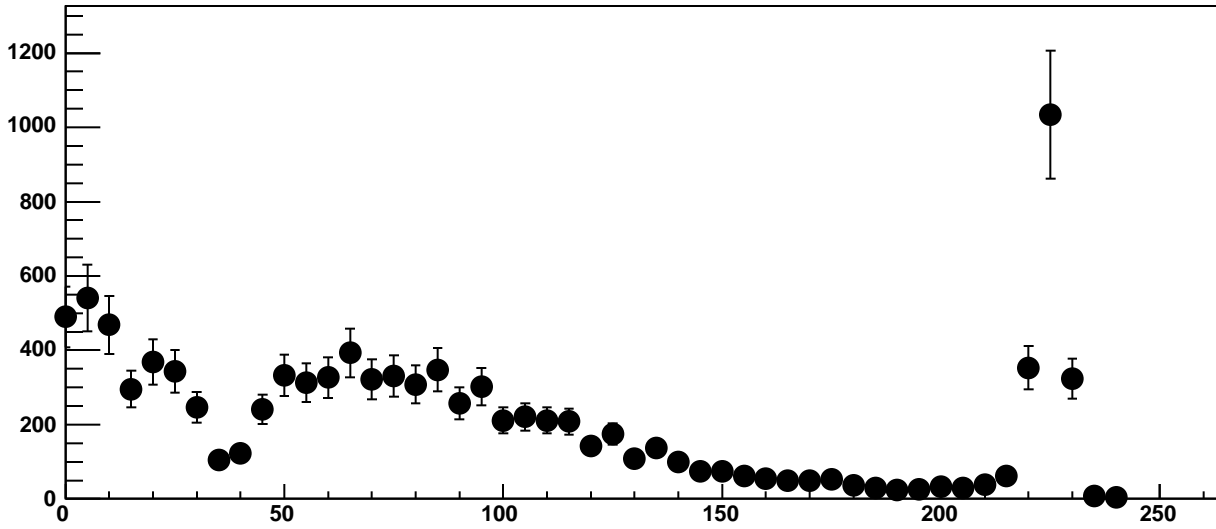


Chip 4, Channel 7, Enable 5!, DAC=4000, ADC Mean vs Hold

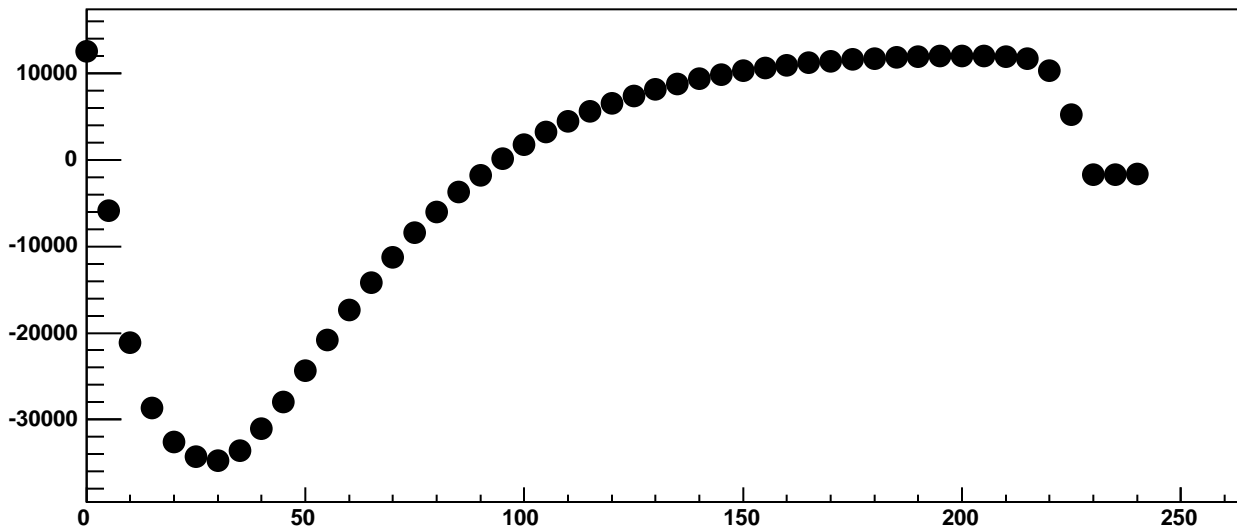


χ^2 / ndf	3.54e+07 / 42
p0	-1.221e+04 ± 0.001092
p1	1.823 ± 2.975
p2	7.716e+04 ± 0.001084
p3	28.73 ± 0.01743

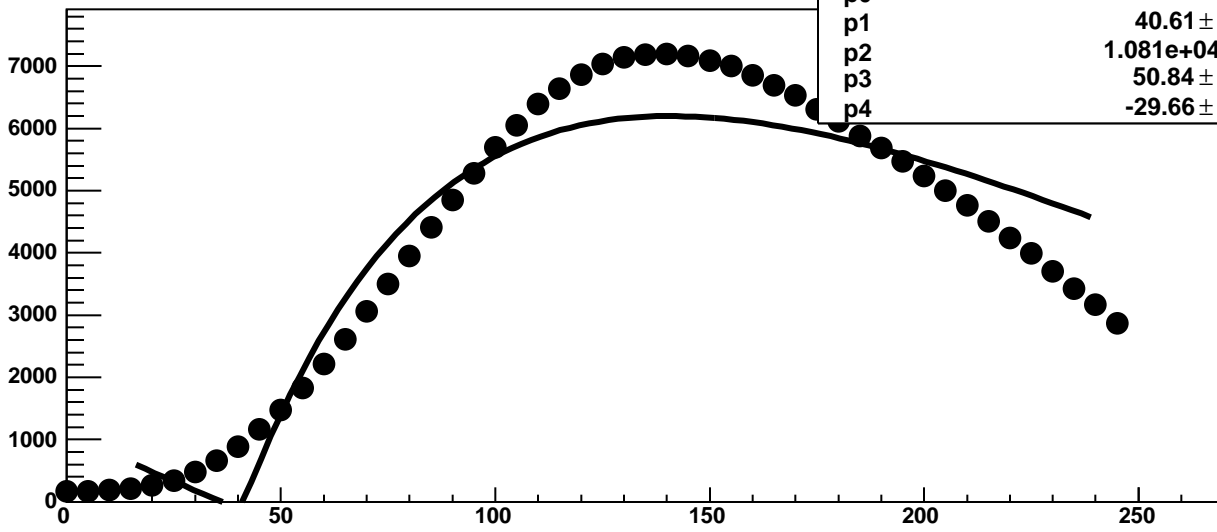
Chip 4, Channel 7, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 7, Enable 5!, DAC=4000, ADC Residuals vs Hold

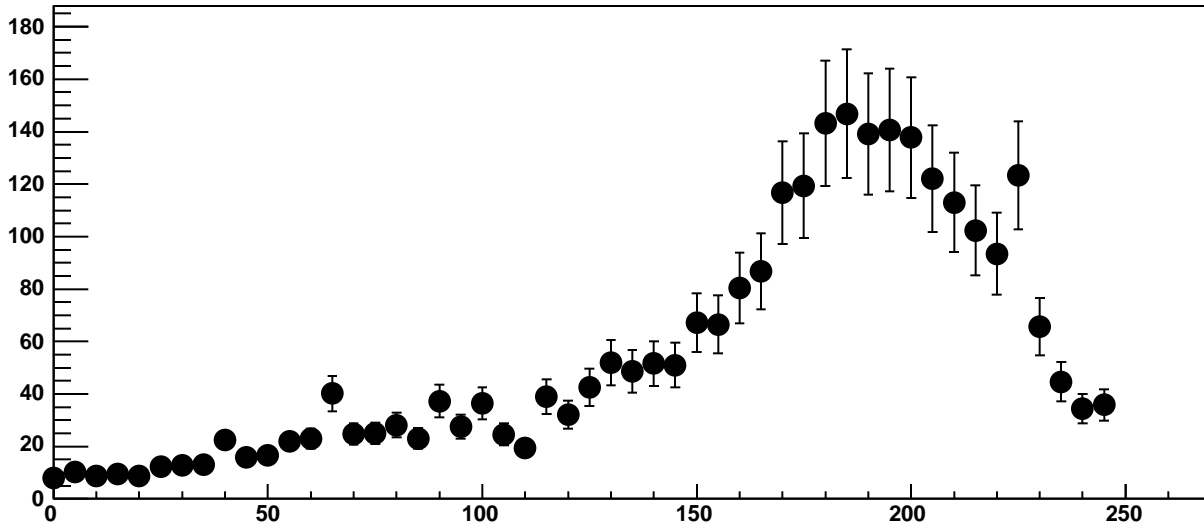


Chip 4, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

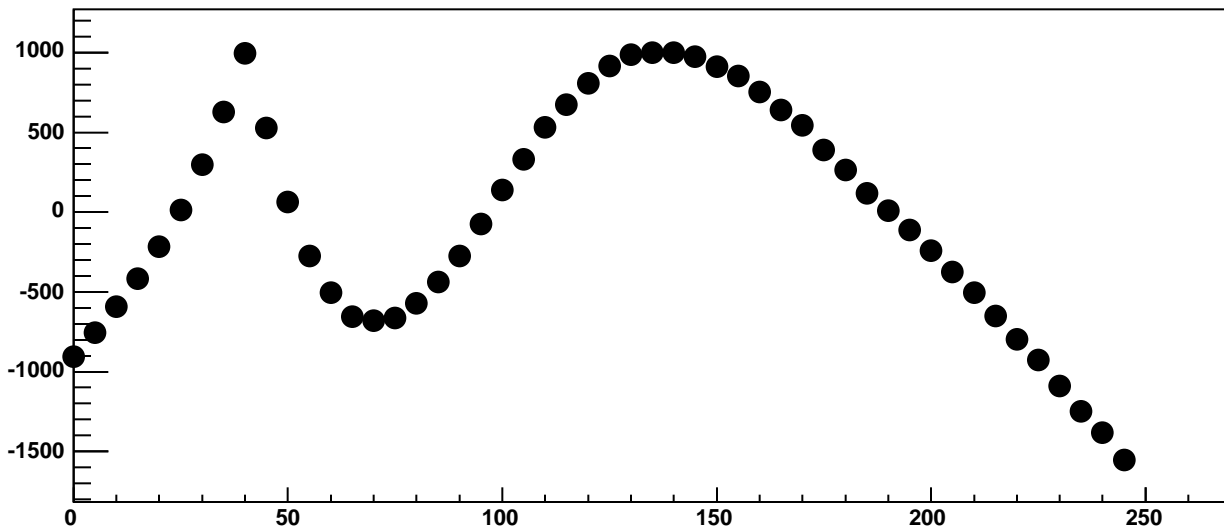


χ^2 / ndf	3.621e+05 / 41
p0	-131.1 ± 1.539
p1	40.61 ± 0.01779
p2	1.081e+04 ± 16.77
p3	50.84 ± 0.08433
p4	-29.66 ± 0.08094

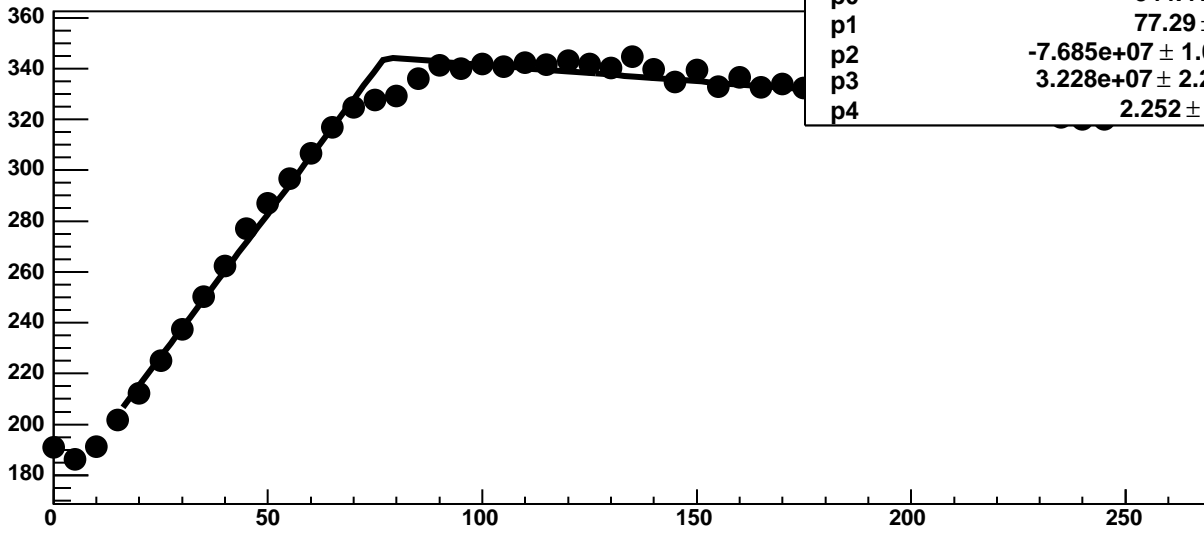
Chip 4, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

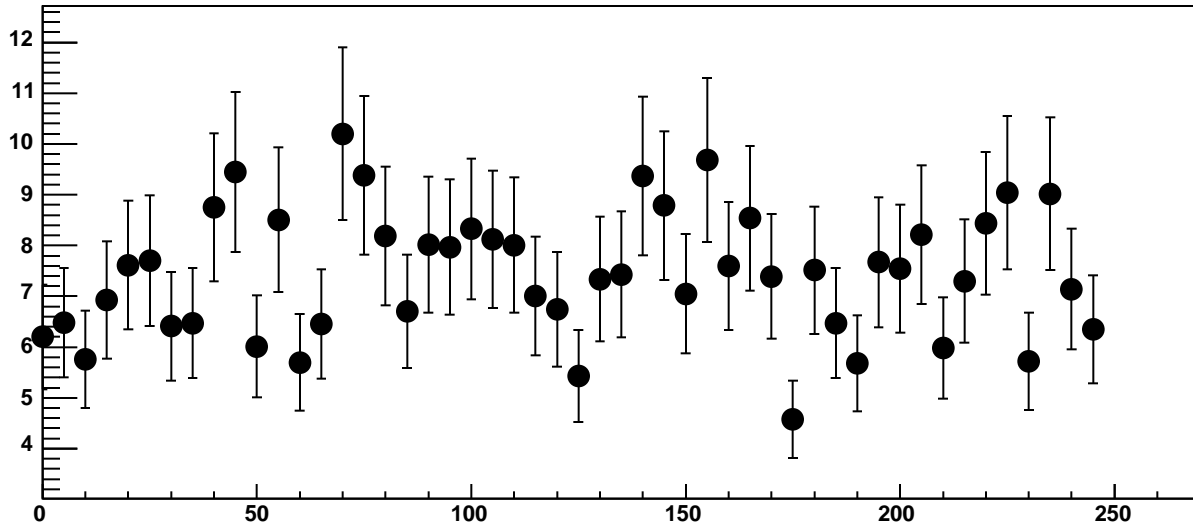


Chip 4, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

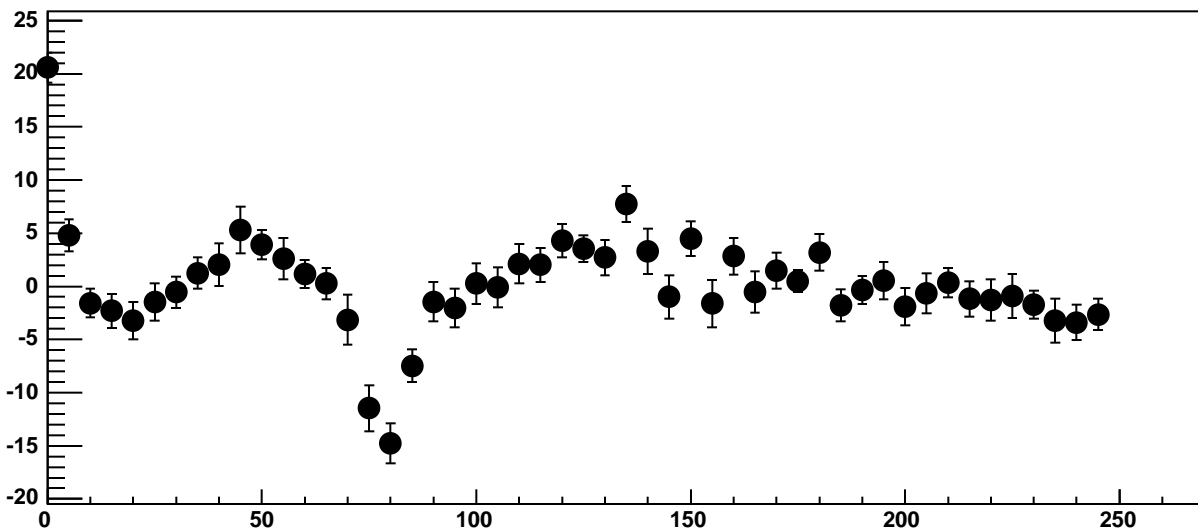


χ^2 / ndf	214.6 / 41
p0	344.4 ± 0.5699
p1	77.29 ± 0.4812
p2	$-7.685e+07 \pm 1.003e+06$
p3	$3.228e+07 \pm 2.201e+05$
p4	2.252 ± 0.02579

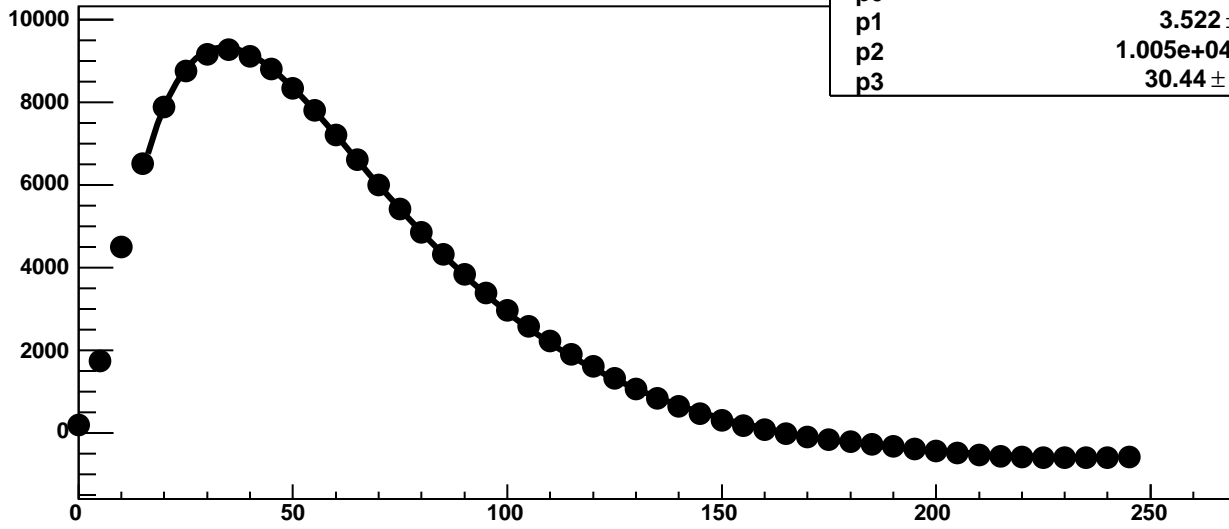
Chip 4, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

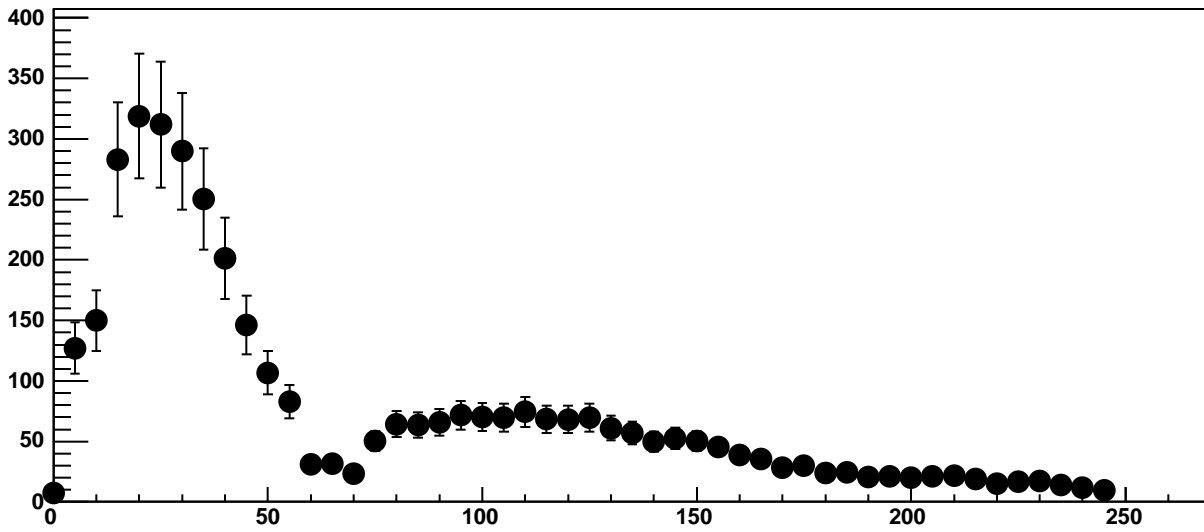


Chip 4, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold

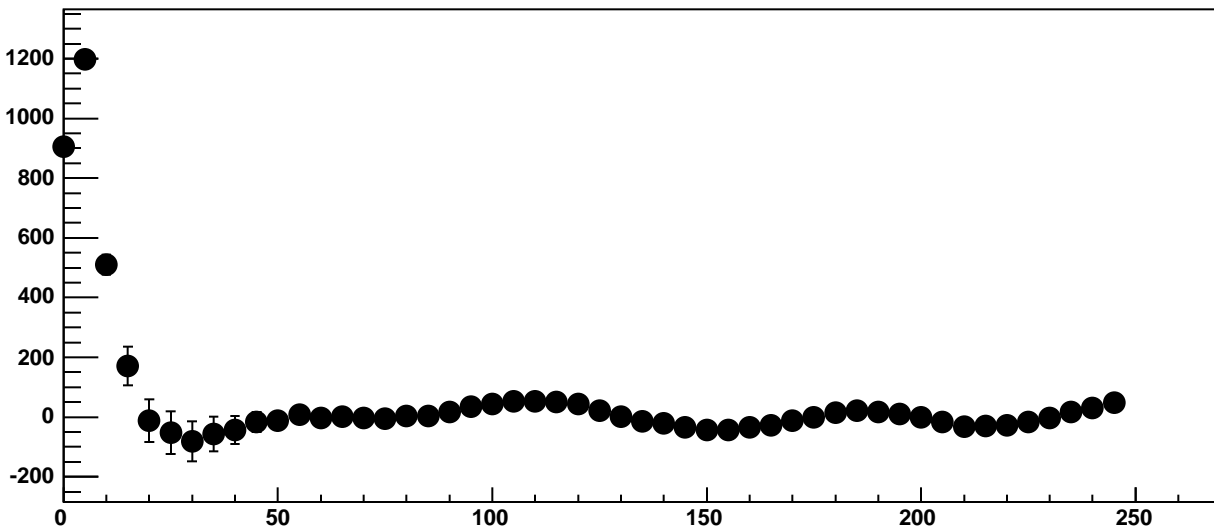


χ^2 / ndf	518.1 / 42
p0	-712.7 ± 1.706
p1	3.522 ± 0.1132
p2	$1.005\text{e}+04 \pm 13.36$
p3	30.44 ± 0.03952

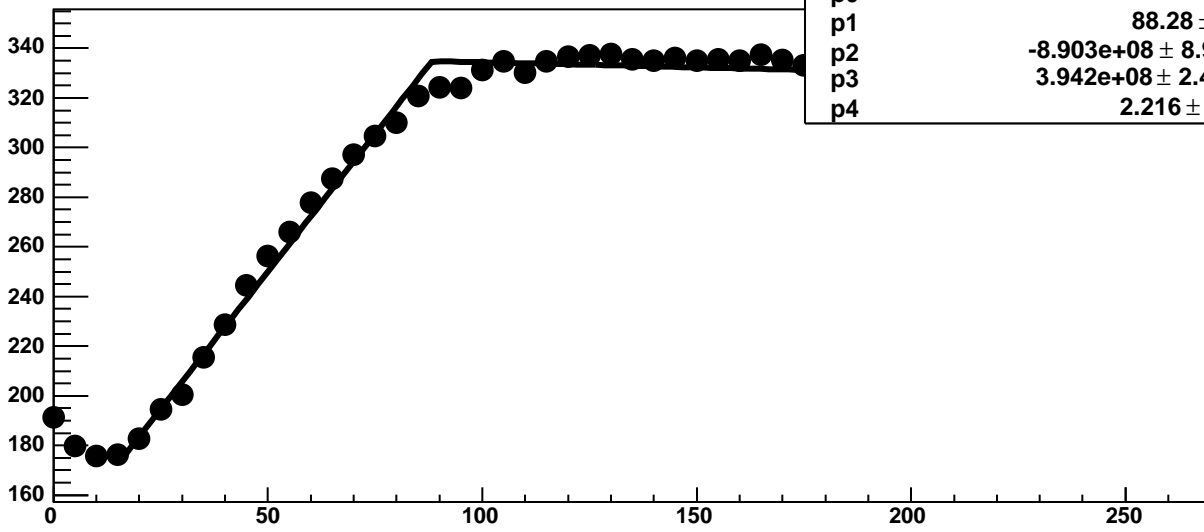
Chip 4, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold

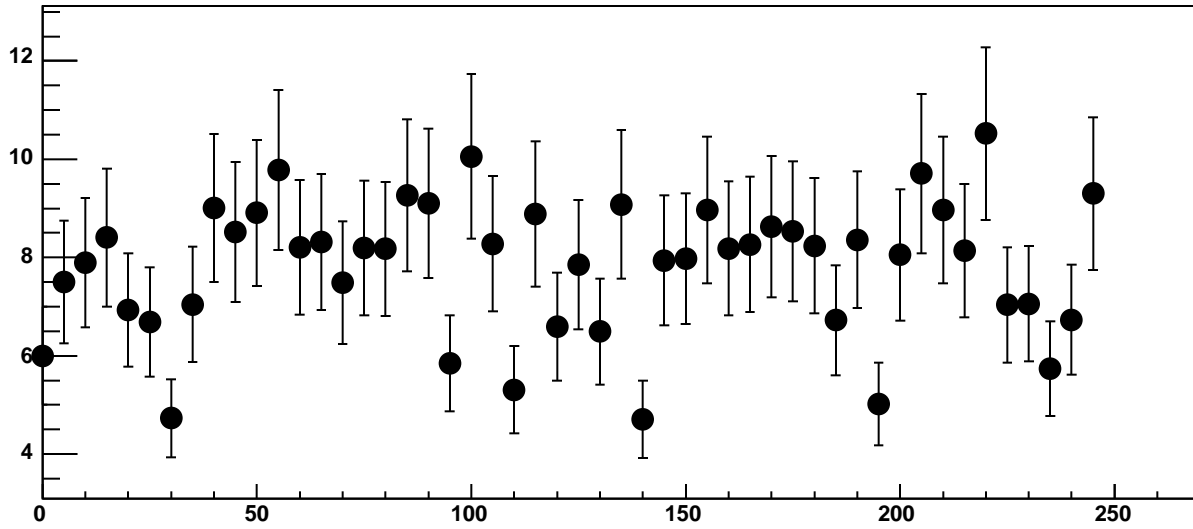


Chip 4, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

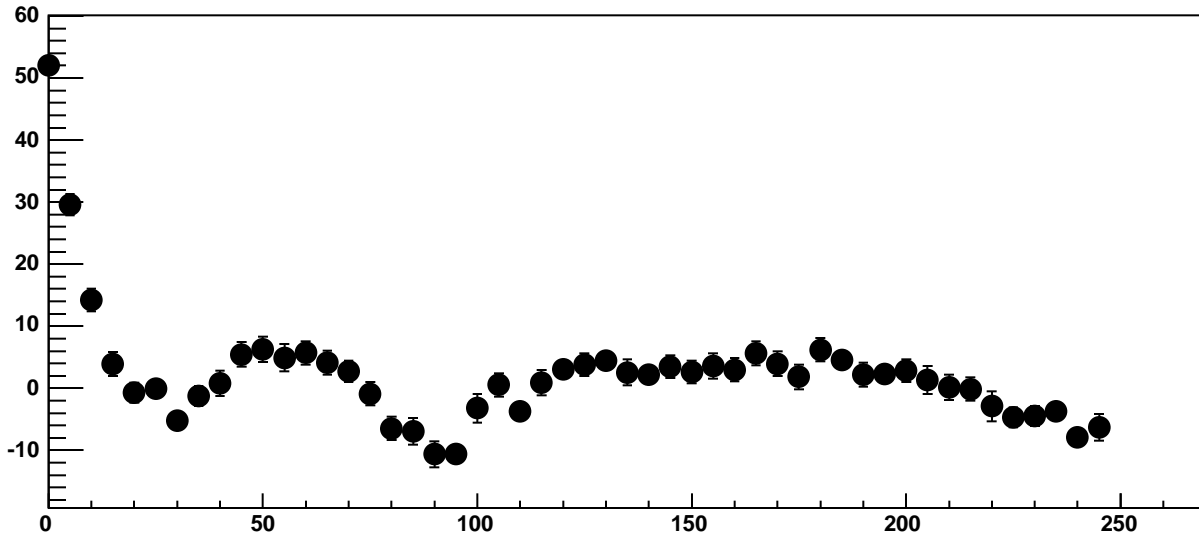


χ^2 / ndf	315.7 / 41
p0	334.9 ± 0.5787
p1	88.28 ± 0.5256
p2	$-8.903\text{e}+08 \pm 8.973\text{e}+06$
p3	$3.942\text{e}+08 \pm 2.445\text{e}+05$
p4	2.216 ± 0.02155

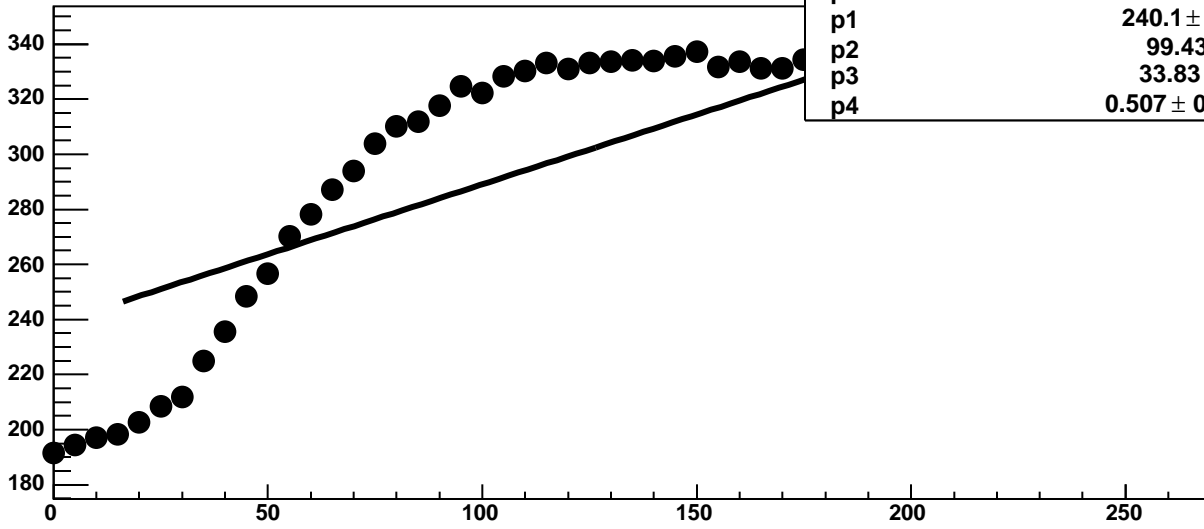
Chip 4, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

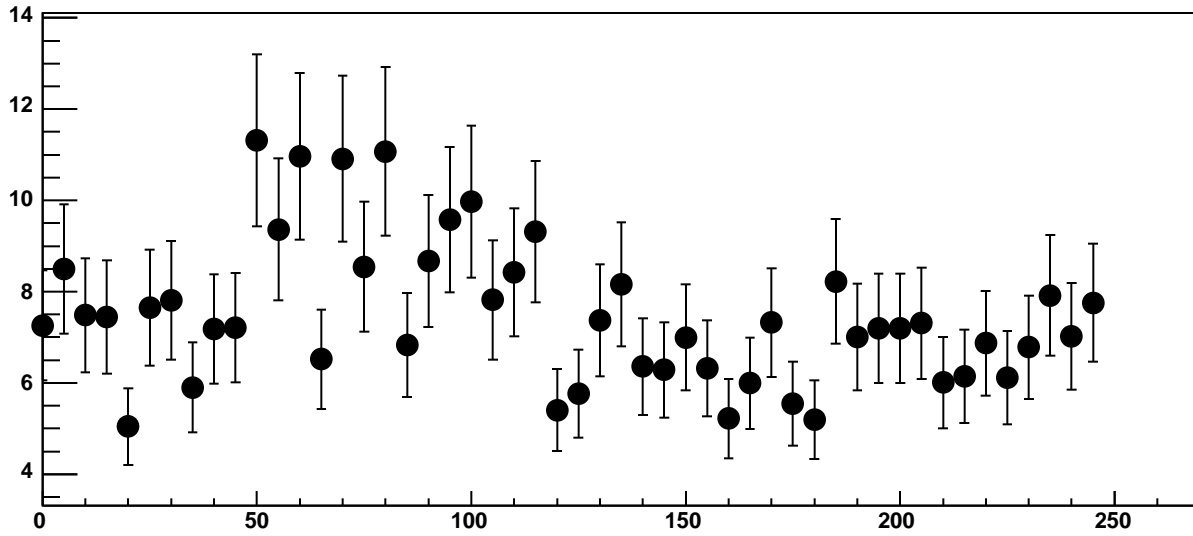


Chip 4, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

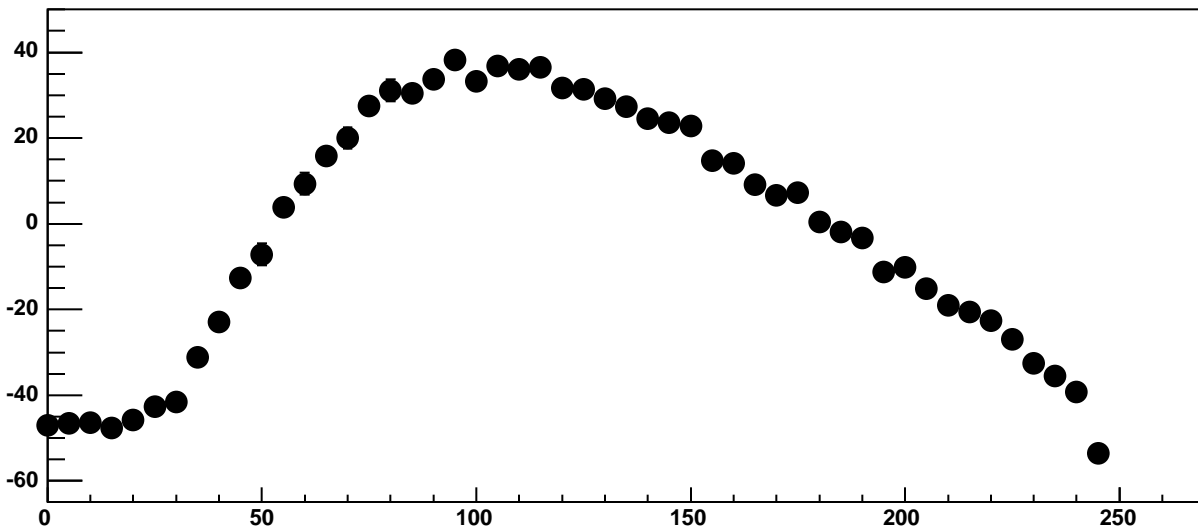


χ^2 / ndf	1.244e+04 / 41
p0	360.1 ± 0.4417
p1	240.1 ± 0.04778
p2	99.43 ± 2.466
p3	33.83 ± 0.2111
p4	0.507 ± 0.003547

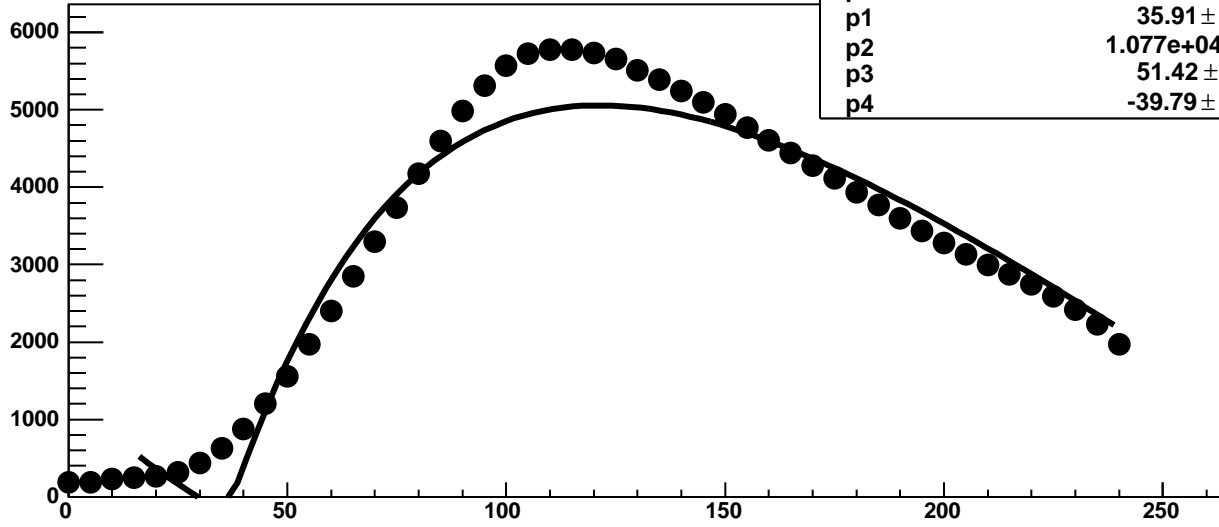
Chip 4, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

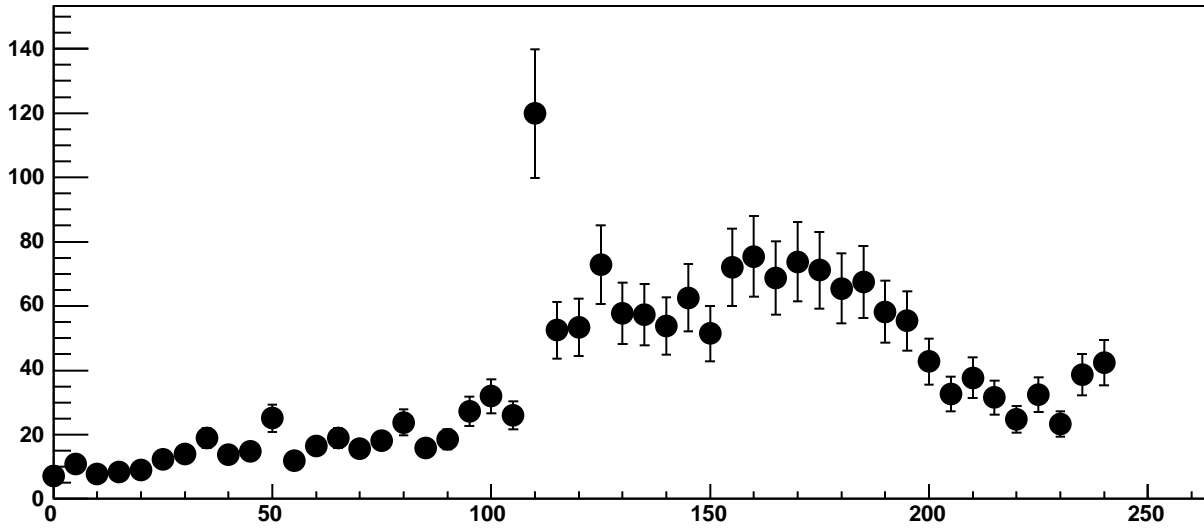


Chip 4, Channel 8, Enable 5, DAC=4000, ADC Mean vs Hold

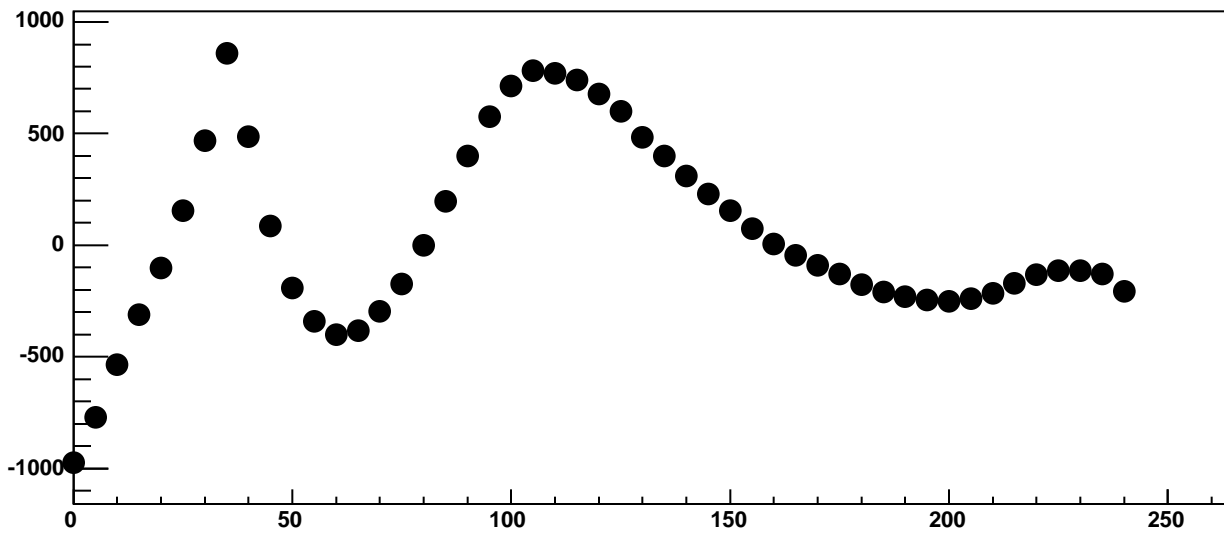


χ^2 / ndf	2.263e+05 / 41
p0	-267.7 ± 1.374
p1	35.91 ± 0.01663
p2	1.077e+04 ± 18.17
p3	51.42 ± 0.08921
p4	-39.79 ± 0.08309

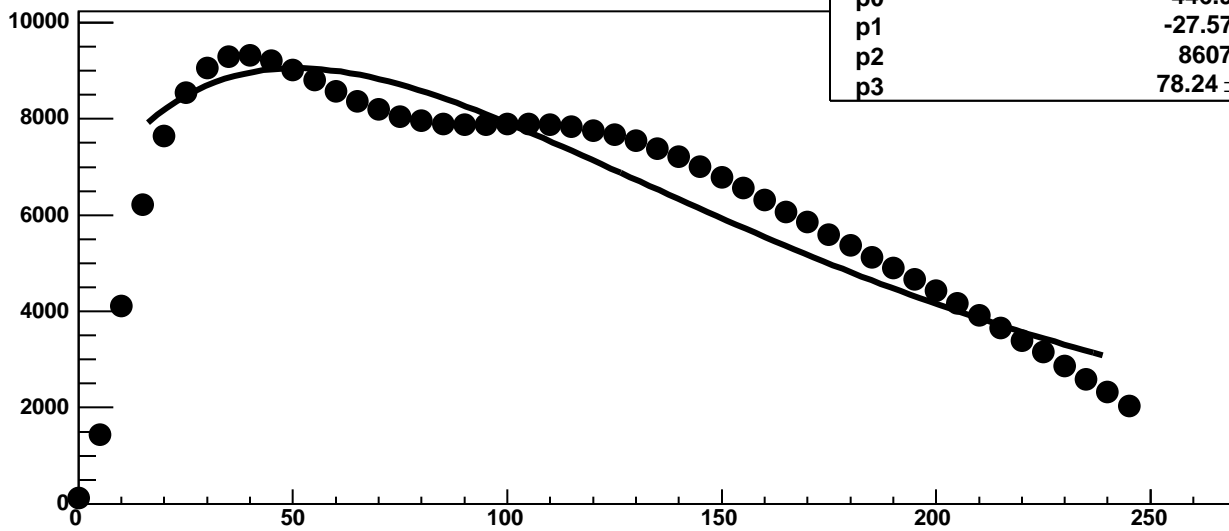
Chip 4, Channel 8, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 8, Enable 5, DAC=4000, ADC Residuals vs Hold

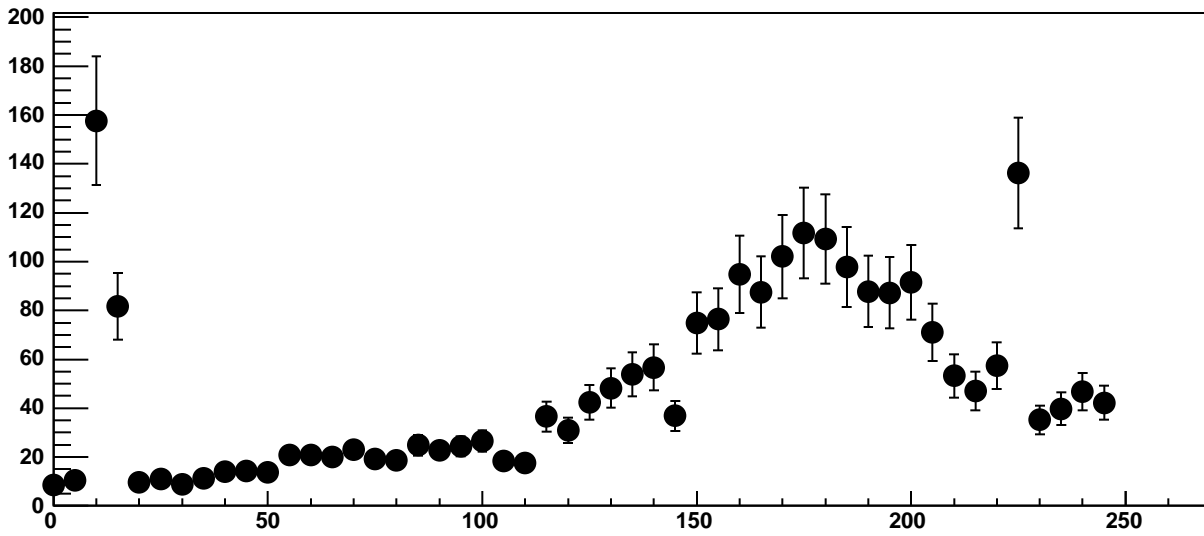


Chip 4, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold

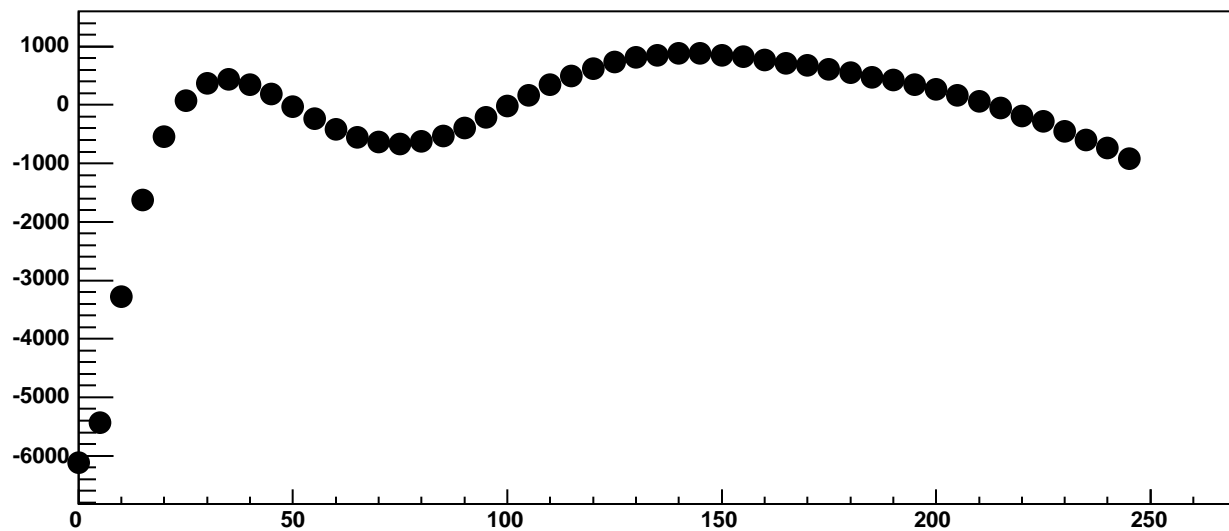


χ^2 / ndf	3.165e+05 / 42
p0	446.9 ± 20.42
p1	-27.57 ± 0.174
p2	8607 ± 19.66
p3	78.24 ± 0.1806

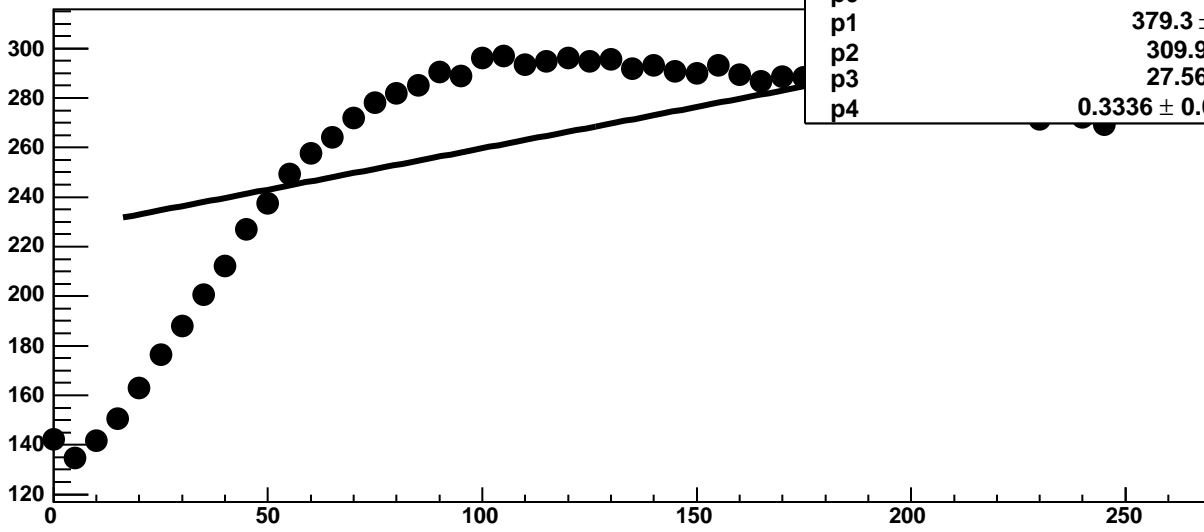
Chip 4, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold

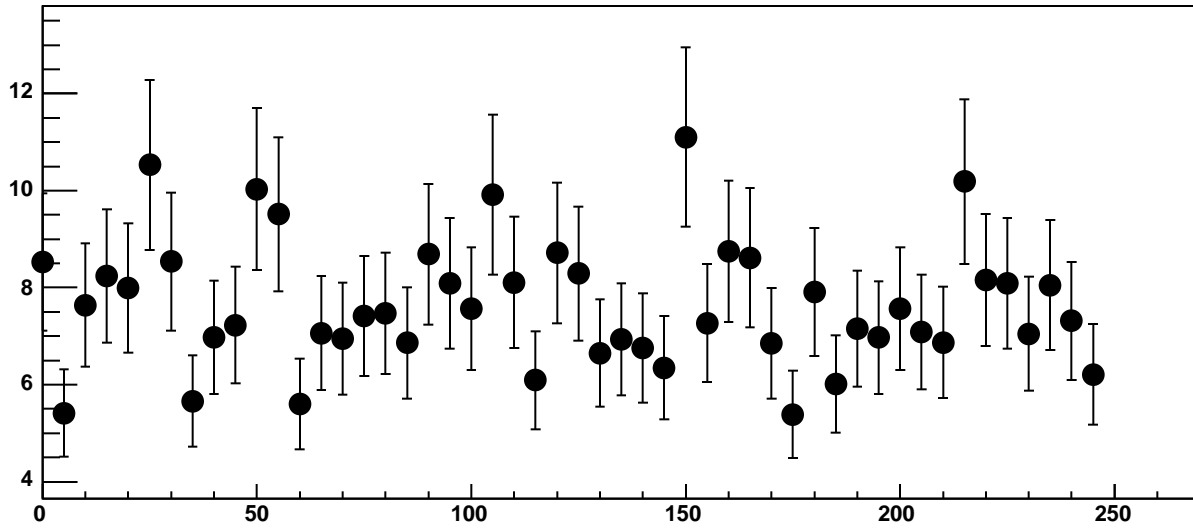


Chip 4, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

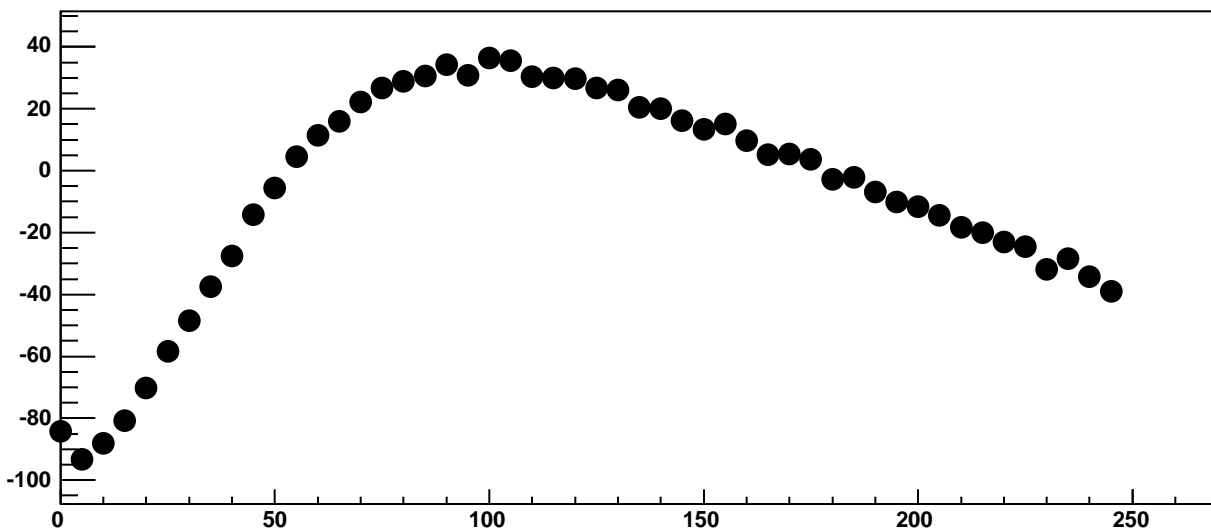


χ^2 / ndf	1.205e+04 / 41
p0	352.9 ± 0.2508
p1	379.3 ± 0.7516
p2	309.9 ± 84.85
p3	27.56 ± 1.414
p4	0.3336 ± 0.0009685

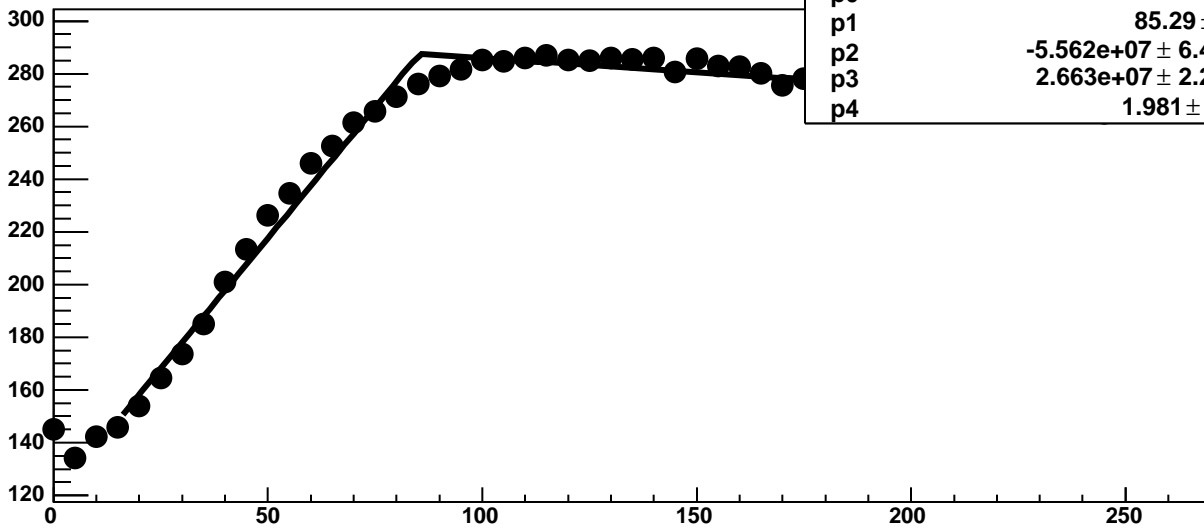
Chip 4, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

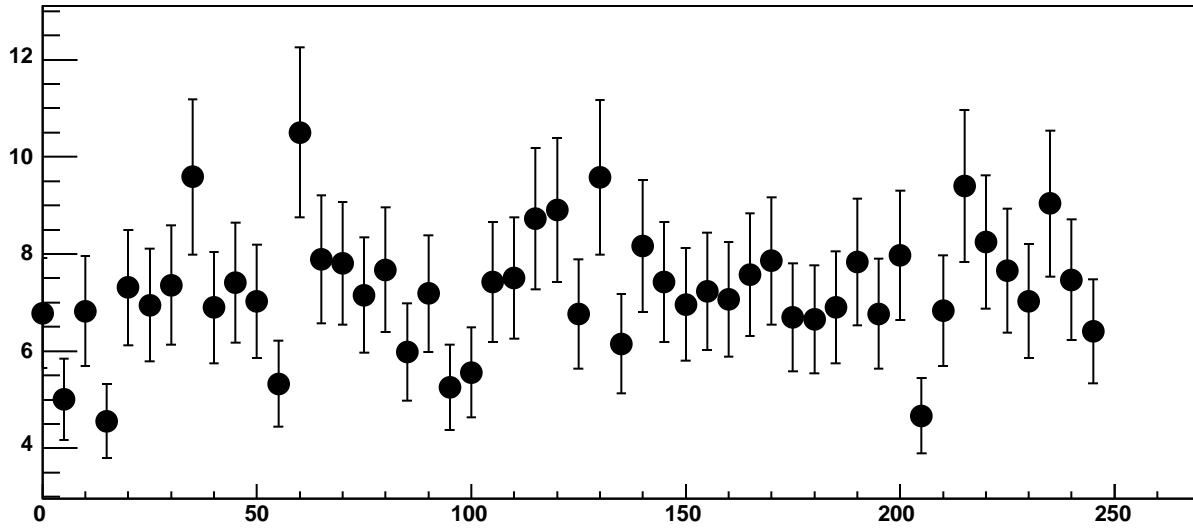


Chip 4, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

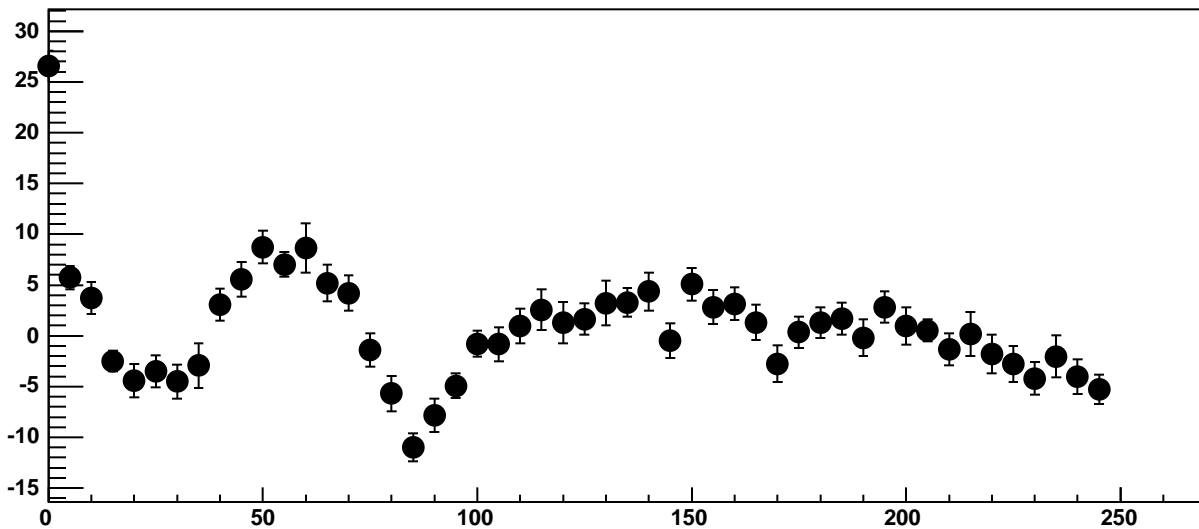


χ^2 / ndf	304.7 / 41
p0	287.5 ± 0.552
p1	85.29 ± 0.4628
p2	$-5.562\text{e}+07 \pm 6.478\text{e}+05$
p3	$2.663\text{e}+07 \pm 2.275\text{e}+05$
p4	1.981 ± 0.01737

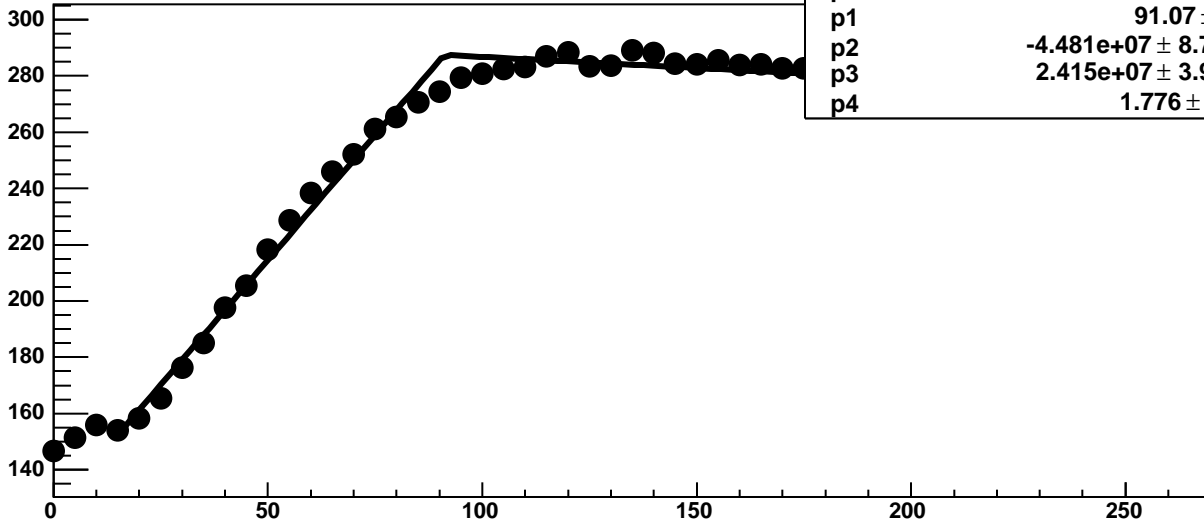
Chip 4, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



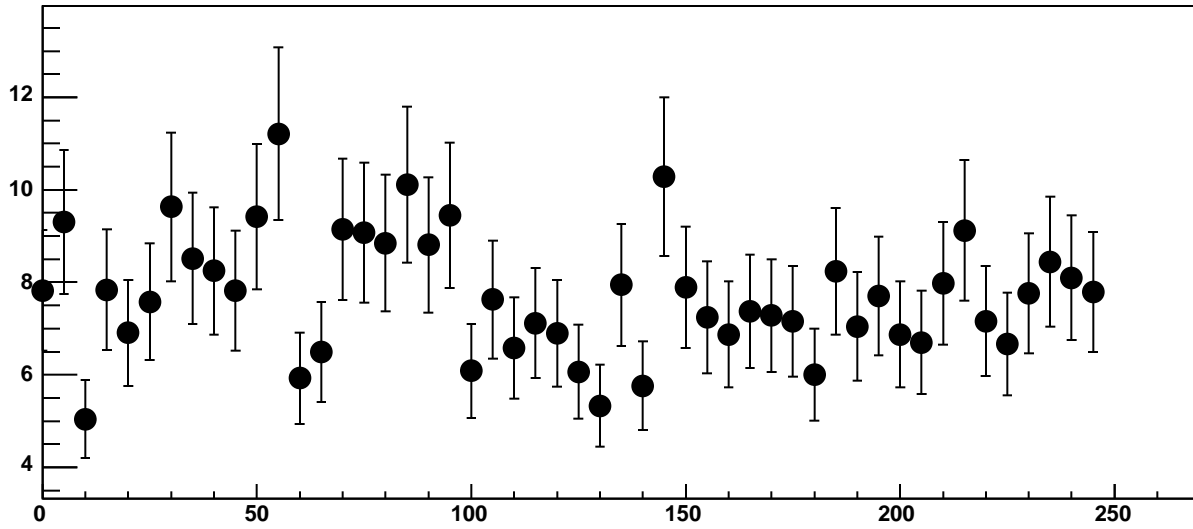
Chip 4, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold



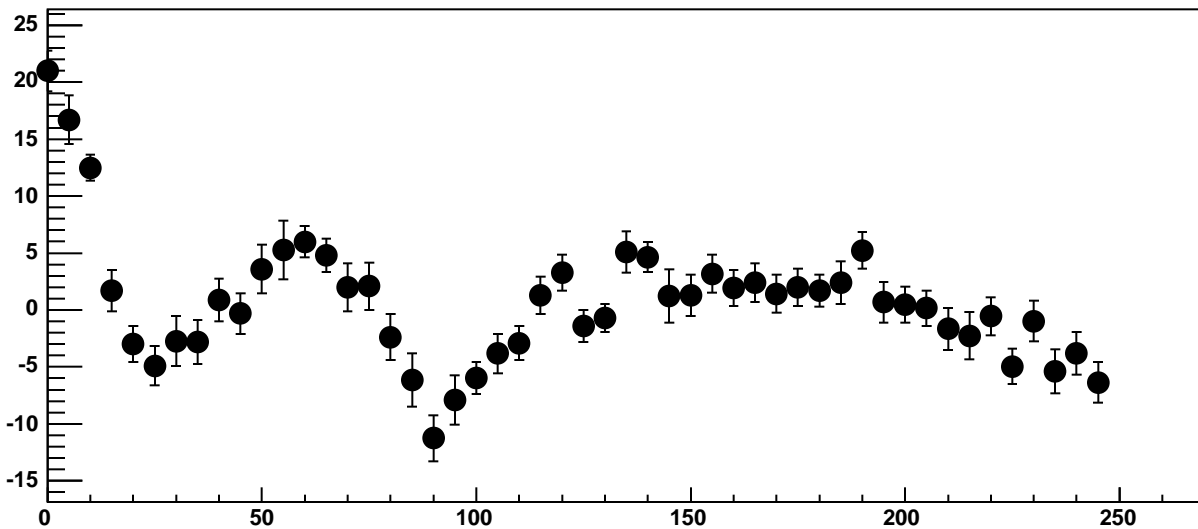
Chip 4, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold



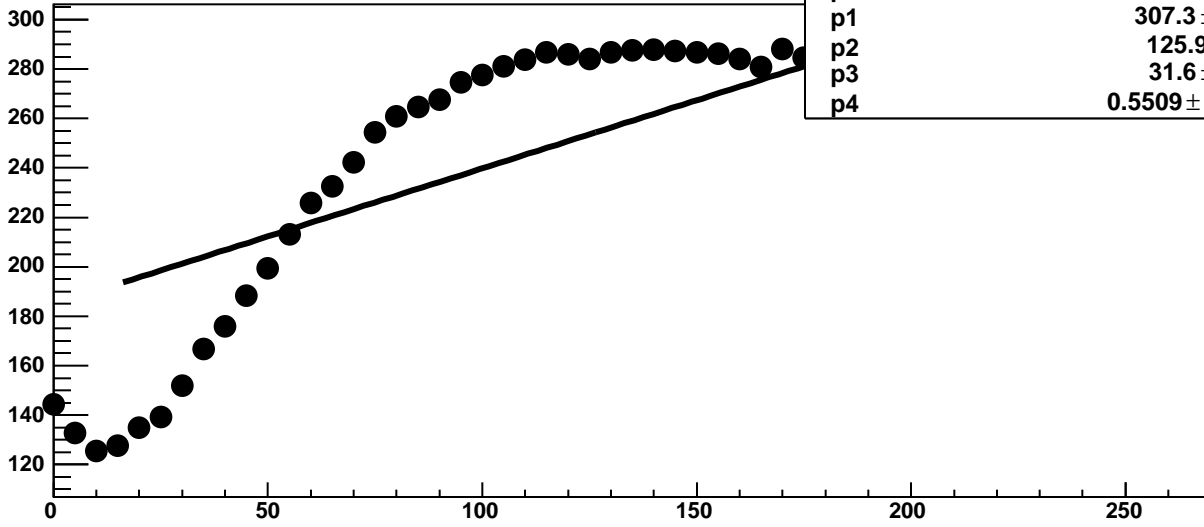
Chip 4, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold



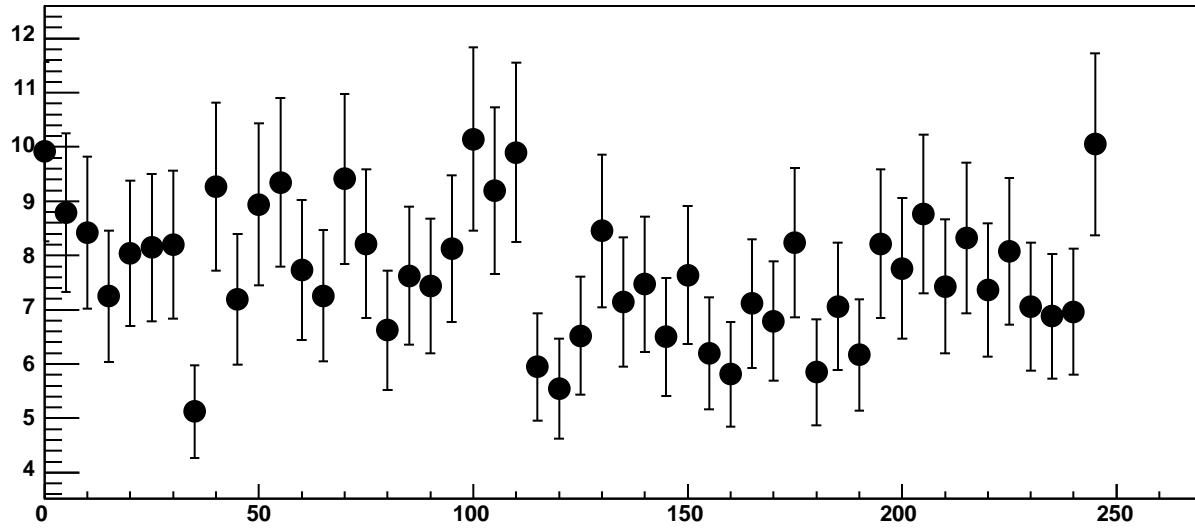
Chip 4, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold



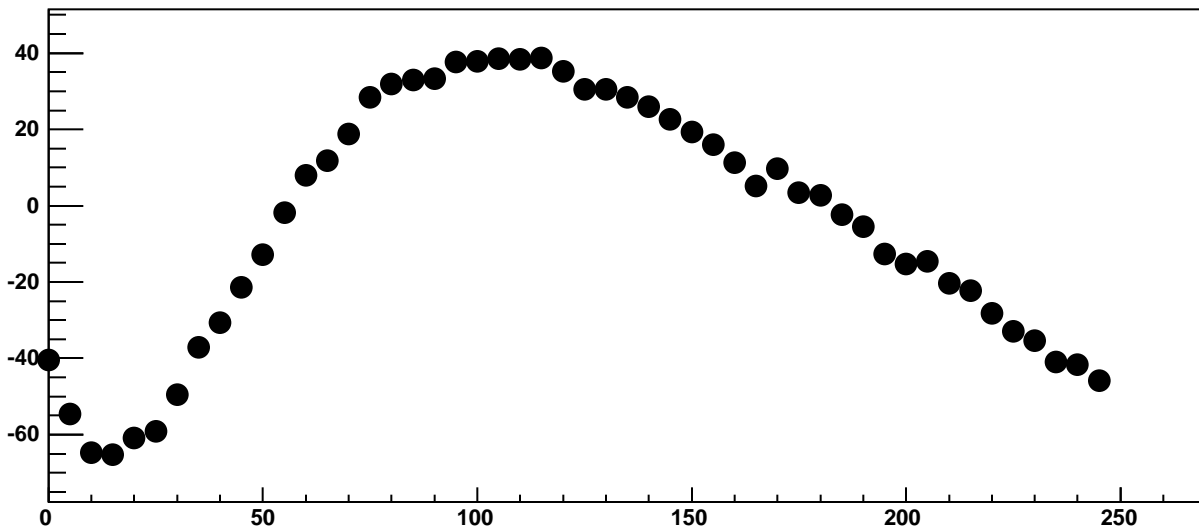
Chip 4, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold



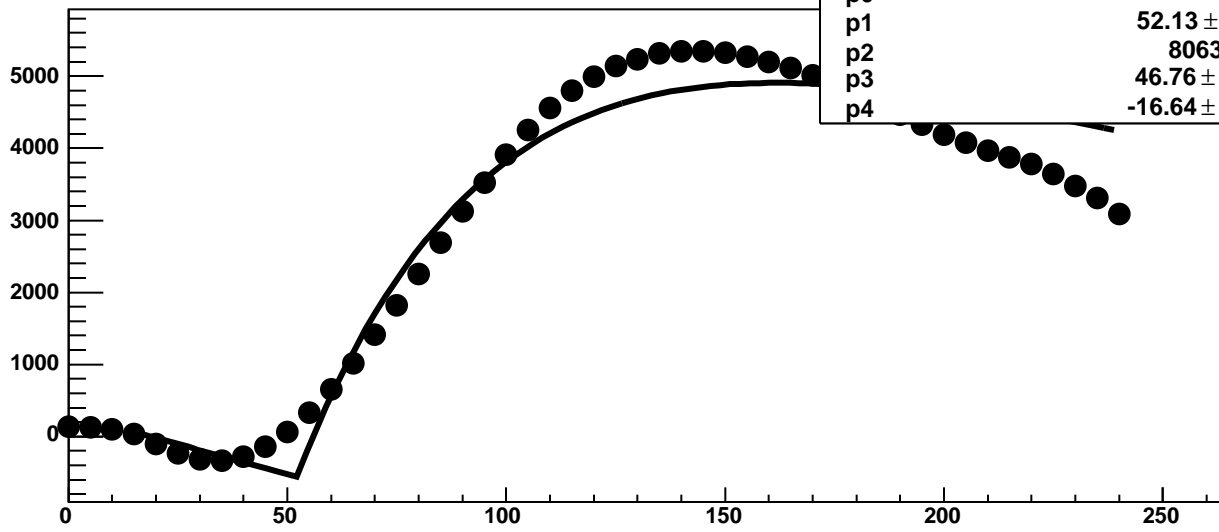
Chip 4, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

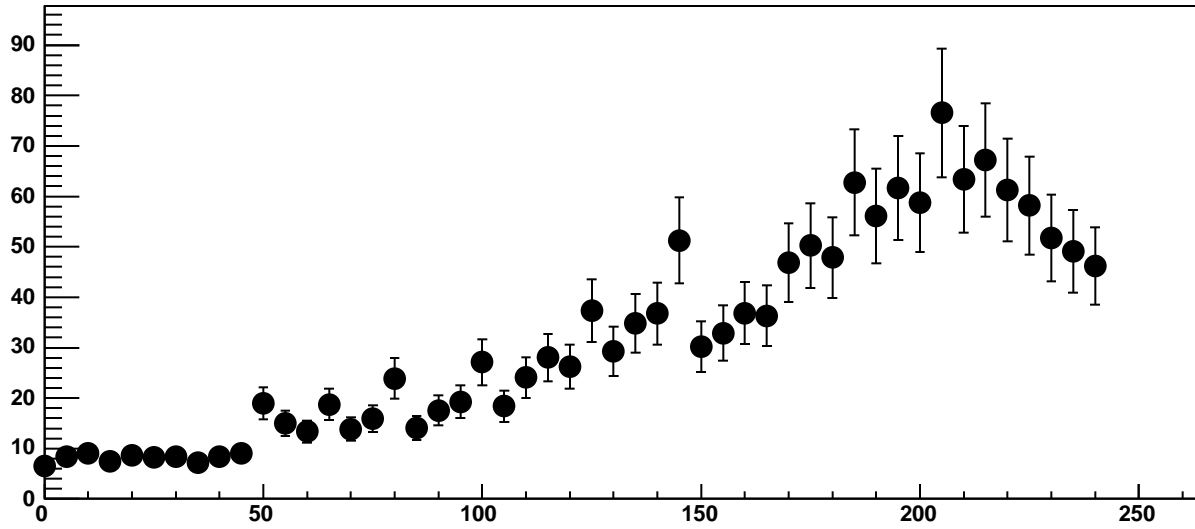


Chip 4, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold

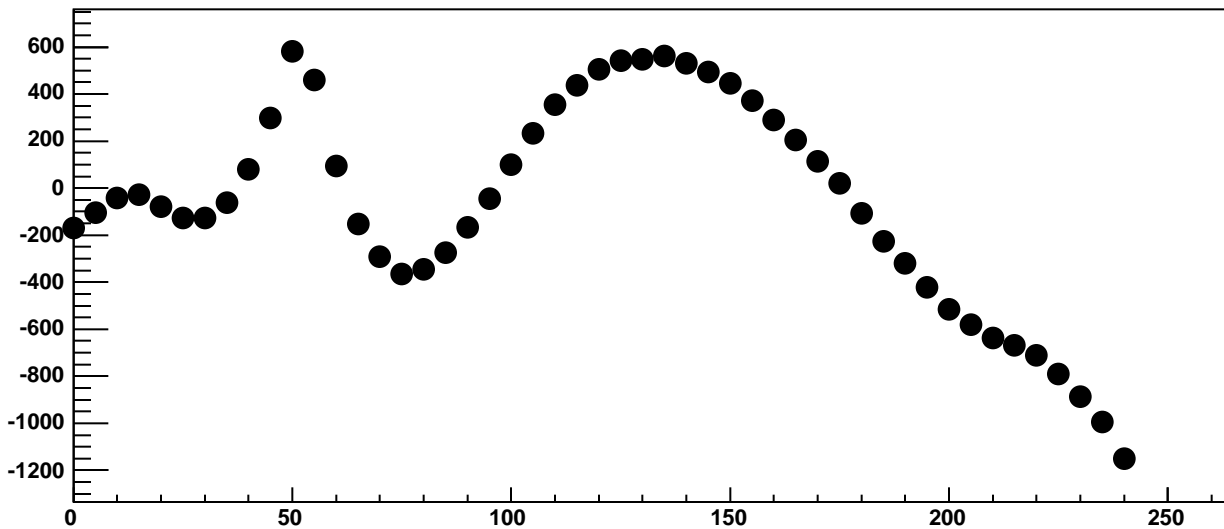


χ^2 / ndf	1.921e+05 / 41
p0	-557.4 ± 1.28
p1	52.13 ± 0.01791
p2	8063 ± 10.83
p3	46.76 ± 0.07092
p4	-16.64 ± 0.05513

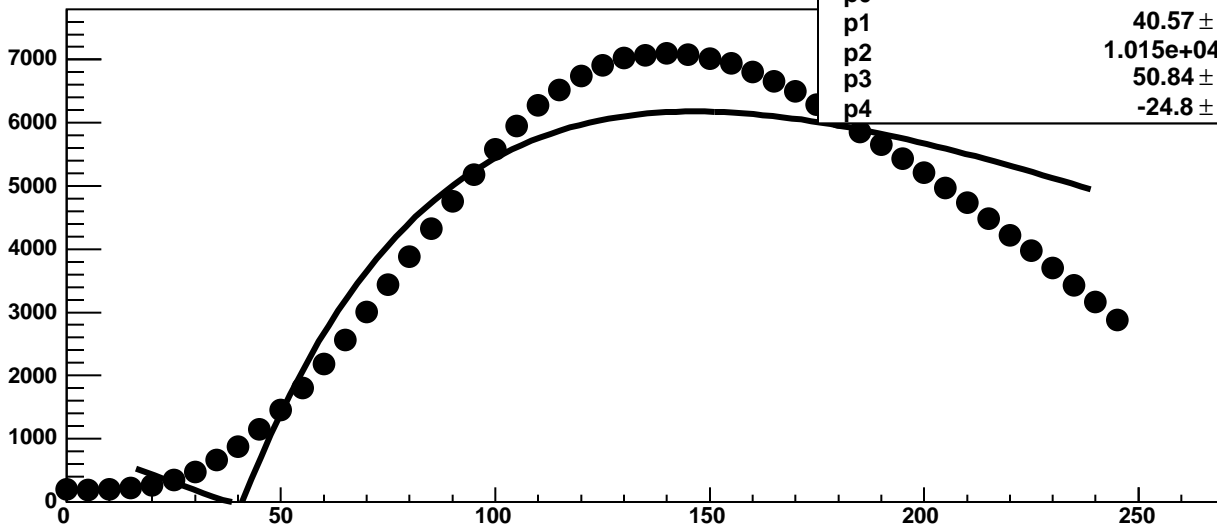
Chip 4, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold

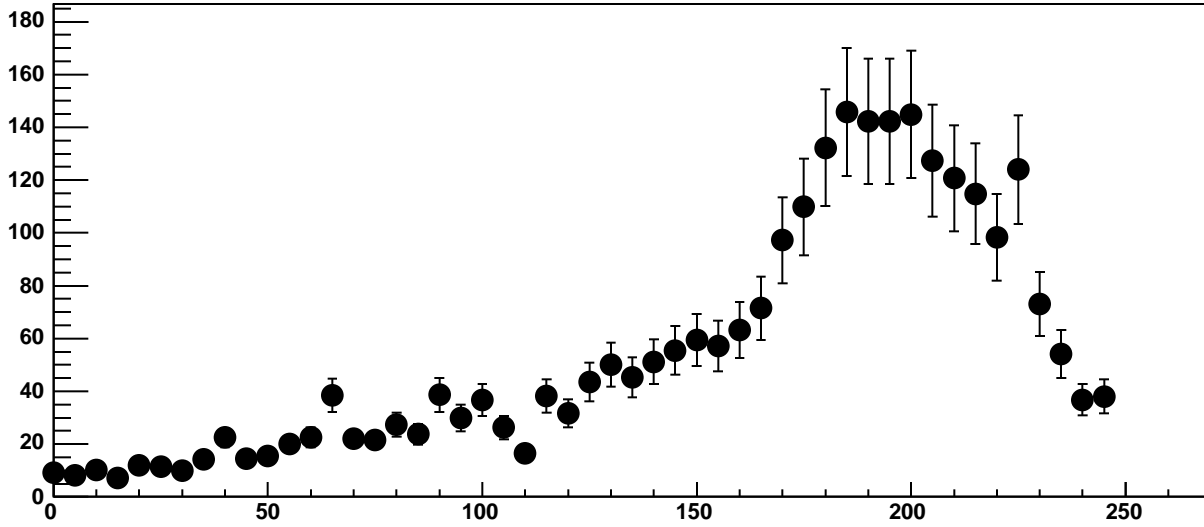


Chip 4, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold

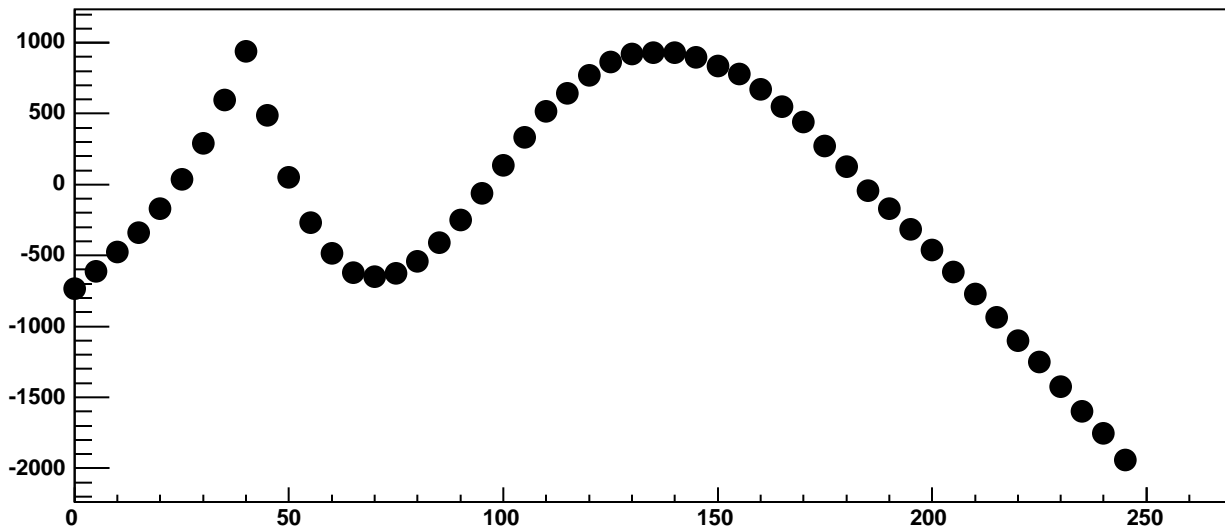


χ^2 / ndf	3.741e+05 / 41
p0	-77.56 ± 1.517
p1	40.57 ± 0.01776
p2	1.015e+04 ± 15.62
p3	50.84 ± 0.08319
p4	-24.8 ± 0.07768

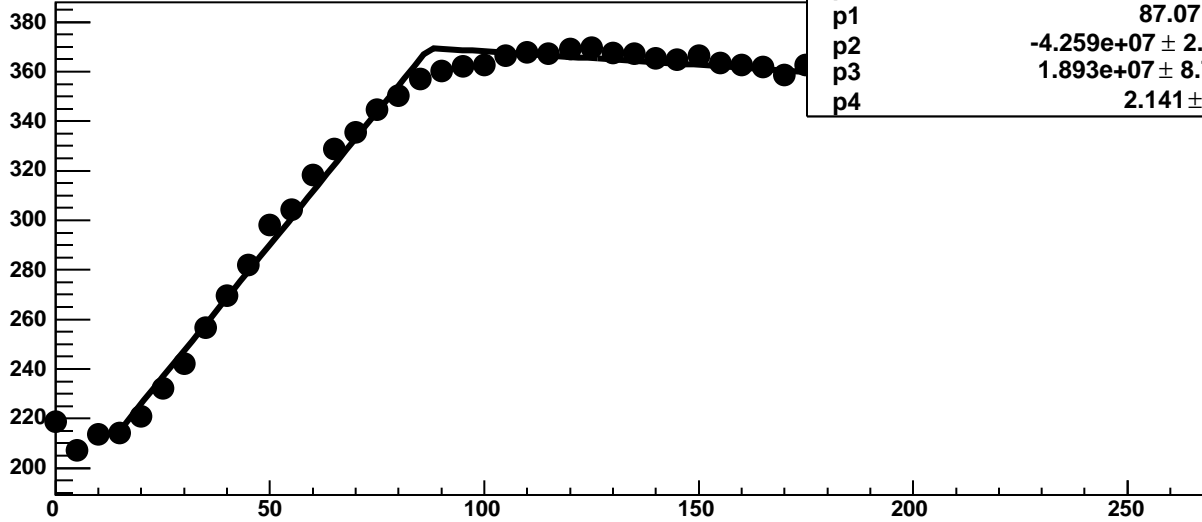
Chip 4, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold

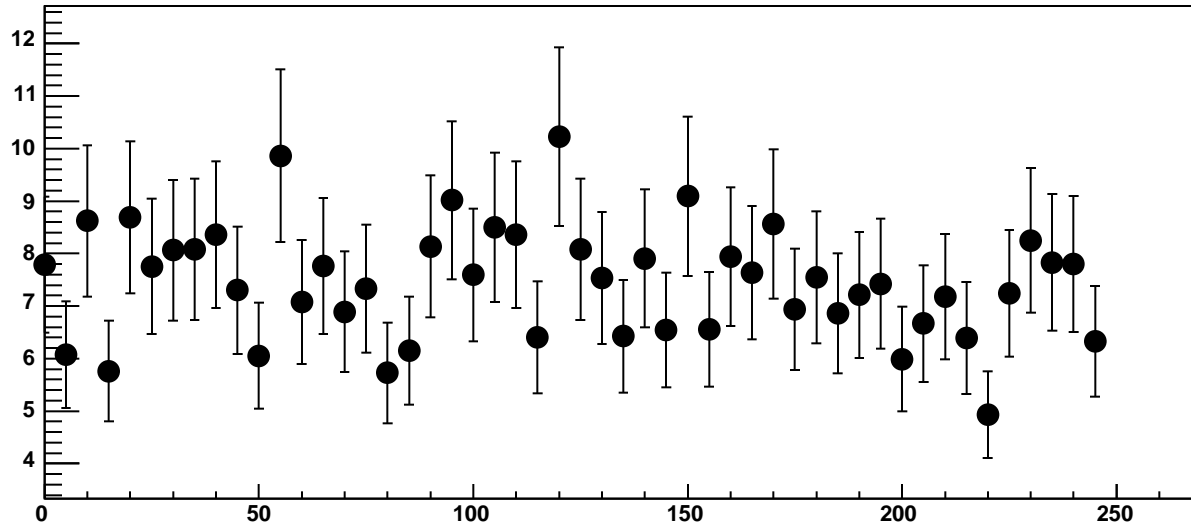


Chip 4, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

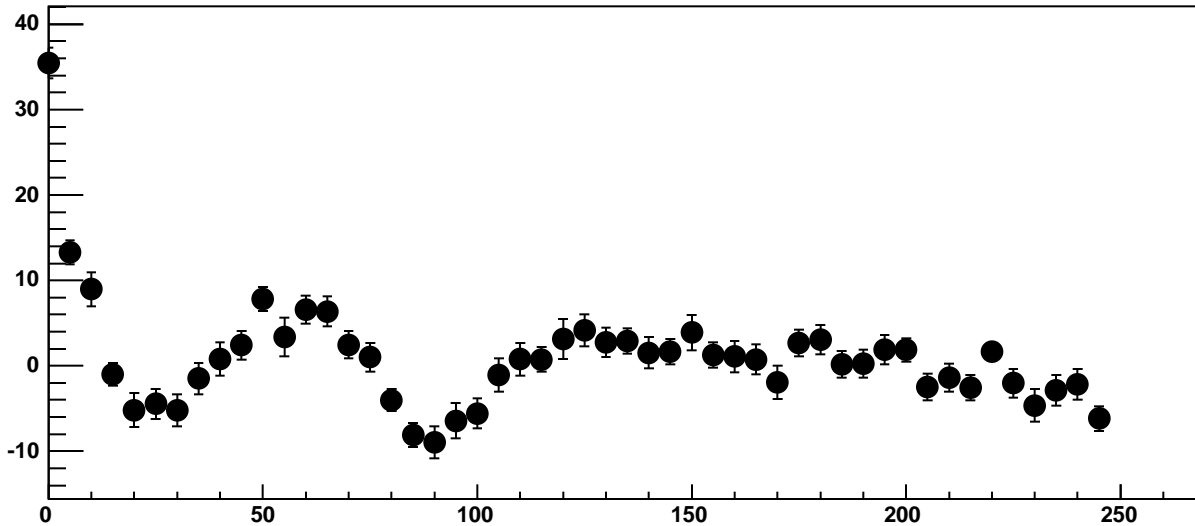


χ^2 / ndf	225.6 / 41
p0	369.6 ± 0.6138
p1	87.07 ± 0.4321
p2	$-4.259\text{e}+07 \pm 2.008\text{e}+06$
p3	$1.893\text{e}+07 \pm 8.755\text{e}+05$
p4	2.141 ± 0.01809

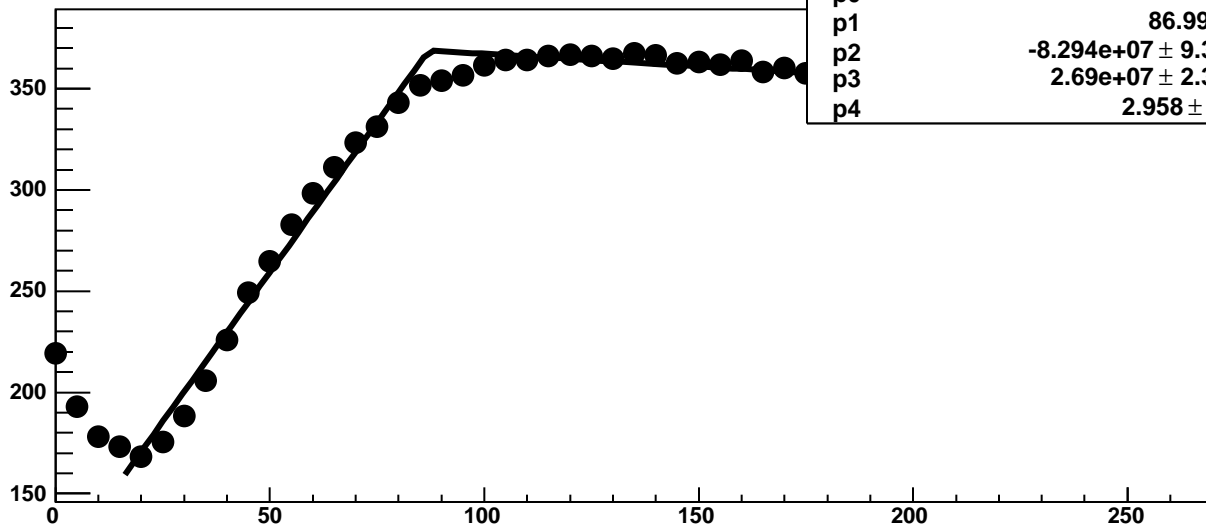
Chip 4, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

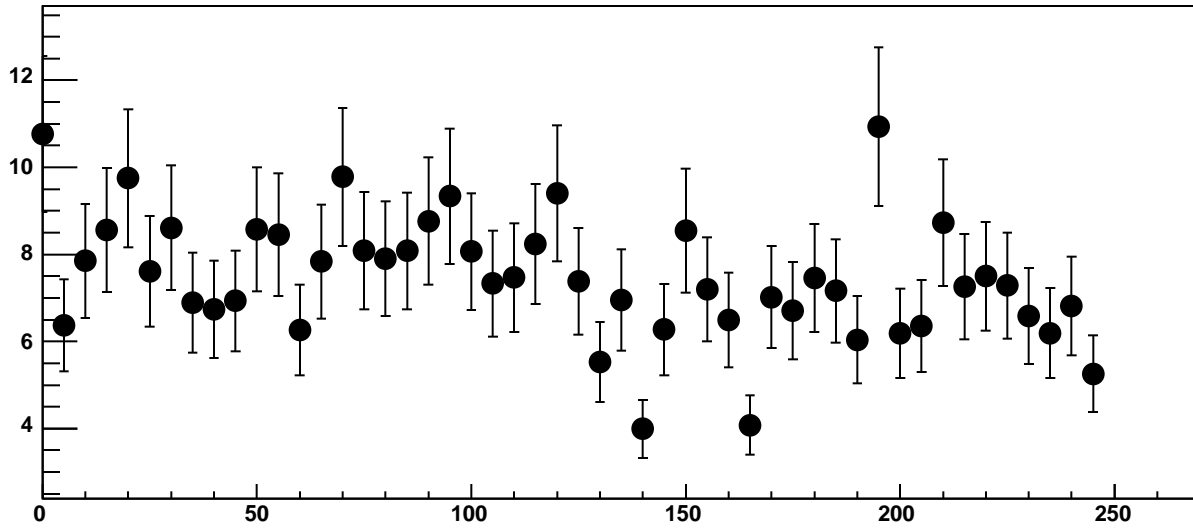


Chip 4, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

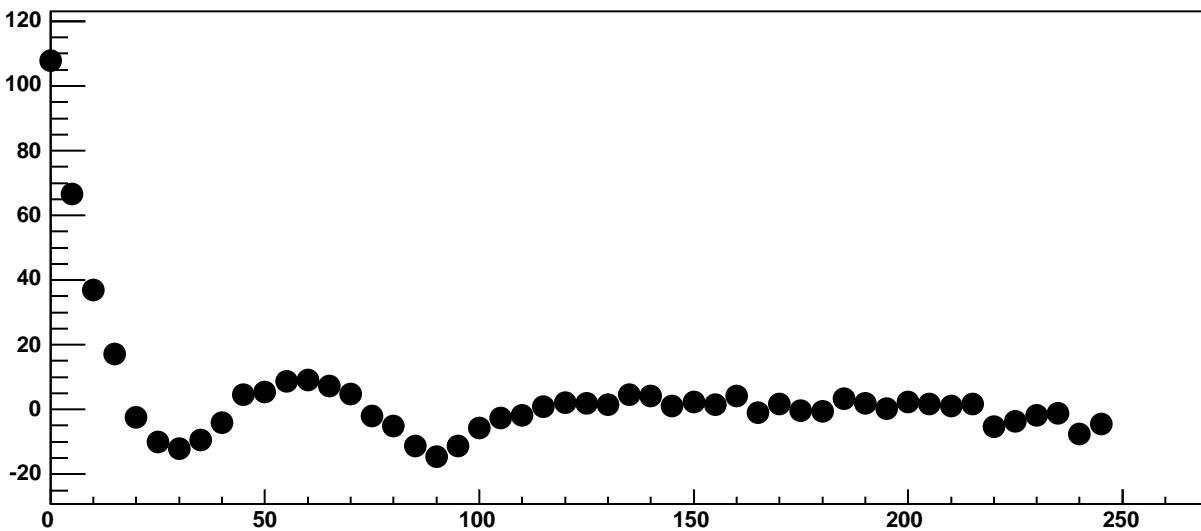


χ^2 / ndf	523 / 41
p0	368.9 ± 0.5776
p1	86.99 ± 0.362
p2	$-8.294e+07 \pm 9.384e+05$
p3	$2.69e+07 \pm 2.369e+05$
p4	2.958 ± 0.02238

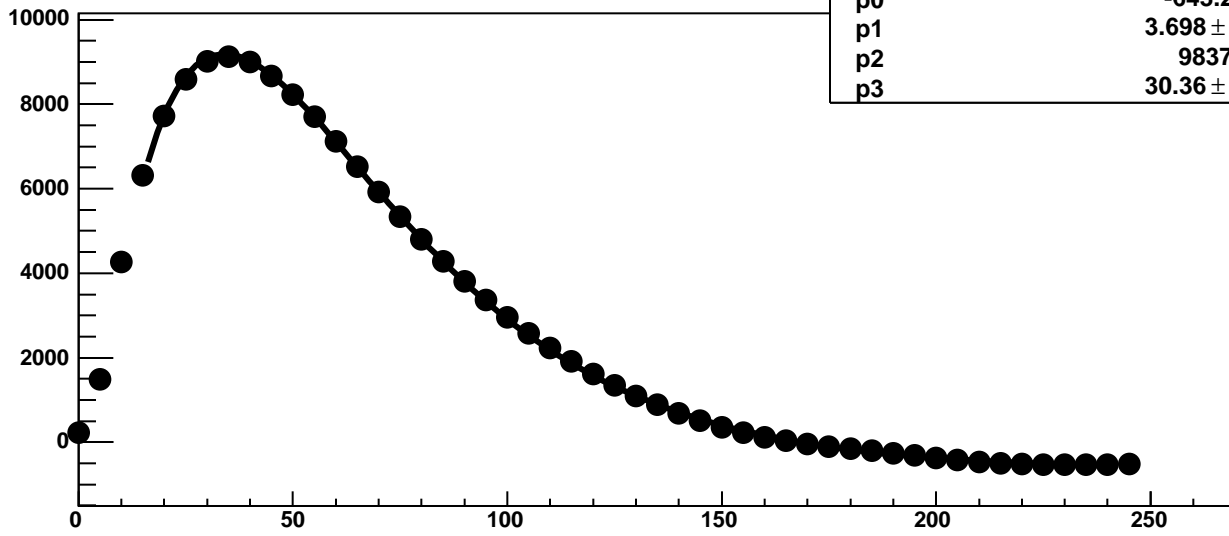
Chip 4, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

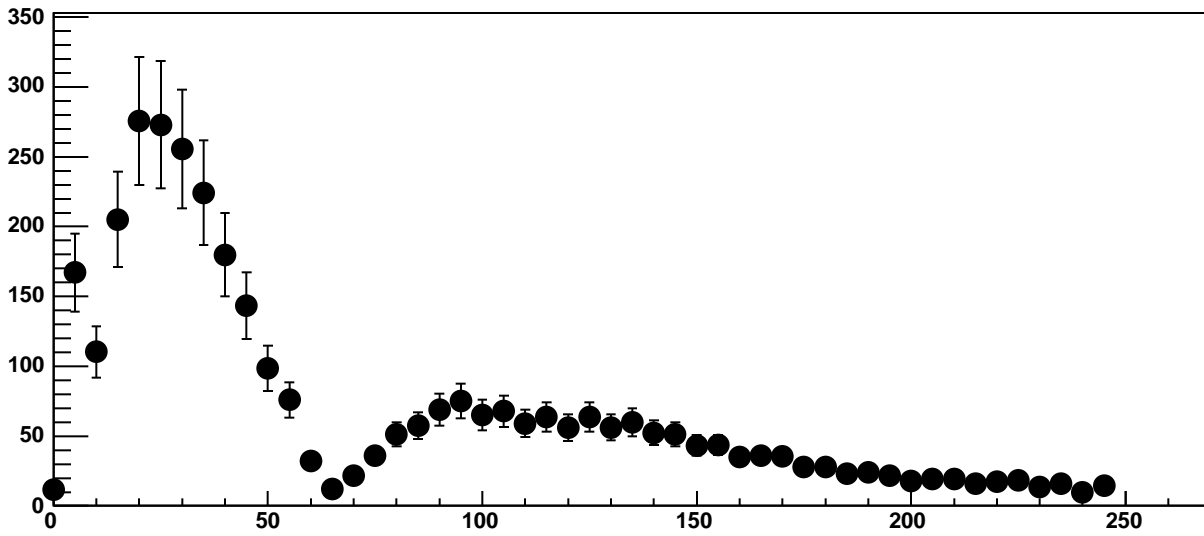


Chip 4, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold

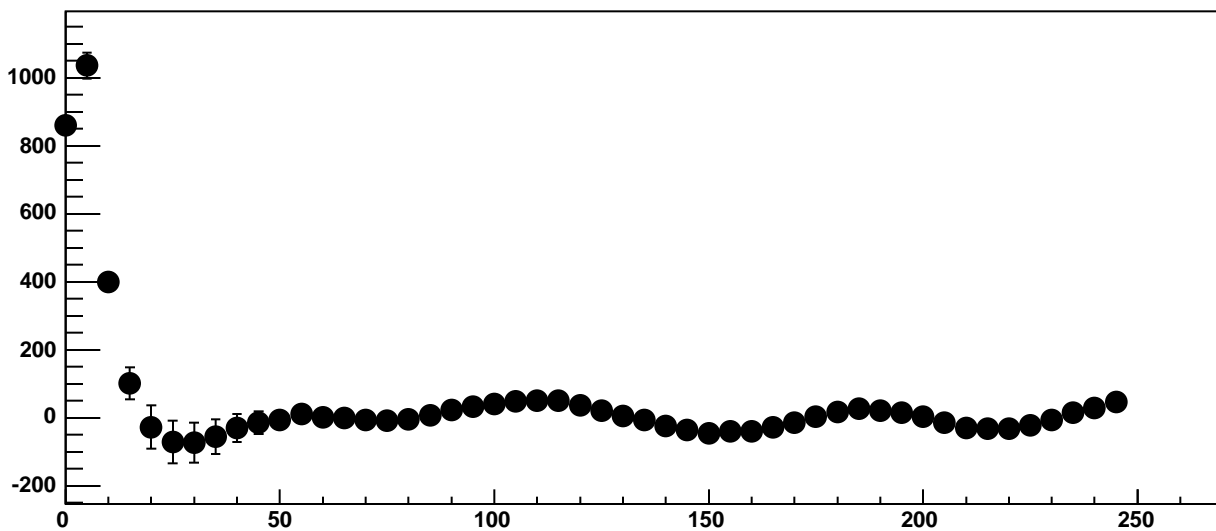


χ^2 / ndf	623.6 / 42
p0	-643.2 ± 1.565
p1	3.698 ± 0.09167
p2	9837 ± 10.86
p3	30.36 ± 0.03578

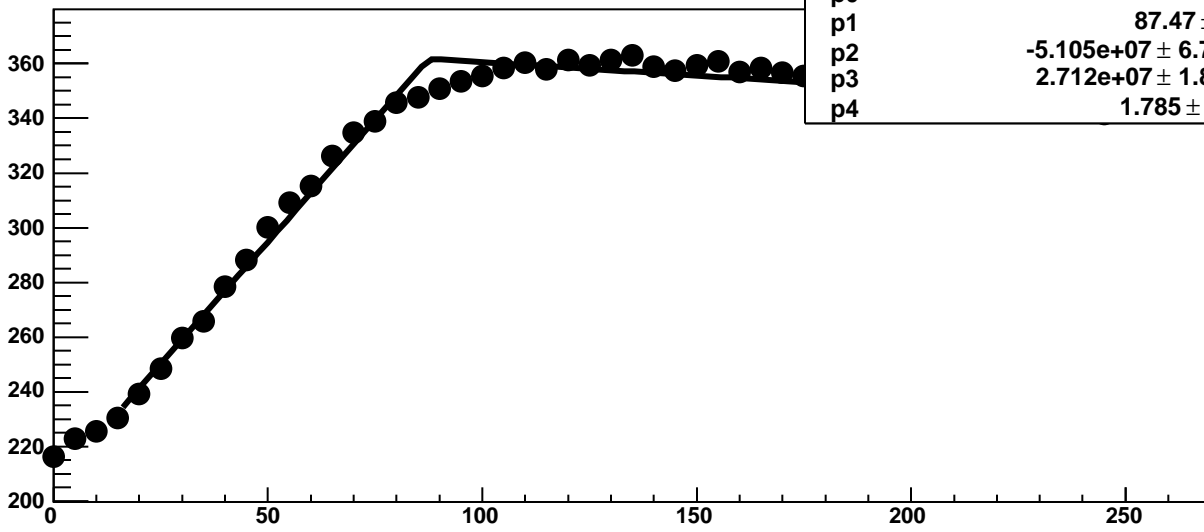
Chip 4, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold

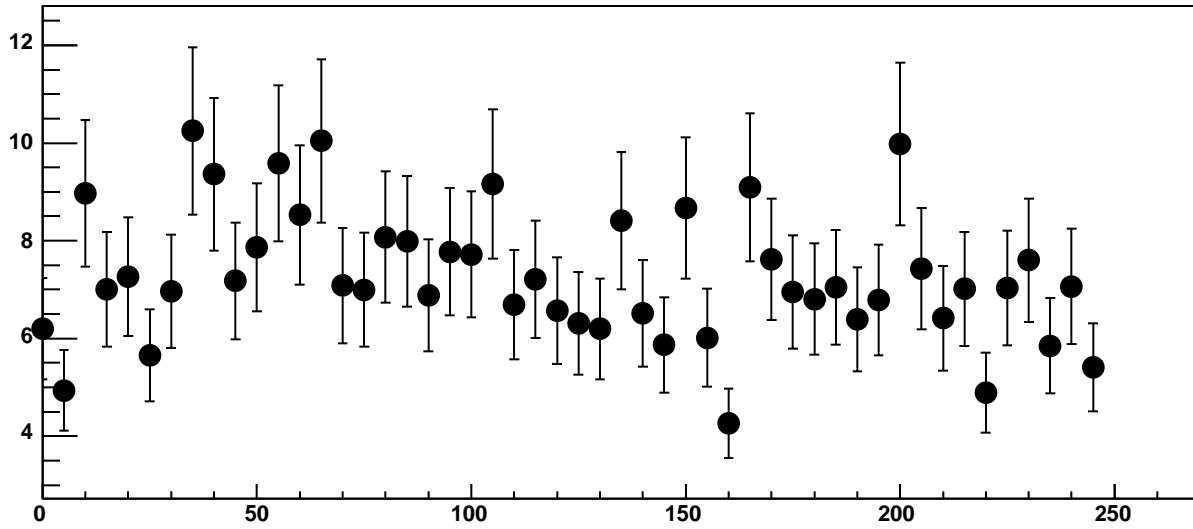


Chip 4, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

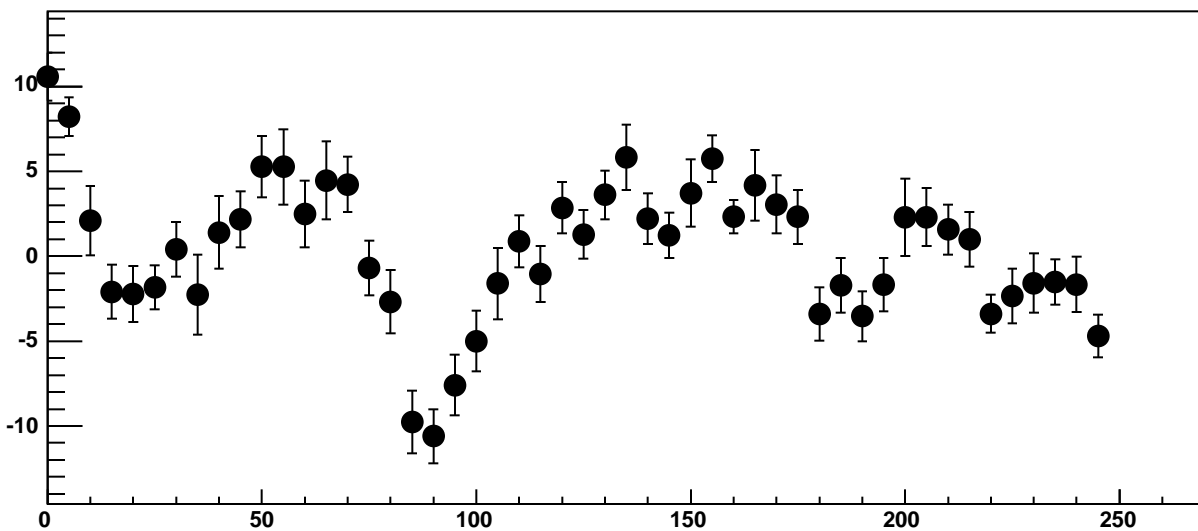


χ^2 / ndf	228.4 / 41
p0	361.7 ± 0.5492
p1	87.47 ± 0.5807
p2	$-5.105\text{e}+07 \pm 6.725\text{e}+05$
p3	$2.712\text{e}+07 \pm 1.862\text{e}+05$
p4	1.785 ± 0.02012

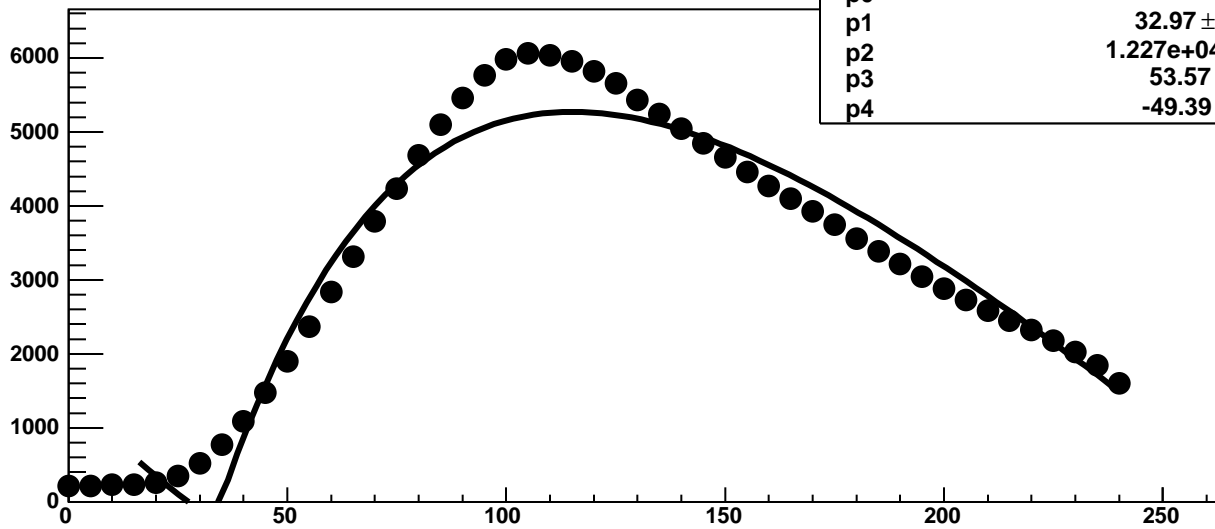
Chip 4, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

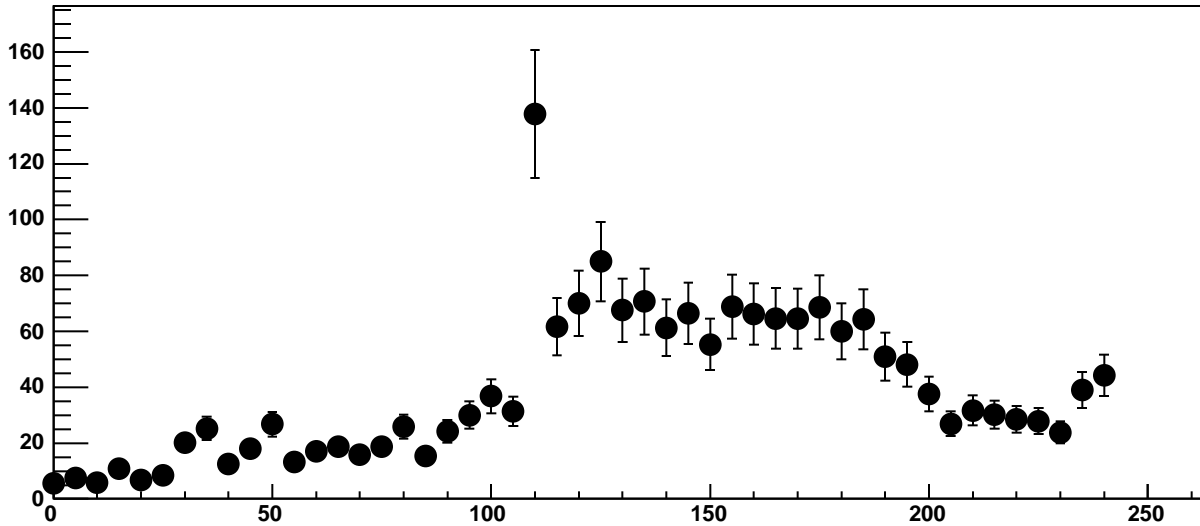


Chip 4, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold

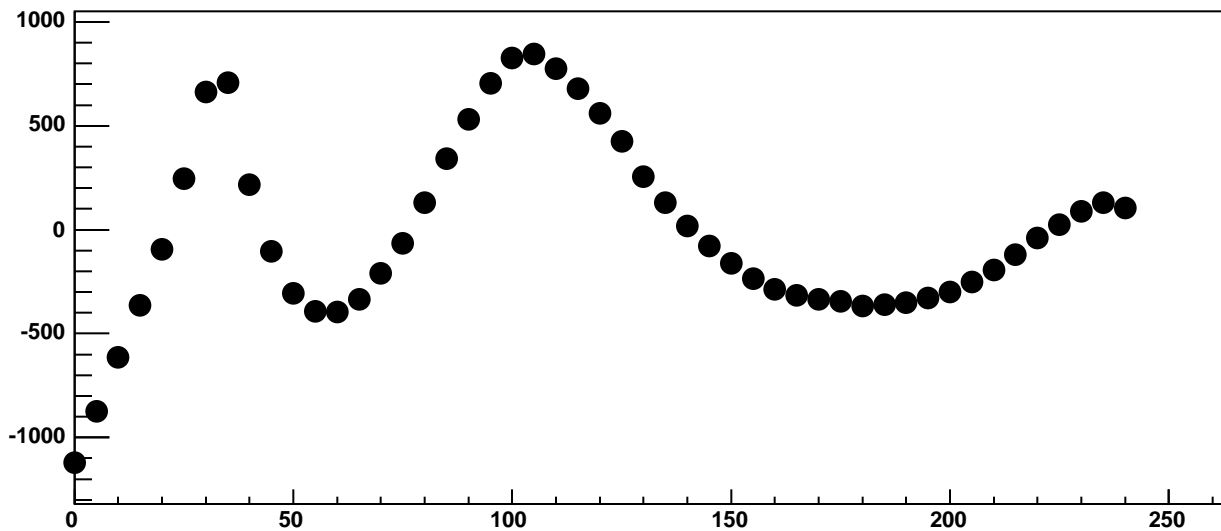


χ^2 / ndf	1.889e+05 / 41
p0	-290.3 ± 1.295
p1	32.97 ± 0.01613
p2	1.227e+04 ± 22.55
p3	53.57 ± 0.1008
p4	-49.39 ± 0.1013

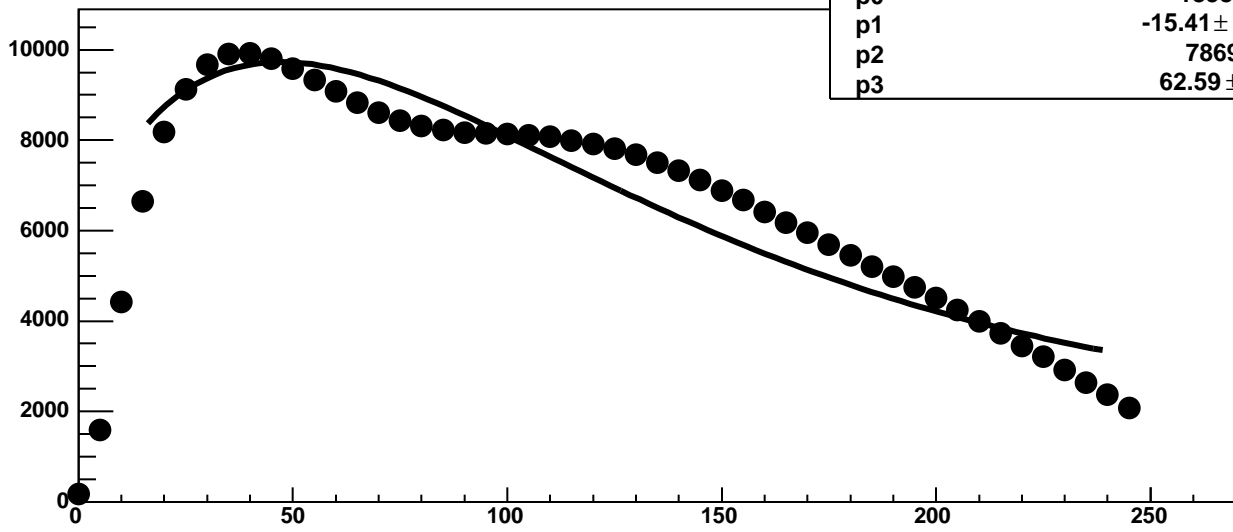
Chip 4, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

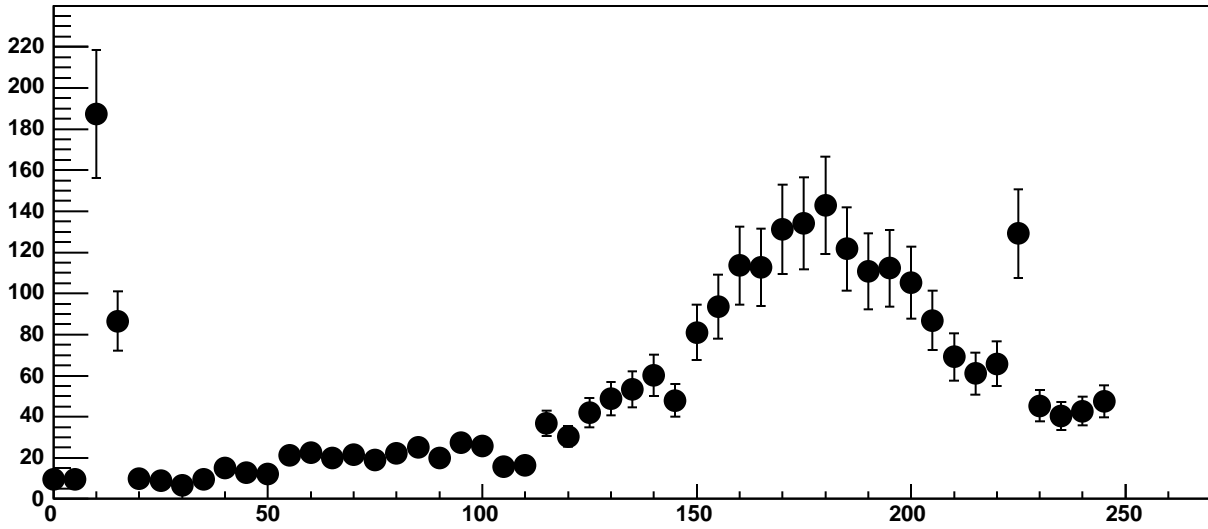


Chip 4, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold

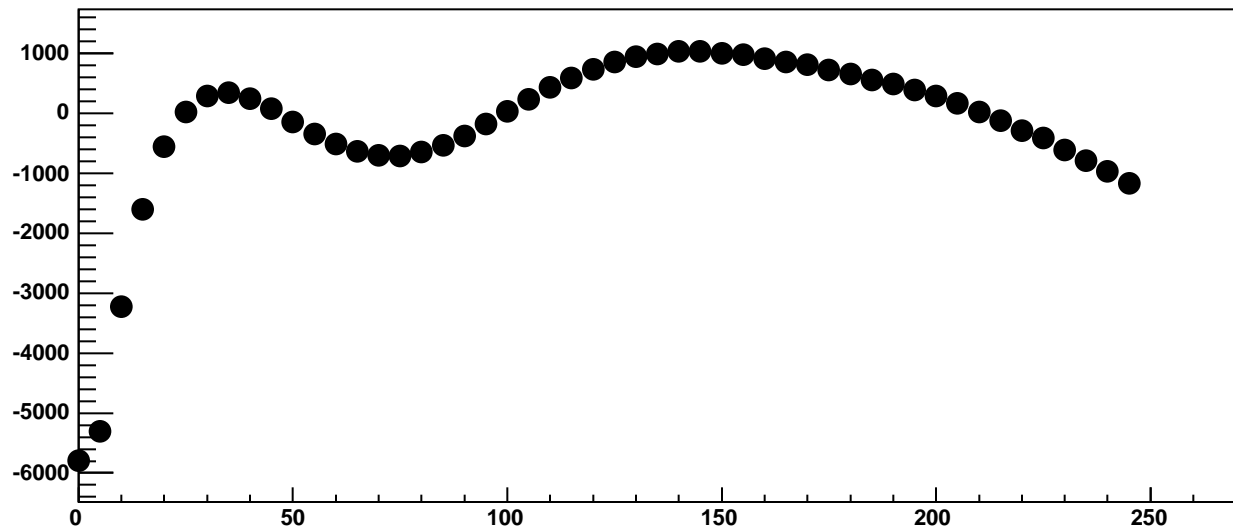


χ^2 / ndf	3.532e+05 / 42
p0	1858 ± 12.42
p1	-15.41 ± 0.09821
p2	7869 ± 11.9
p3	62.59 ± 0.1017

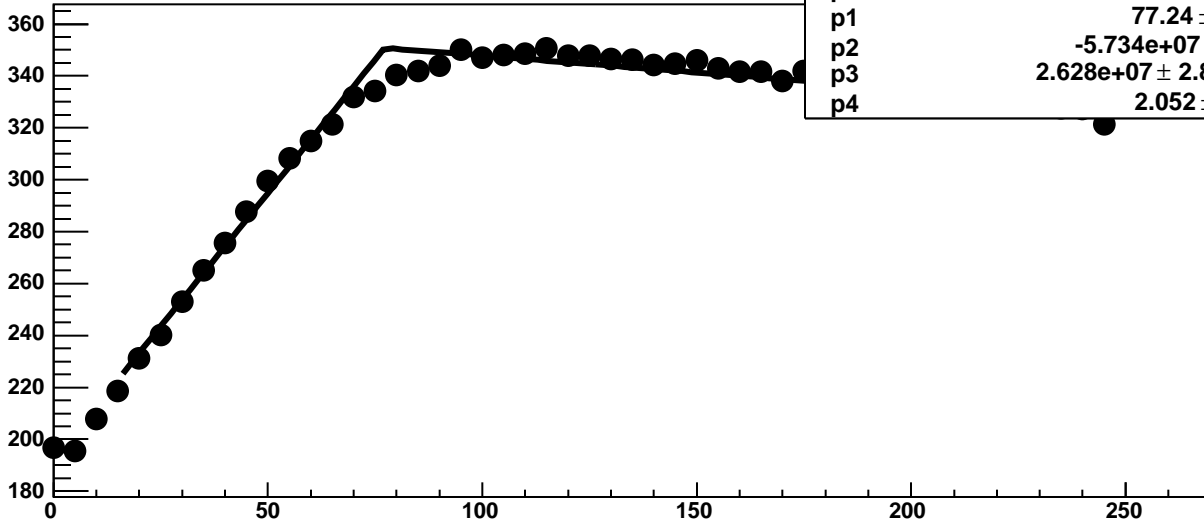
Chip 4, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold

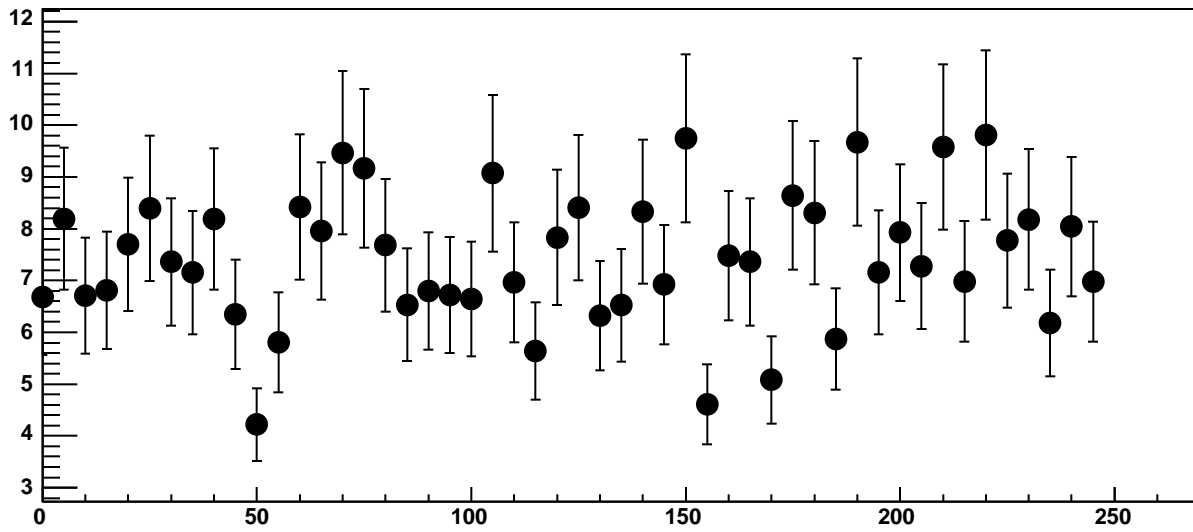


Chip 4, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

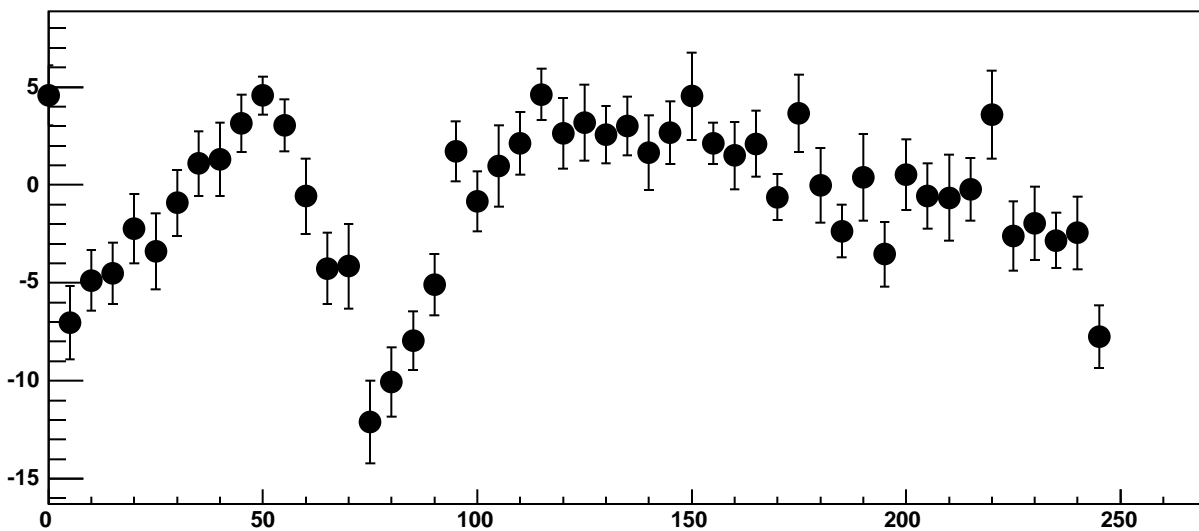


χ^2 / ndf	225.7 / 41
p0	350.8 ± 0.5375
p1	77.24 ± 0.5325
p2	$-5.734\text{e}+07 \pm 9\text{e}+05$
p3	$2.628\text{e}+07 \pm 2.878\text{e}+05$
p4	2.052 ± 0.0272

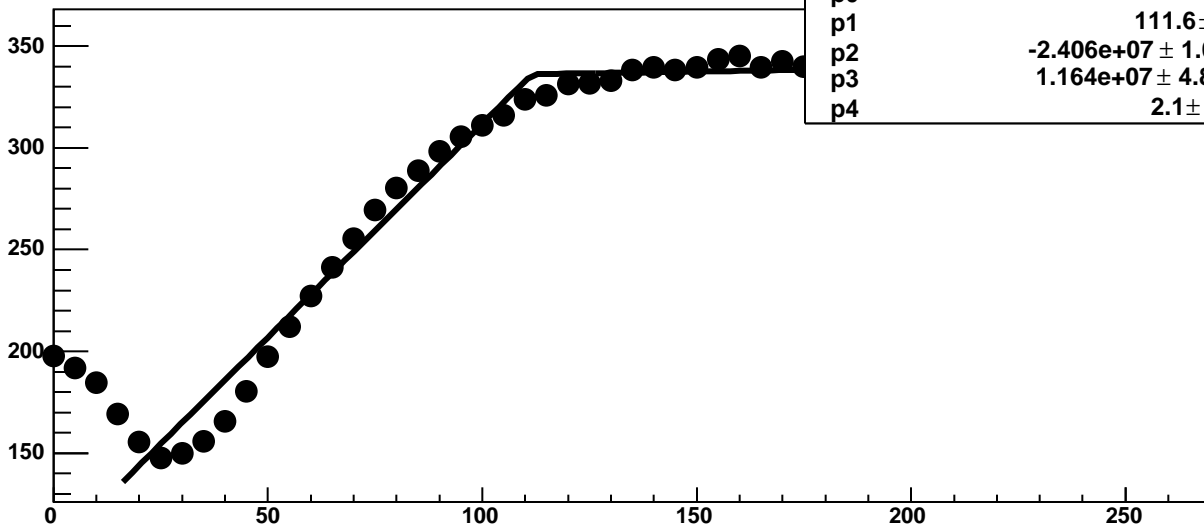
Chip 4, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold

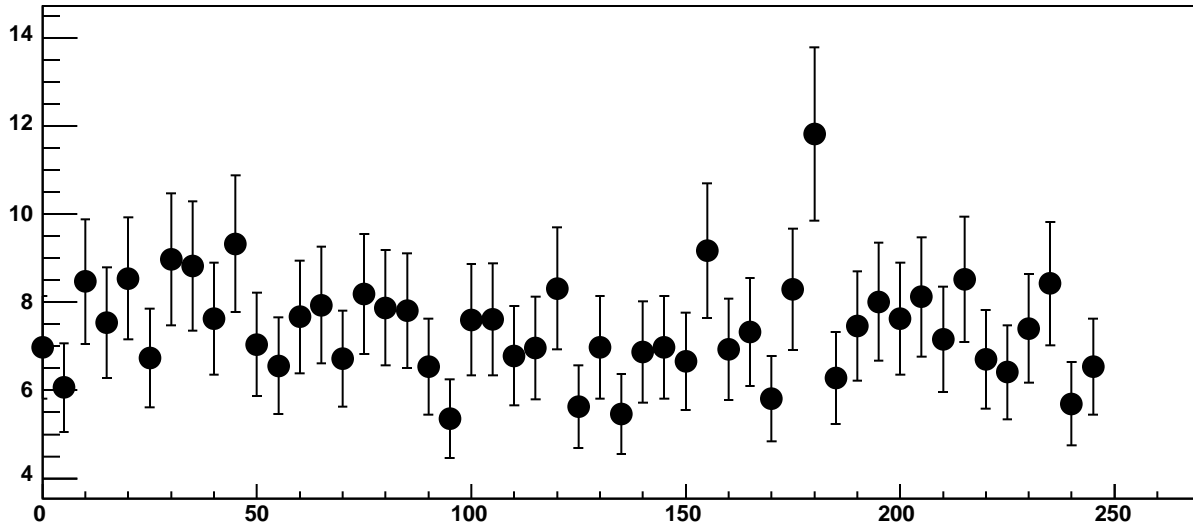


Chip 4, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

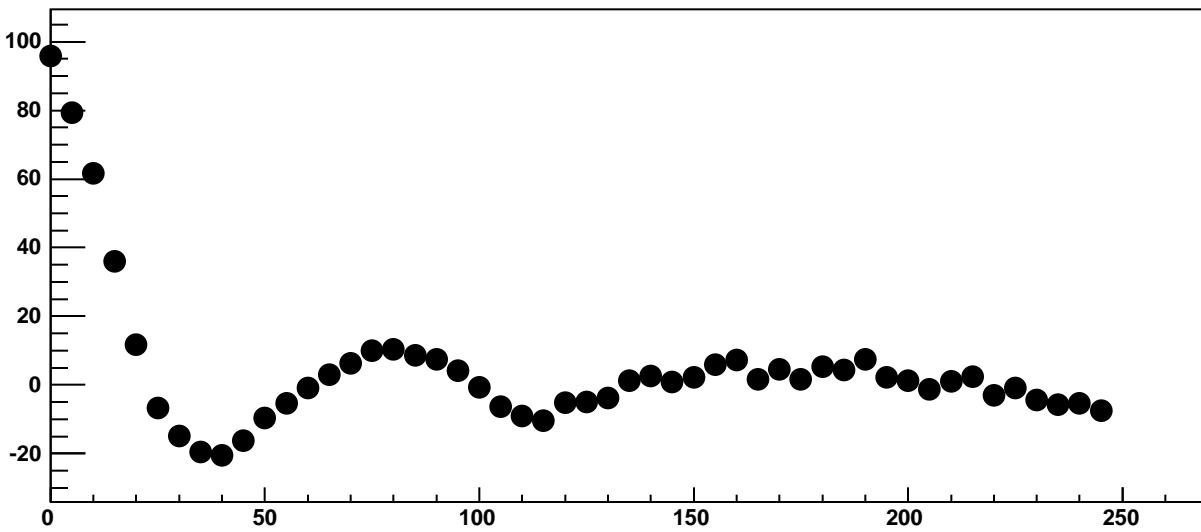


χ^2 / ndf	1257 / 41
p0	336.3 ± 0.6227
p1	111.6 ± 0.4534
p2	$-2.406\text{e}+07 \pm 1.019\text{e}+06$
p3	$1.164\text{e}+07 \pm 4.857\text{e}+05$
p4	2.1 ± 0.01305

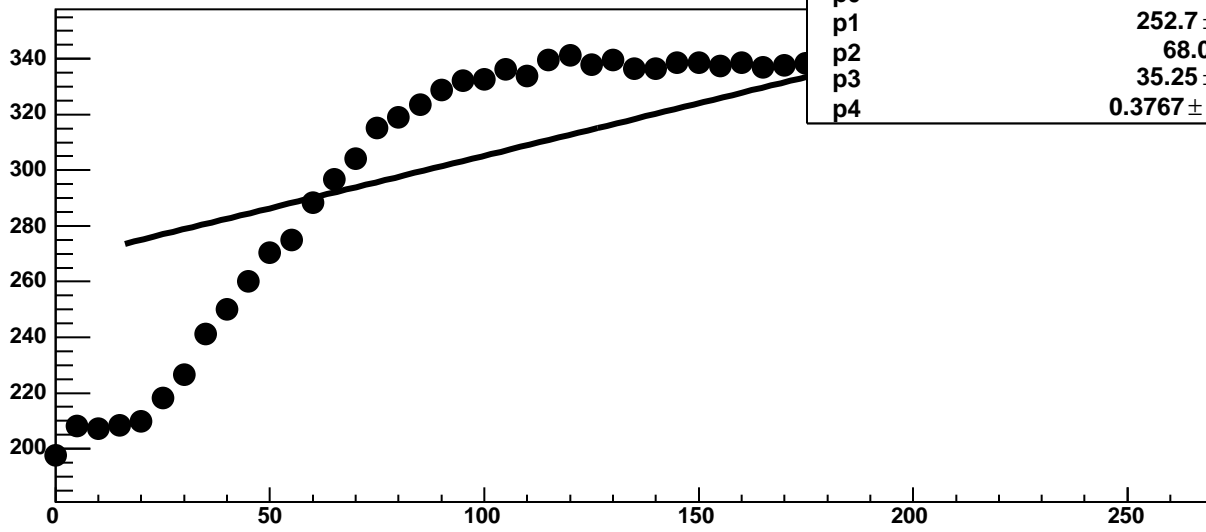
Chip 4, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

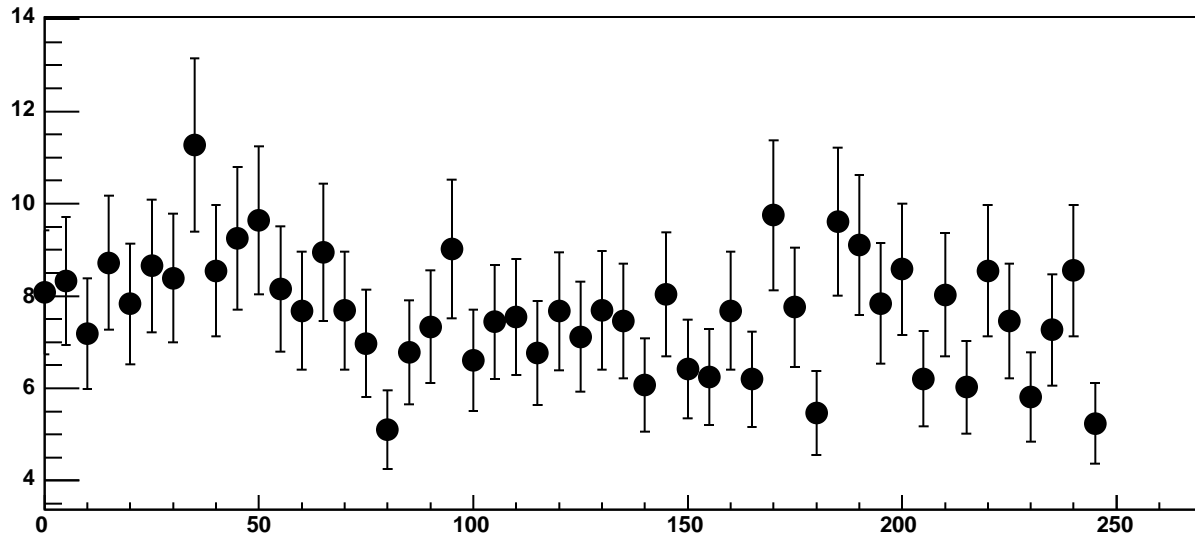


Chip 4, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold

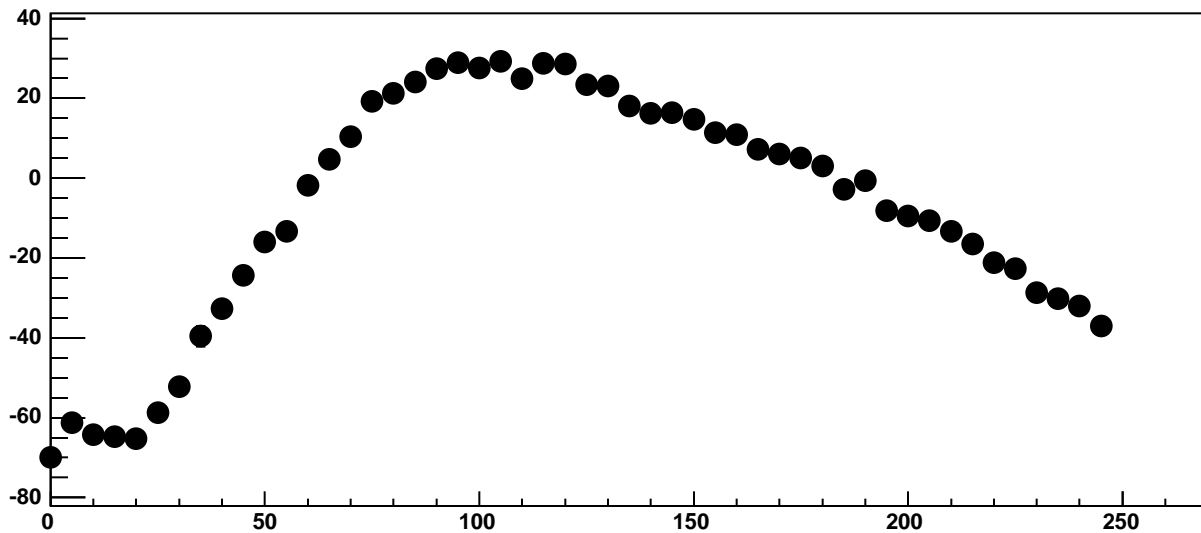


χ^2 / ndf	9973 / 41
p0	362.7 ± 0.4354
p1	252.7 ± 0.4922
p2	68.03 ± 2.3
p3	35.25 ± 0.2042
p4	0.3767 ± 0.00395

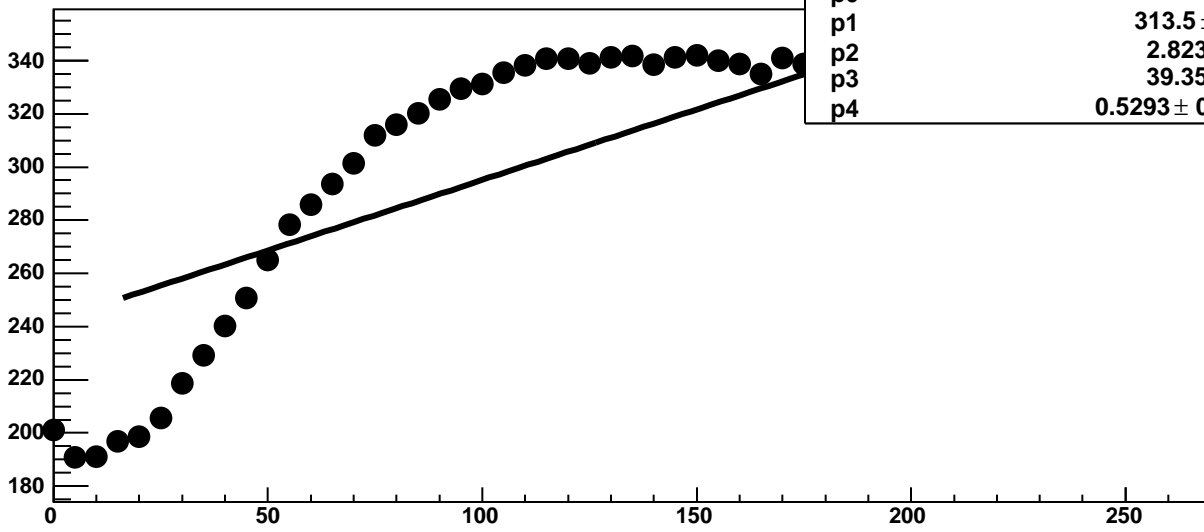
Chip 4, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold



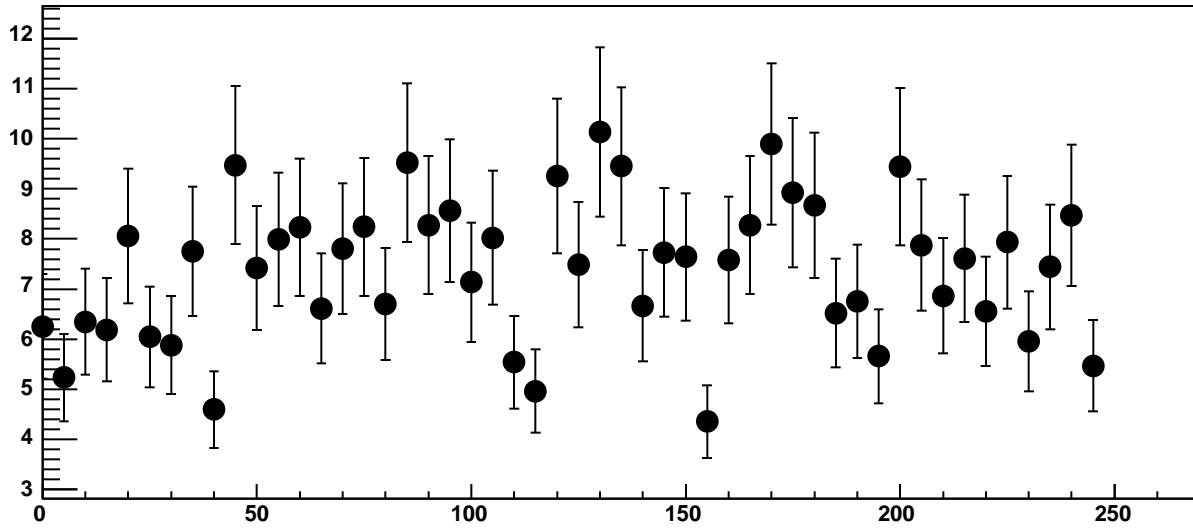
Chip 4, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold



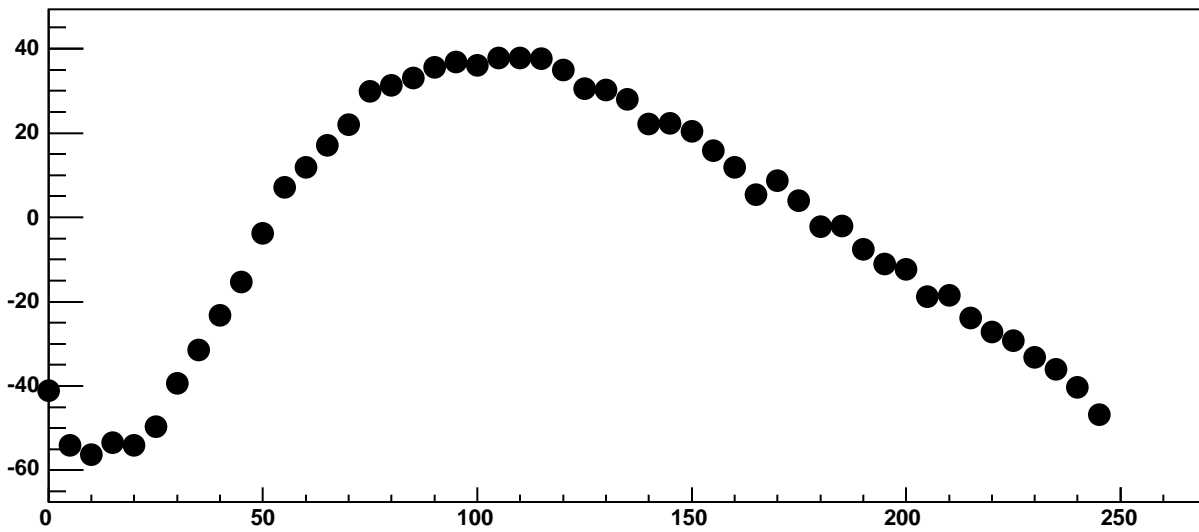
Chip 4, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold



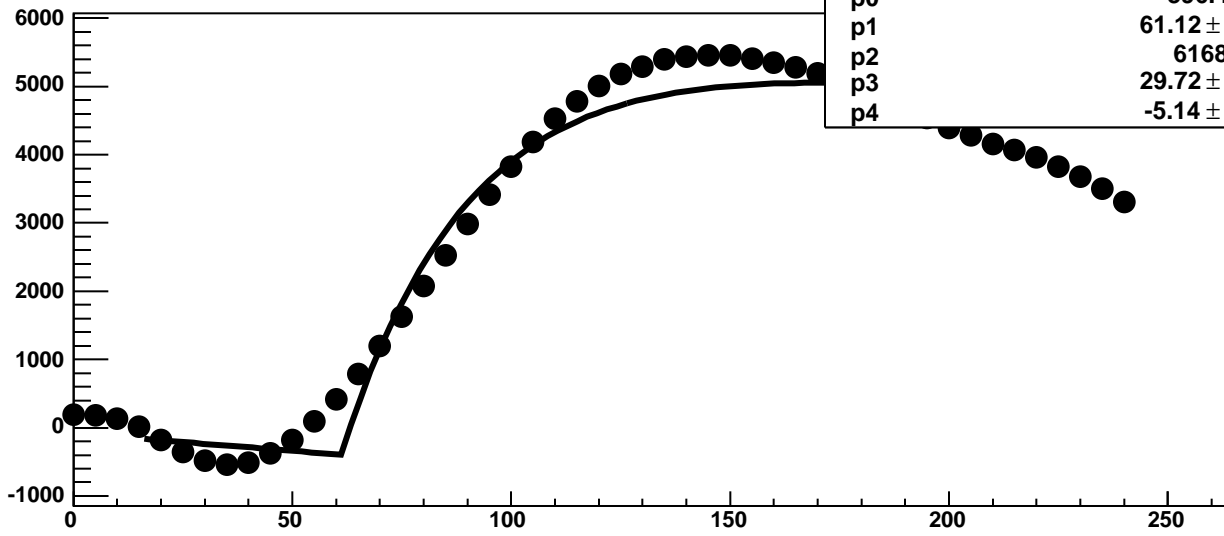
Chip 4, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

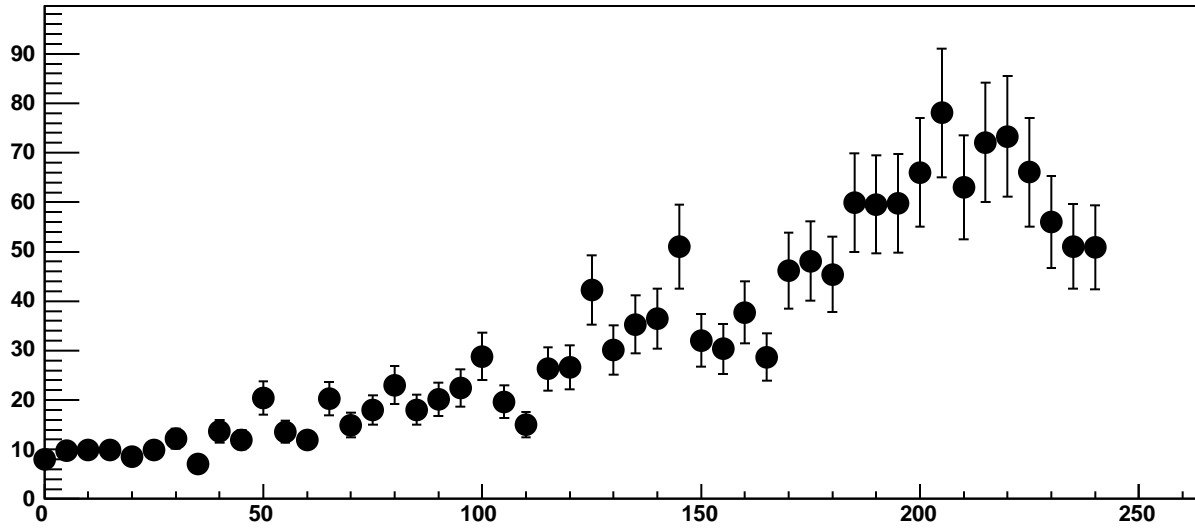


Chip 4, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold

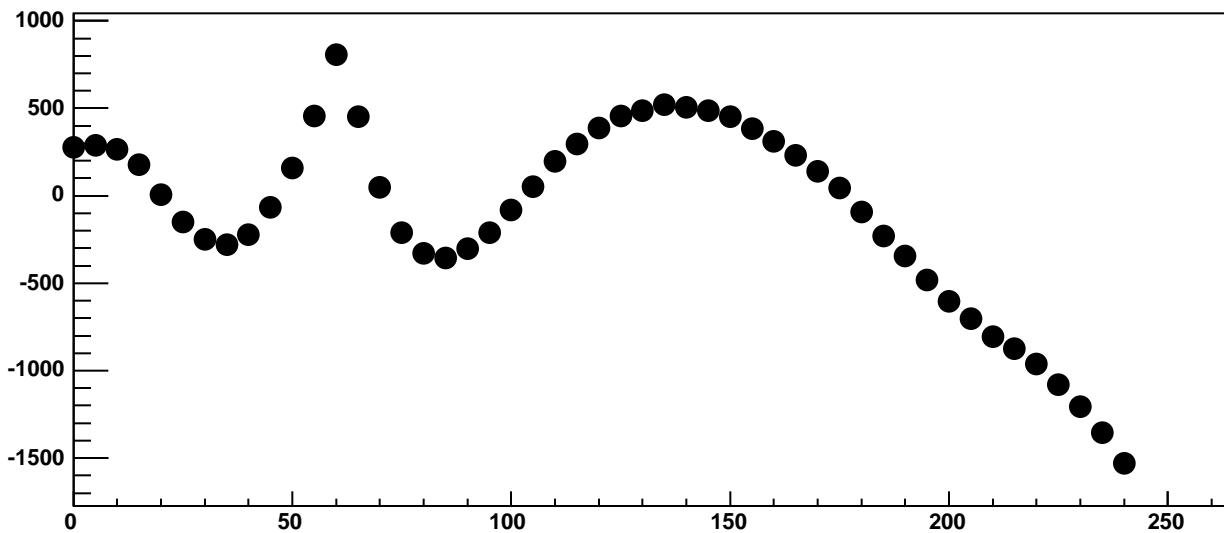


χ^2 / ndf	2.868e+05 / 41
p0	-396.4 ± 1.427
p1	61.12 ± 0.02128
p2	6168 ± 7.359
p3	29.72 ± 0.05287
p4	-5.14 ± 0.04509

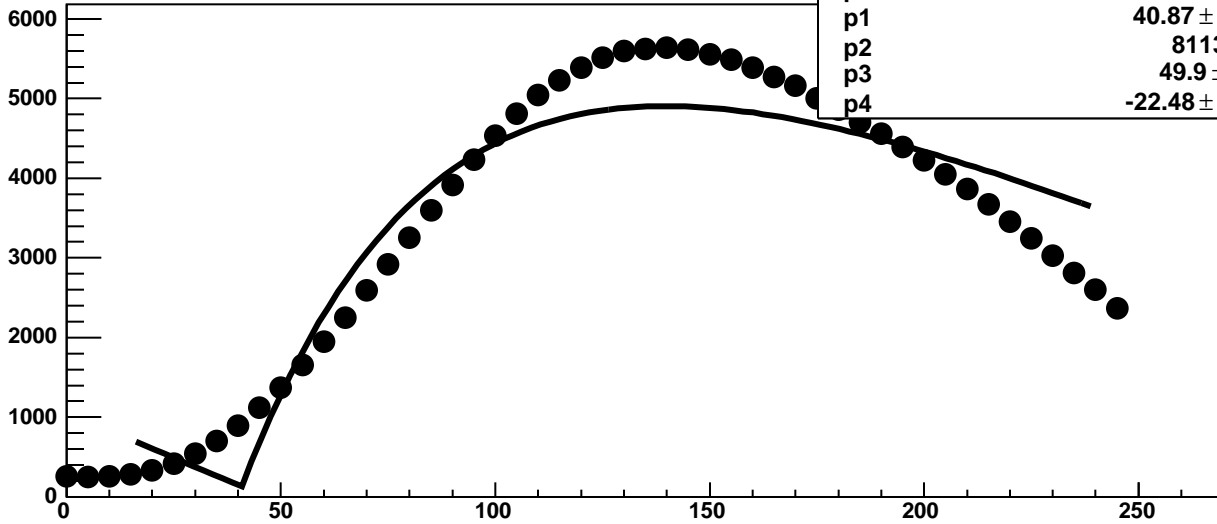
Chip 4, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

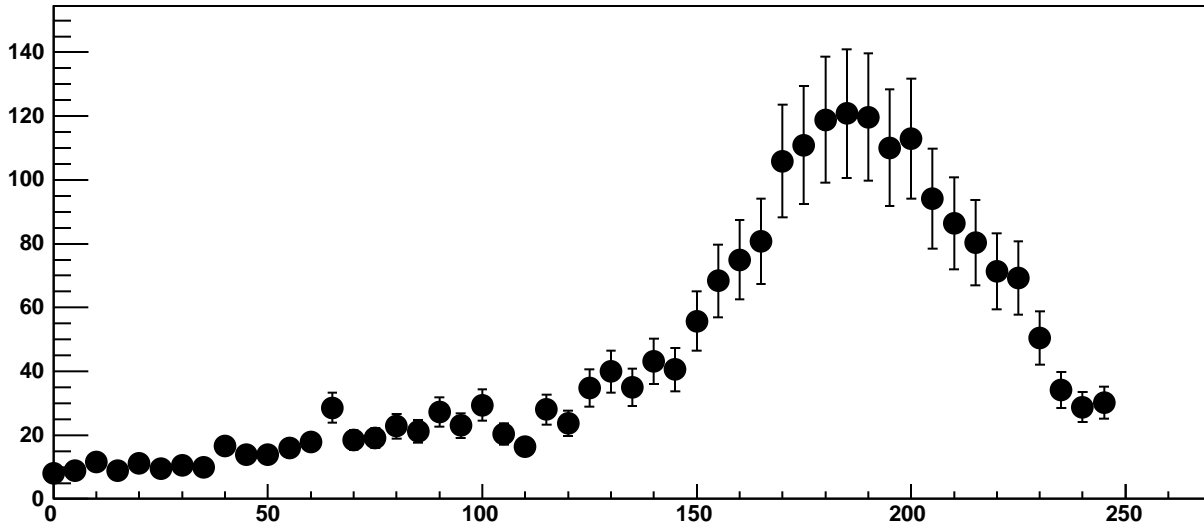


Chip 4, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

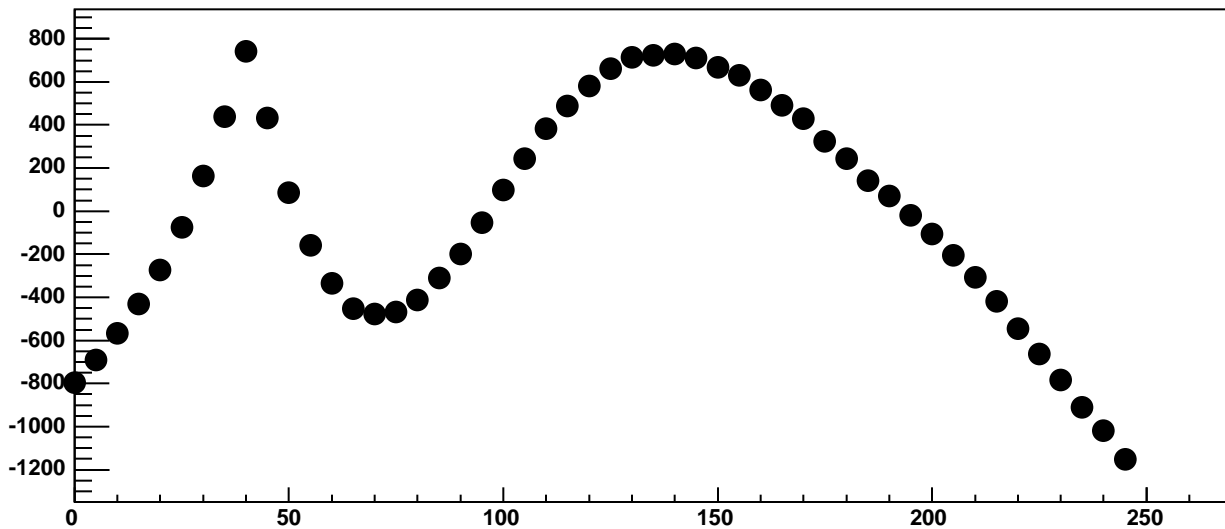


χ^2 / ndf	3.224e+05 / 41
p0	134.1 ± 1.24
p1	40.87 ± 0.01923
p2	8113 ± 13.41
p3	49.9 ± 0.0895
p4	-22.48 ± 0.06566

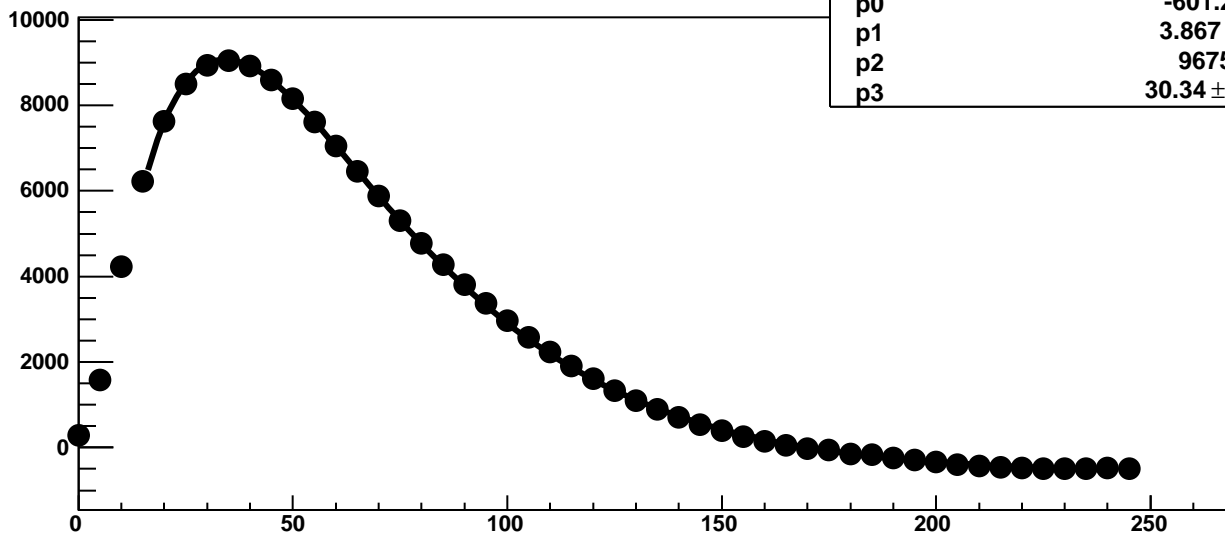
Chip 4, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

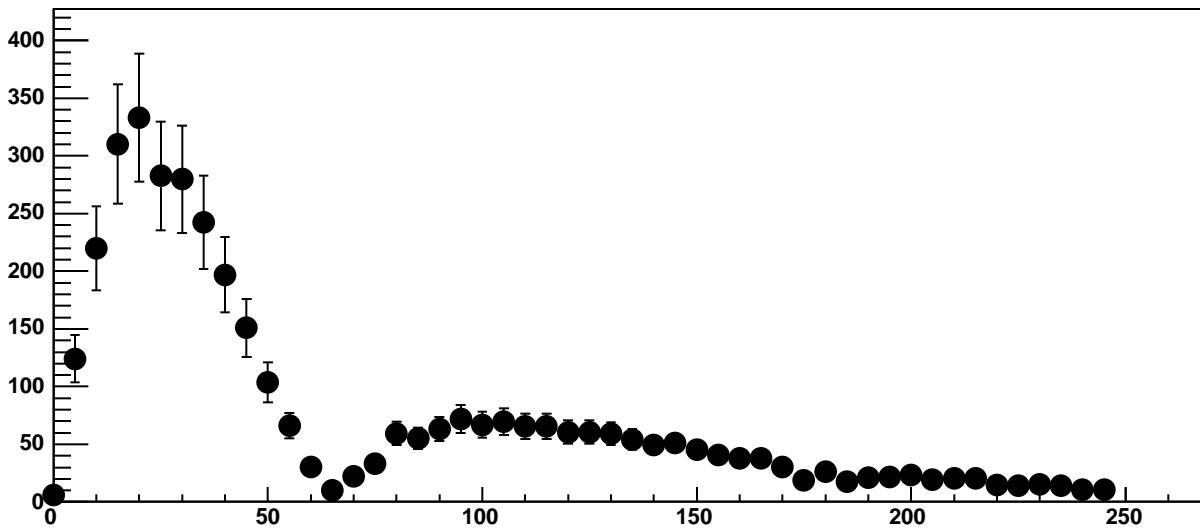


Chip 4, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold

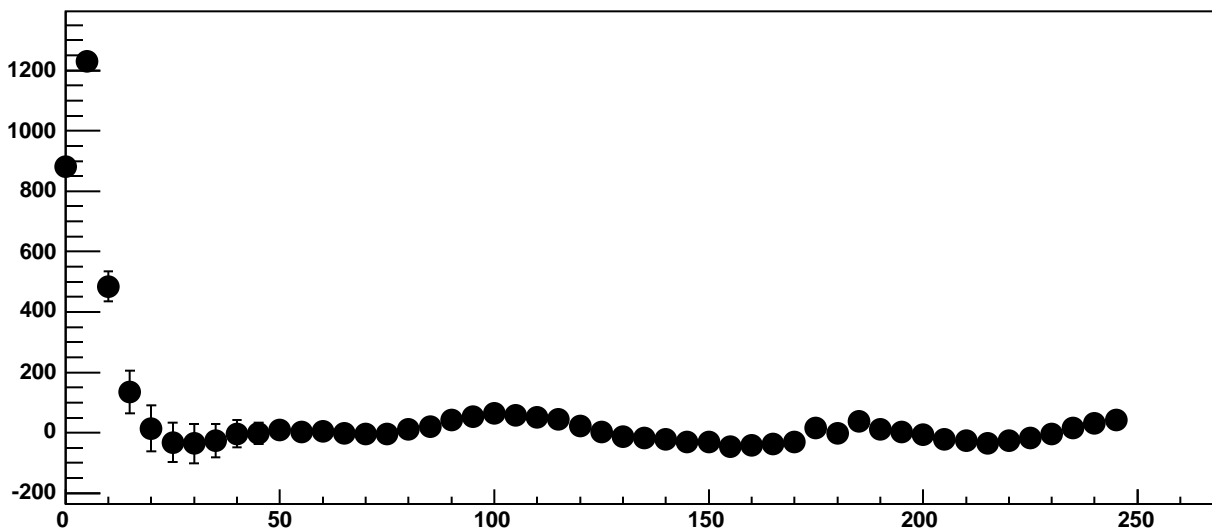


χ^2 / ndf	696.5 / 42
p0	-601.2 ± 1.586
p1	3.867 ± 0.1163
p2	9675 ± 13.12
p3	30.34 ± 0.03776

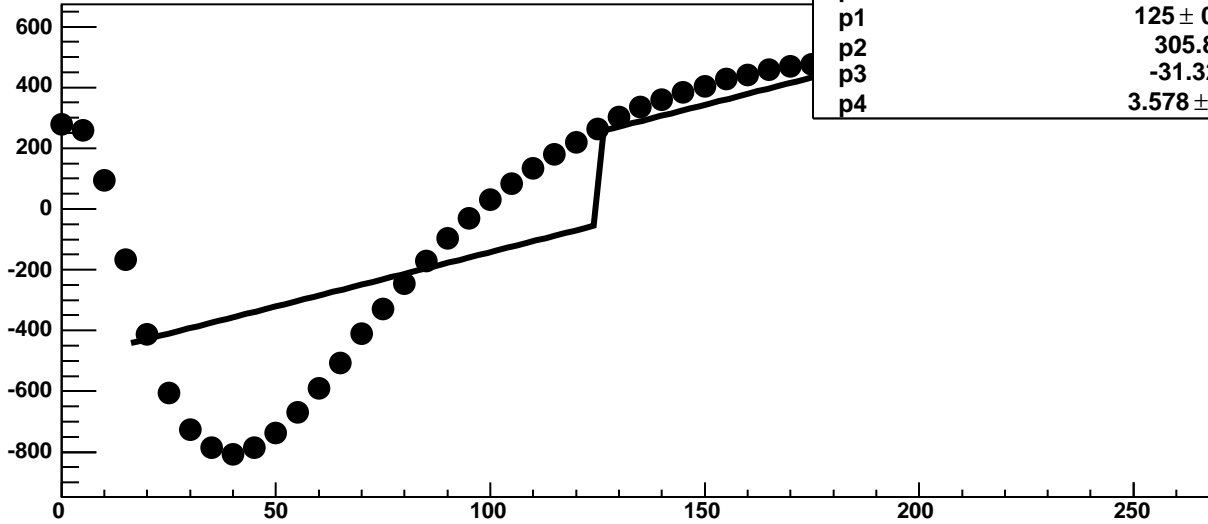
Chip 4, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold

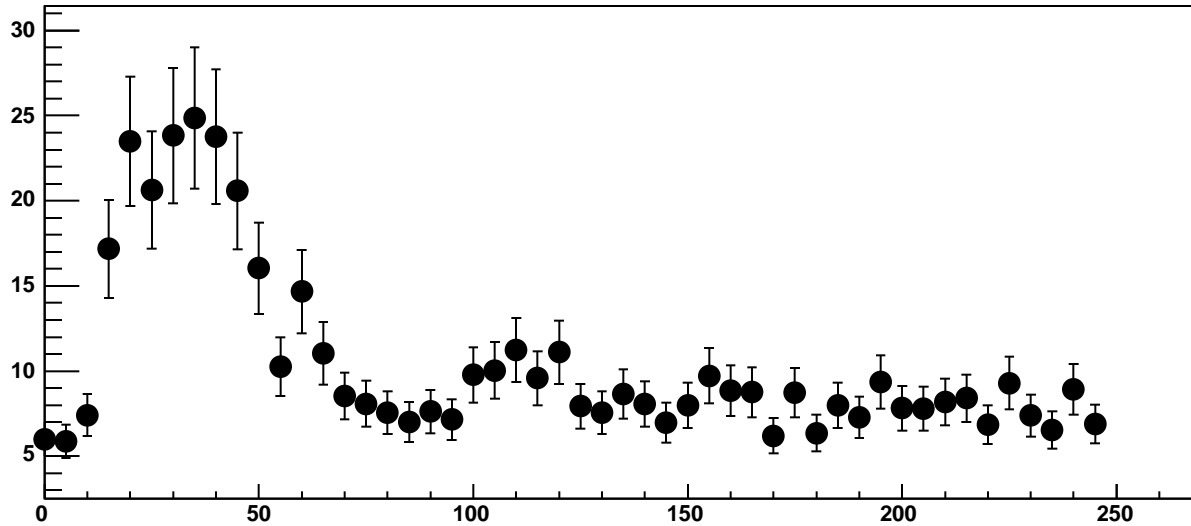


Chip 4, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

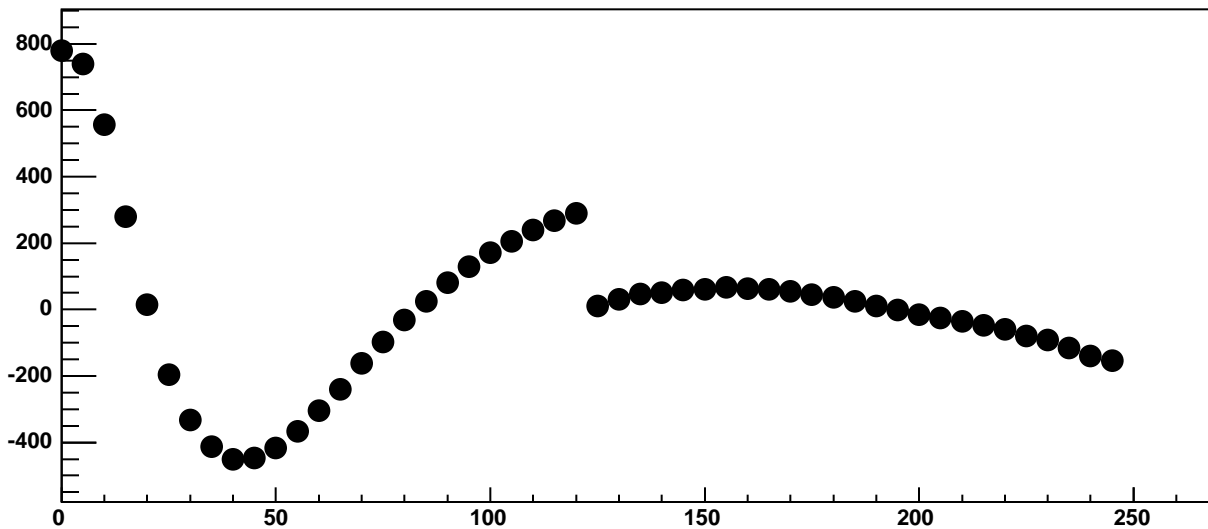


χ^2 / ndf	1.823e+05 / 41
p0	-52.35 ± 3.525
p1	125 ± 0.001256
p2	305.8 ± 3.759
p3	-31.32 ± 2.241
p4	3.578 ± 0.01695

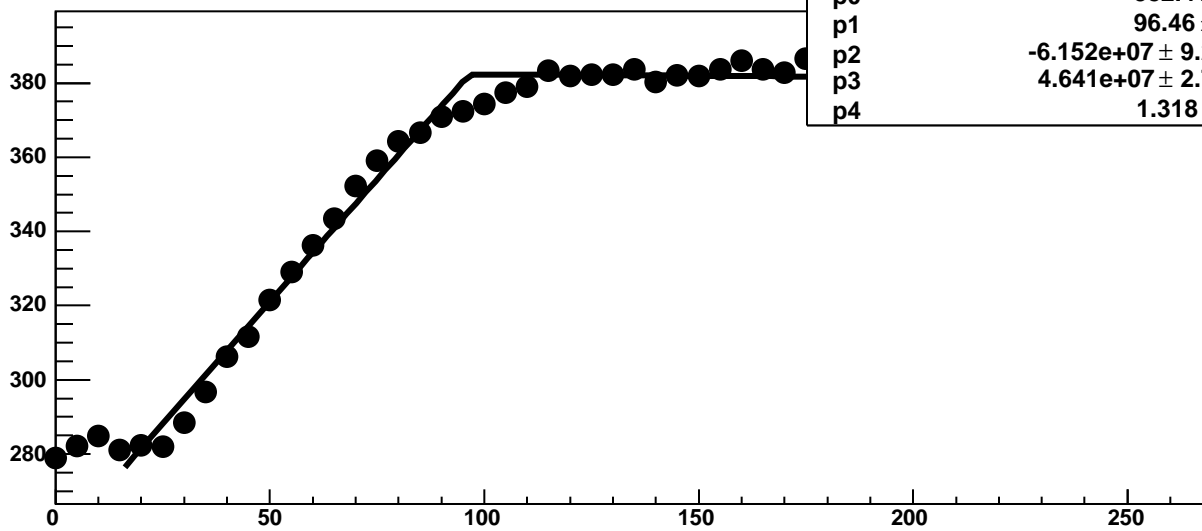
Chip 4, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

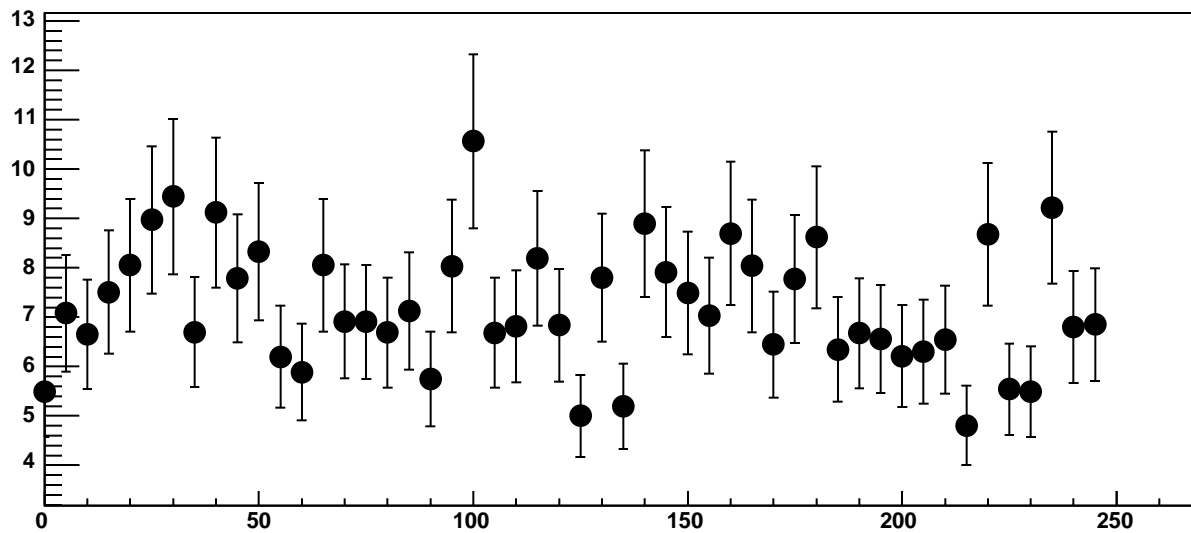


Chip 4, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

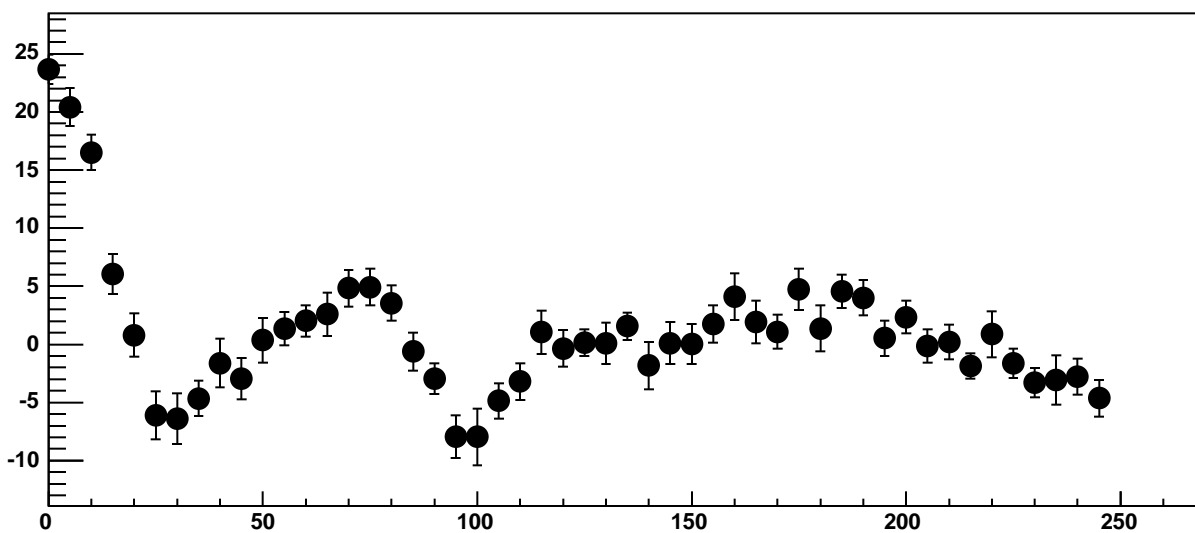


χ^2 / ndf	174.9 / 41
p0	382.4 ± 0.6005
p1	96.46 ± 0.7324
p2	$-6.152e+07 \pm 9.284e+05$
p3	$4.641e+07 \pm 2.723e+05$
p4	1.318 ± 0.0169

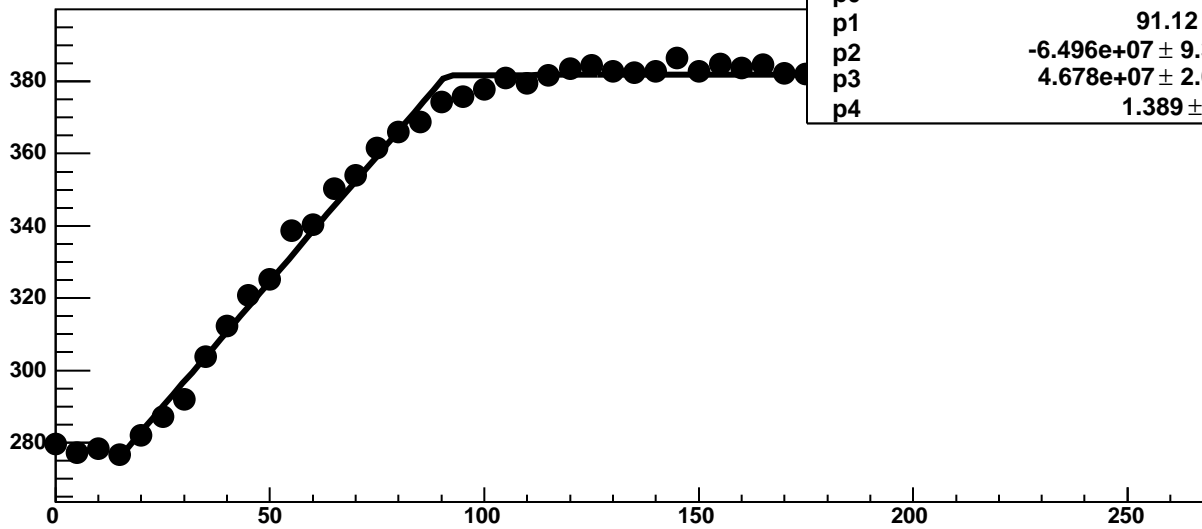
Chip 4, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

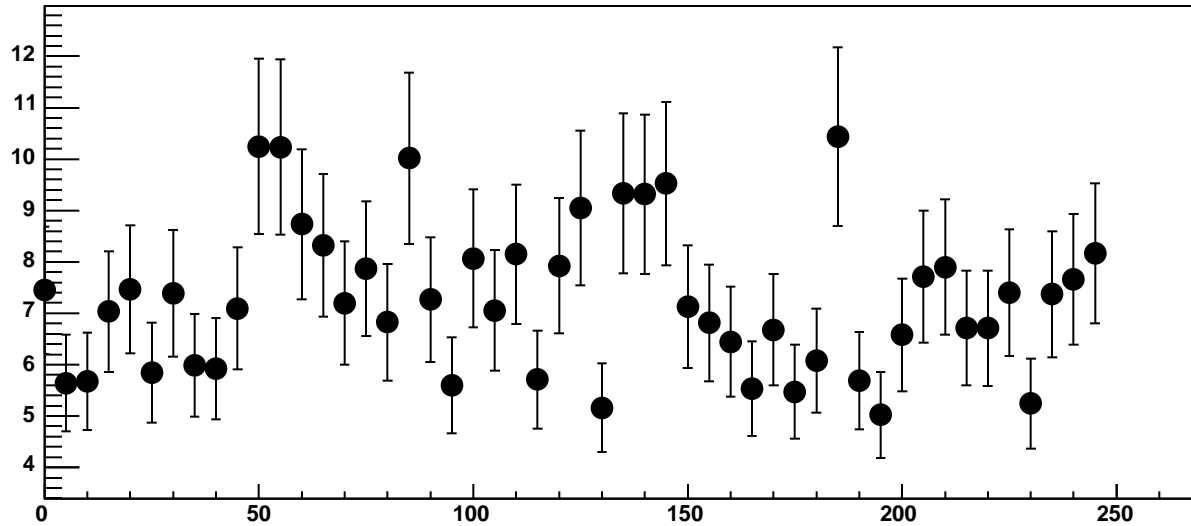


Chip 4, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold

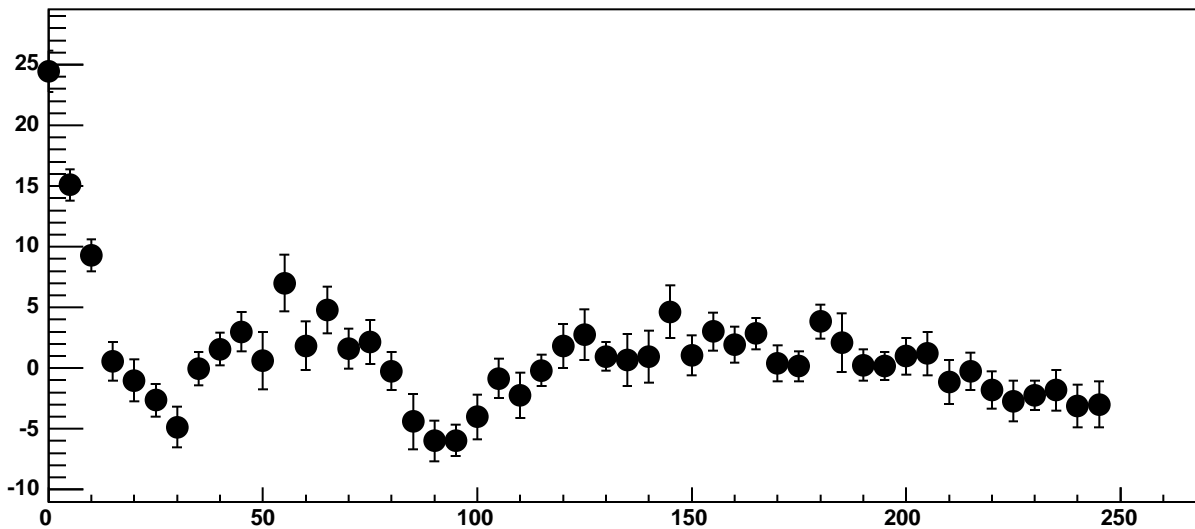


χ^2 / ndf	121.1 / 41
p0	381.8 ± 0.5858
p1	91.12 ± 0.7538
p2	$-6.496e+07 \pm 9.325e+05$
p3	$4.678e+07 \pm 2.058e+05$
p4	1.389 ± 0.01812

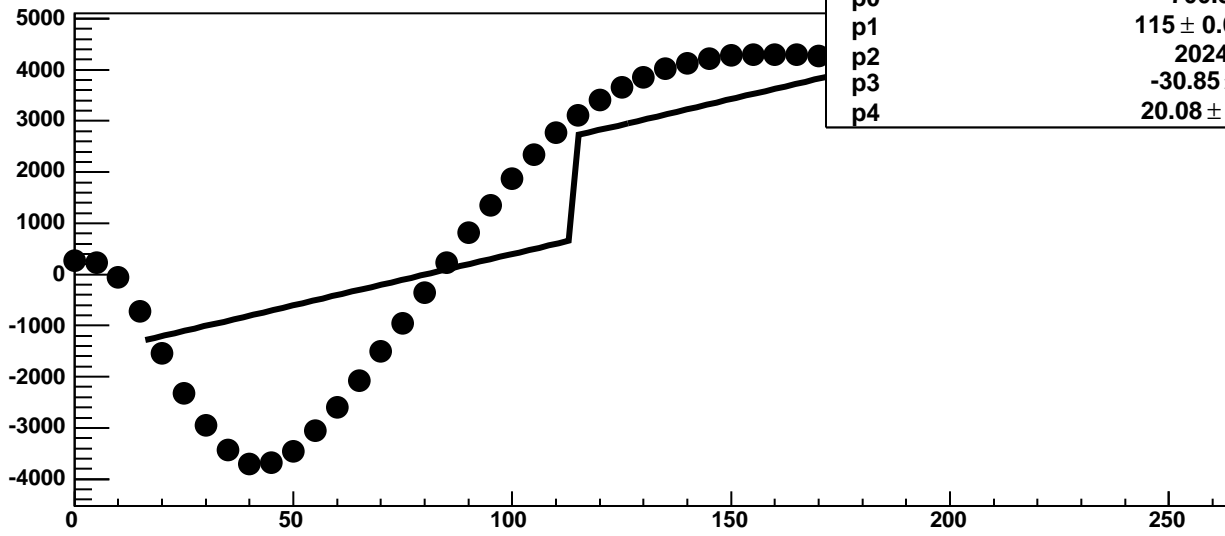
Chip 4, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold

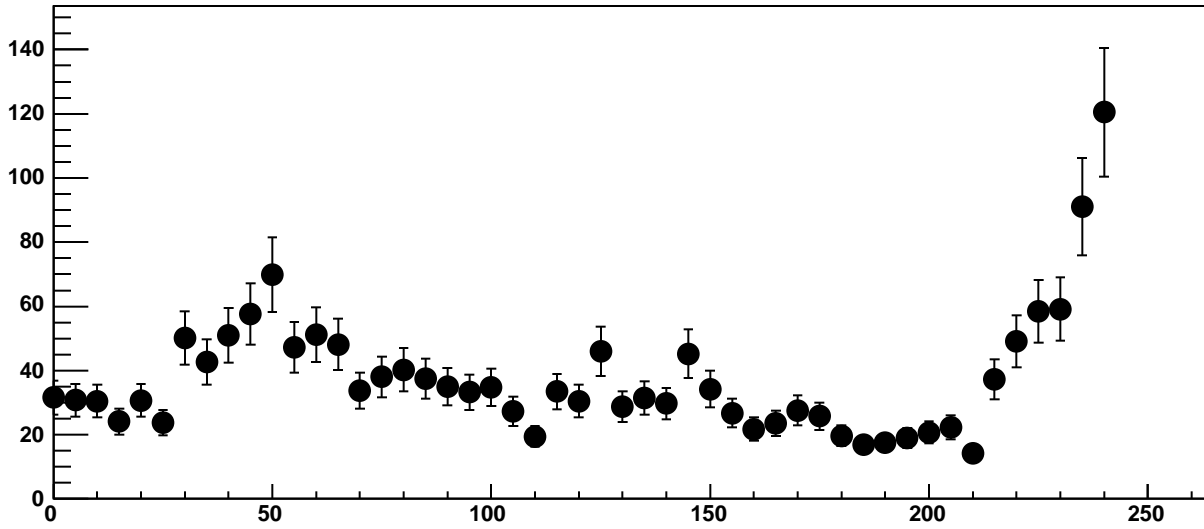


Chip 4, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

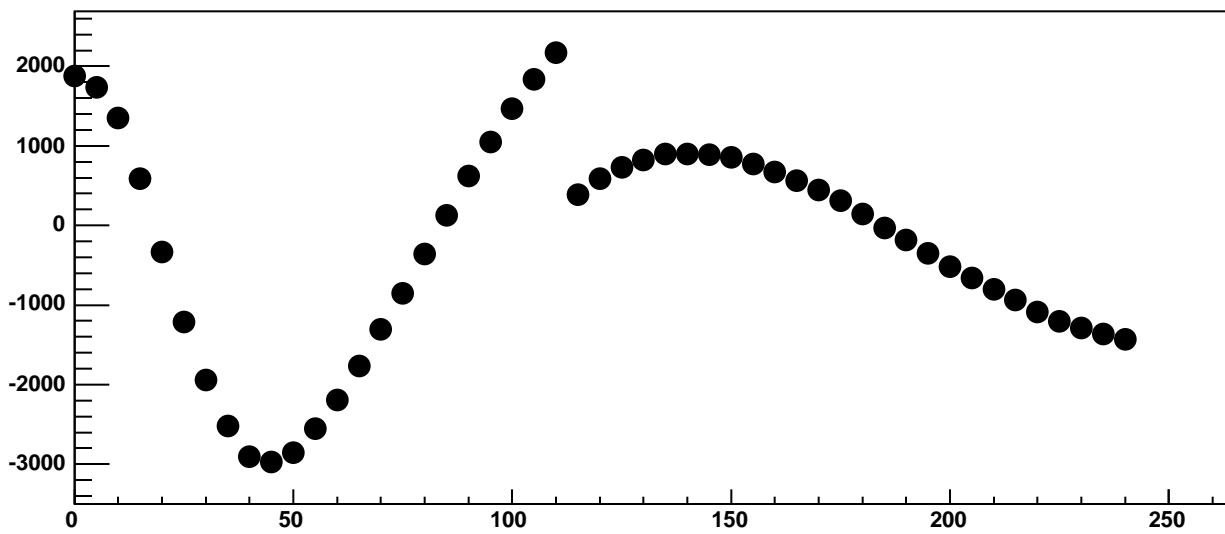


χ^2 / ndf	1.117e+06 / 41
p0	700.5 ± 10.81
p1	115 ± 0.0006154
p2	2024 ± 15.83
p3	-30.85 ± 0.7211
p4	20.08 ± 0.09188

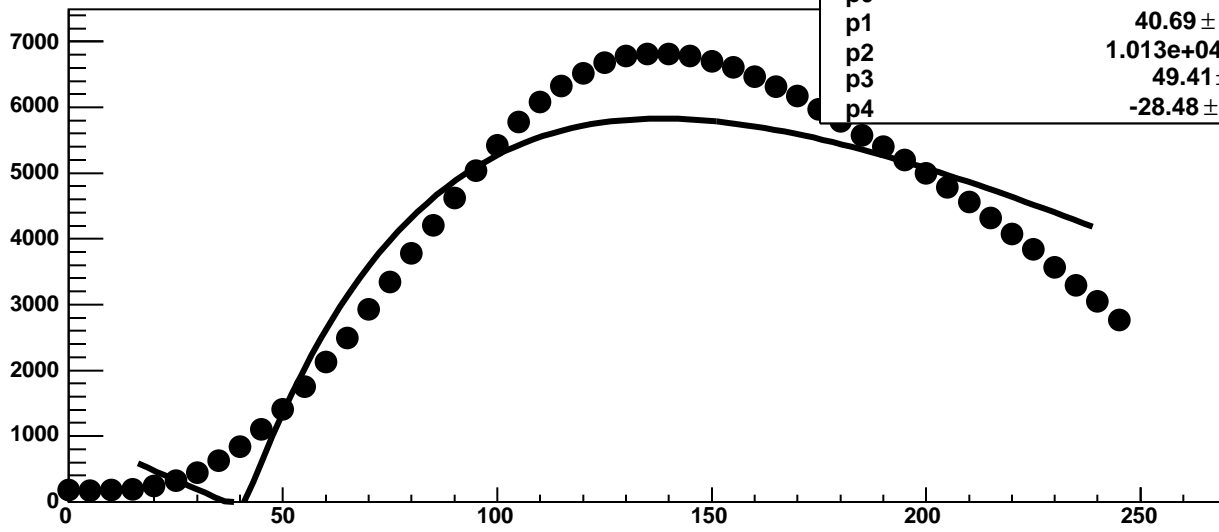
Chip 4, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold

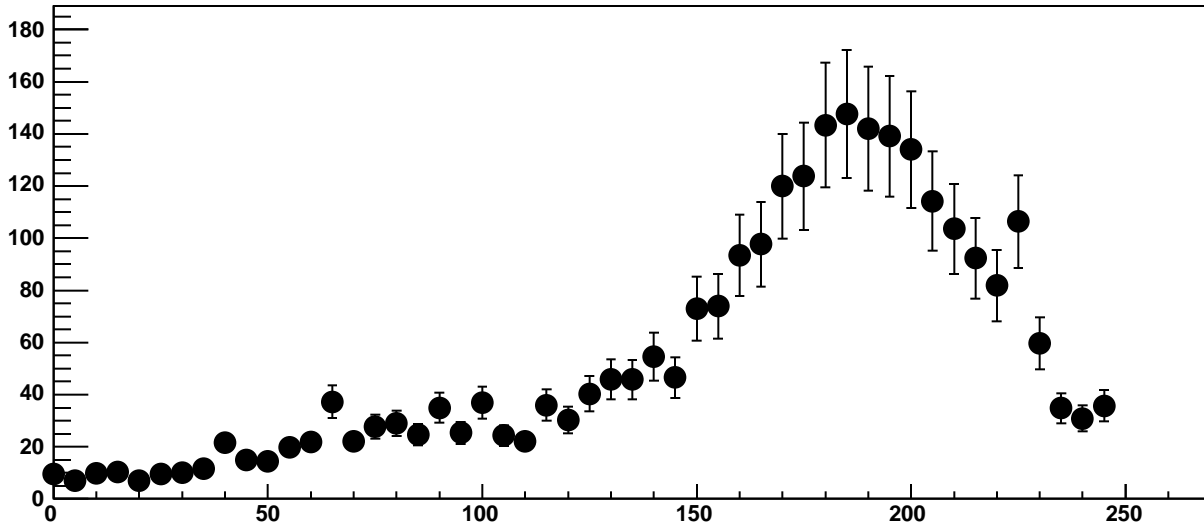


Chip 4, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold

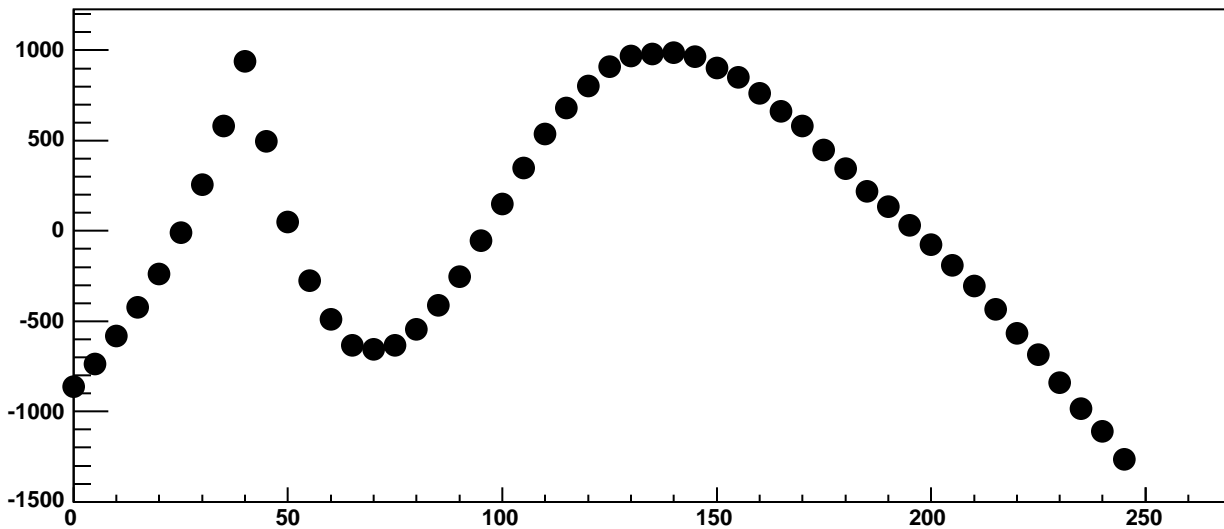


χ^2 / ndf	3.593e+05 / 41
p0	-114.3 ± 1.333
p1	40.69 ± 0.01689
p2	1.013e+04 ± 14.78
p3	49.41 ± 0.0809
p4	-28.48 ± 0.07051

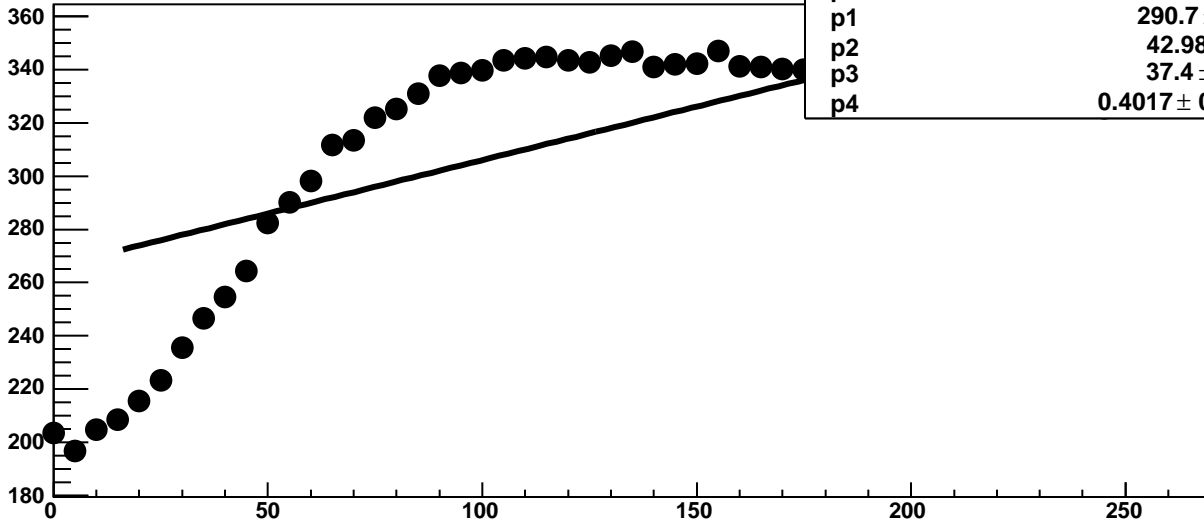
Chip 4, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



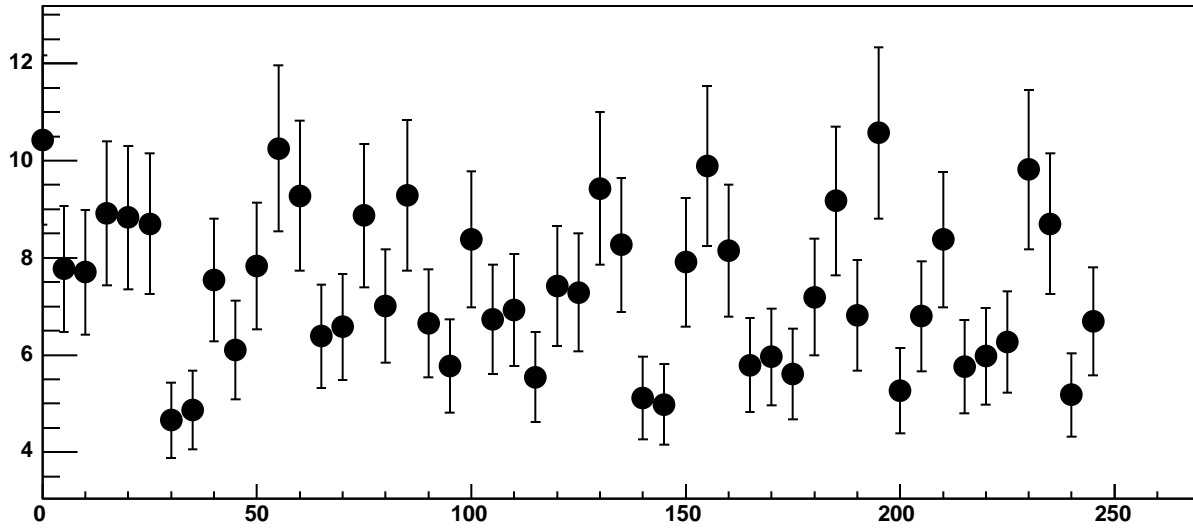
Chip 4, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold



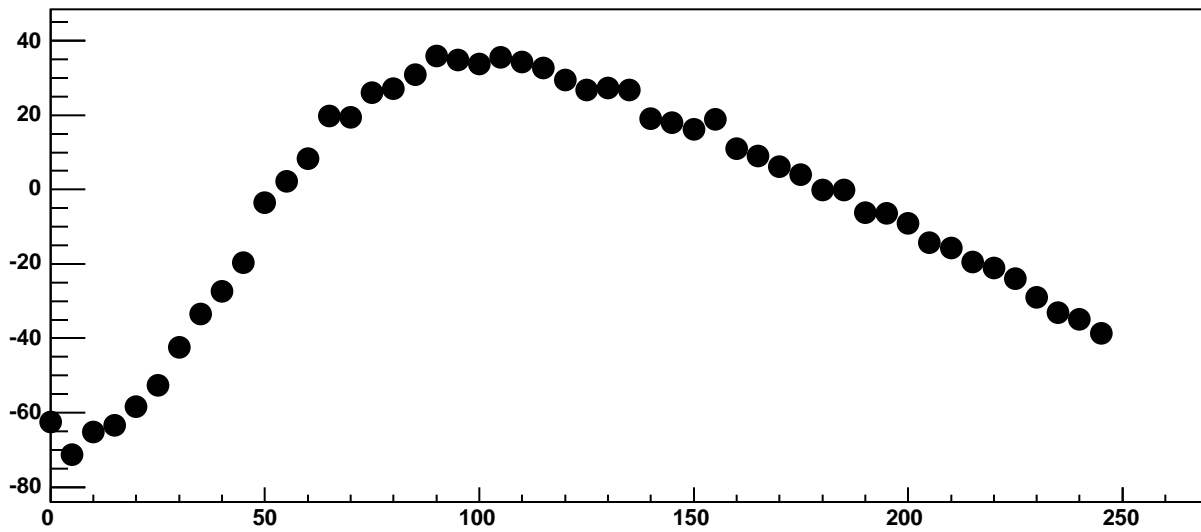
Chip 4, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold



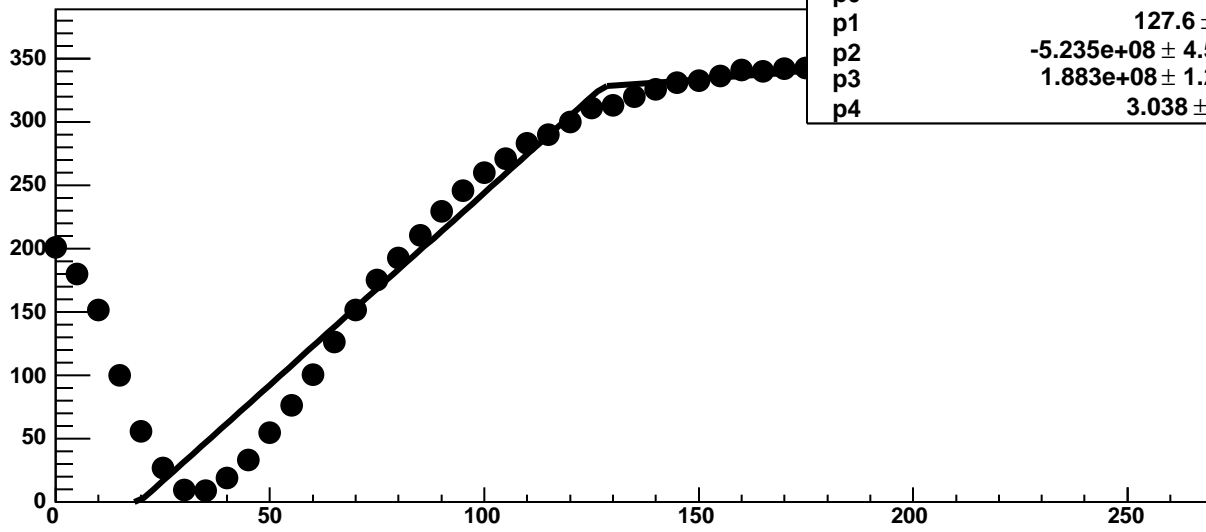
Chip 4, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold

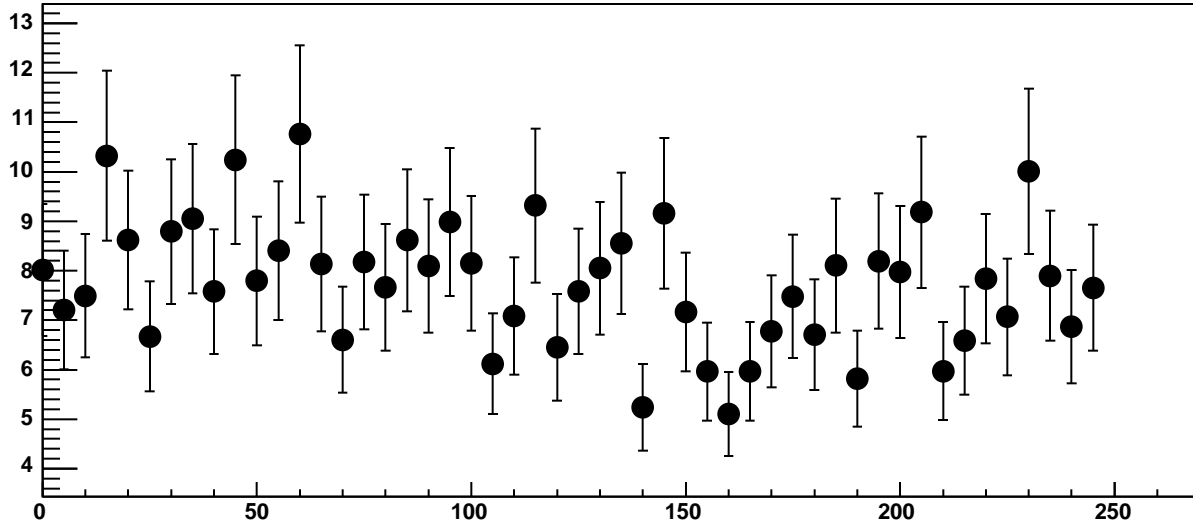


Chip 4, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

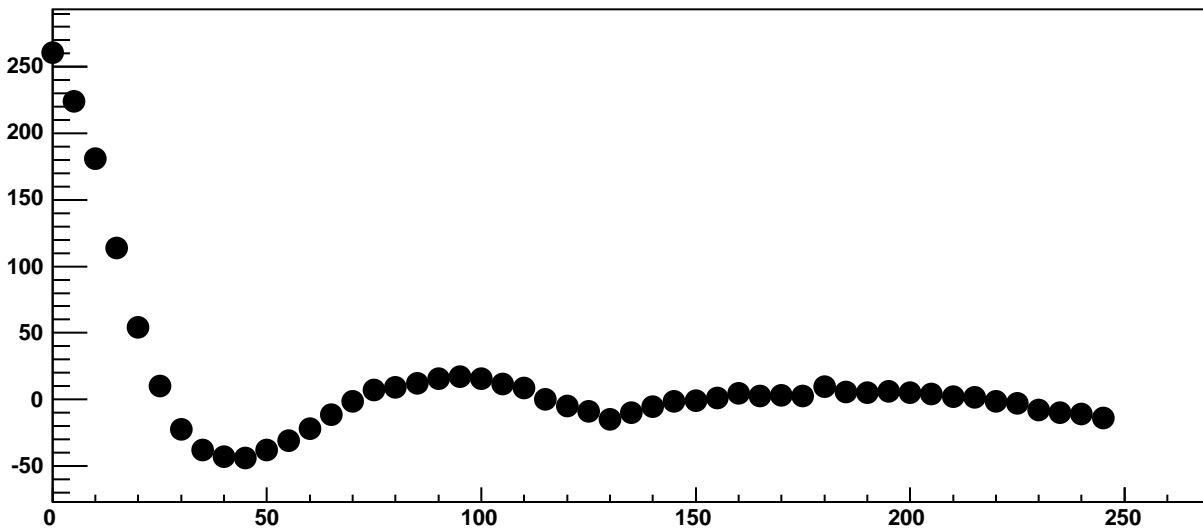


χ^2 / ndf	6152 / 41
p0	327.9 ± 0.7134
p1	127.6 ± 0.3455
p2	$-5.235\text{e}+08 \pm 4.531\text{e}+06$
p3	$1.883\text{e}+08 \pm 1.243\text{e}+06$
p4	3.038 ± 0.01141

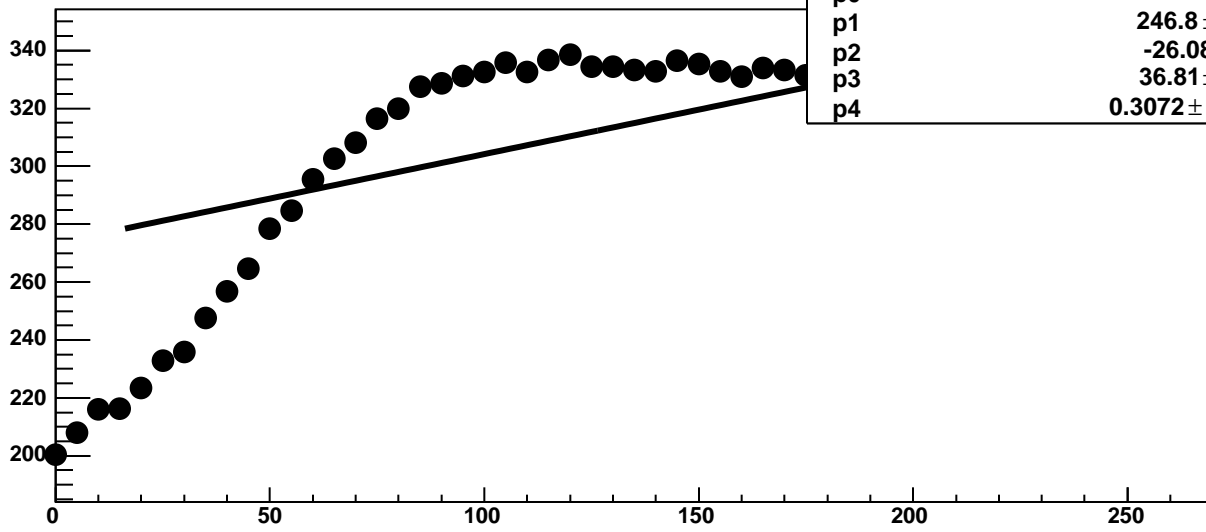
Chip 4, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

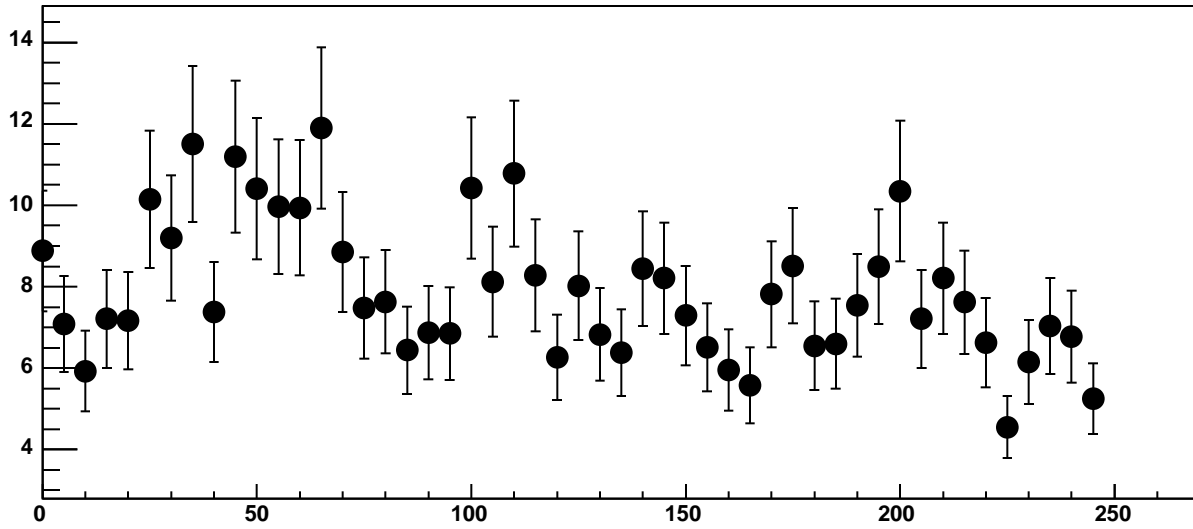


Chip 4, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

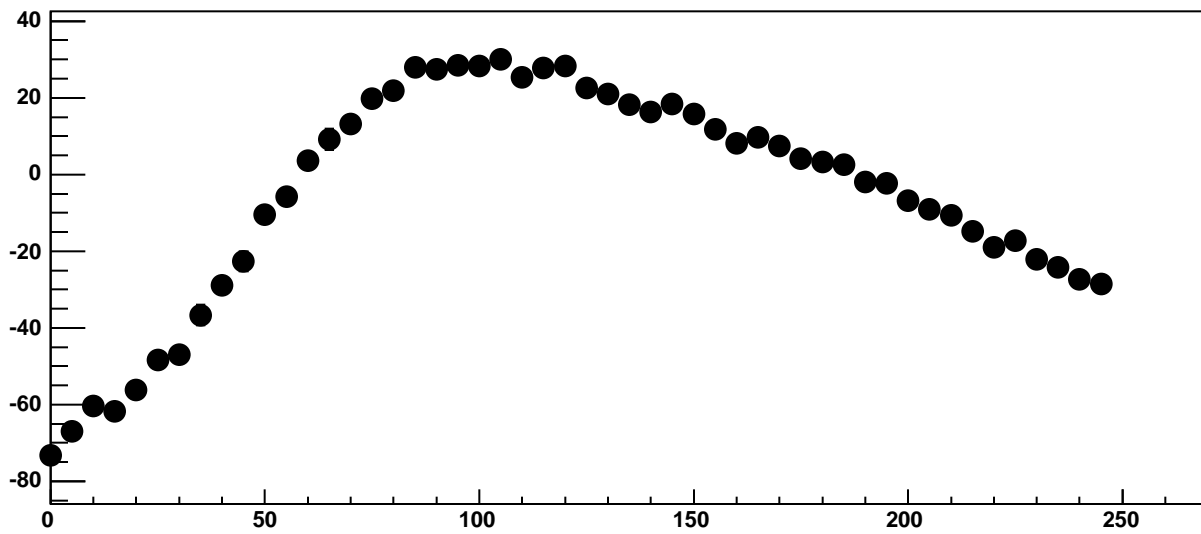


χ^2 / ndf	8788 / 41
p0	349.3 ± 0.4275
p1	246.8 ± 0.3782
p2	-26.08 ± 3.781
p3	36.81 ± 0.2797
p4	0.3072 ± 0.00387

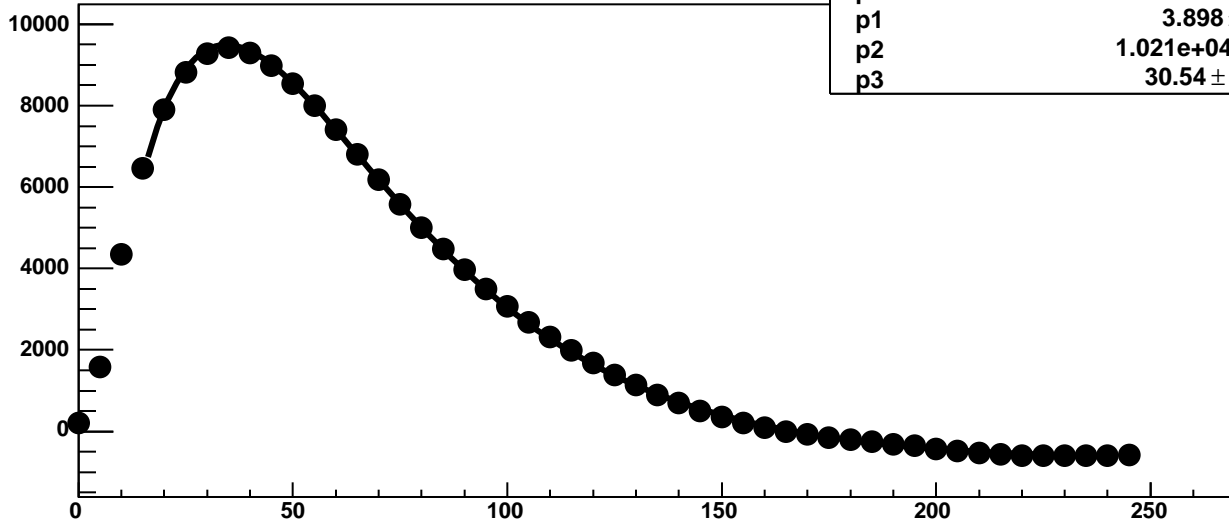
Chip 4, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

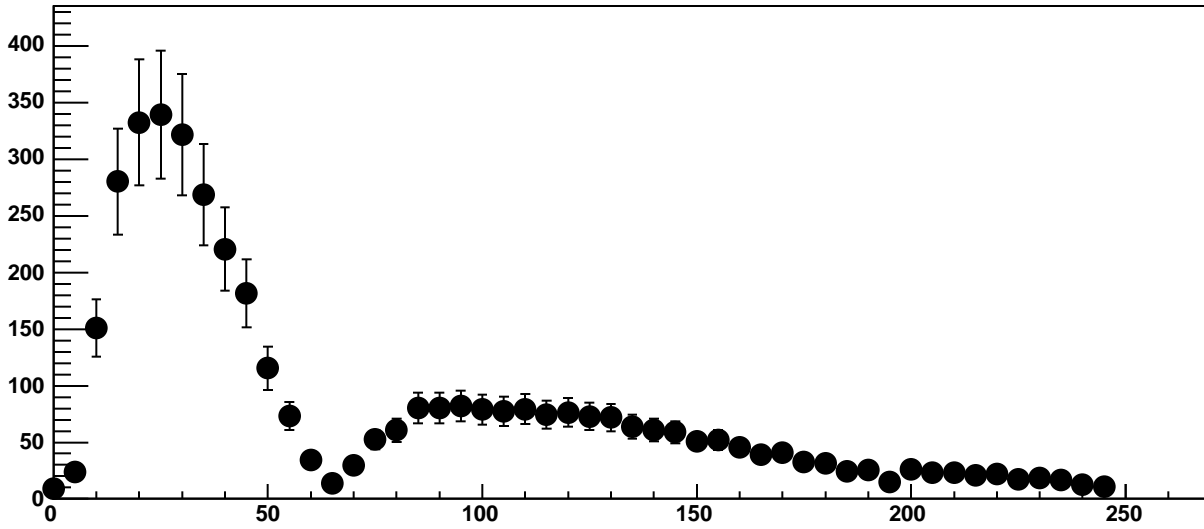


Chip 4, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold

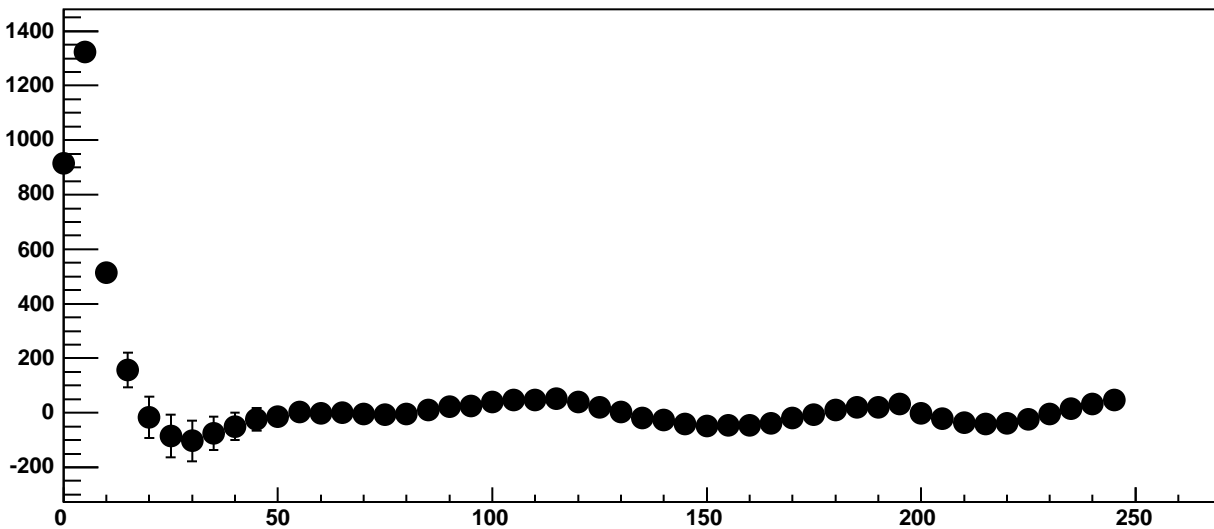


χ^2 / ndf	632.5 / 42
p0	-714.9 ± 1.9
p1	3.898 ± 0.1115
p2	1.021e+04 ± 13.32
p3	30.54 ± 0.04133

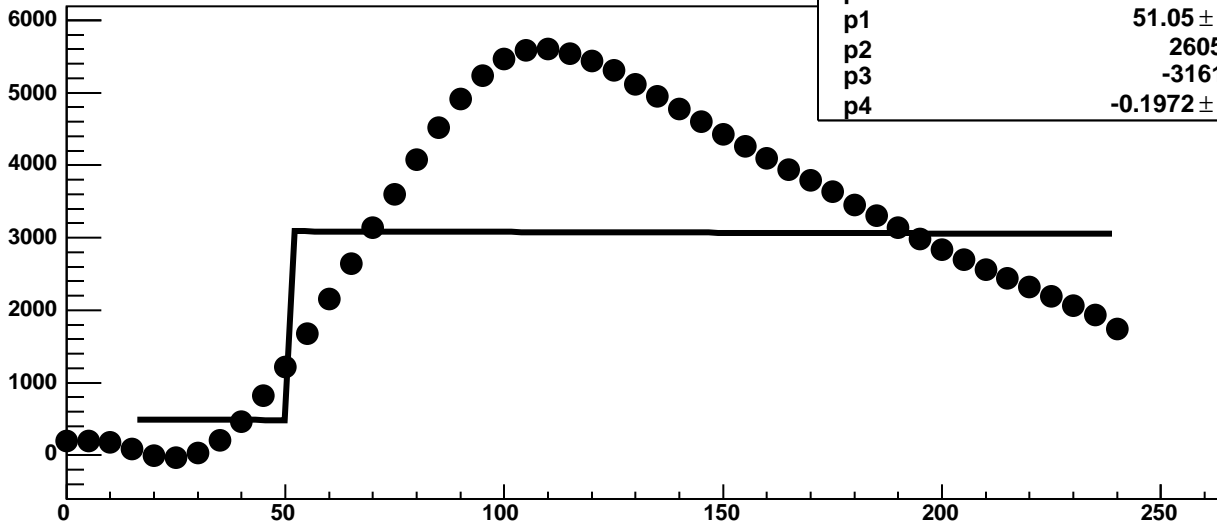
Chip 4, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold

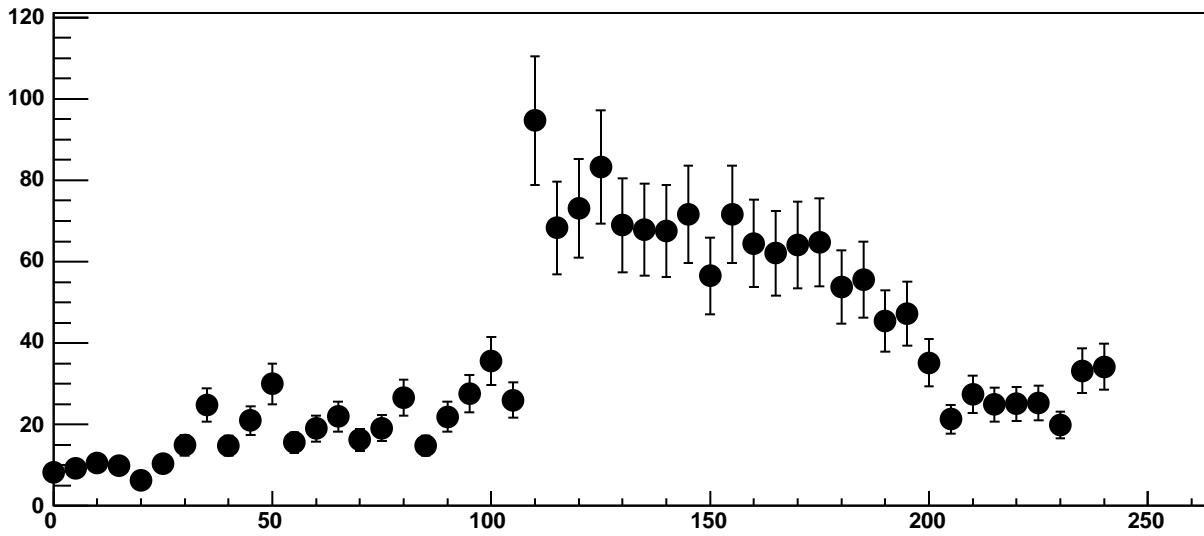


Chip 4, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

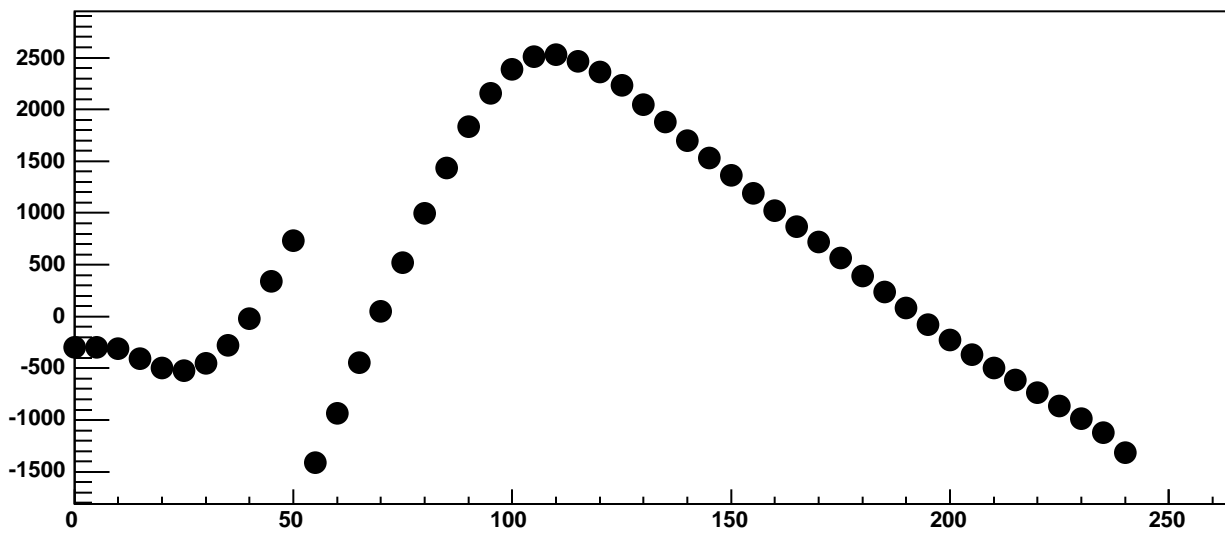


χ^2 / ndf	1.489e+06 / 41
p0	484.8 ± 0.9518
p1	51.05 ± 0.02662
p2	2605 ± 2.141
p3	-3161 ± 4.538
p4	-0.1972 ± 0.01425

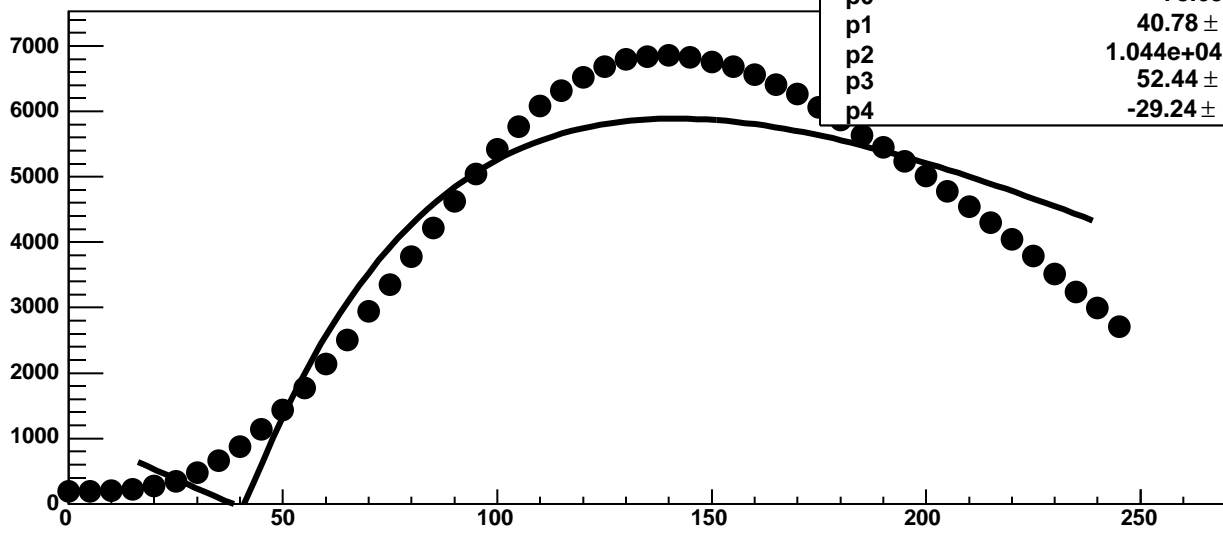
Chip 4, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold

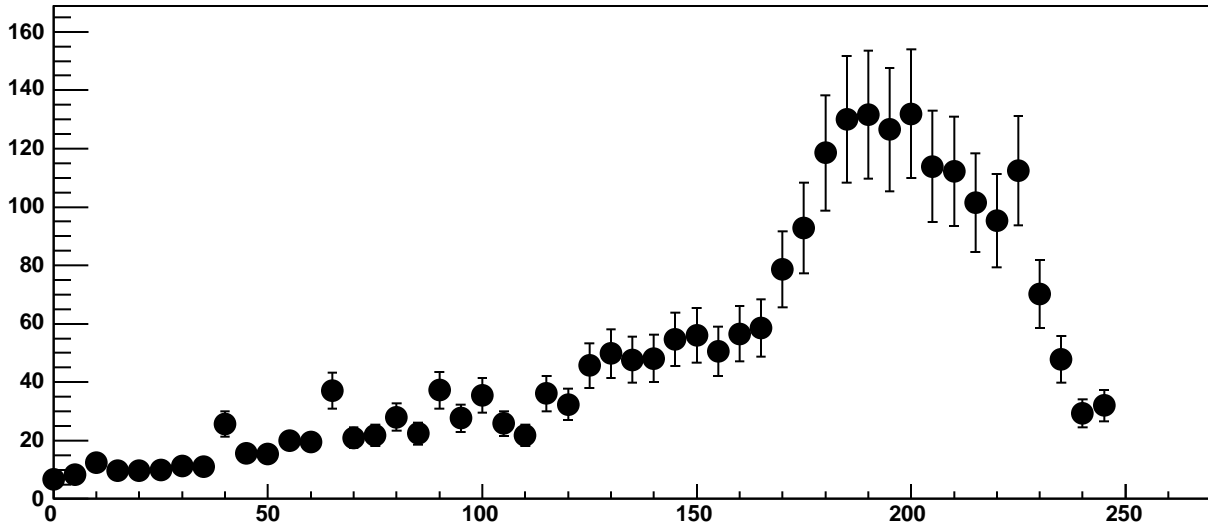


Chip 4, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

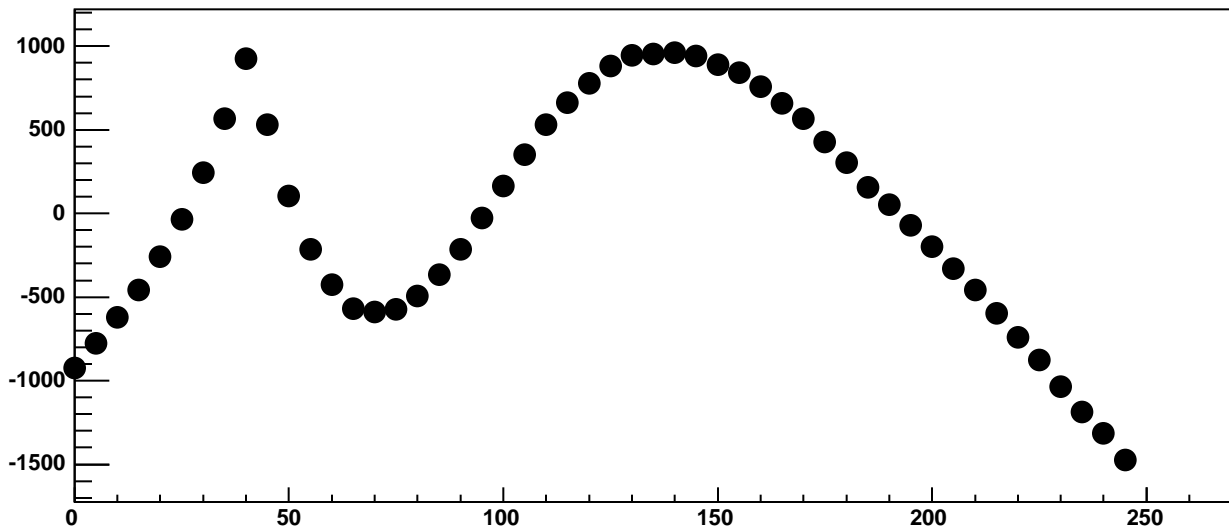


χ^2 / ndf	3.617e+05 / 41
p0	-78.65 ± 1.446
p1	40.78 ± 0.01791
p2	1.044e+04 ± 16.58
p3	52.44 ± 0.08701
p4	-29.24 ± 0.07912

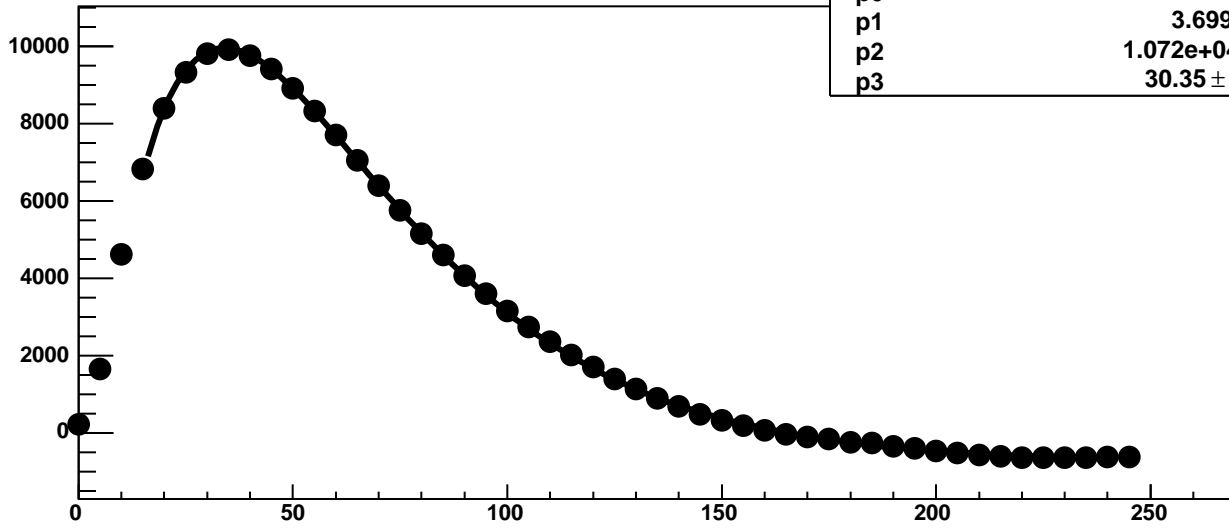
Chip 4, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

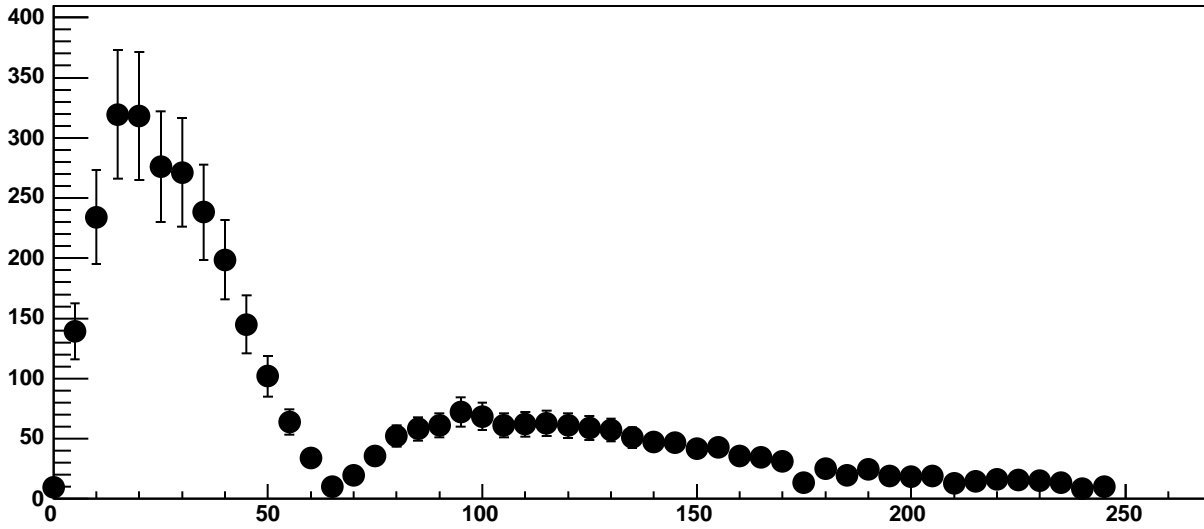


Chip 4, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold

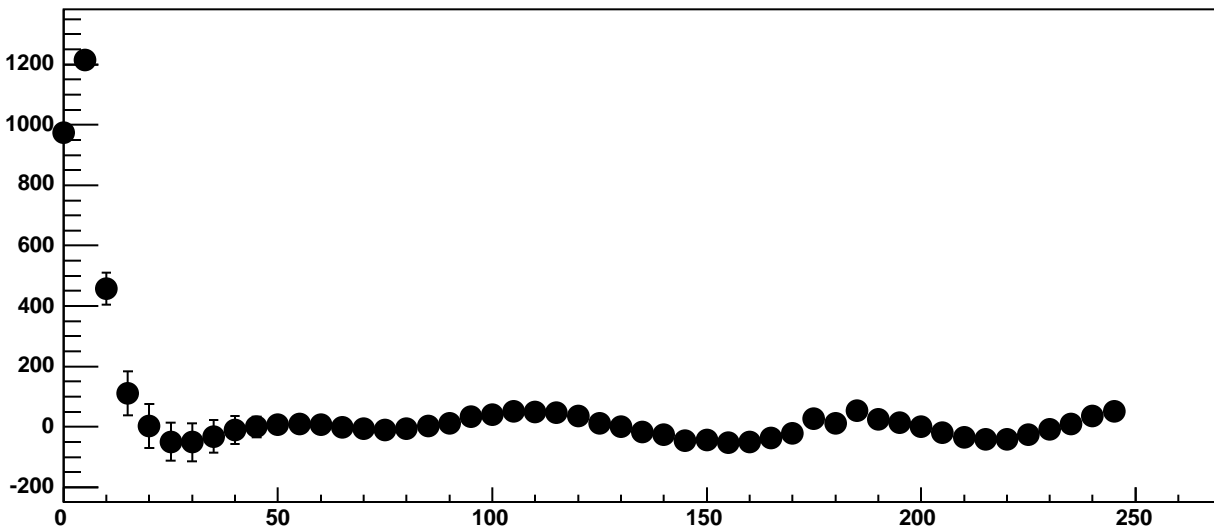


χ^2 / ndf	1253 / 42
p0	-762.1 ± 1.472
p1	3.699 ± 0.105
p2	1.072e+04 ± 13.2
p3	30.35 ± 0.03297

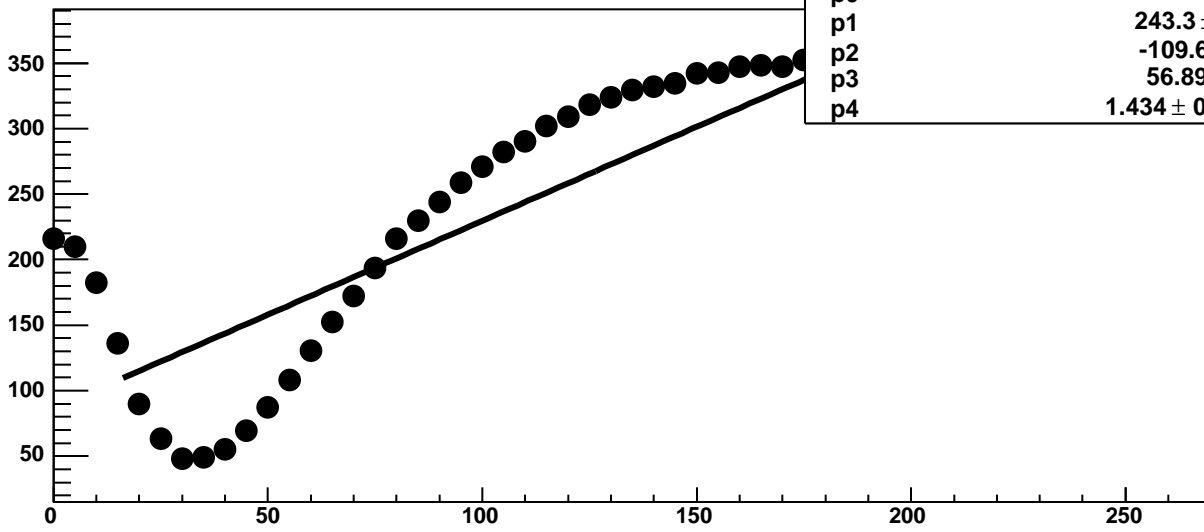
Chip 4, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold

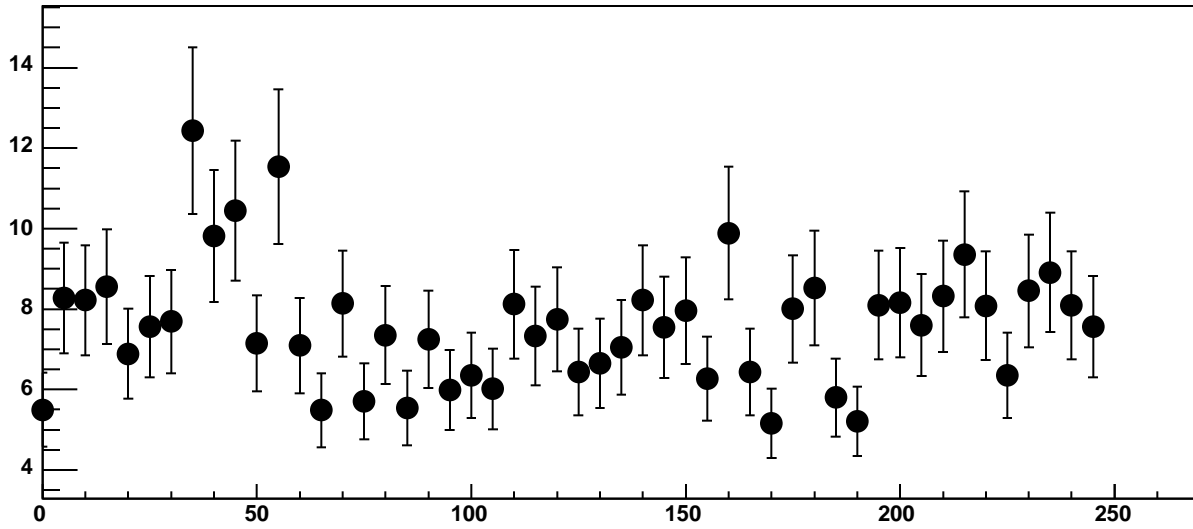


Chip 4, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

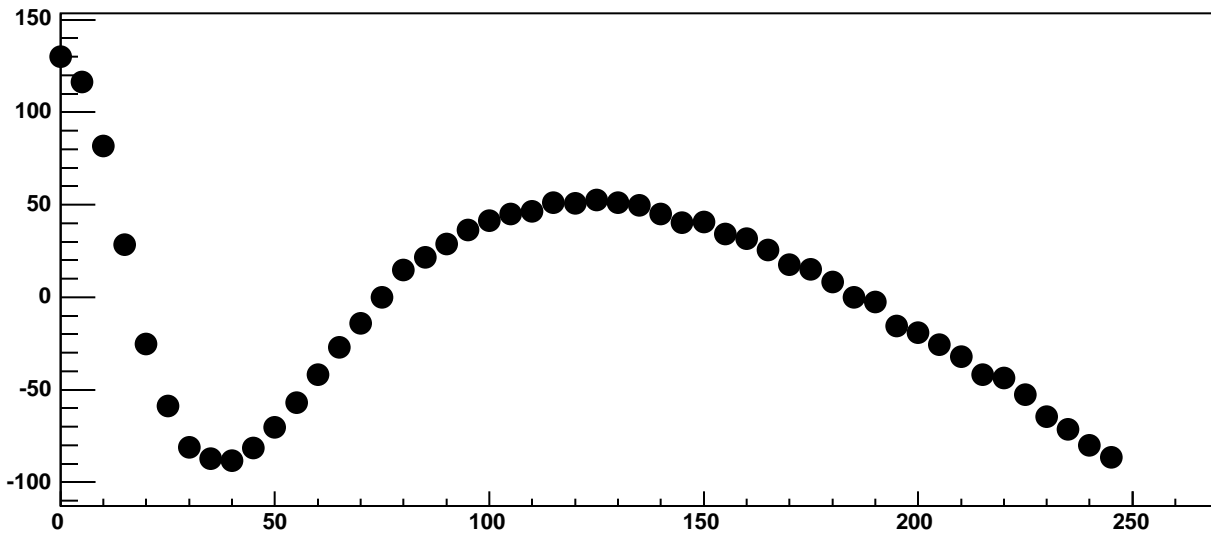


χ^2 / ndf	2.993e+04 / 41
p0	435.3 ± 0.2434
p1	243.3 ± 0.1698
p2	-109.6 ± 84.85
p3	56.89 ± 1.414
p4	1.434 ± 0.001859

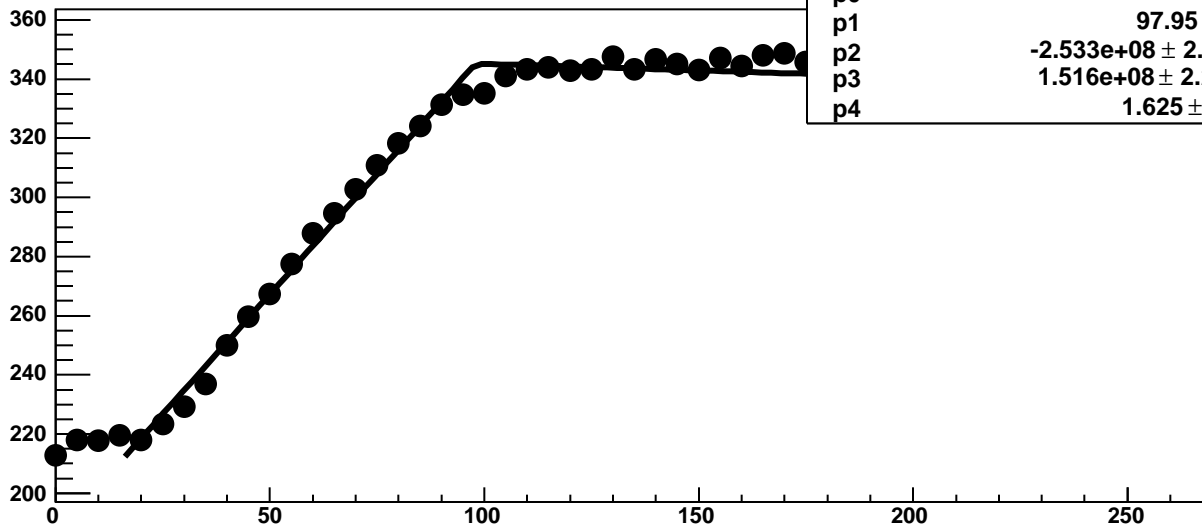
Chip 4, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



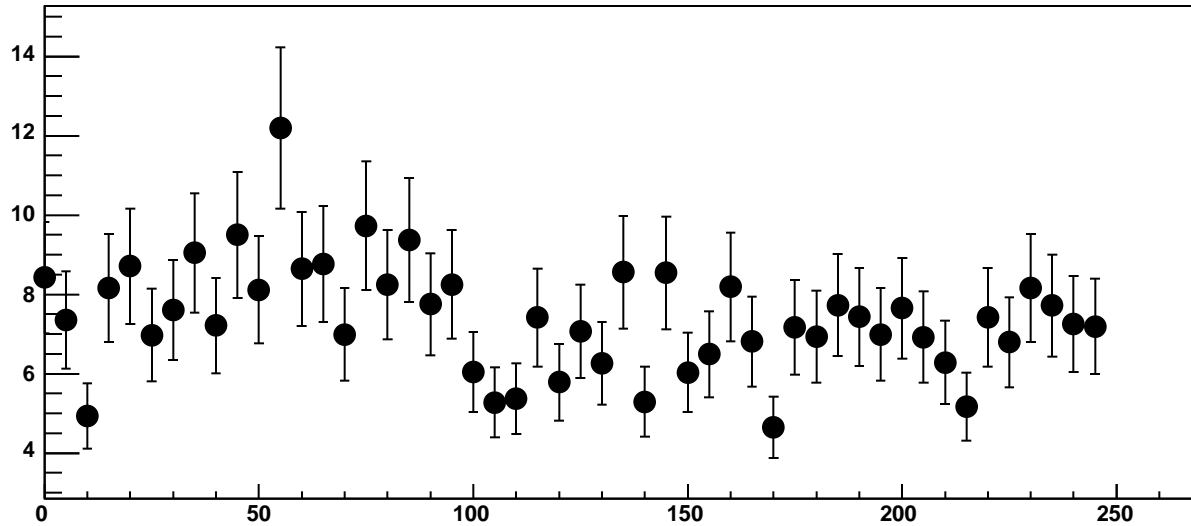
Chip 4, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold



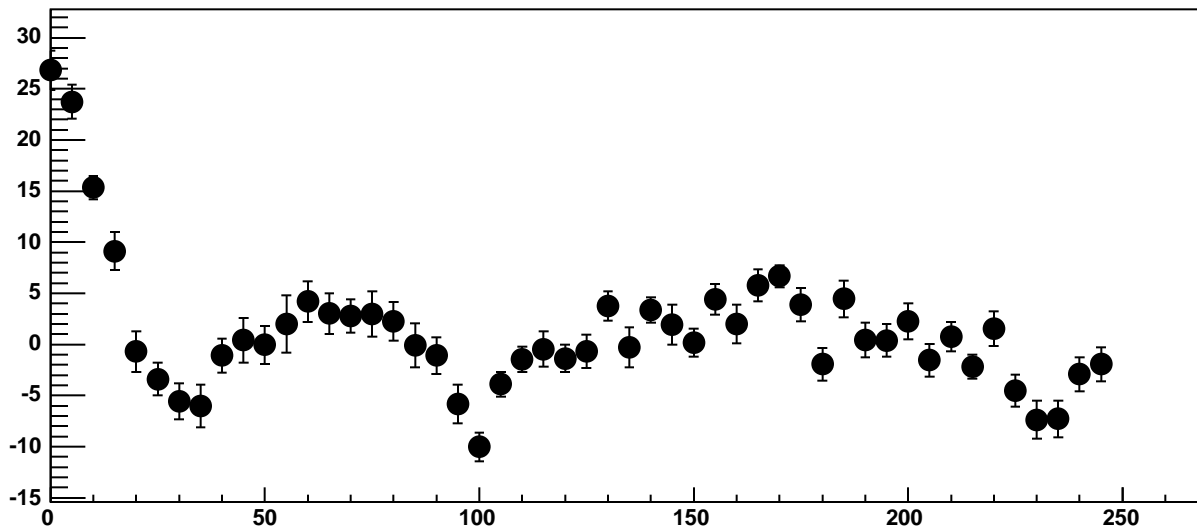
Chip 4, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold



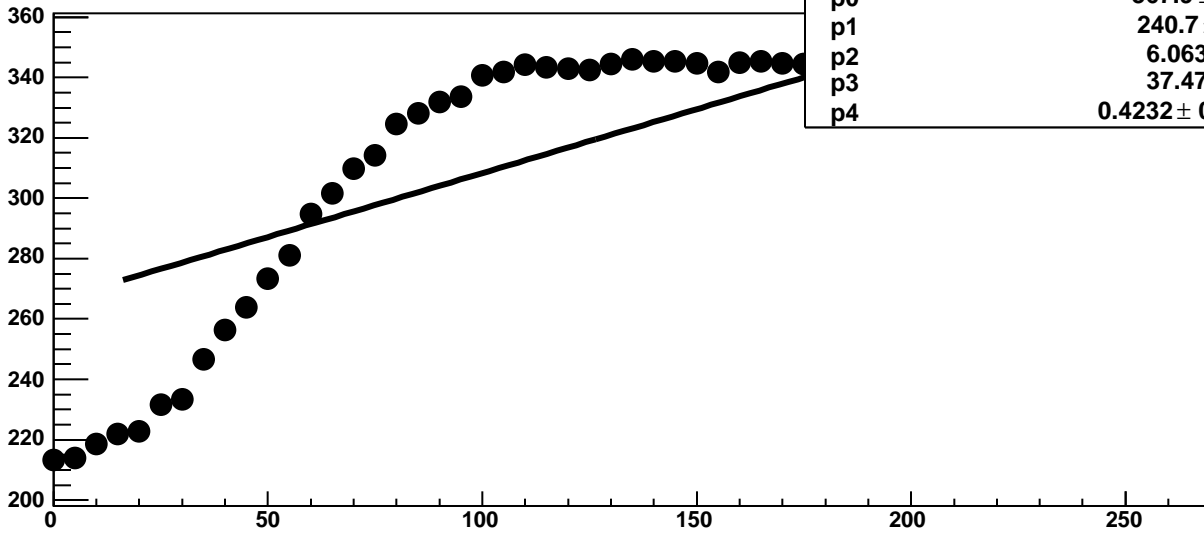
Chip 4, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



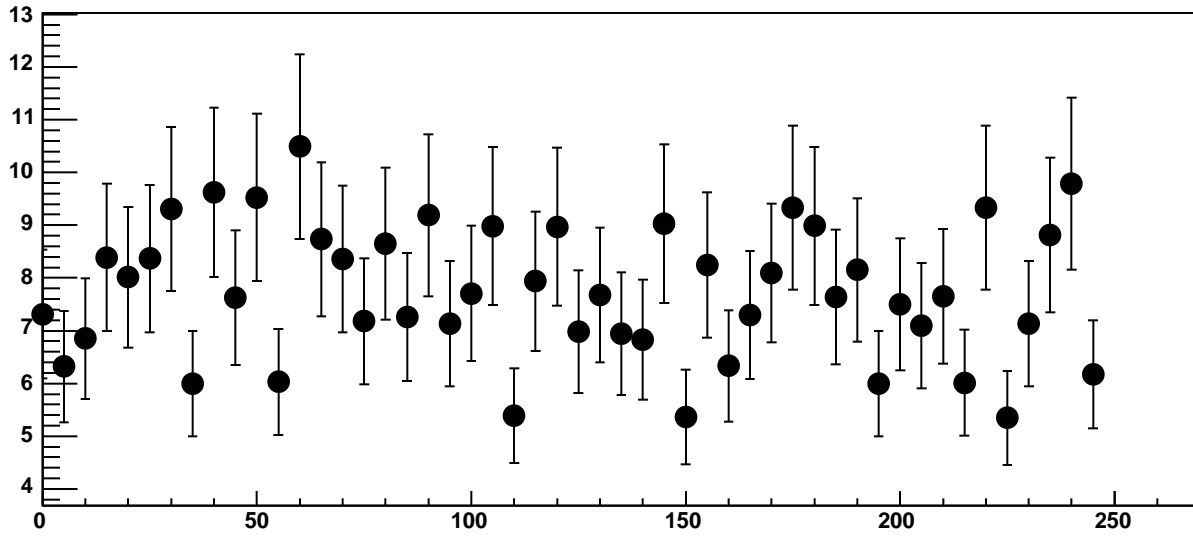
Chip 4, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold



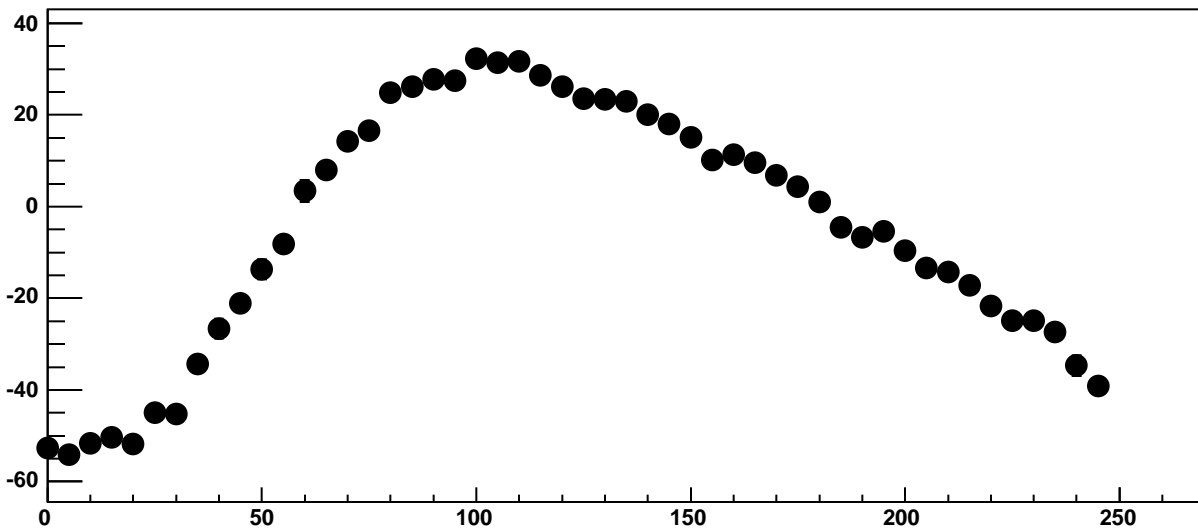
Chip 4, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold



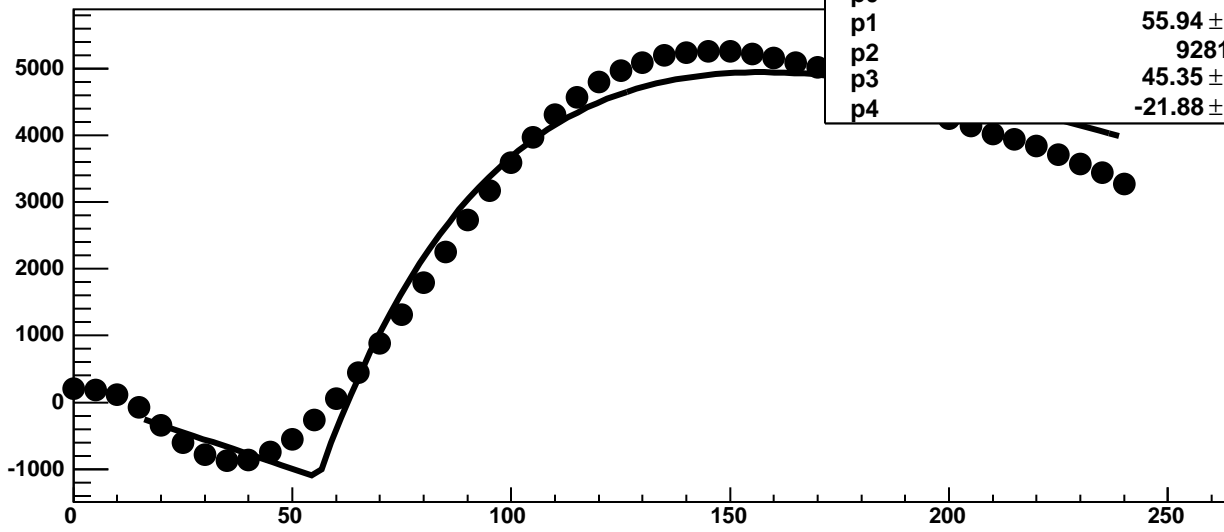
Chip 4, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold

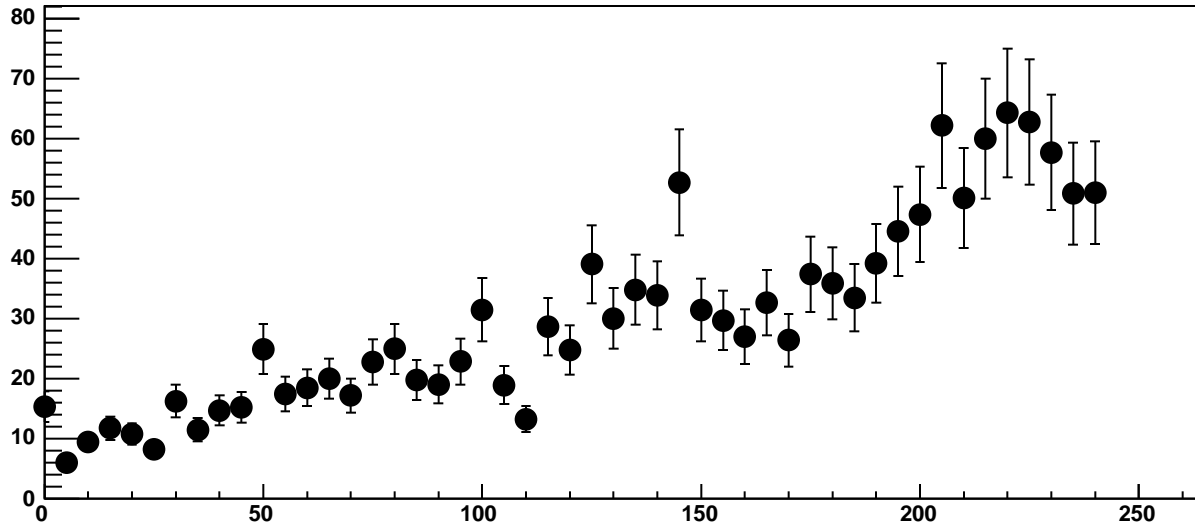


Chip 4, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

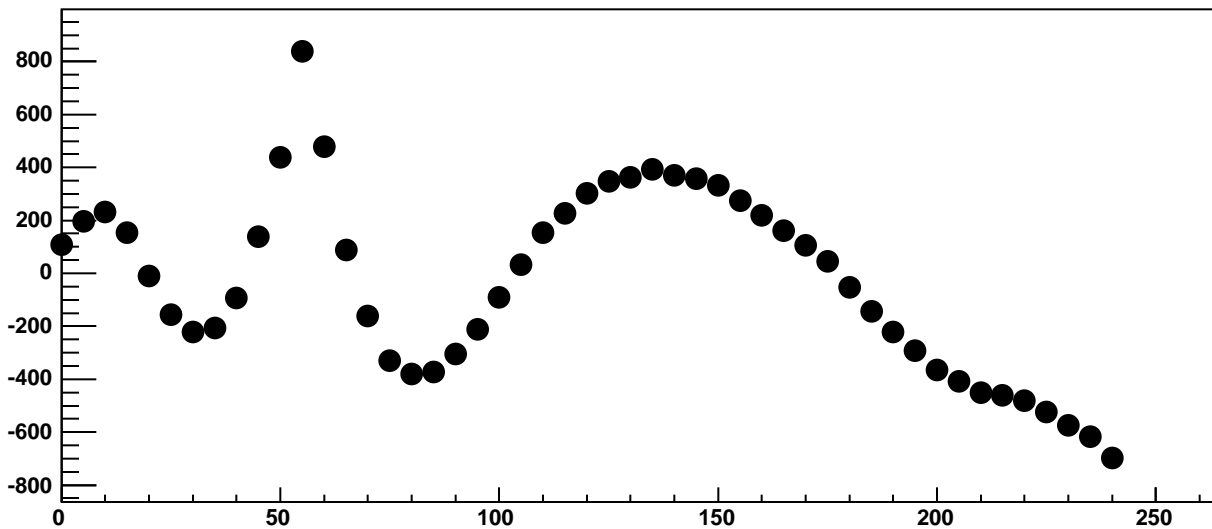


χ^2 / ndf	1.471e+05 / 41
p0	-1125 ± 1.825
p1	55.94 ± 0.02141
p2	9281 ± 13.49
p3	45.35 ± 0.07421
p4	-21.88 ± 0.06861

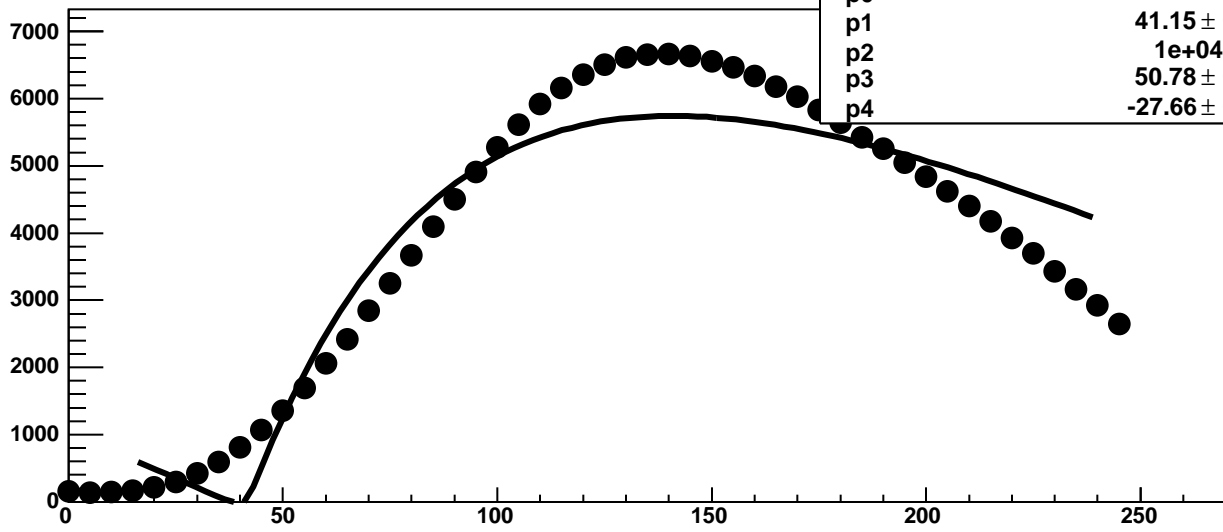
Chip 4, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

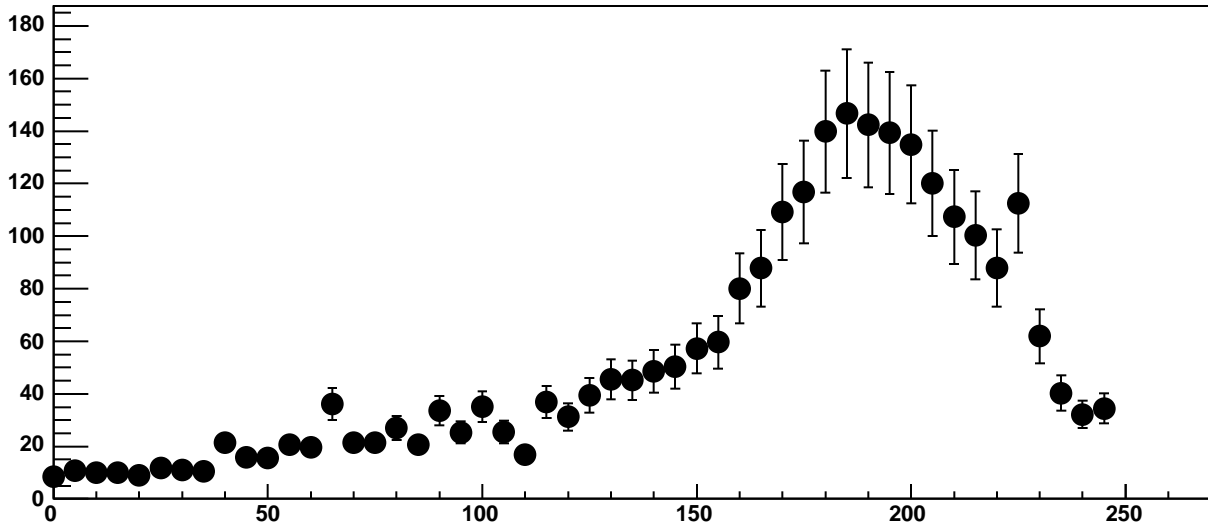


Chip 4, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

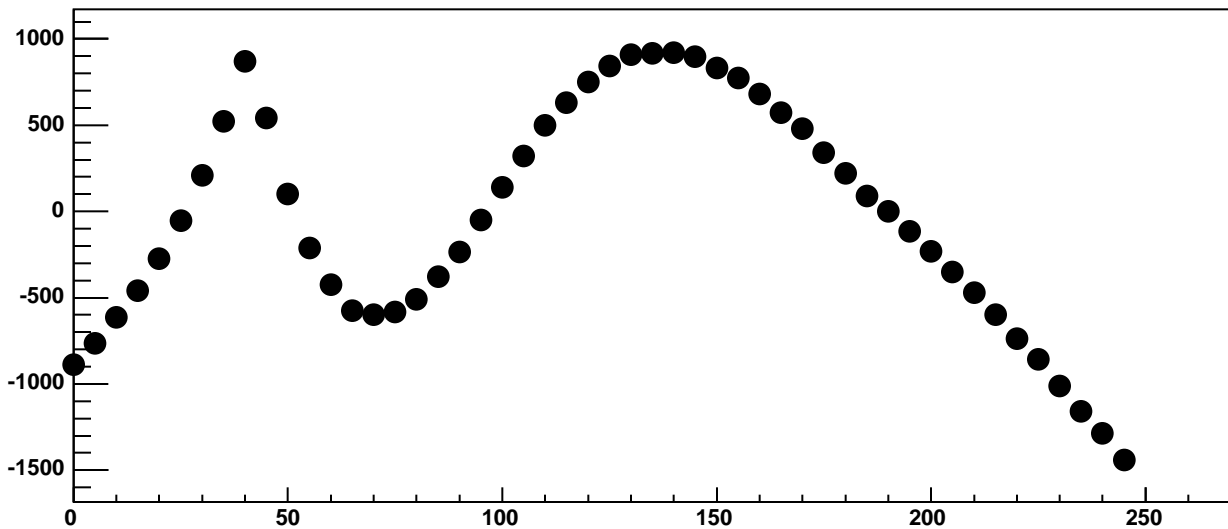


χ^2 / ndf	3.645e+05 / 41
p0	-95.94 ± 1.412
p1	41.15 ± 0.01782
p2	1e+04 ± 15.46
p3	50.78 ± 0.08354
p4	-27.66 ± 0.07518

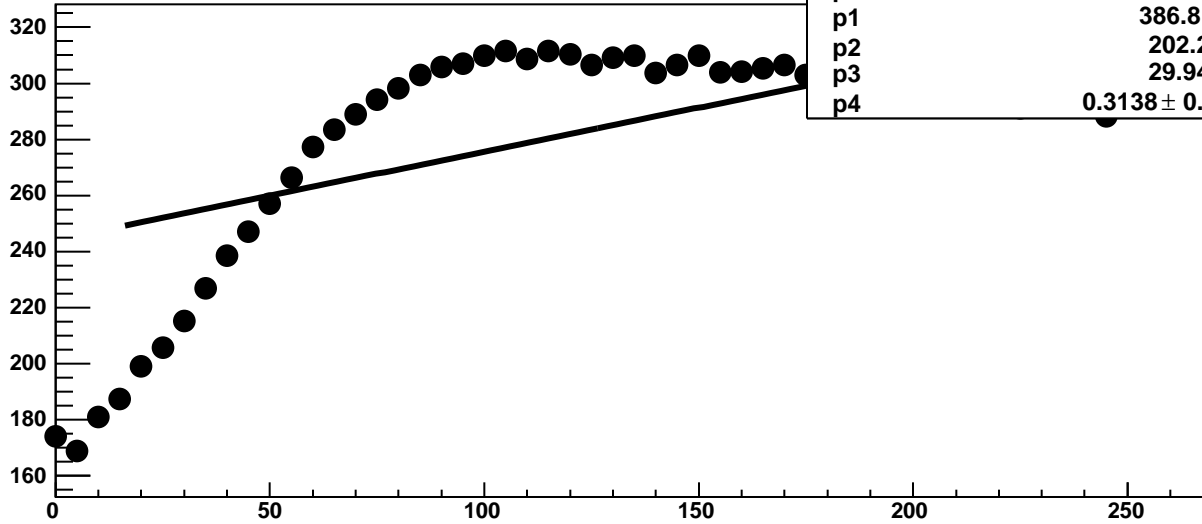
Chip 4, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



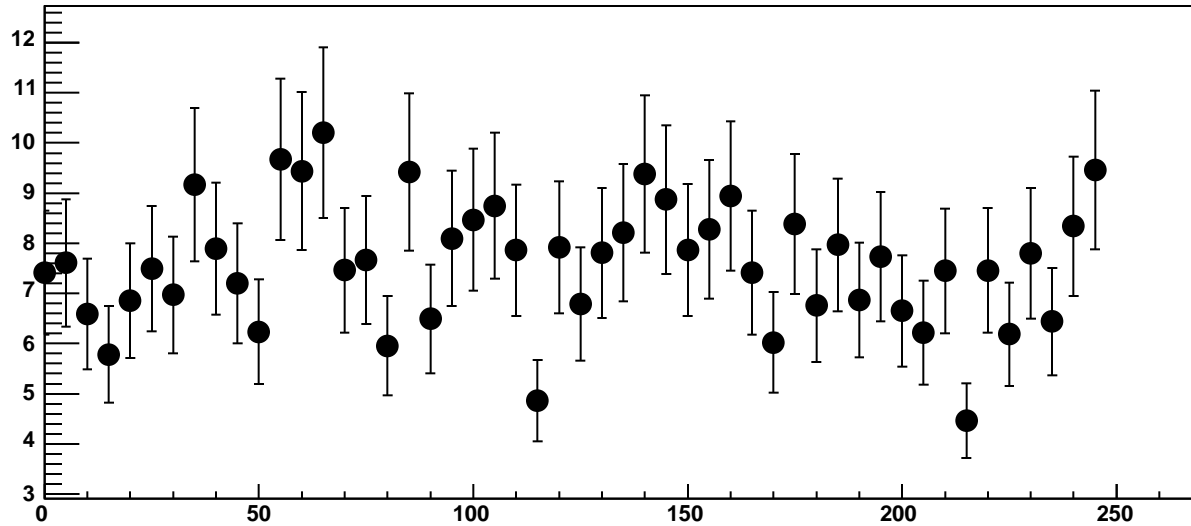
Chip 4, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold



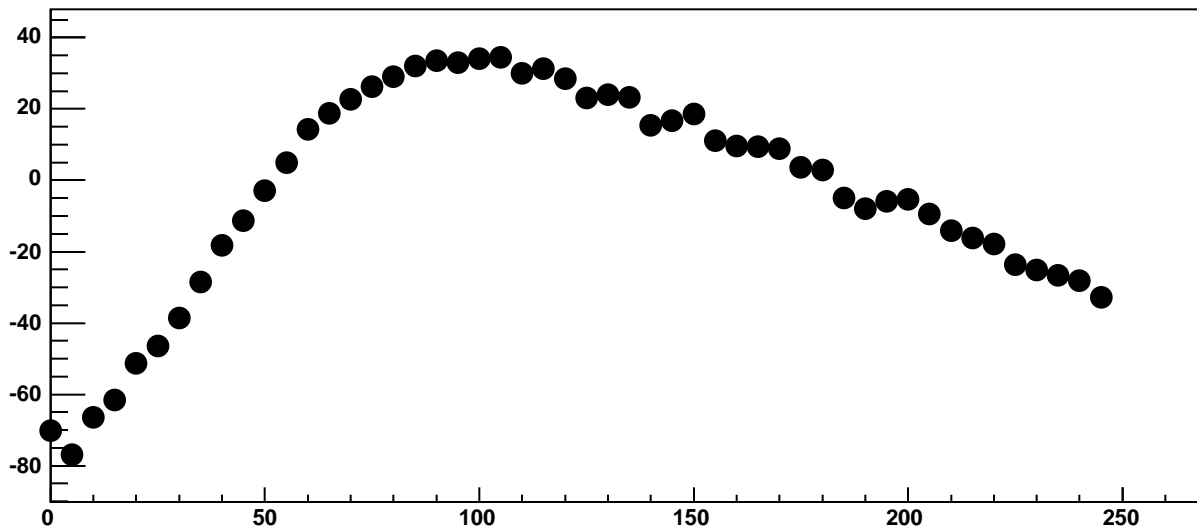
Chip 4, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 4, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold

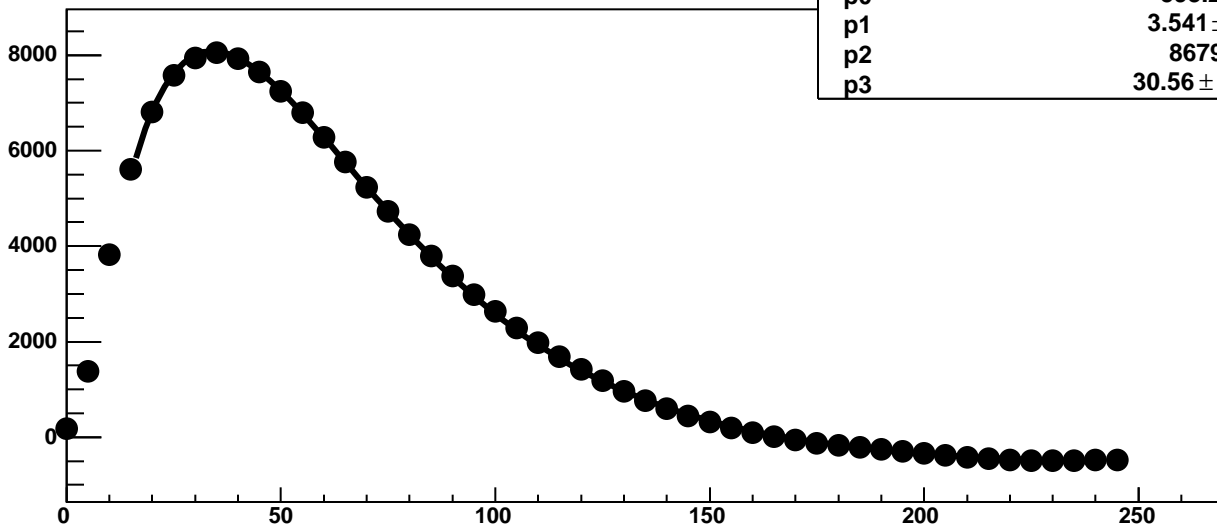


Chip 4, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

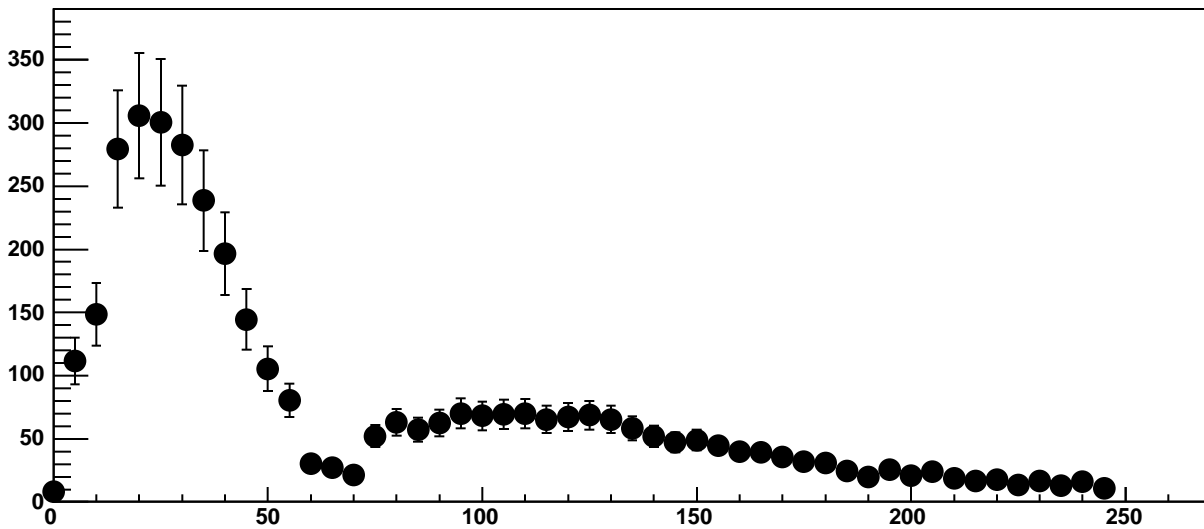


Chip 4, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold

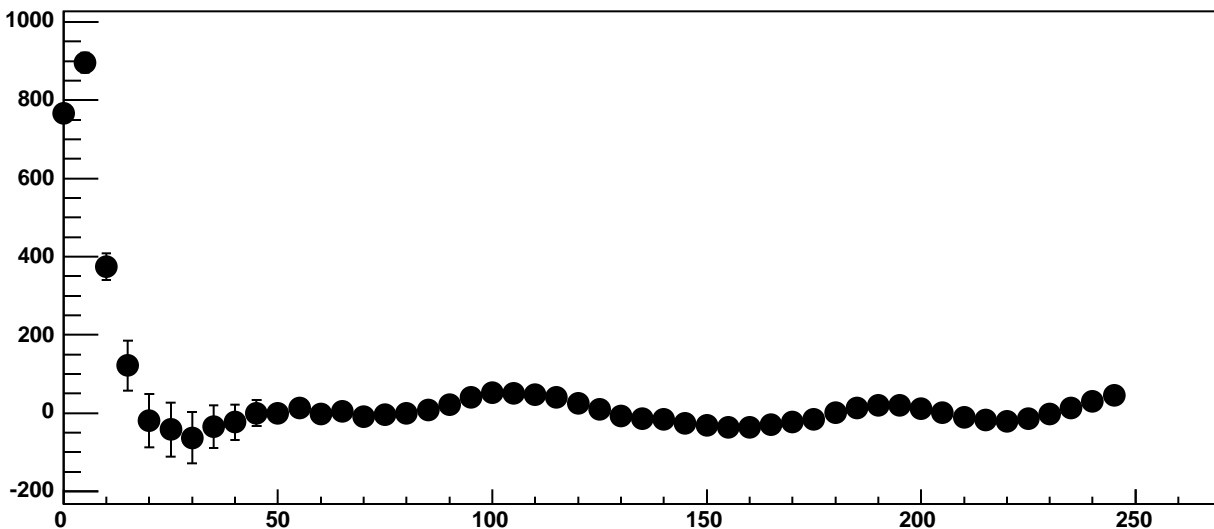
χ^2 / ndf	334.8 / 42
p0	-593.2 ± 1.736
p1	3.541 ± 0.1286
p2	8679 ± 12.91
p3	30.56 ± 0.04589



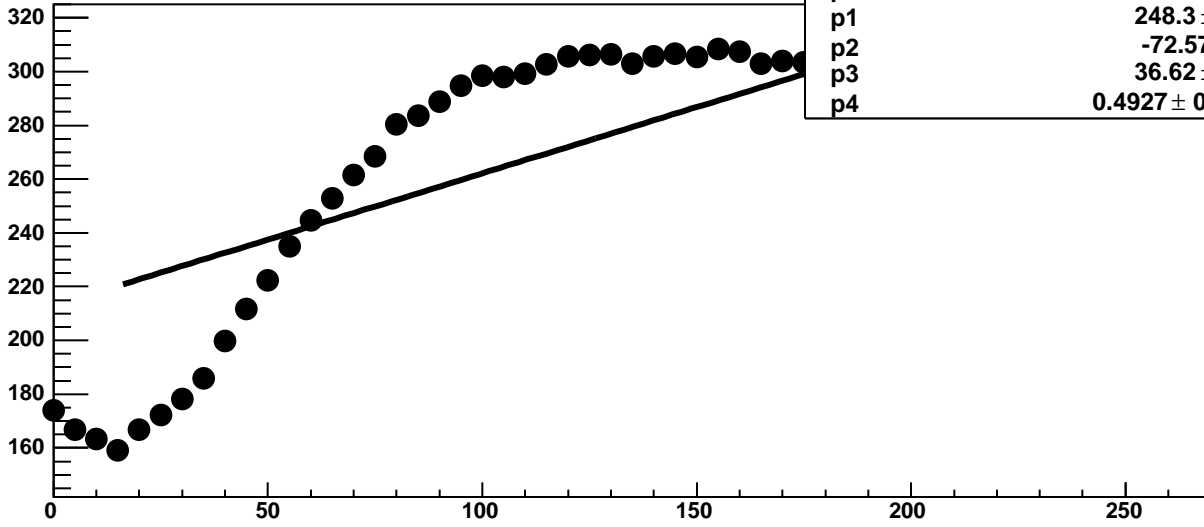
Chip 4, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold

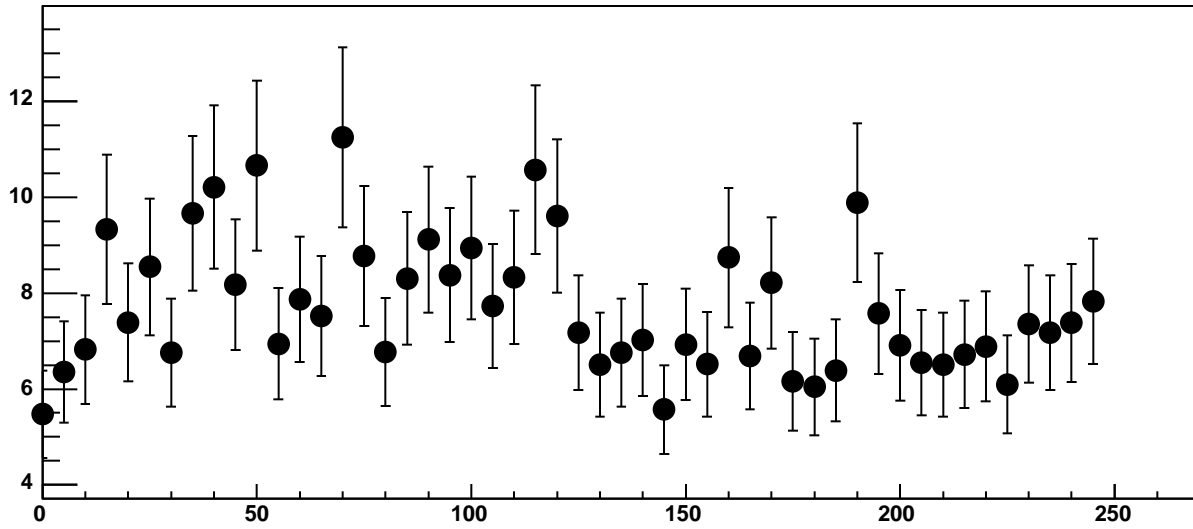


Chip 4, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

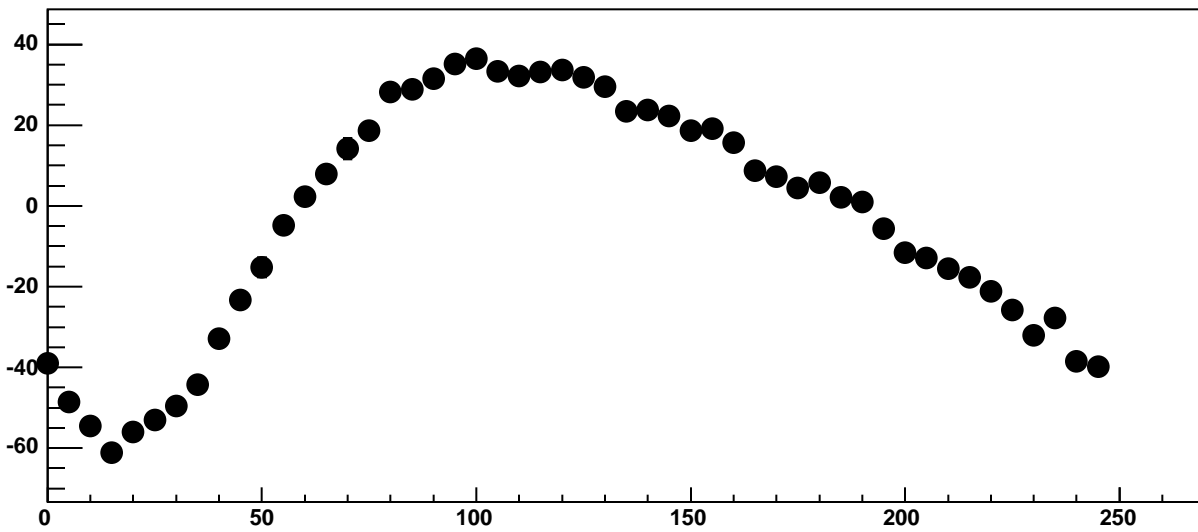


χ^2 / ndf	1.11e+04 / 41
p0	335.2 ± 0.343
p1	248.3 ± 0.5039
p2	-72.57 ± 4.781
p3	36.62 ± 0.3138
p4	0.4927 ± 0.003877

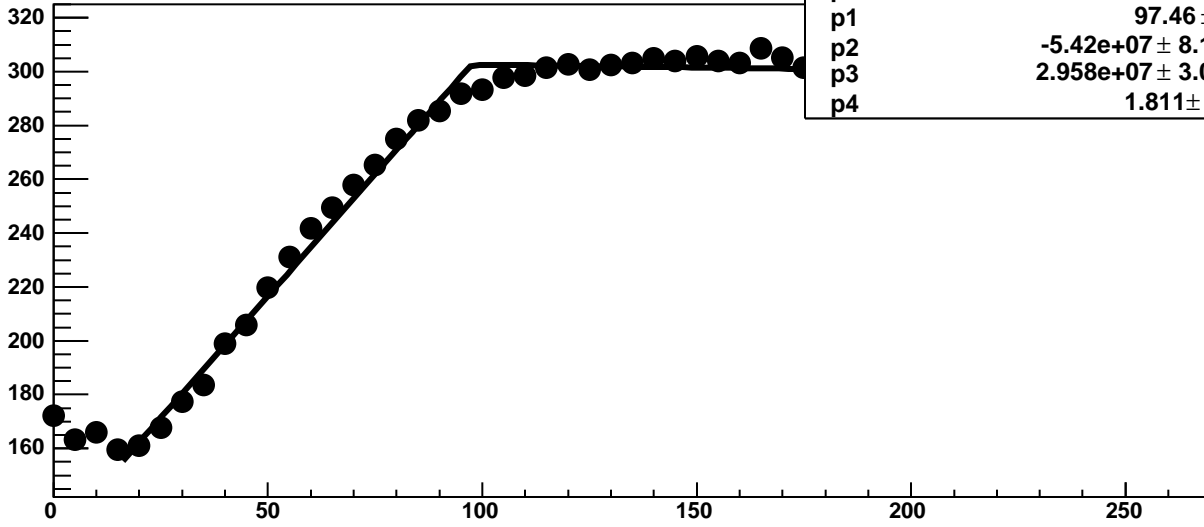
Chip 4, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

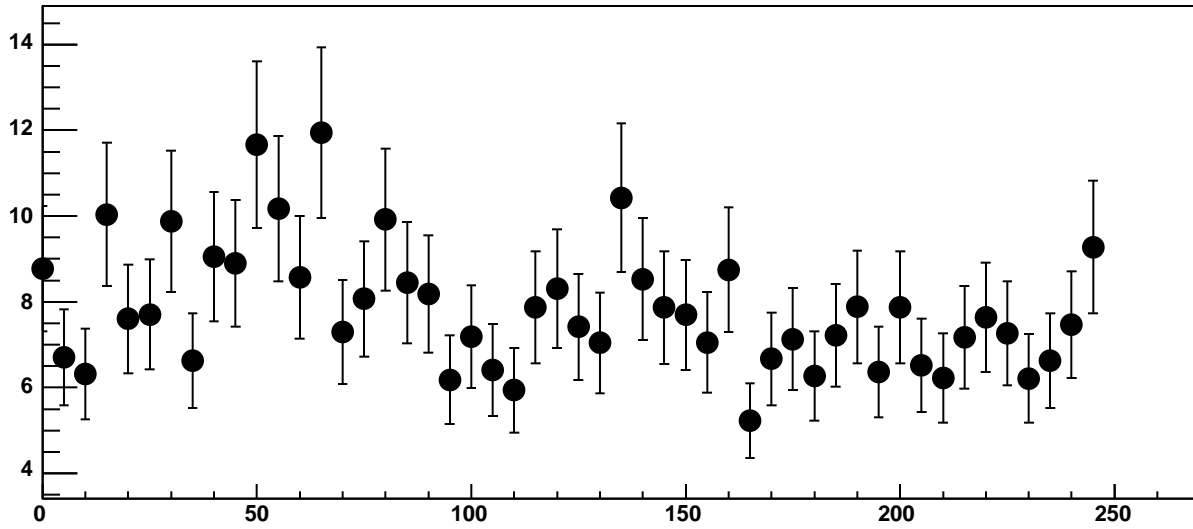


Chip 4, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

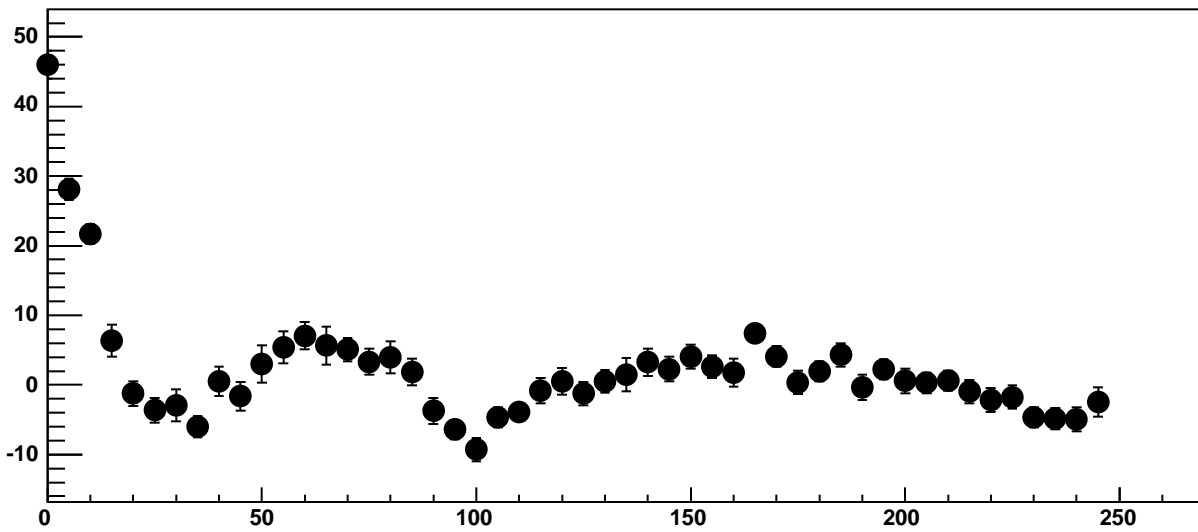


χ^2 / ndf	248.2 / 41
p0	302.6 ± 0.6062
p1	97.46 ± 0.5787
p2	$-5.42e+07 \pm 8.158e+05$
p3	$2.958e+07 \pm 3.063e+05$
p4	1.811 ± 0.01797

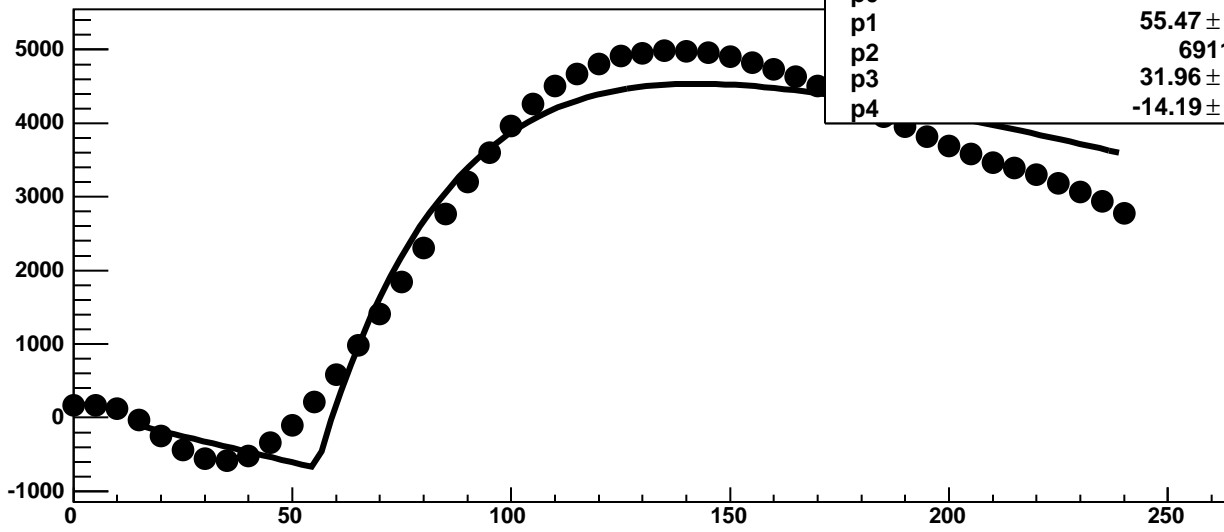
Chip 4, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

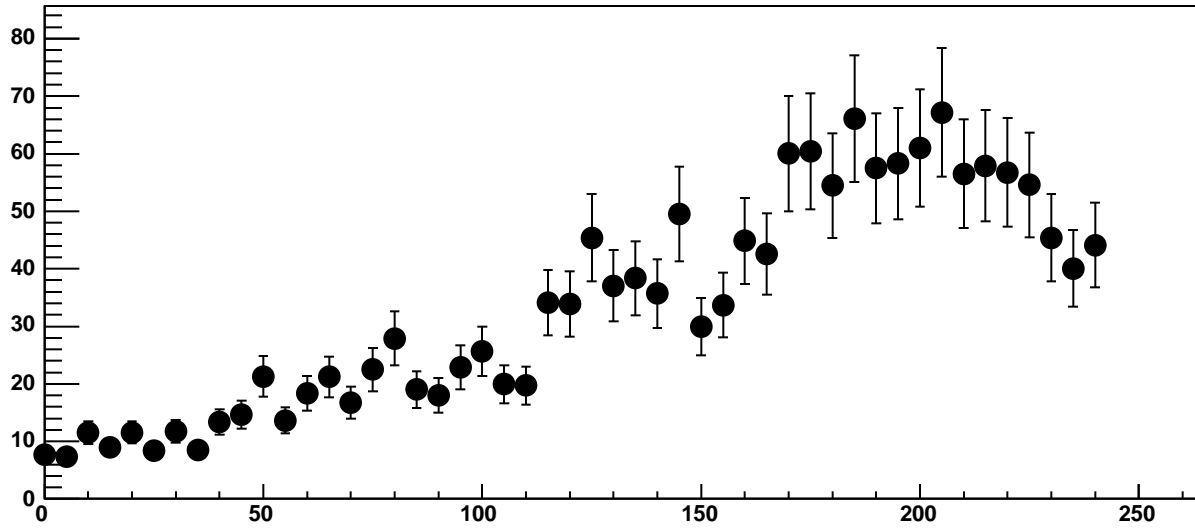


Chip 4, Channel 15, Enable 5, DAC=4000, ADC Mean vs Hold

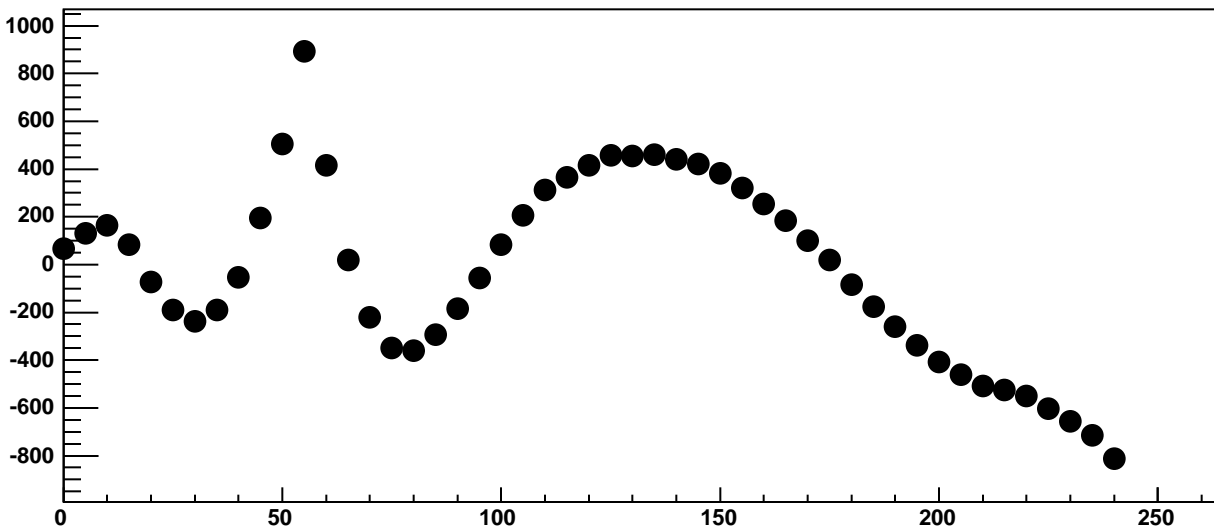


χ^2 / ndf	2.09e+05 / 41
p0	-685.7 \pm 1.424
p1	55.47 \pm 0.02178
p2	6911 \pm 8.789
p3	31.96 \pm 0.05782
p4	-14.19 \pm 0.05067

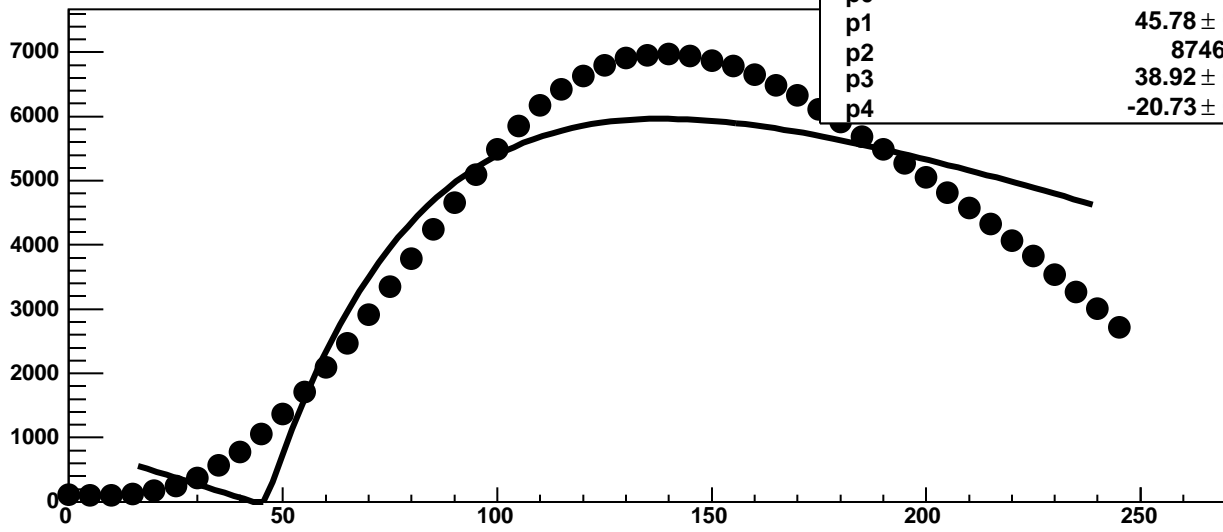
Chip 4, Channel 15, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 15, Enable 5, DAC=4000, ADC Residuals vs Hold

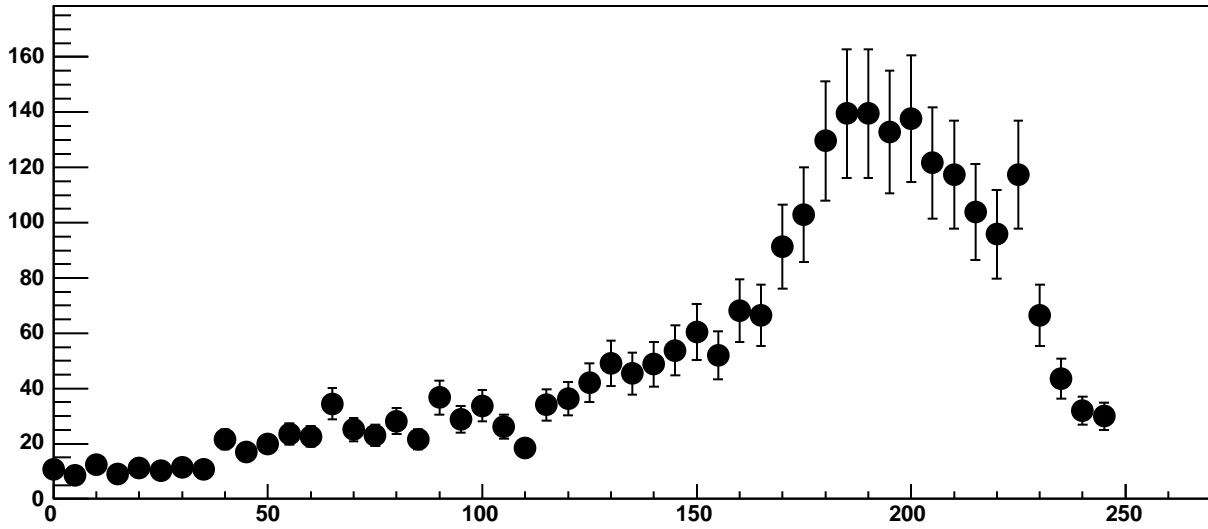


Chip 4, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

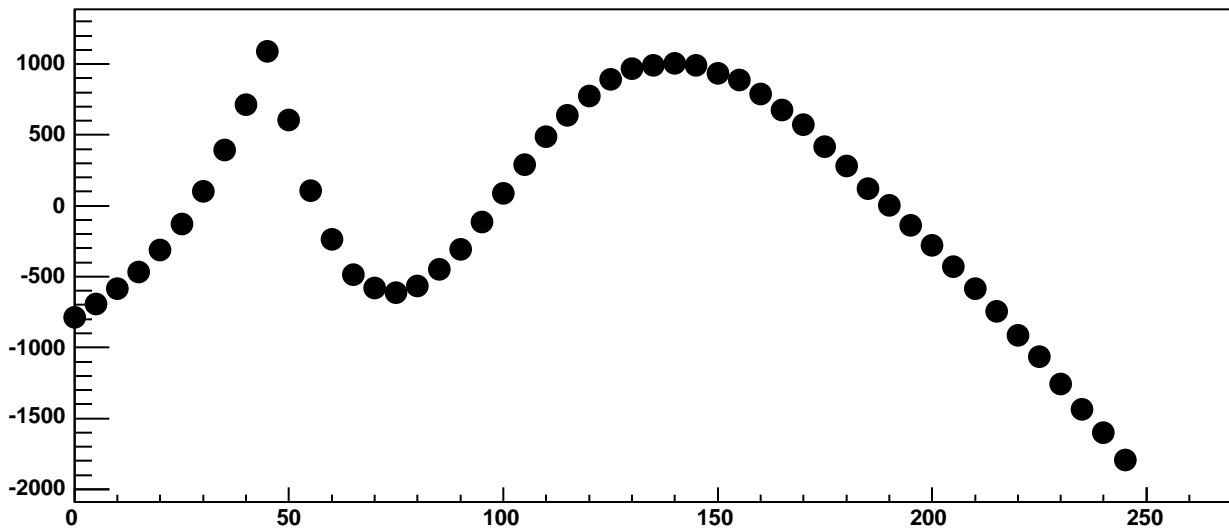


χ^2 / ndf	4.265e+05 / 41
p0	-52.23 ± 1.479
p1	45.78 ± 0.02023
p2	8746 ± 11.46
p3	38.92 ± 0.06376
p4	-20.73 ± 0.06247

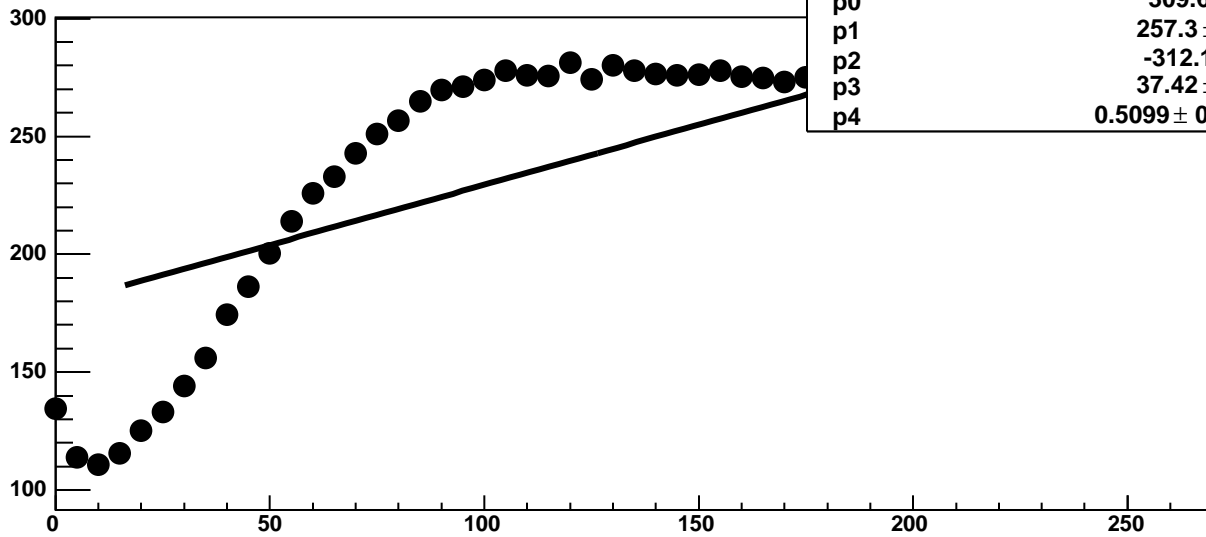
Chip 4, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

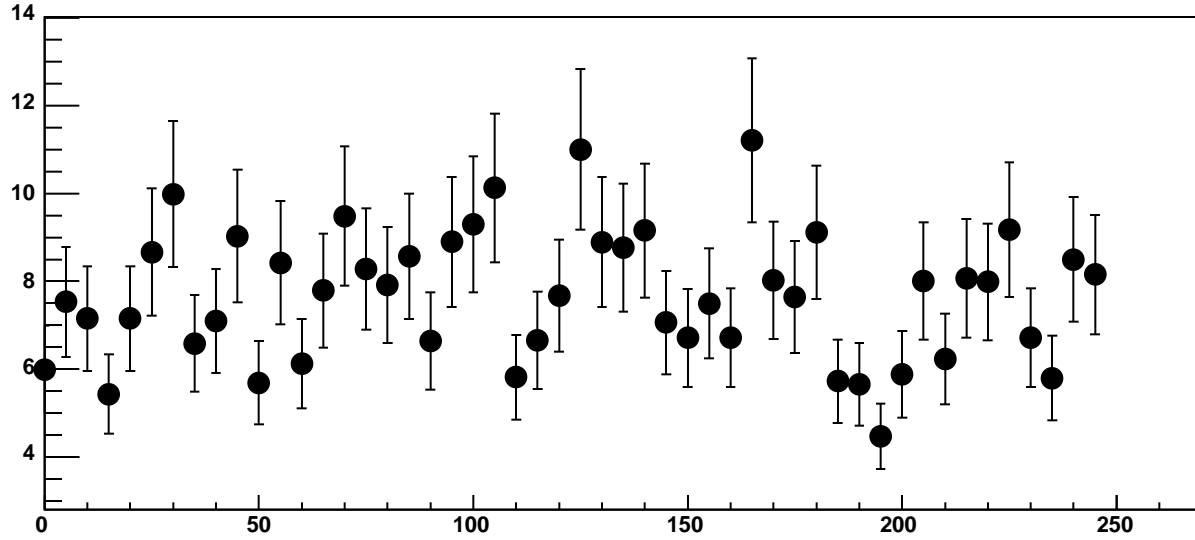


Chip 4, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold

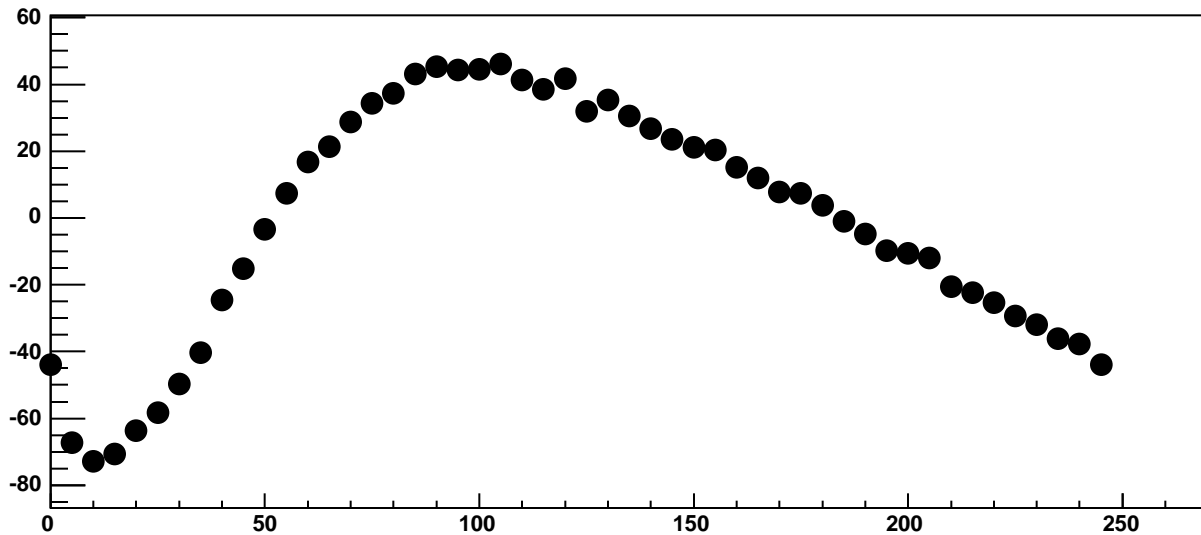


χ^2 / ndf	1.712e+04 / 41
p0	309.6 ± 0.263
p1	257.3 ± 0.7679
p2	-312.1 ± 7.625
p3	37.42 ± 0.3732
p4	0.5099 ± 0.003576

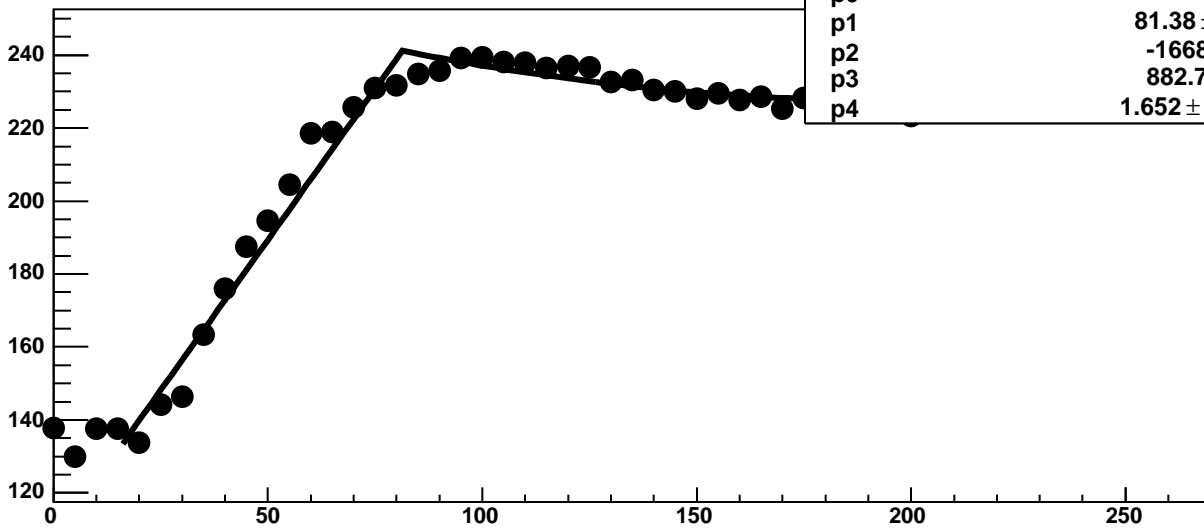
Chip 4, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold

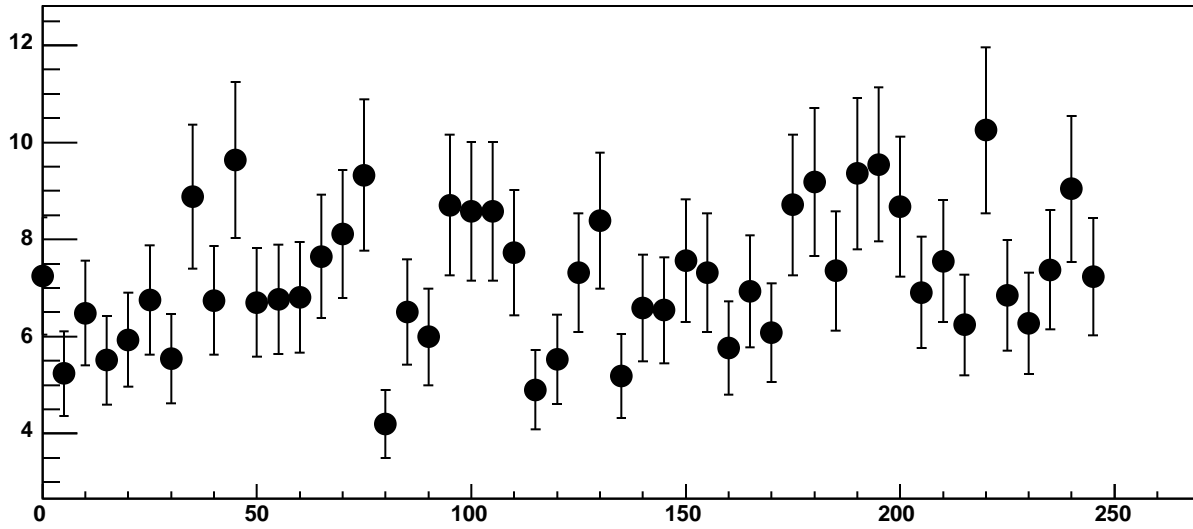


Chip 4, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold

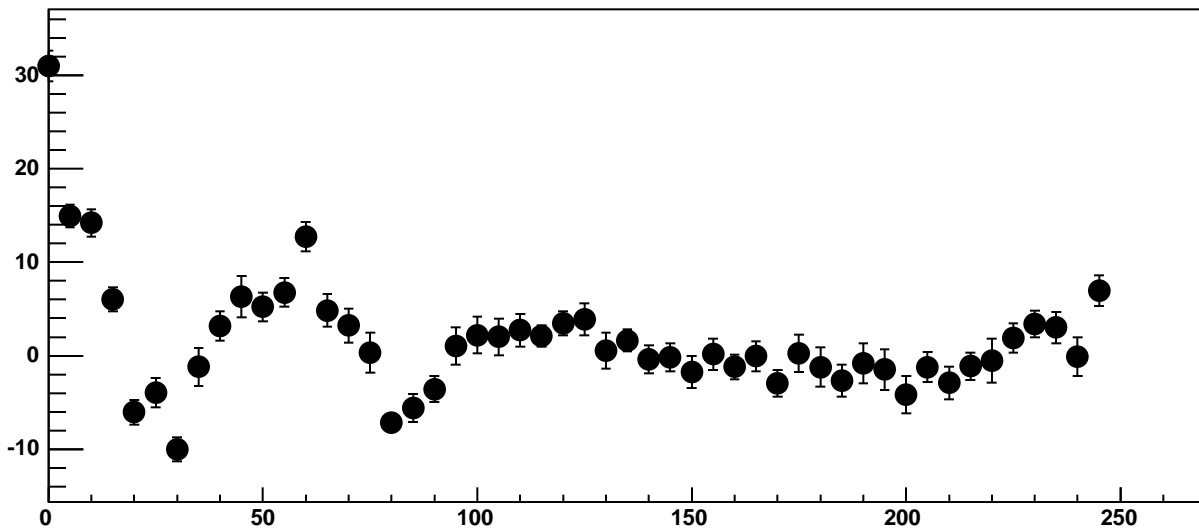


χ^2 / ndf	360 / 41
p0	241.2 ± 0.6927
p1	81.38 ± 0.5557
p2	-1668 ± 184.4
p3	882.7 ± 105.9
p4	1.652 ± 0.01765

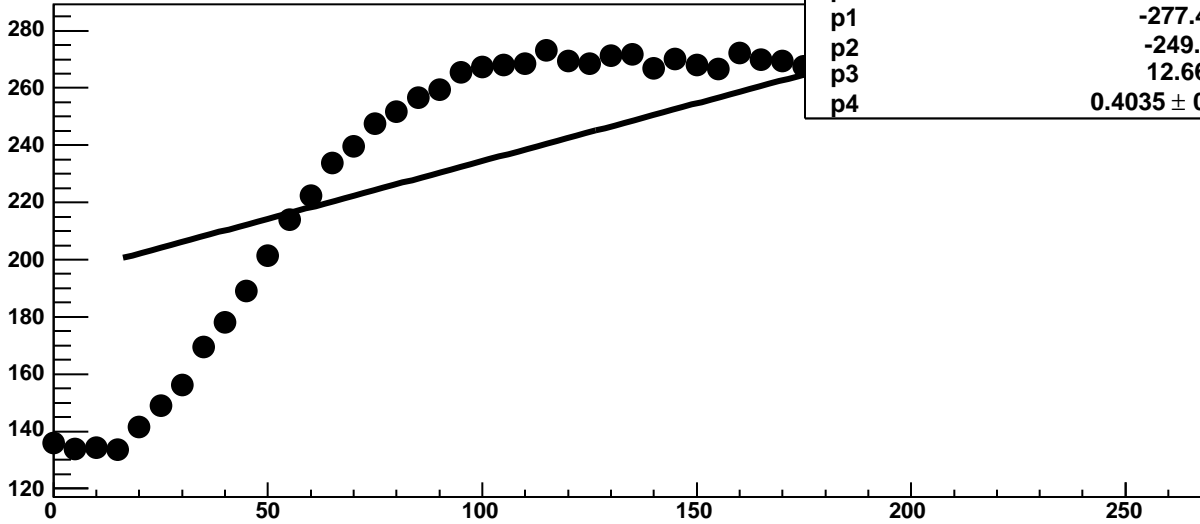
Chip 4, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold

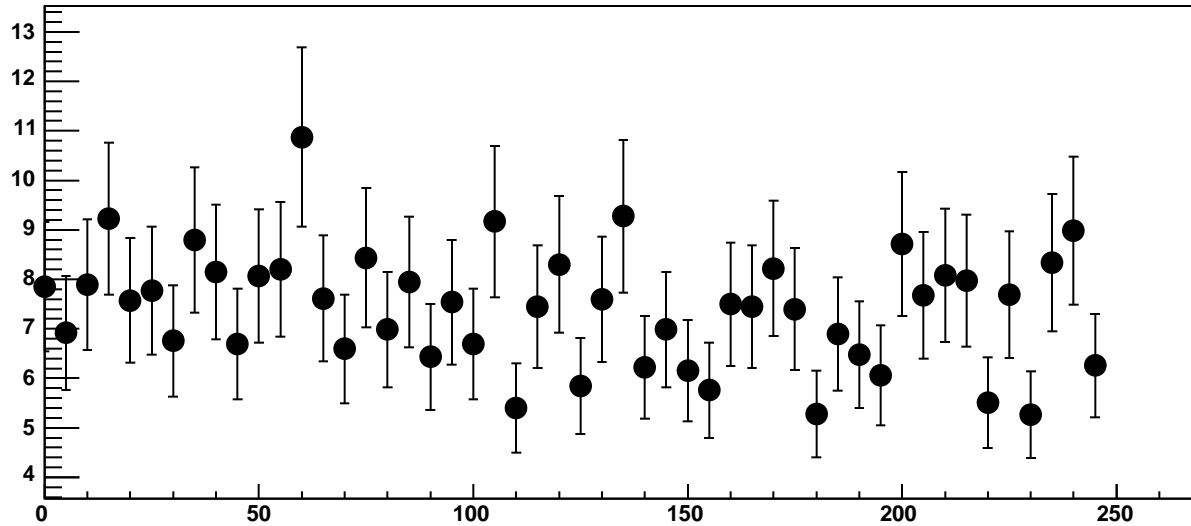


Chip 4, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

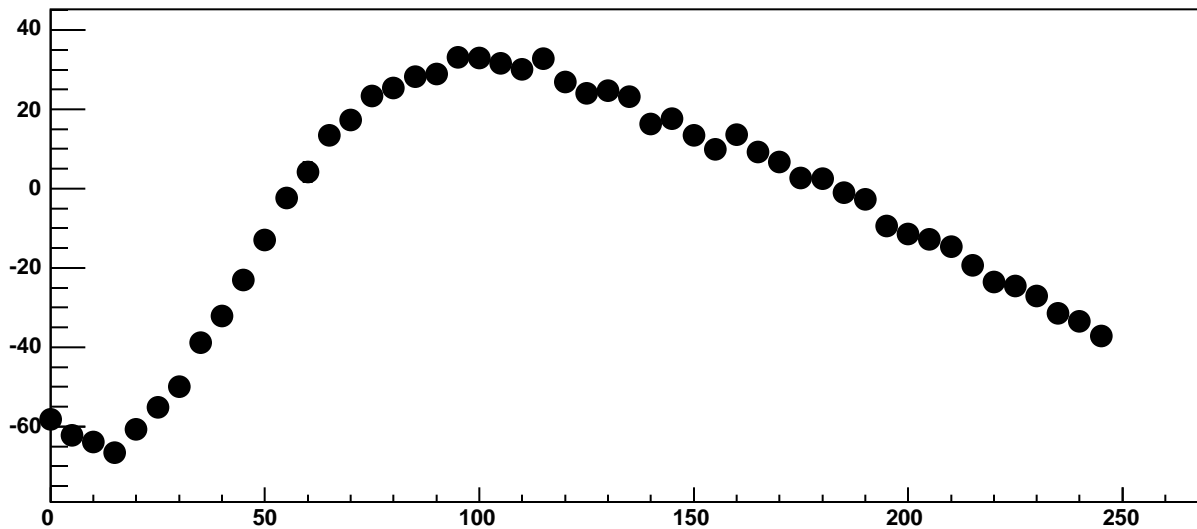


χ^2 / ndf	1.159e+04 / 41
p0	331.2 ± 3.161
p1	-277.4 ± 7.833
p2	-249.1 ± 3.161
p3	12.66 ± 474.9
p4	0.4035 ± 0.003404

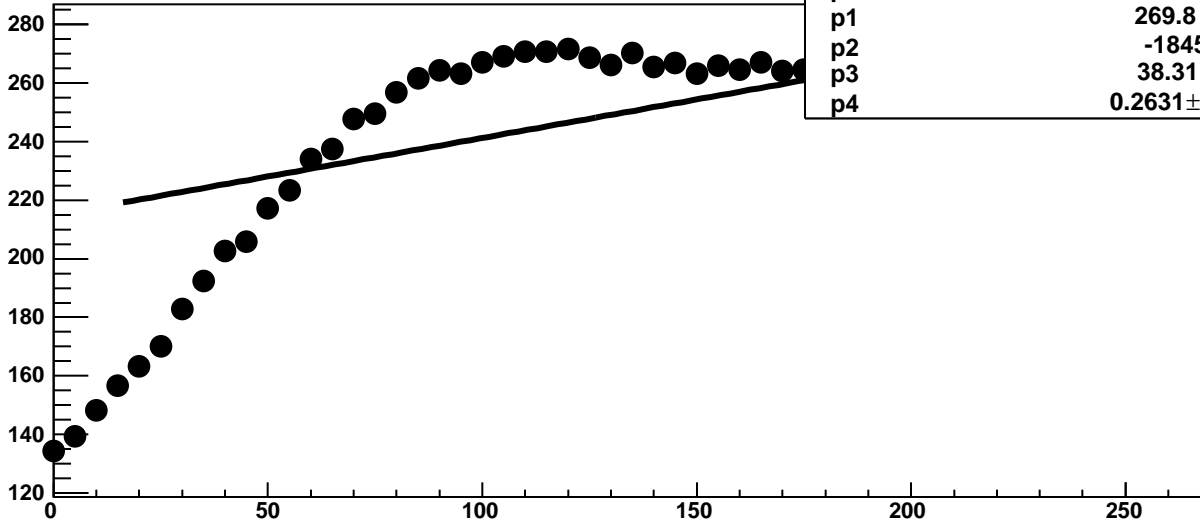
Chip 4, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

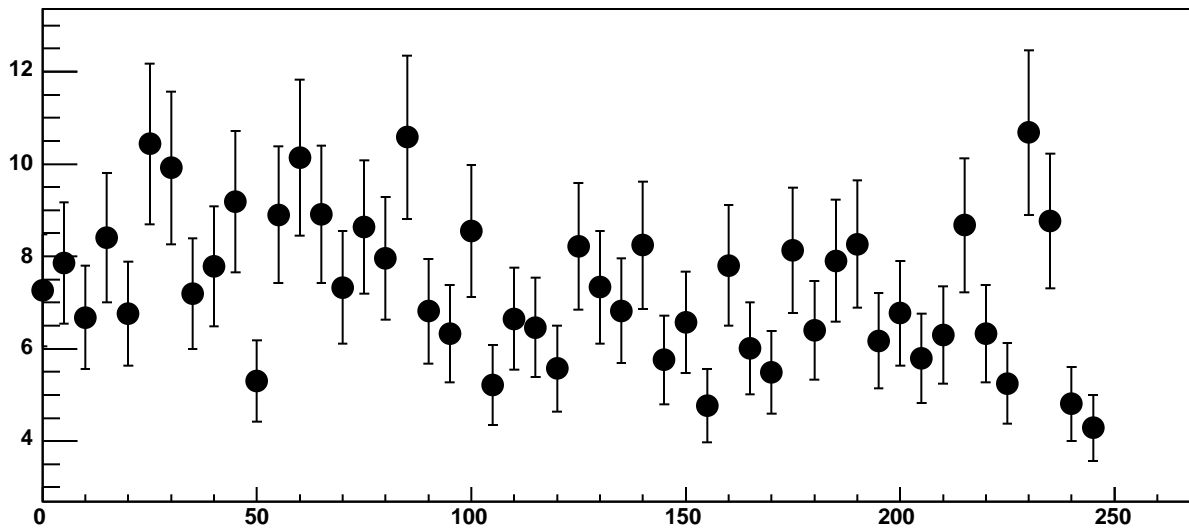


Chip 4, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

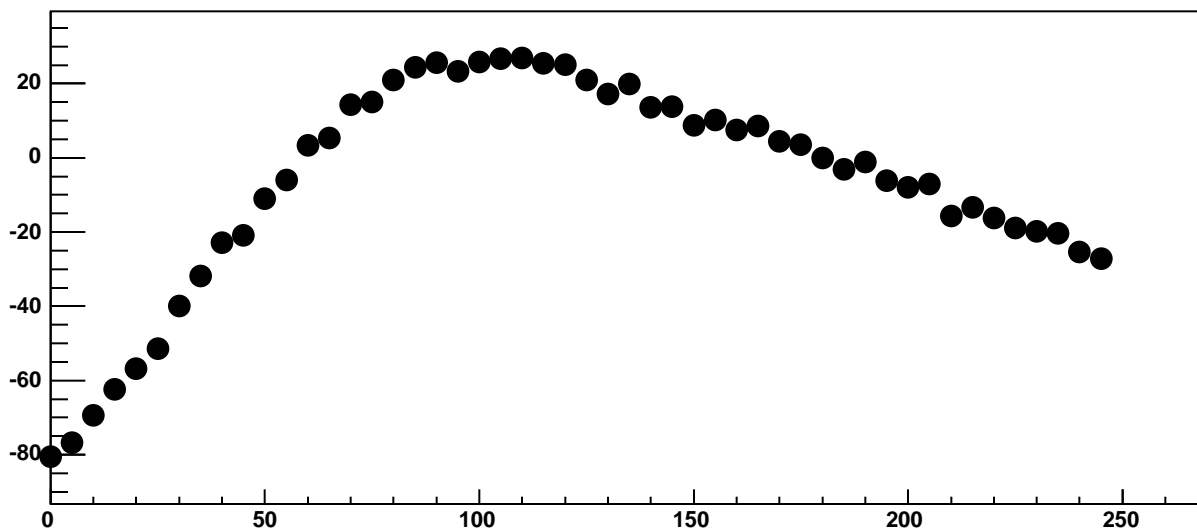


χ^2 / ndf	8495 / 41
p0	285.9 ± 0.4217
p1	269.8 ± 0.6138
p2	-1845 ± 25.93
p3	38.31 ± 0.6502
p4	0.2631 ± 0.00365

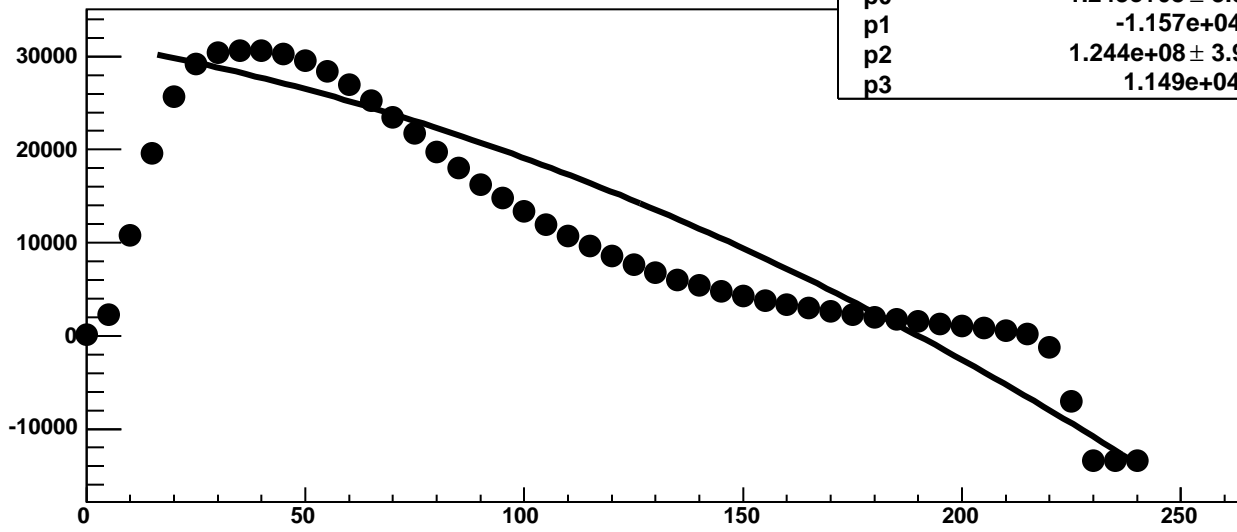
Chip 4, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

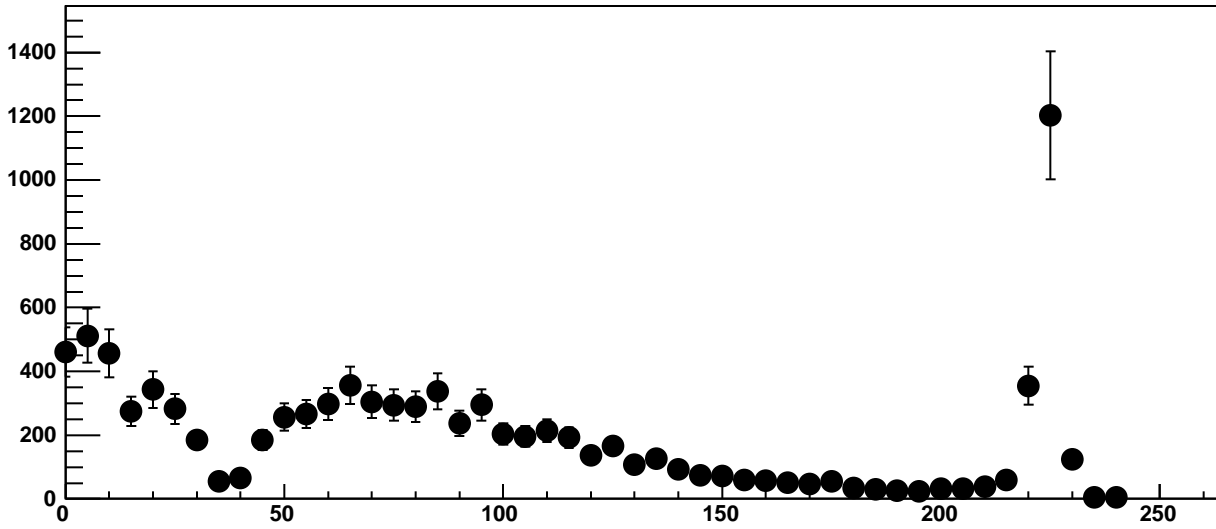


Chip 4, Channel 16, Enable 5!, DAC=4000, ADC Mean vs Hold

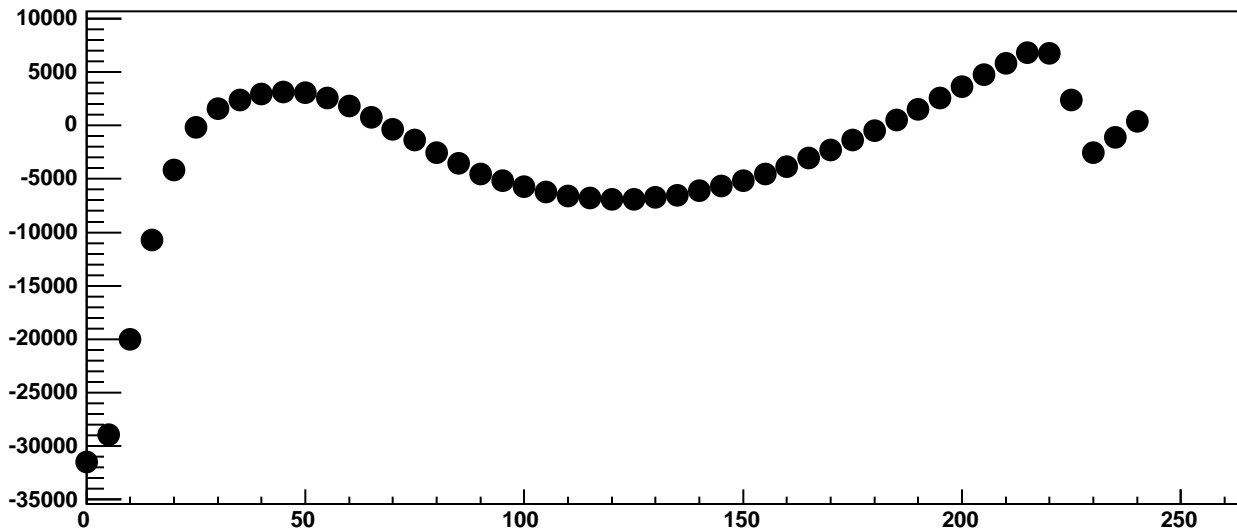


χ^2 / ndf	3.499e+06 / 42
p0	-1.243e+08 ± 3.953e+05
p1	-1.157e+04 ± 20.63
p2	1.244e+08 ± 3.953e+05
p3	1.149e+04 ± 20.52

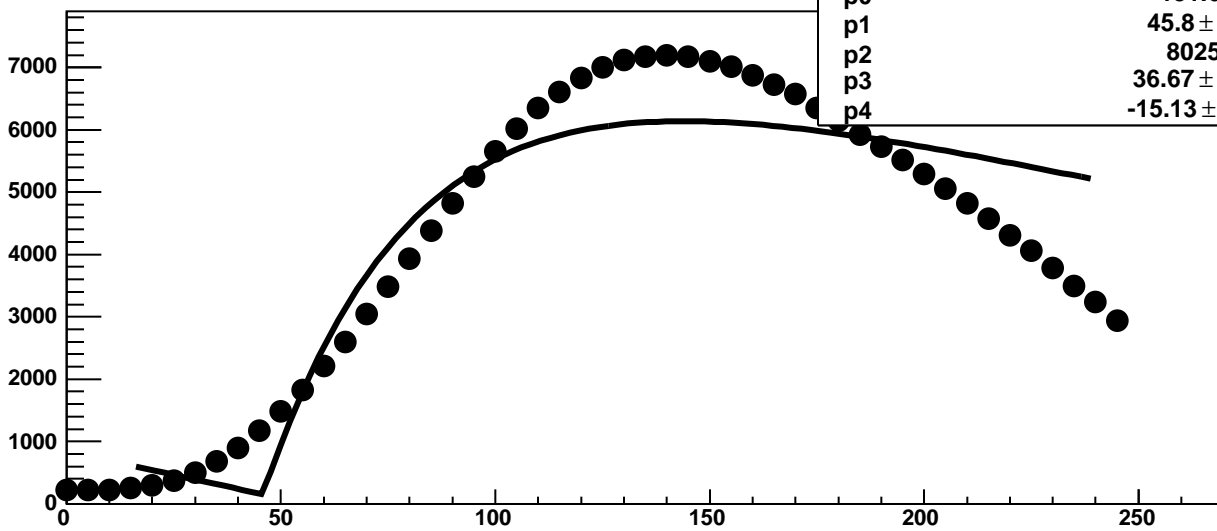
Chip 4, Channel 16, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 16, Enable 5!, DAC=4000, ADC Residuals vs Hold

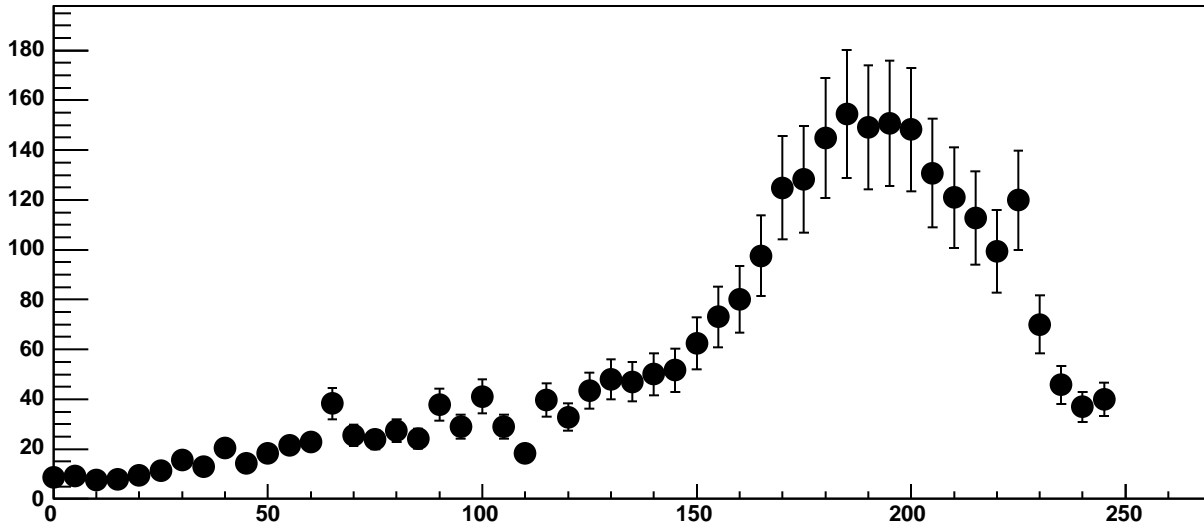


Chip 4, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

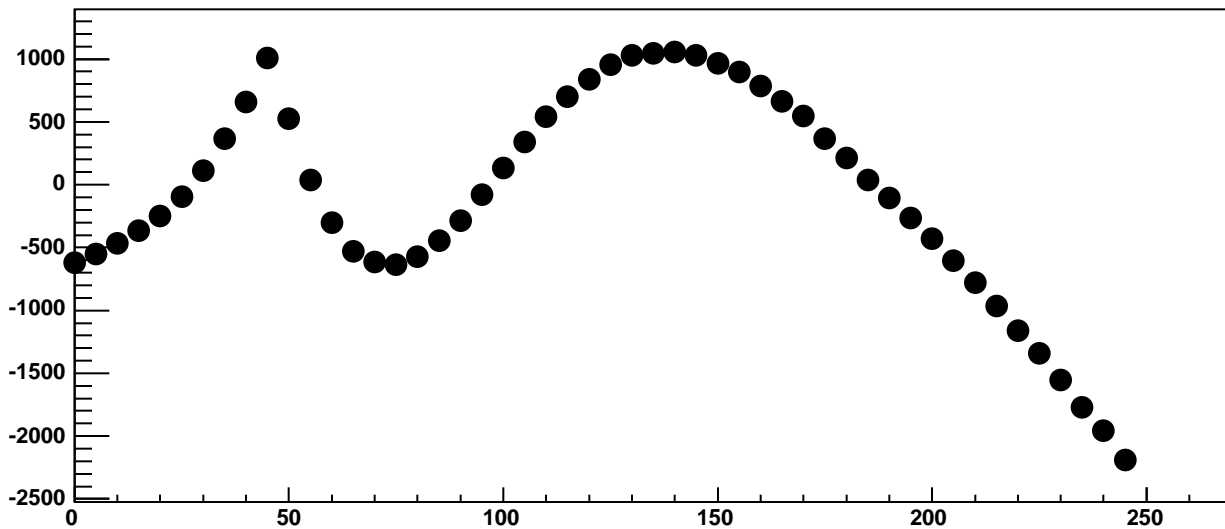


χ^2 / ndf	4.436e+05 / 41
p0	151.6 ± 1.512
p1	45.8 ± 0.02007
p2	8025 ± 10.63
p3	36.67 ± 0.06222
p4	-15.13 ± 0.06021

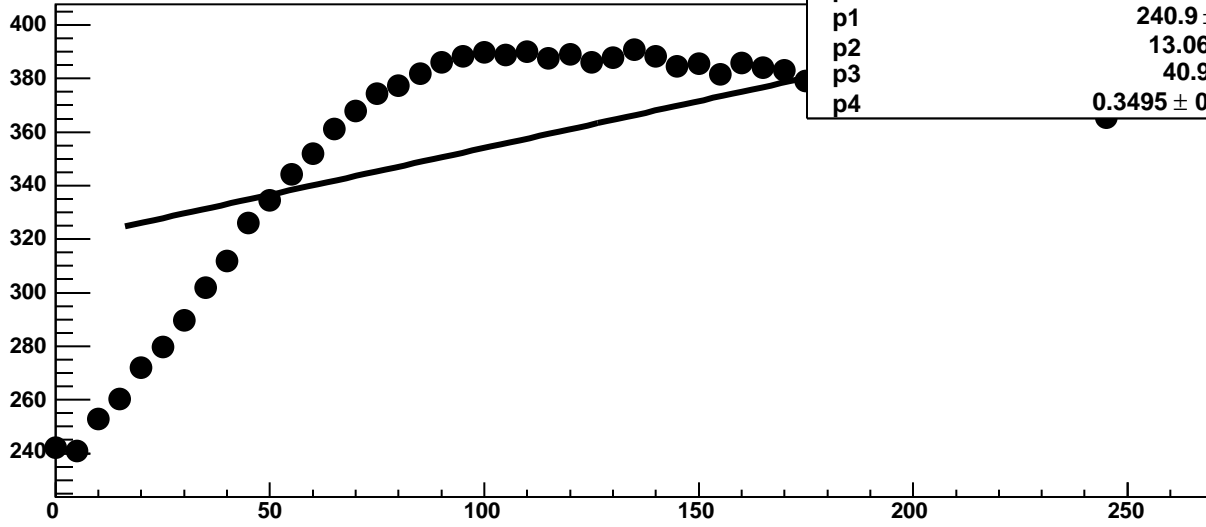
Chip 4, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

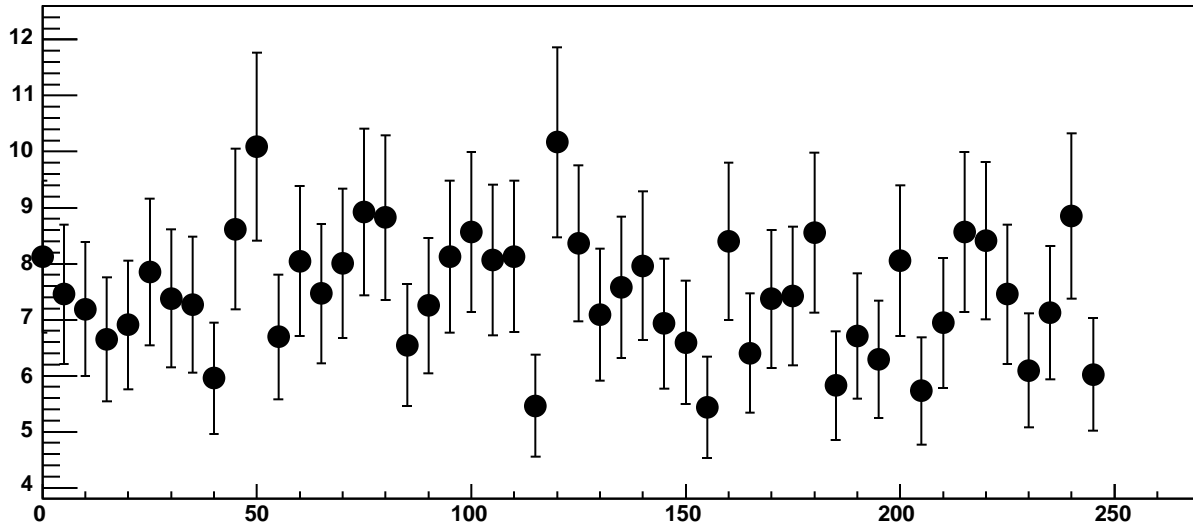


Chip 4, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

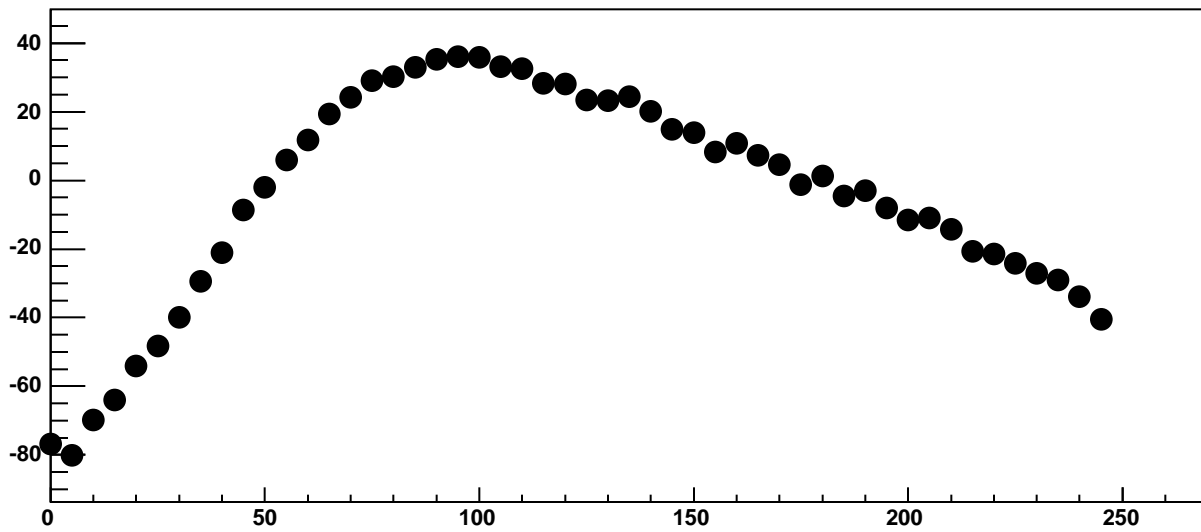


χ^2 / ndf	1.106e+04 / 41
p0	403.3 ± 0.2456
p1	240.9 ± 0.7027
p2	13.06 ± 84.85
p3	40.9 ± 1.414
p4	0.3495 ± 0.001904

Chip 4, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold

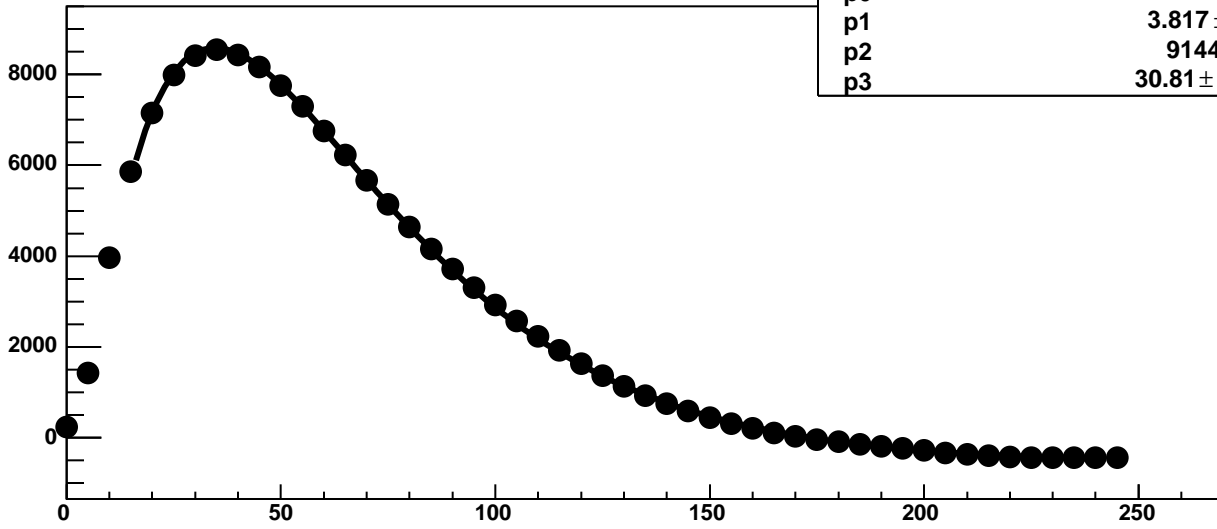


Chip 4, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

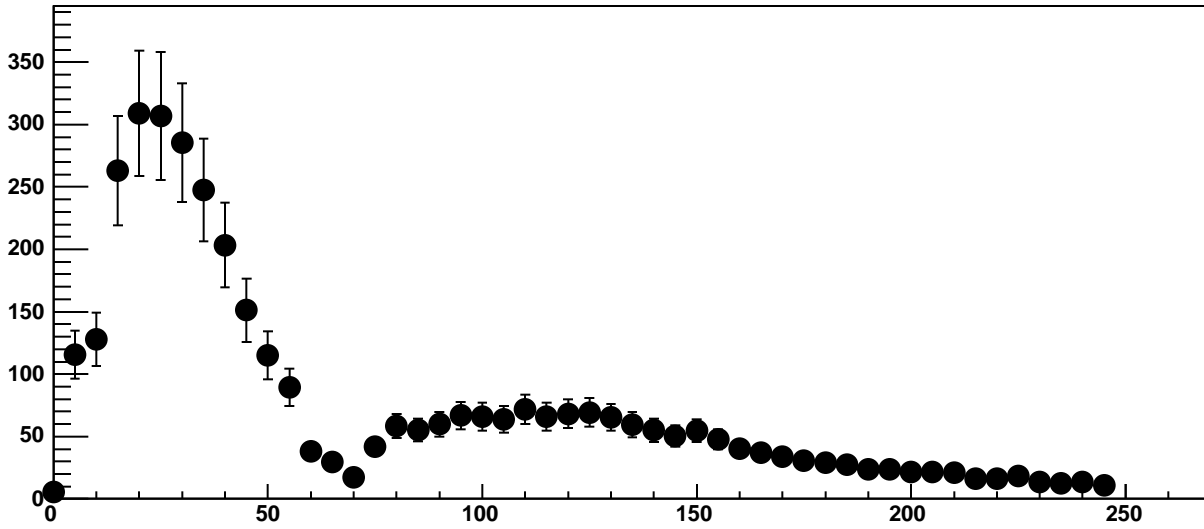


Chip 4, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold

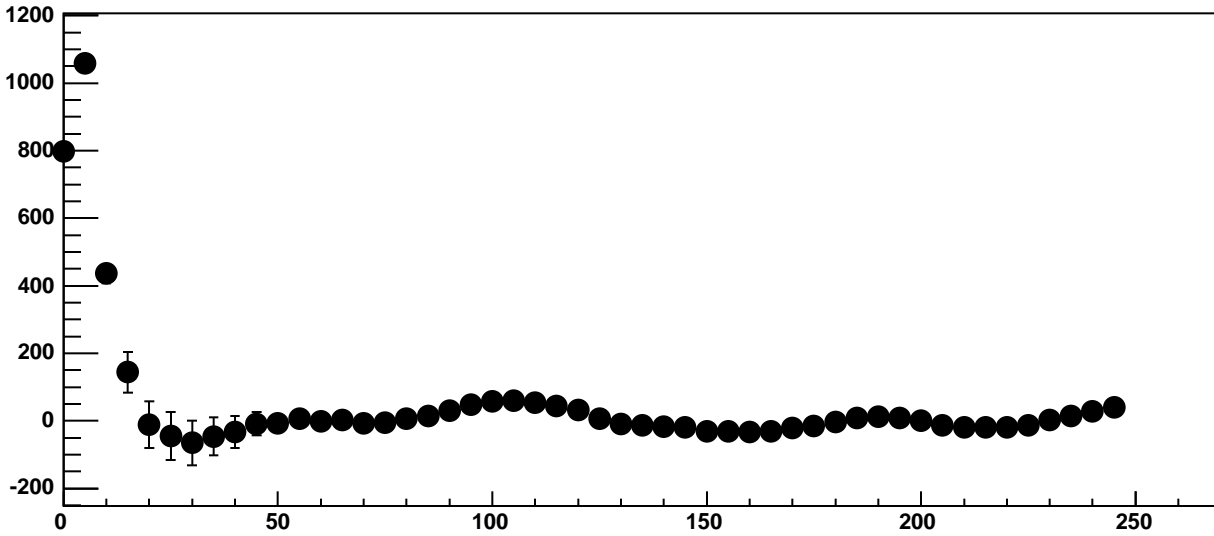
χ^2 / ndf	340.5 / 42
p0	-554.5 ± 1.718
p1	3.817 ± 0.1165
p2	9144 ± 12.59
p3	30.81 ± 0.04319



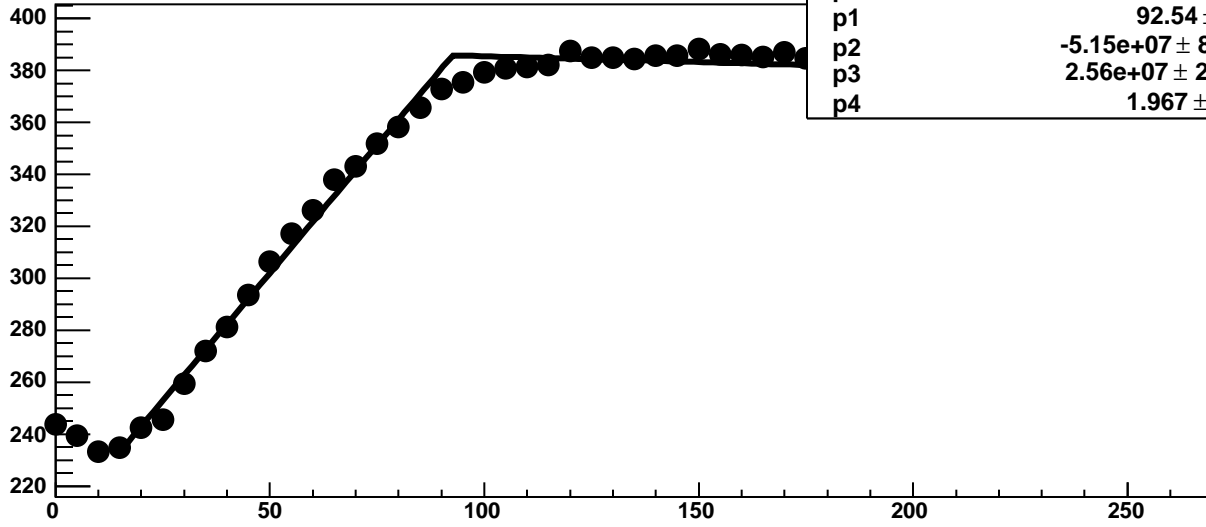
Chip 4, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold

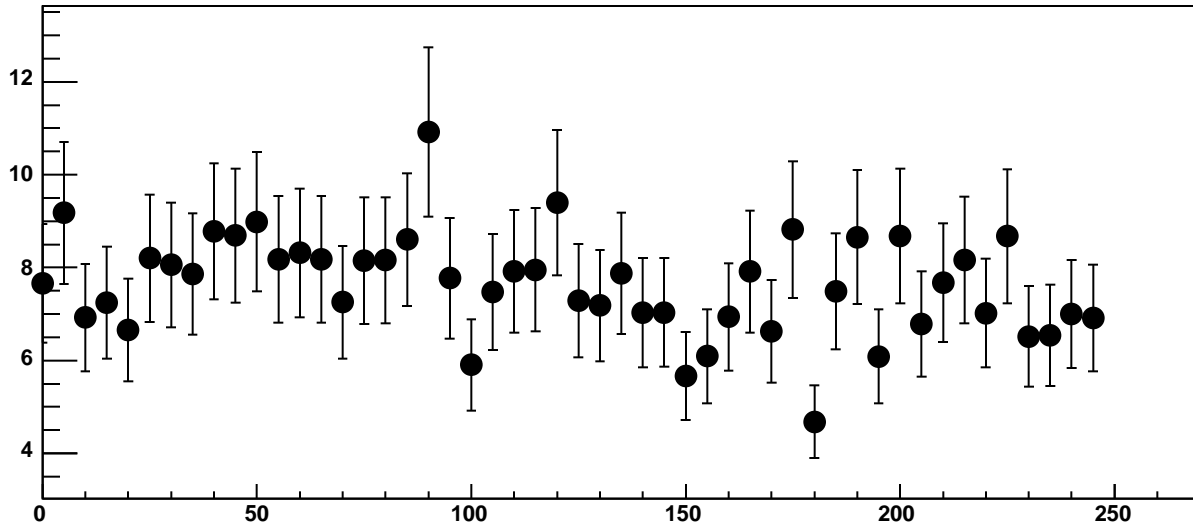


Chip 4, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold

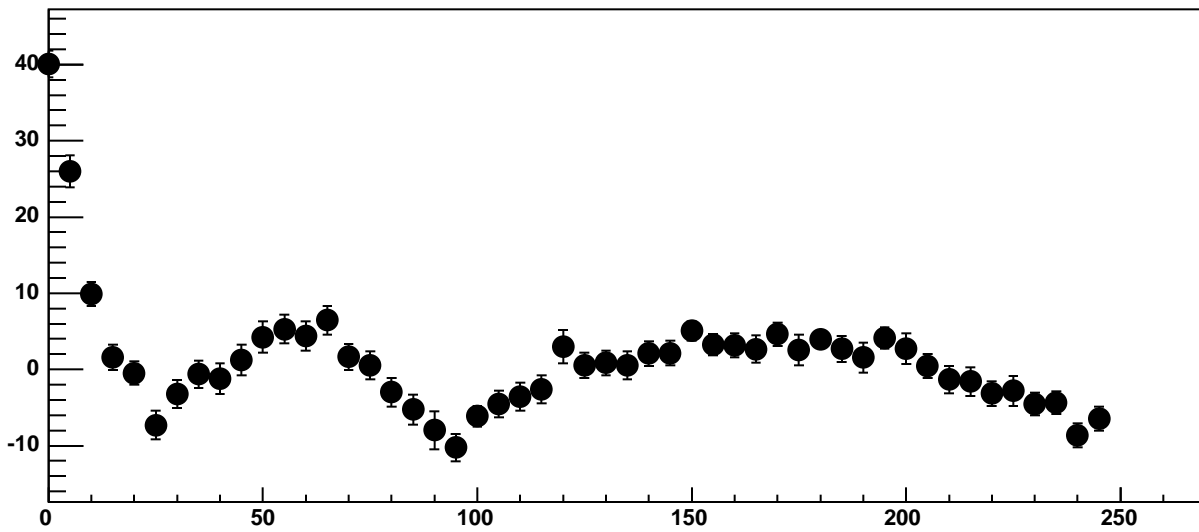


χ^2 / ndf	260.6 / 41
p0	385.8 ± 0.5916
p1	92.54 ± 0.5698
p2	$-5.15e+07 \pm 8.11e+05$
p3	$2.56e+07 \pm 2.98e+05$
p4	1.967 ± 0.02011

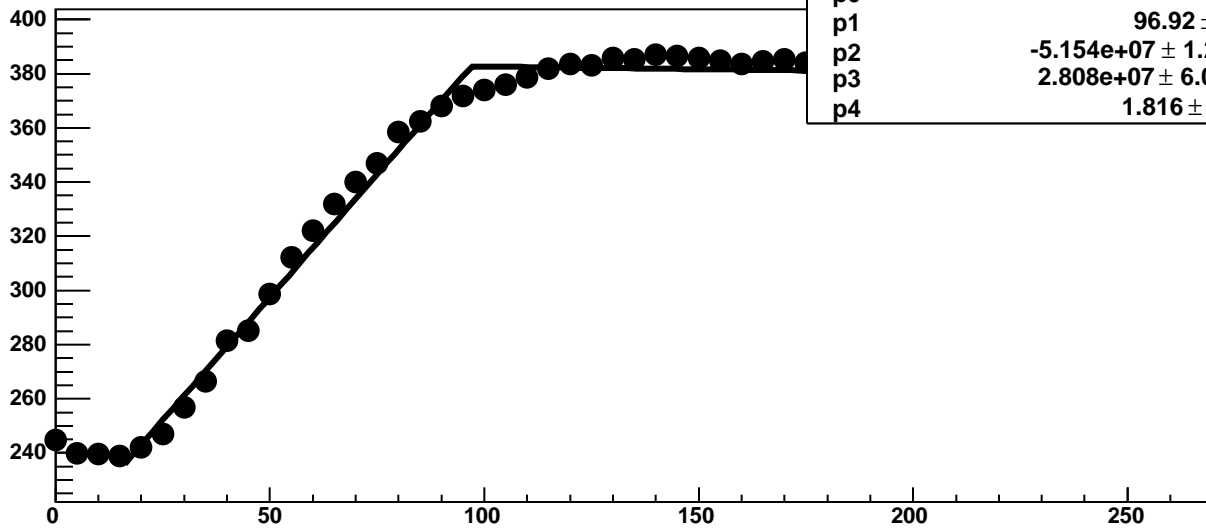
Chip 4, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold

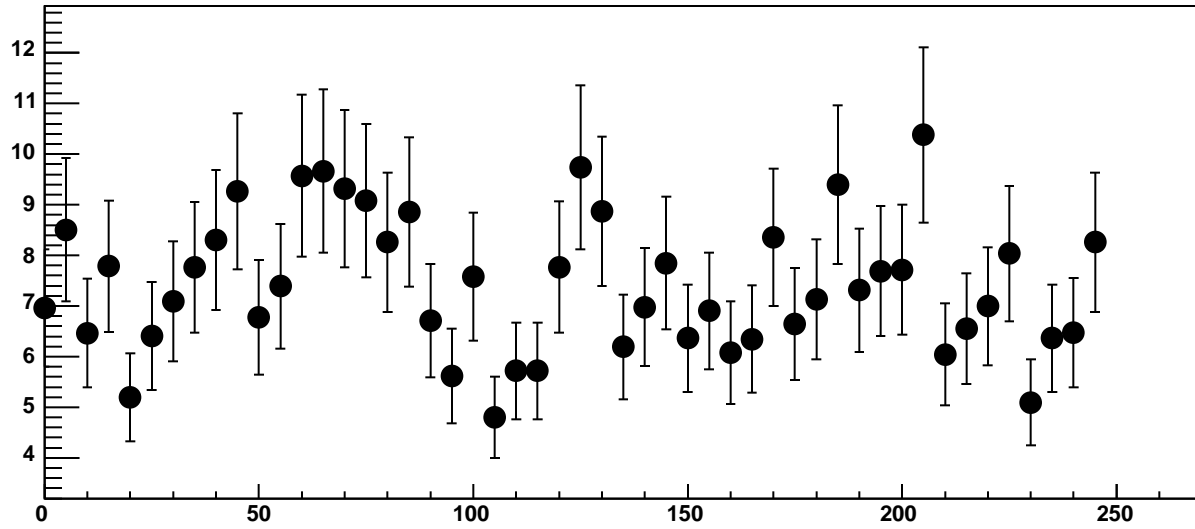


Chip 4, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold

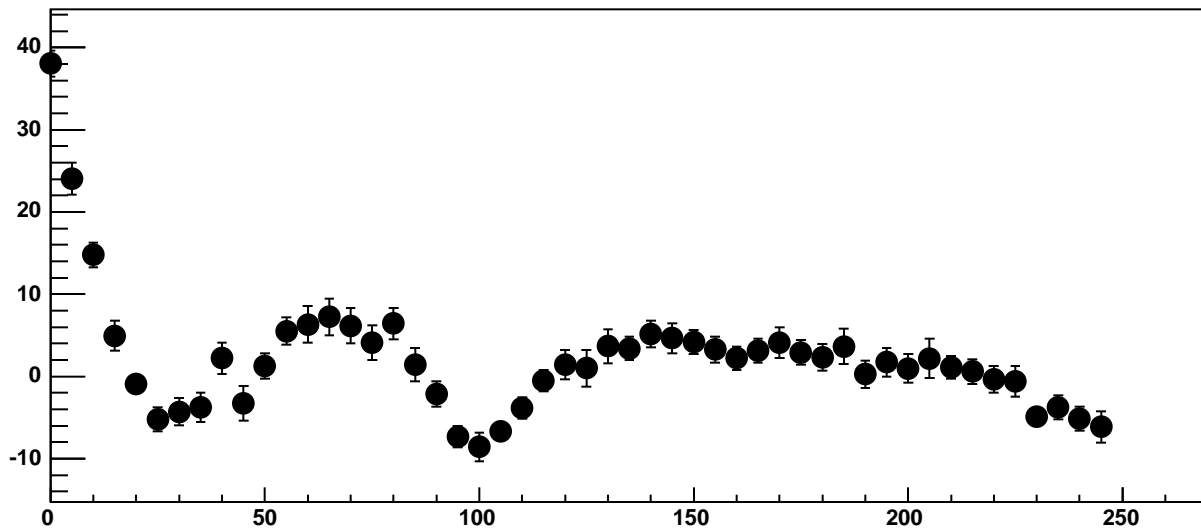


χ^2 / ndf	293.8 / 41
p0	382.7 ± 0.54
p1	96.92 ± 0.5094
p2	$-5.154\text{e}+07 \pm 1.208\text{e}+06$
p3	$2.808\text{e}+07 \pm 6.064\text{e}+05$
p4	1.816 ± 0.01518

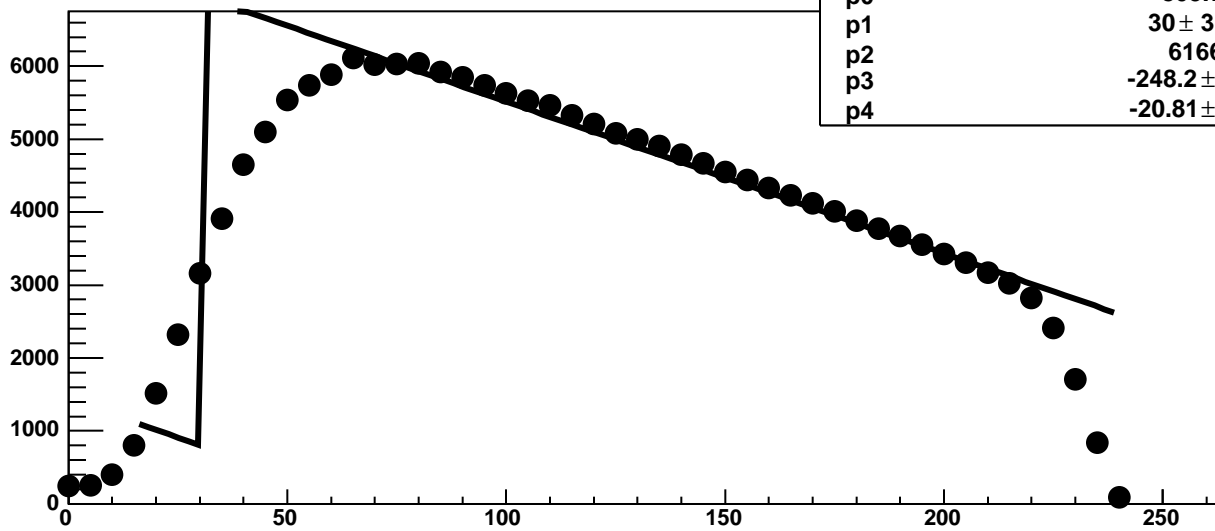
Chip 4, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

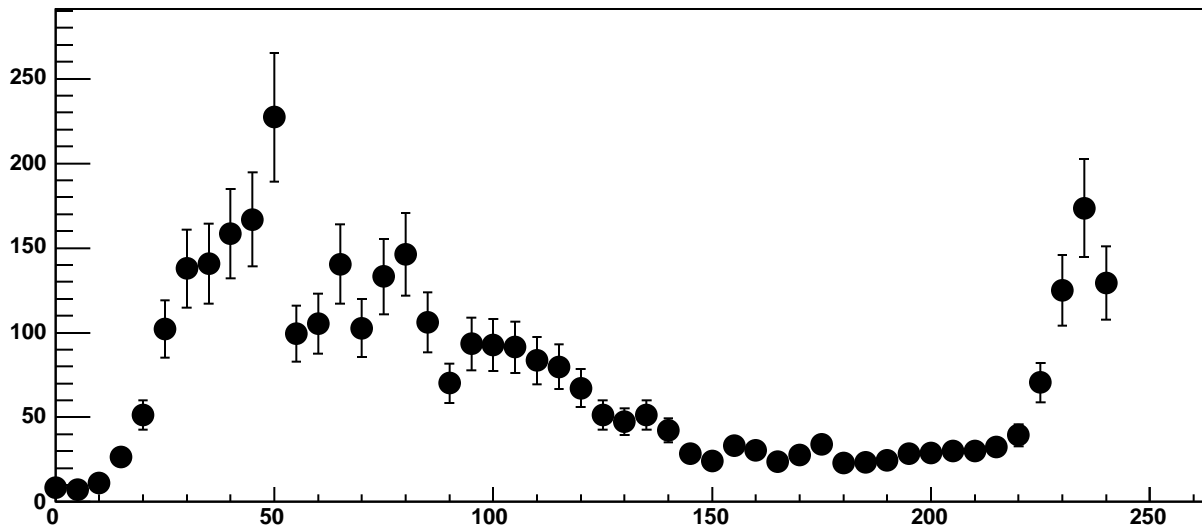


Chip 4, Channel 17, Enable 5, DAC=4000, ADC Mean vs Hold

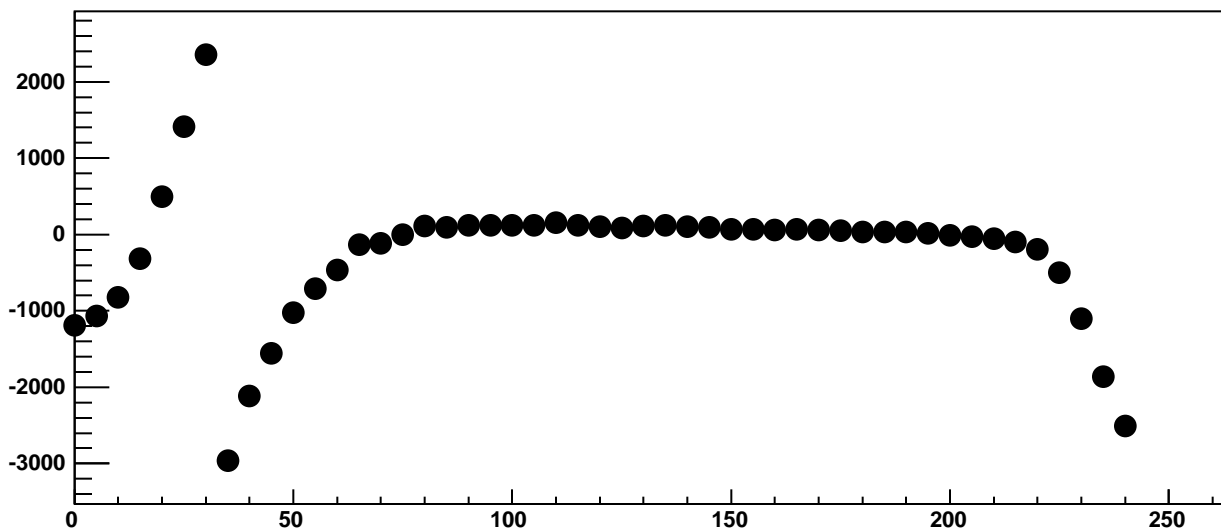


χ^2 / ndf	4.3e+04 / 41
p0	805.7 ± 2.724
p1	30 ± 3.451e-06
p2	6166 ± 1.105
p3	-248.2 ± 0.00037
p4	-20.81 ± 0.03168

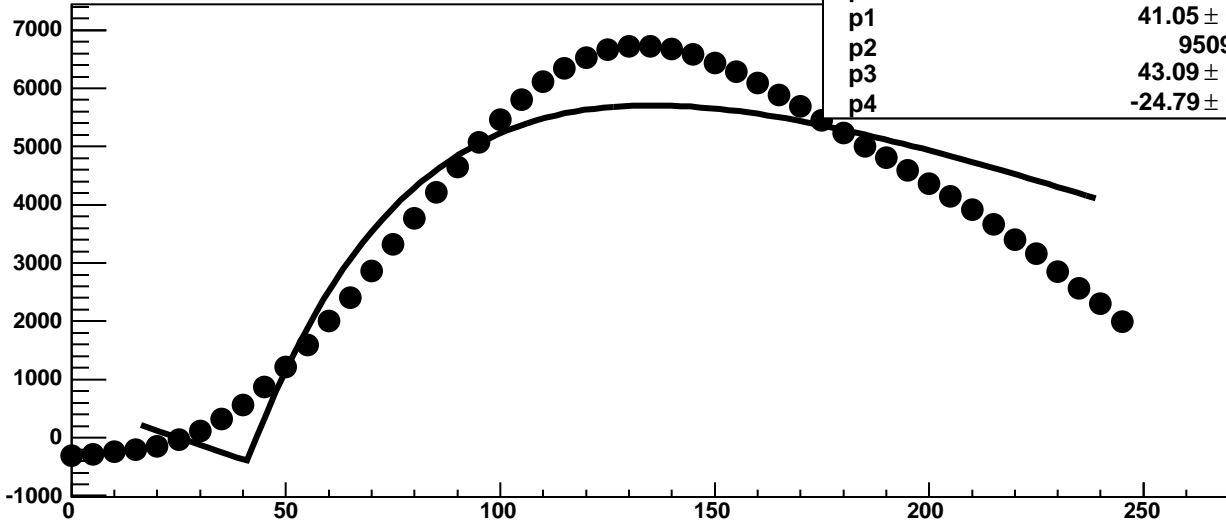
Chip 4, Channel 17, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 4, Channel 17, Enable 5, DAC=4000, ADC Residuals vs Hold

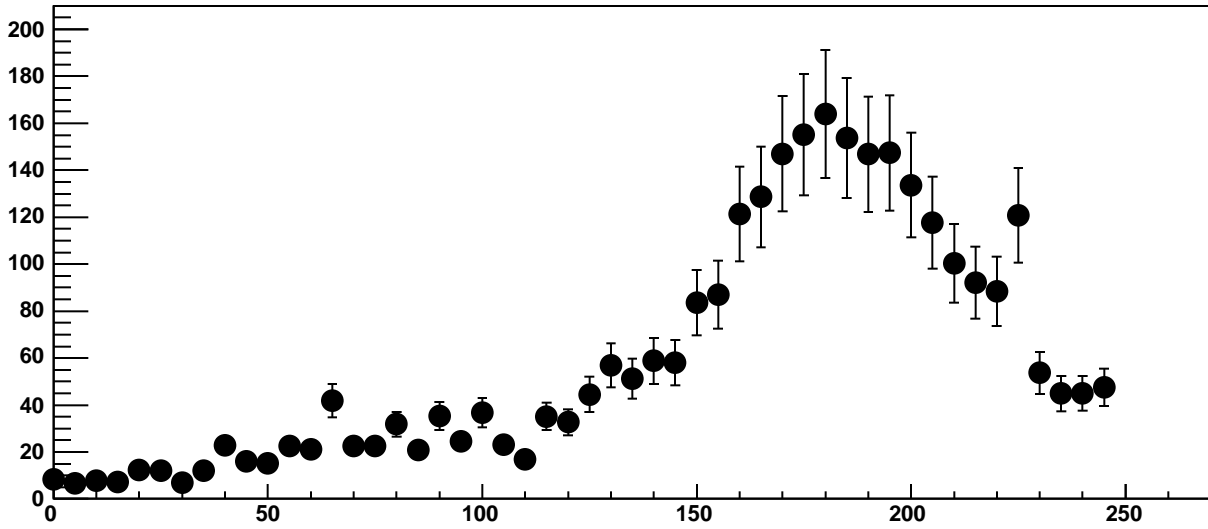


Chip 5, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold

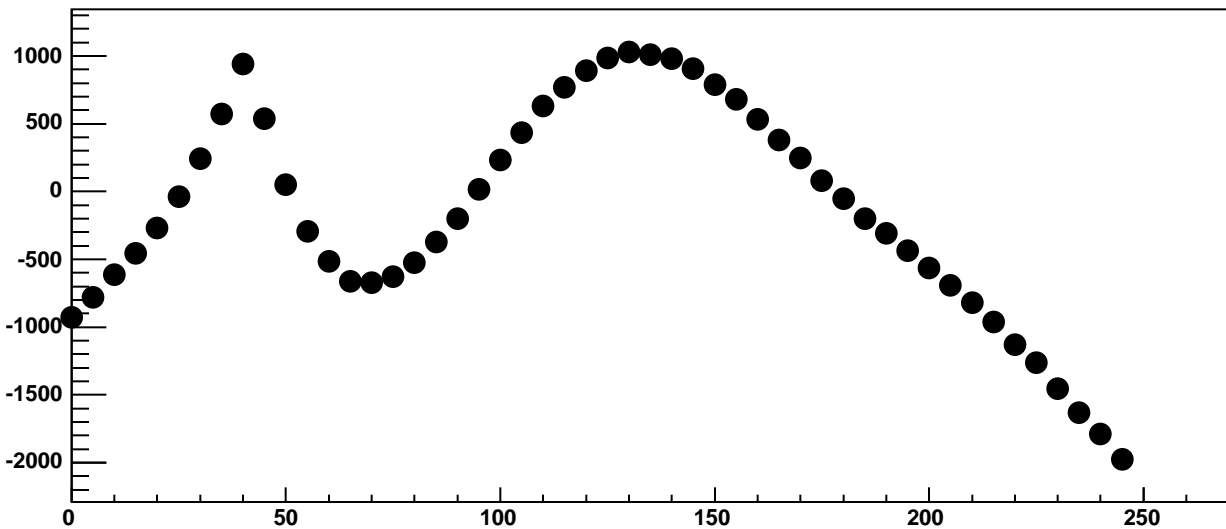


χ^2 / ndf	4.387e+05 / 41
p0	-397.8 ± 1.329
p1	41.05 ± 0.01643
p2	9509 ± 12.8
p3	43.09 ± 0.06804
p4	-24.79 ± 0.06848

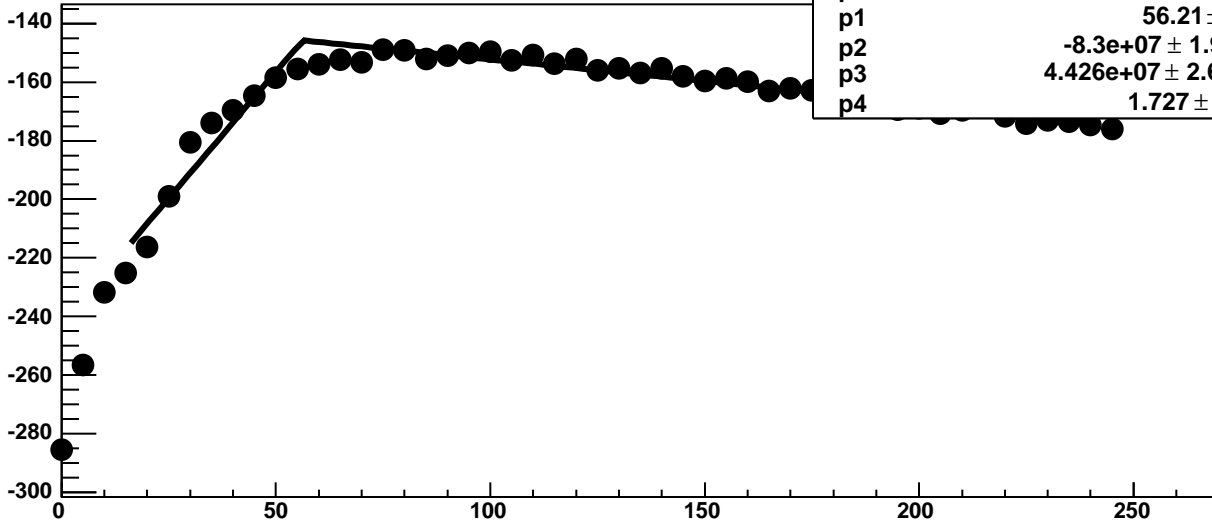
Chip 5, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold

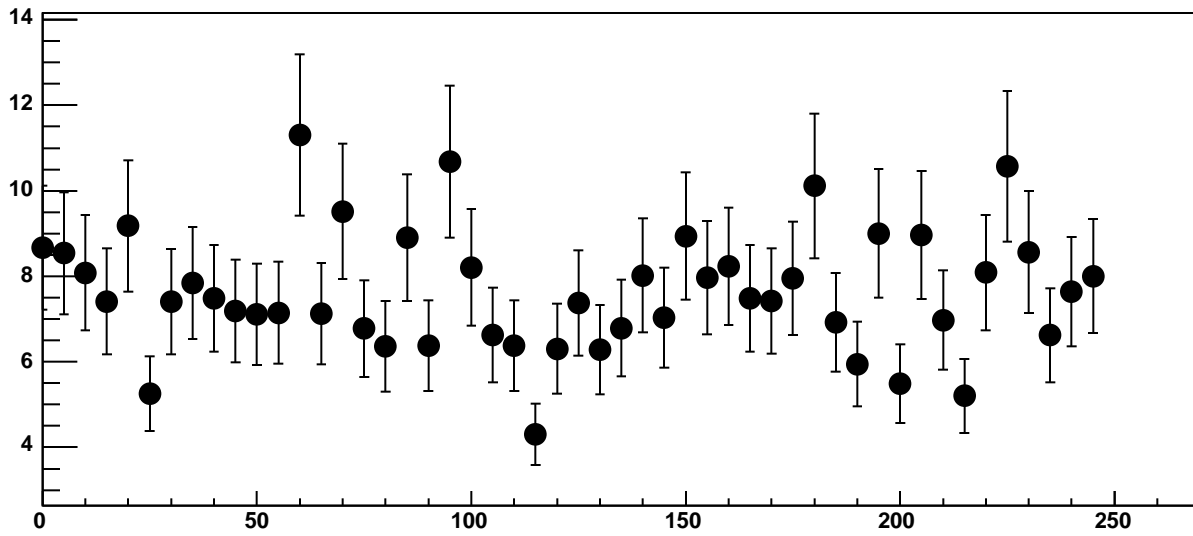


Chip 5, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

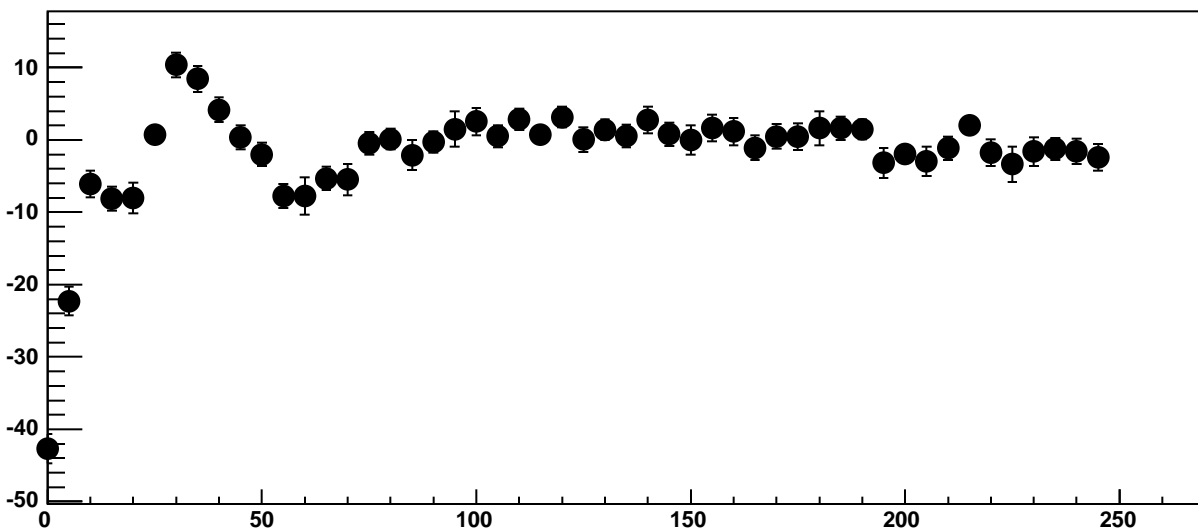


χ^2 / ndf	188.3 / 41
p0	-145.8 ± 0.5223
p1	56.21 ± 0.6415
p2	$-8.3e+07 \pm 1.975e+06$
p3	$4.426e+07 \pm 2.643e+05$
p4	1.727 ± 0.04284

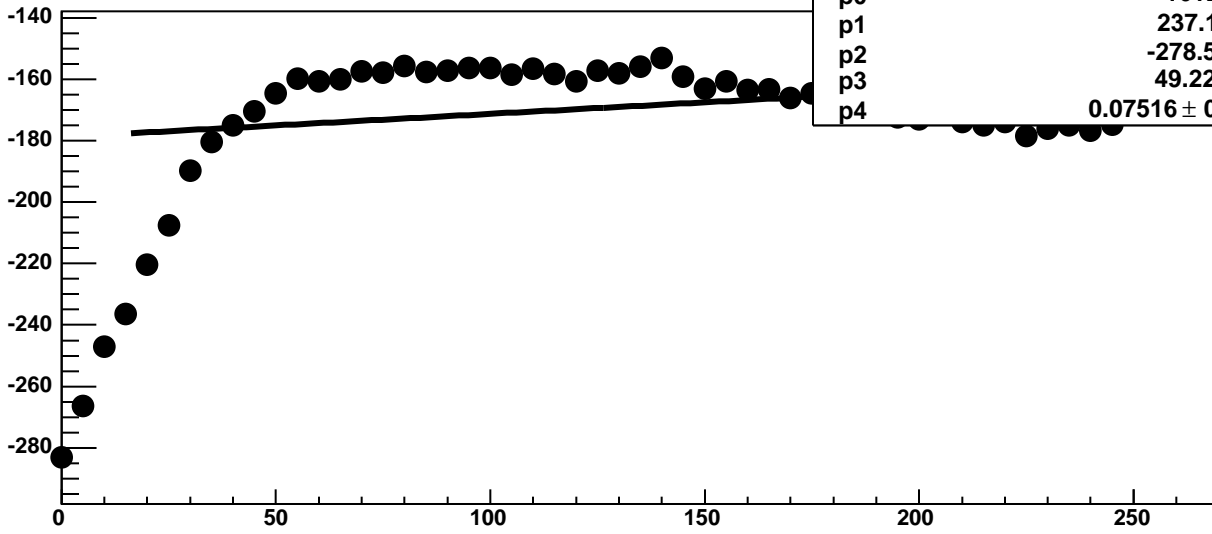
Chip 5, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold

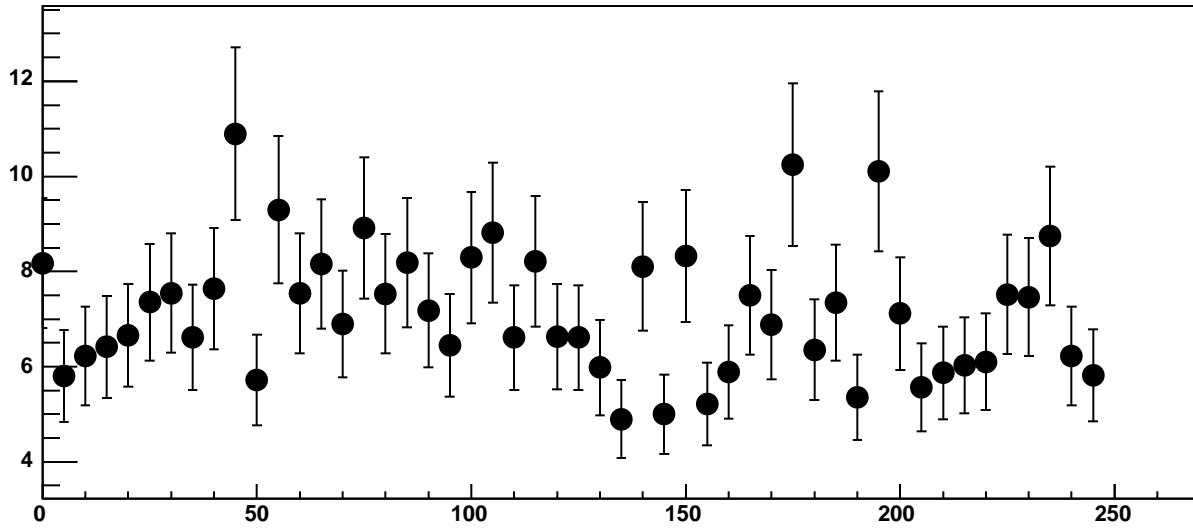


Chip 5, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

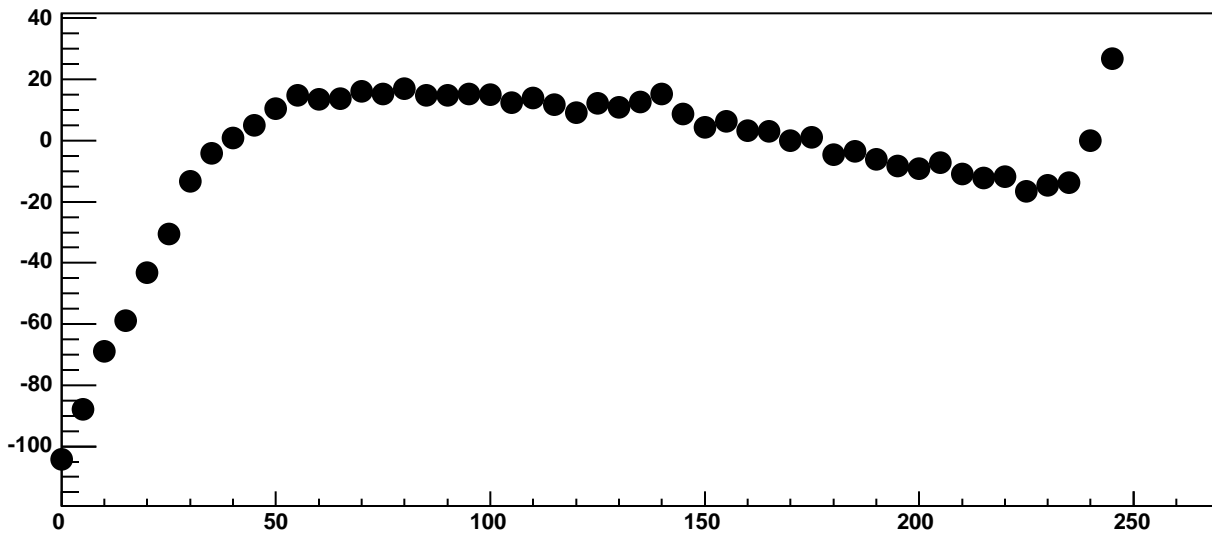


χ^2 / ndf	4811 / 41
p0	-161 ± 0.5006
p1	237.1 ± 2.708
p2	-278.5 ± 248.4
p3	49.22 ± 37.26
p4	0.07516 ± 0.003682

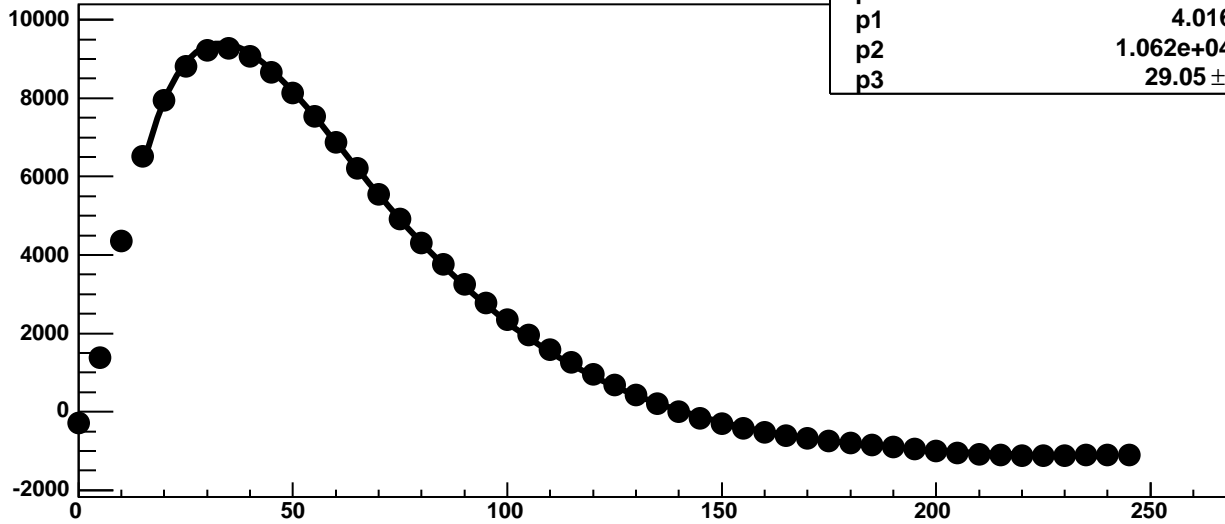
Chip 5, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

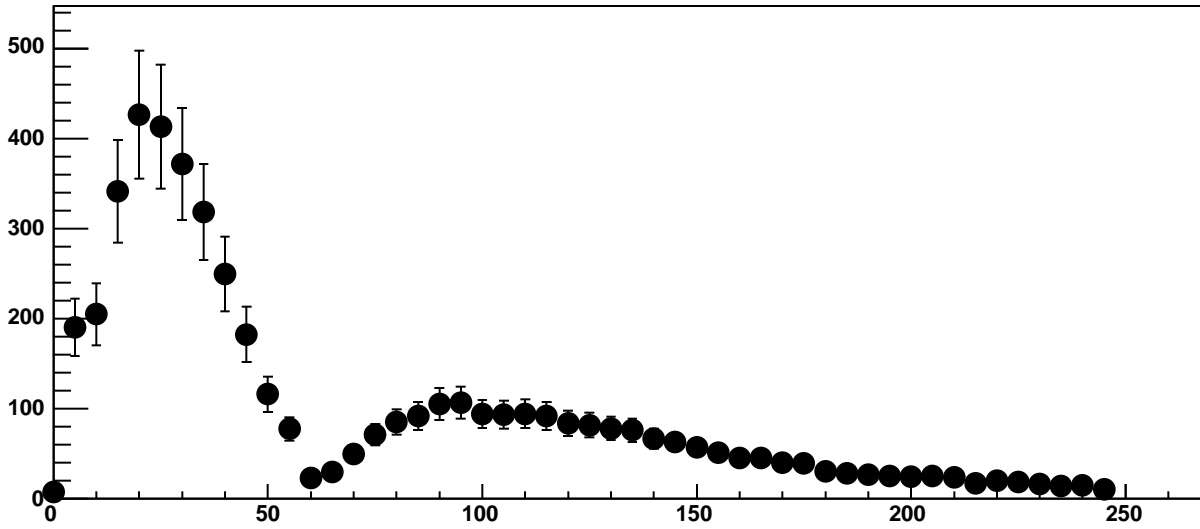


Chip 5, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold

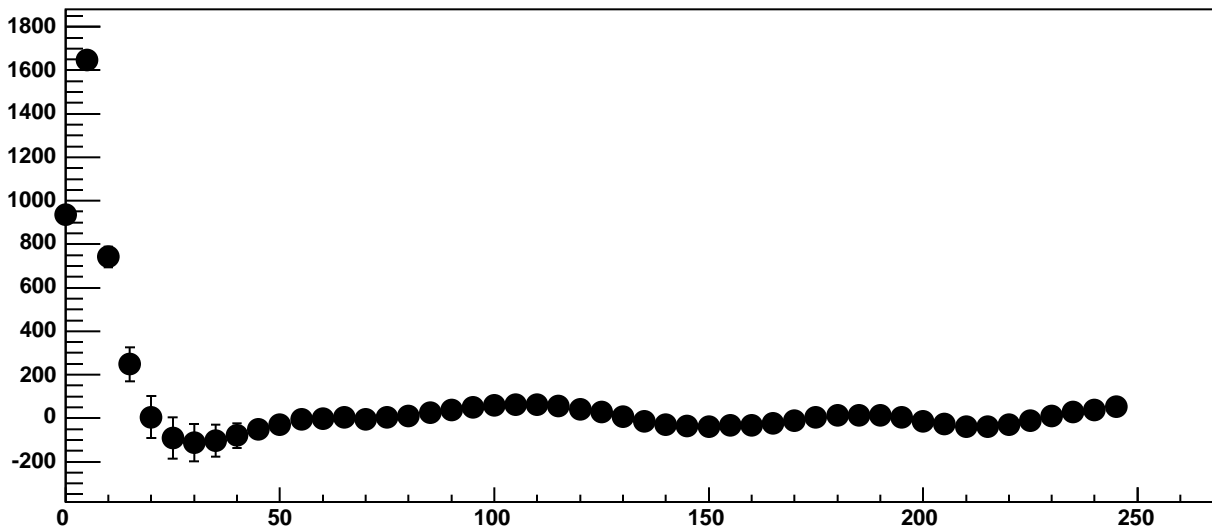


χ^2 / ndf	553.6 / 42
p0	-1215 \pm 1.817
p1	4.016 \pm 0.129
p2	1.062e+04 \pm 17.07
p3	29.05 \pm 0.04426

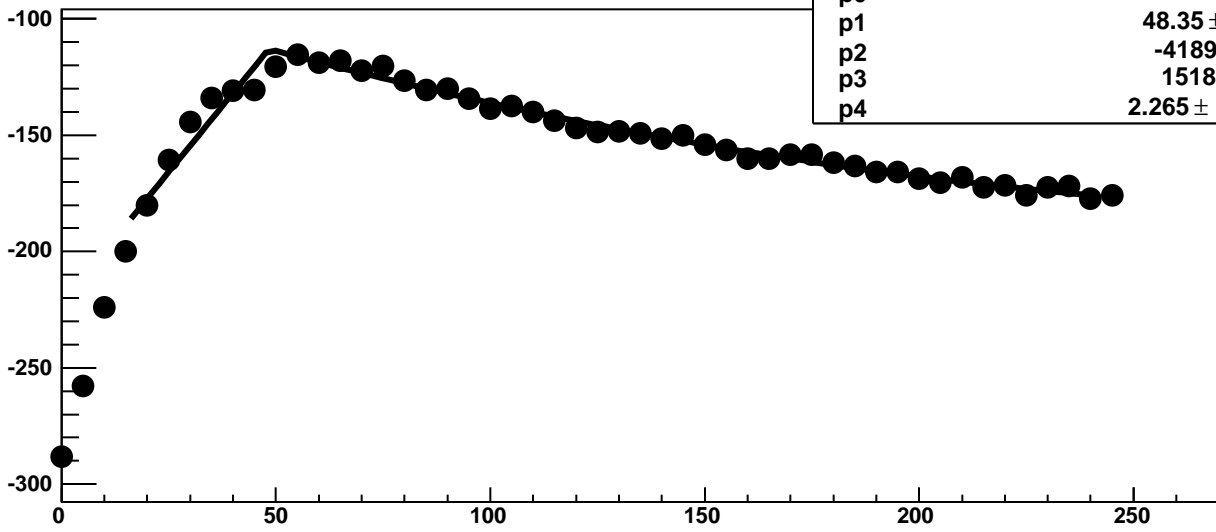
Chip 5, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold

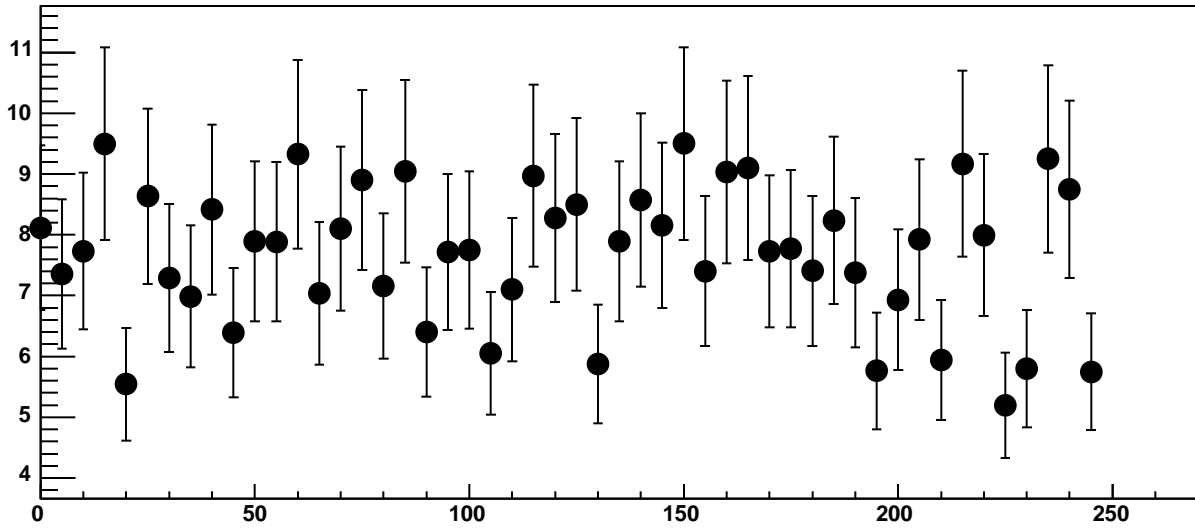


Chip 5, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

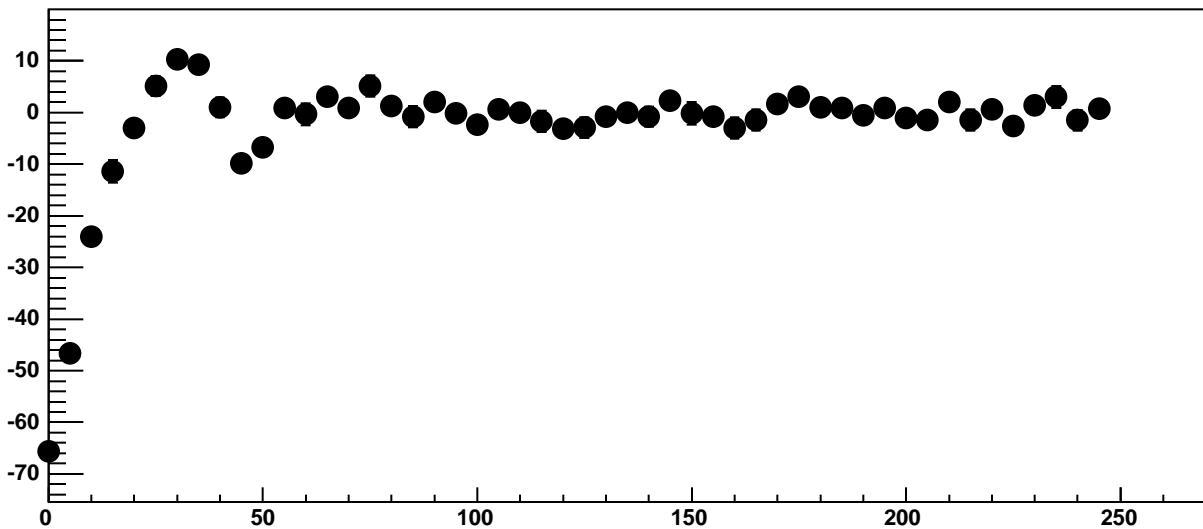


χ^2 / ndf	214.4 / 41
p0	-113 ± 0.6926
p1	48.35 ± 0.5219
p2	-4189 ± 395.2
p3	1518 ± 142.9
p4	2.265 ± 0.06167

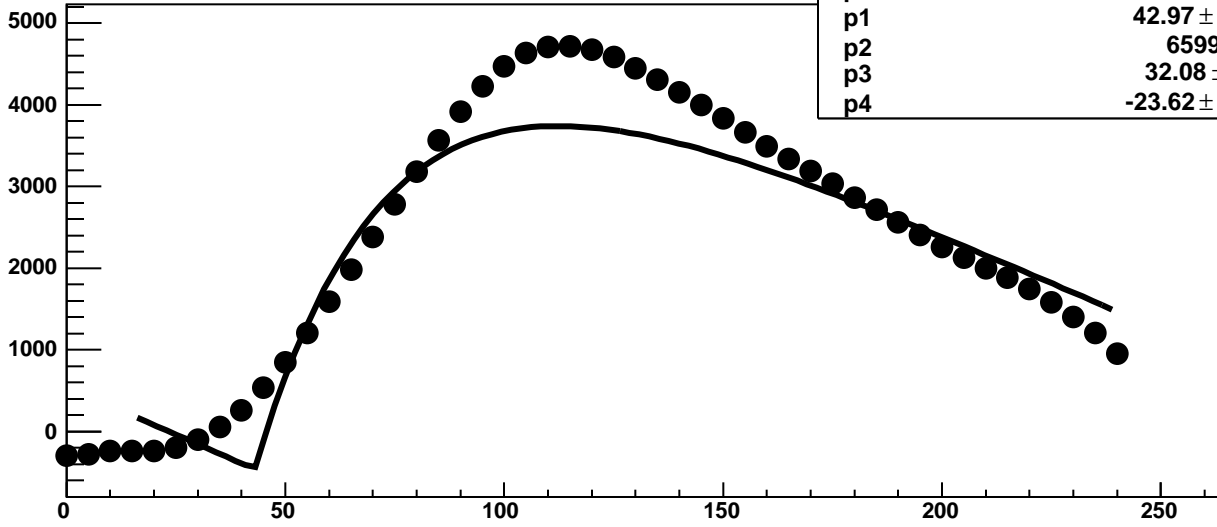
Chip 5, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

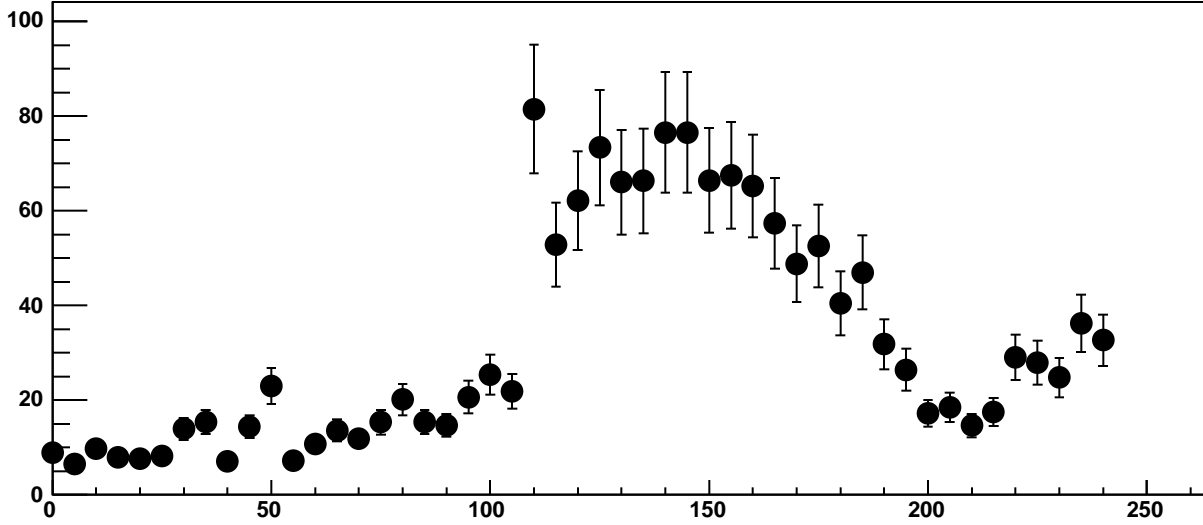


Chip 5, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold

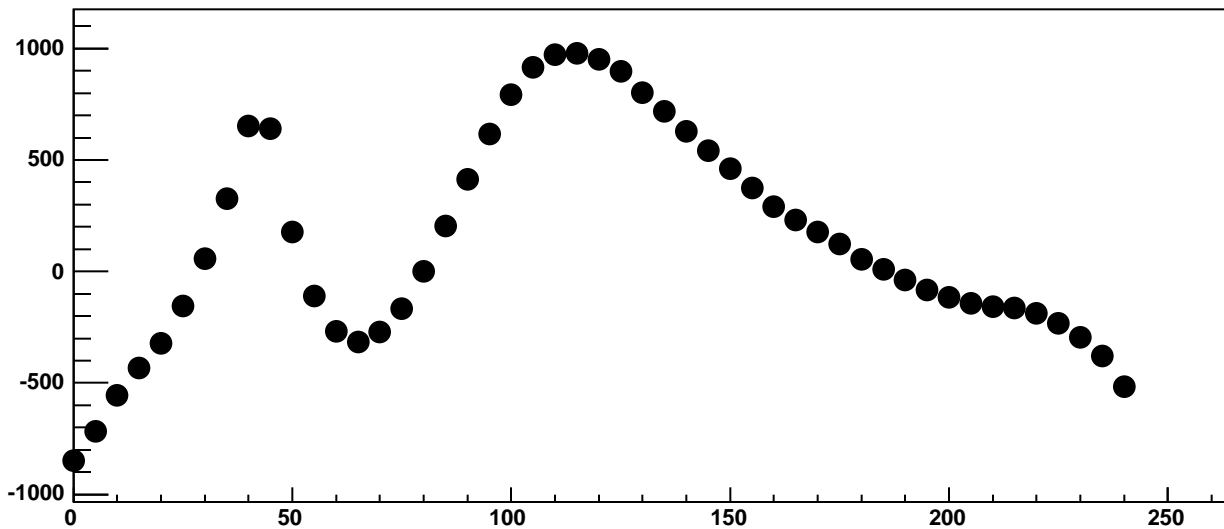


χ^2 / ndf	4.718e+05 / 41
p0	-460.7 ± 0.9489
p1	42.97 ± 0.01378
p2	6599 ± 7.195
p3	32.08 ± 0.0446
p4	-23.62 ± 0.03923

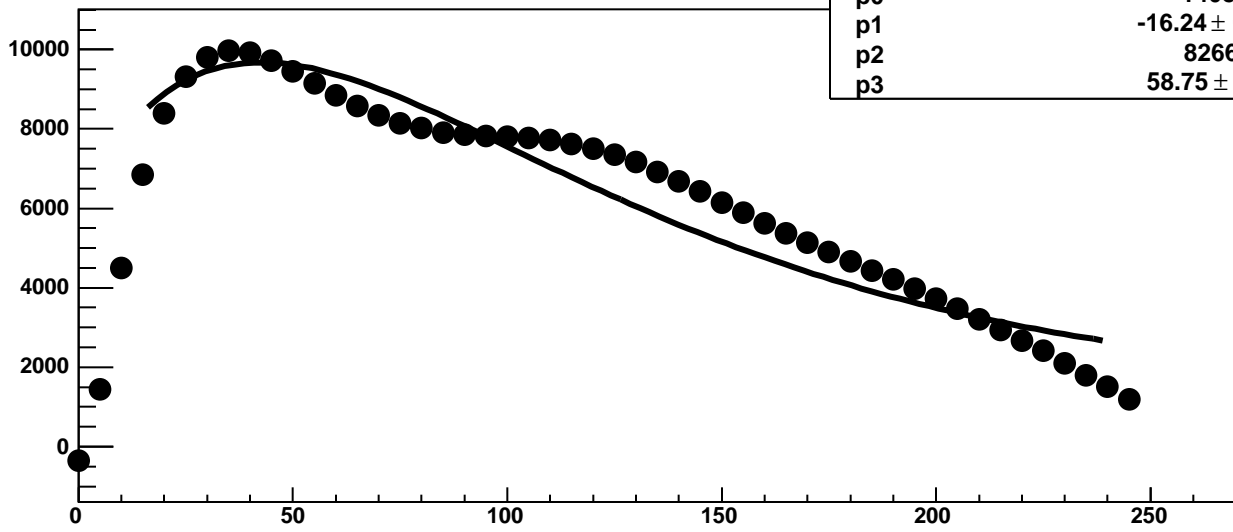
Chip 5, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold

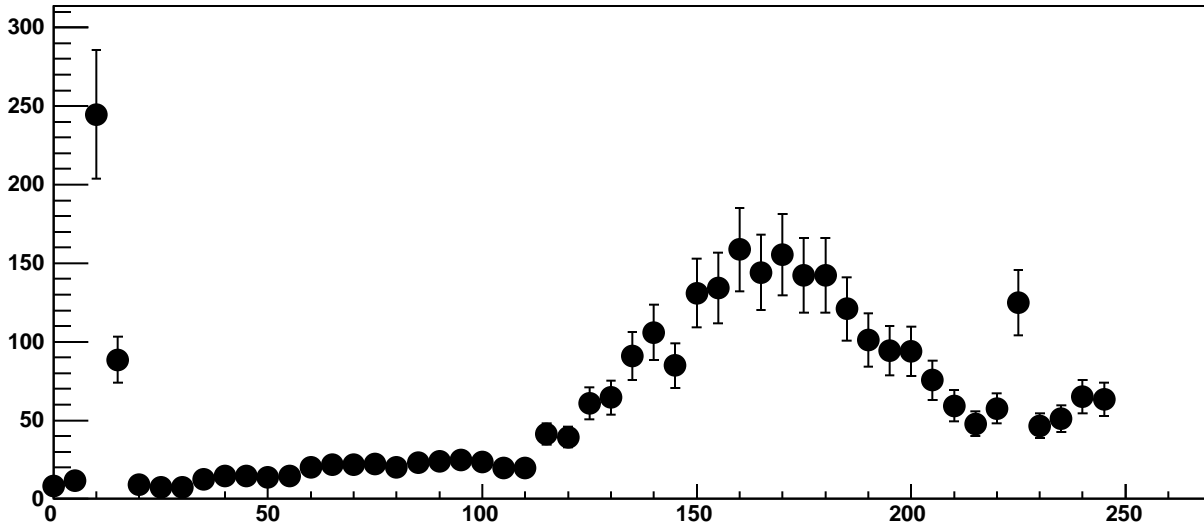


Chip 5, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold

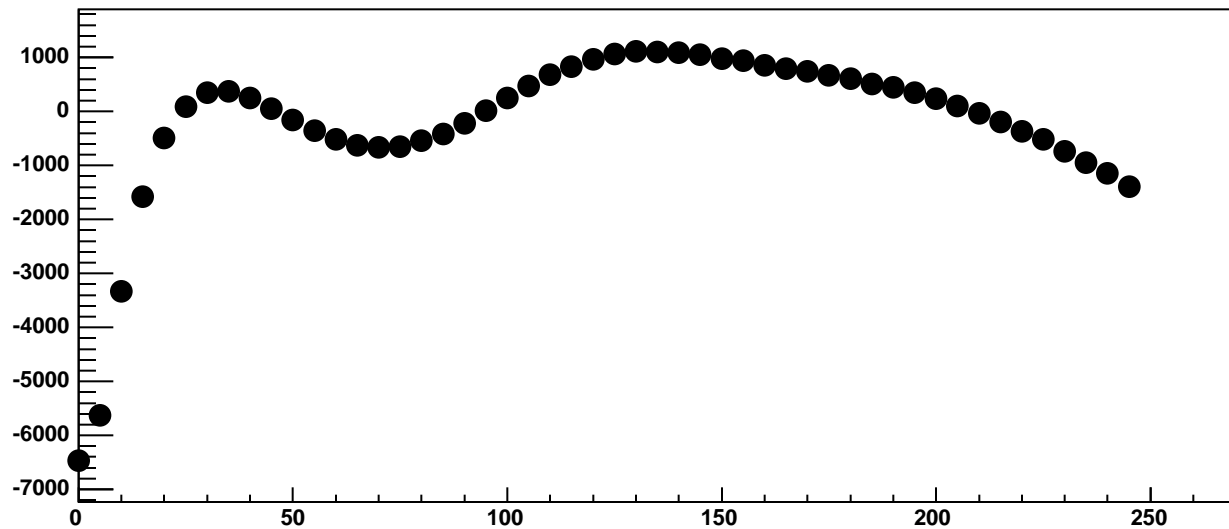


χ^2 / ndf	3.262e+05 / 42
p0	1408 ± 11.78
p1	-16.24 ± 0.09533
p2	8266 ± 11.21
p3	58.75 ± 0.09421

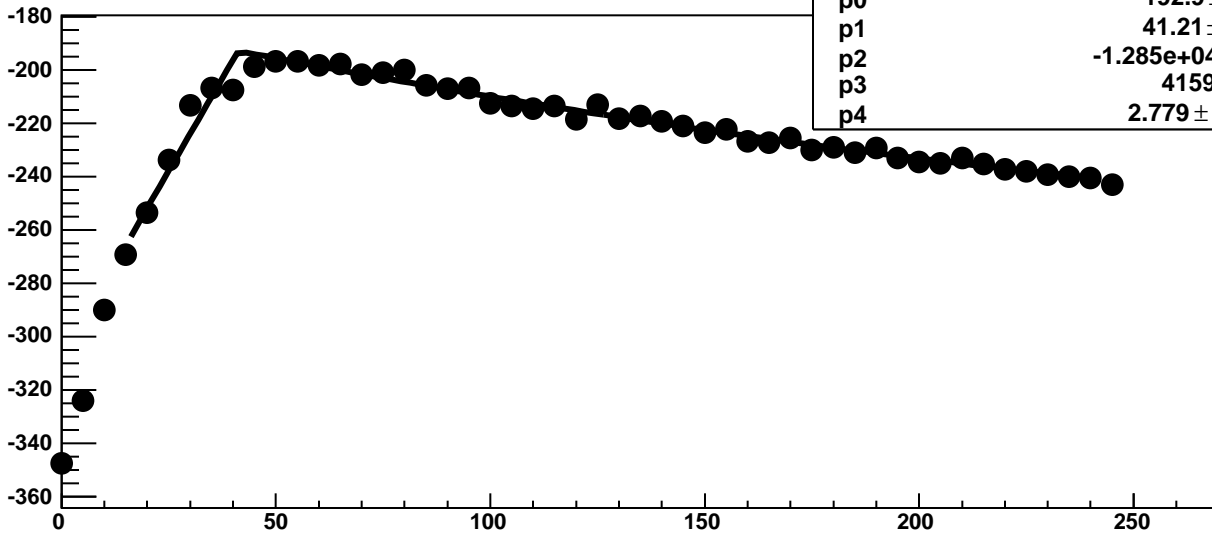
Chip 5, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold

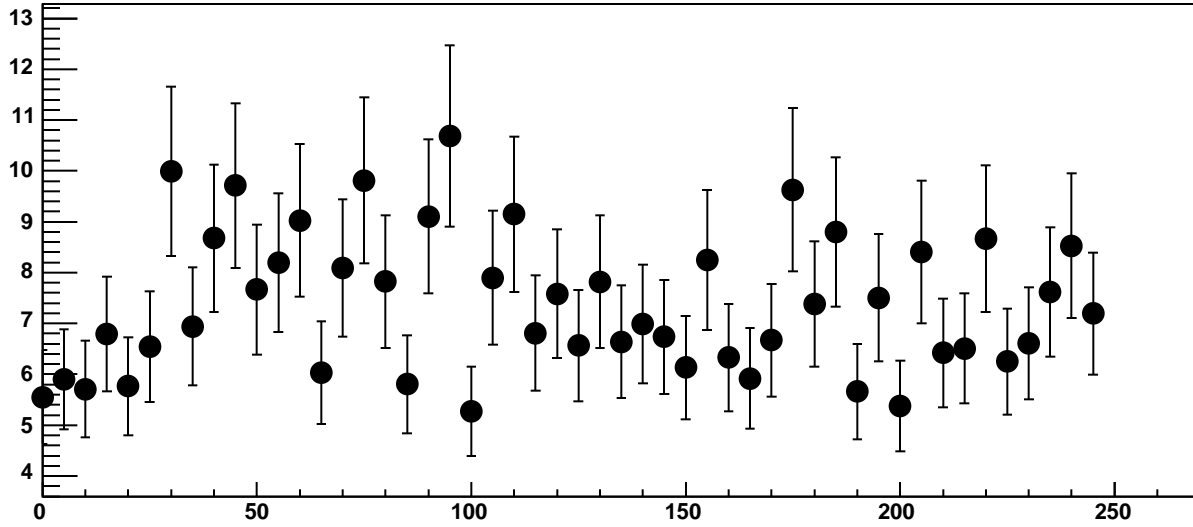


Chip 5, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

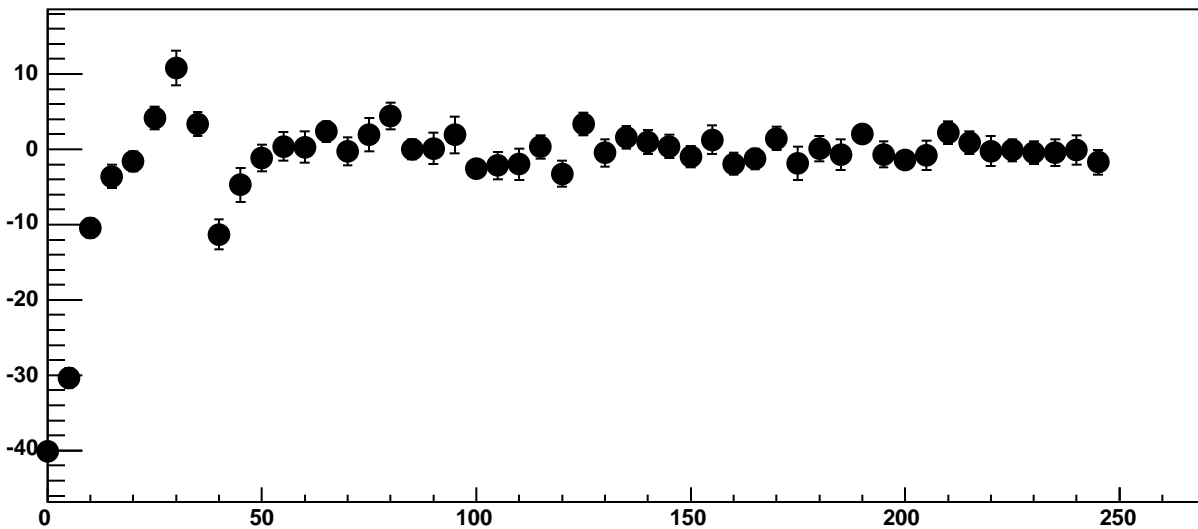


χ^2 / ndf	118.1 / 41
p0	-192.9 ± 0.8469
p1	41.21 ± 0.5424
p2	$-1.285\text{e}+04 \pm 3101$
p3	4159 ± 1014
p4	2.779 ± 0.08054

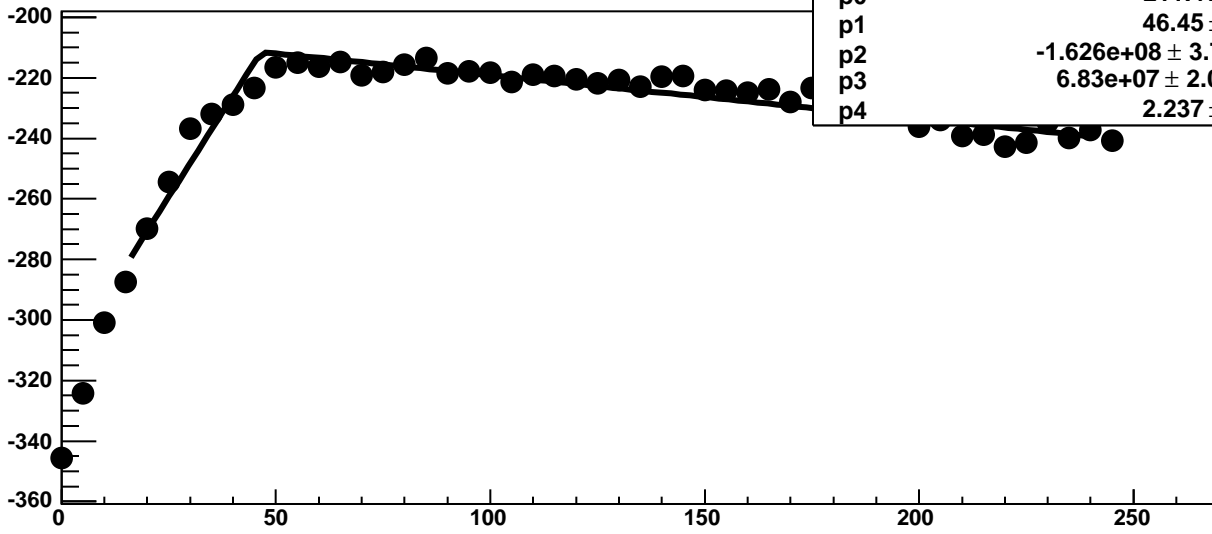
Chip 5, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

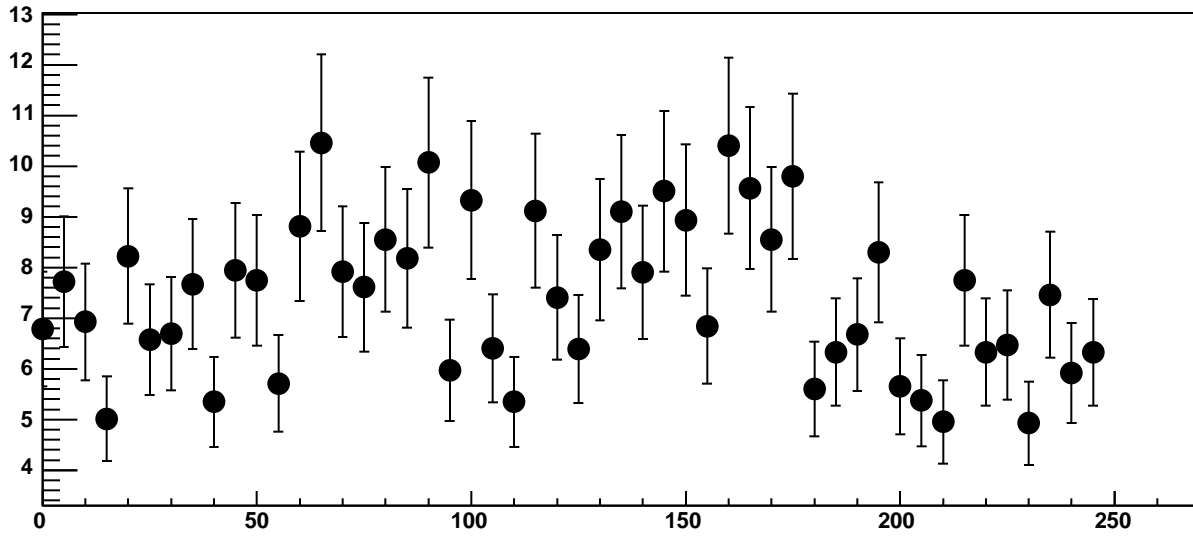


Chip 5, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

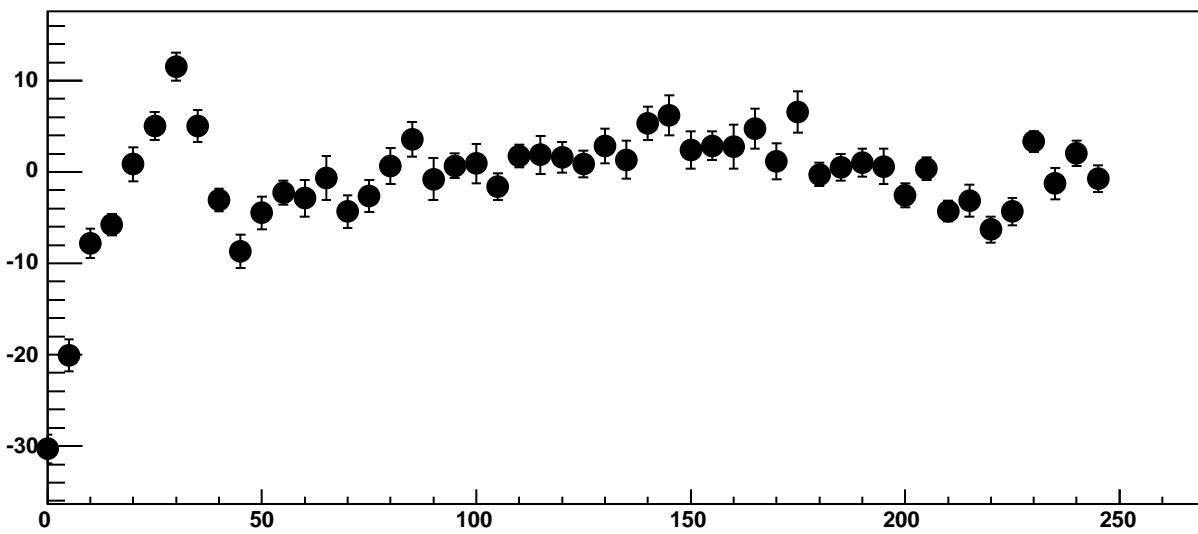


χ^2 / ndf	259.2 / 41
p0	-211.4 ± 0.5139
p1	46.45 ± 0.5149
p2	$-1.626\text{e}+08 \pm 3.705\text{e}+06$
p3	$6.83\text{e}+07 \pm 2.076\text{e}+05$
p4	2.237 ± 0.0537

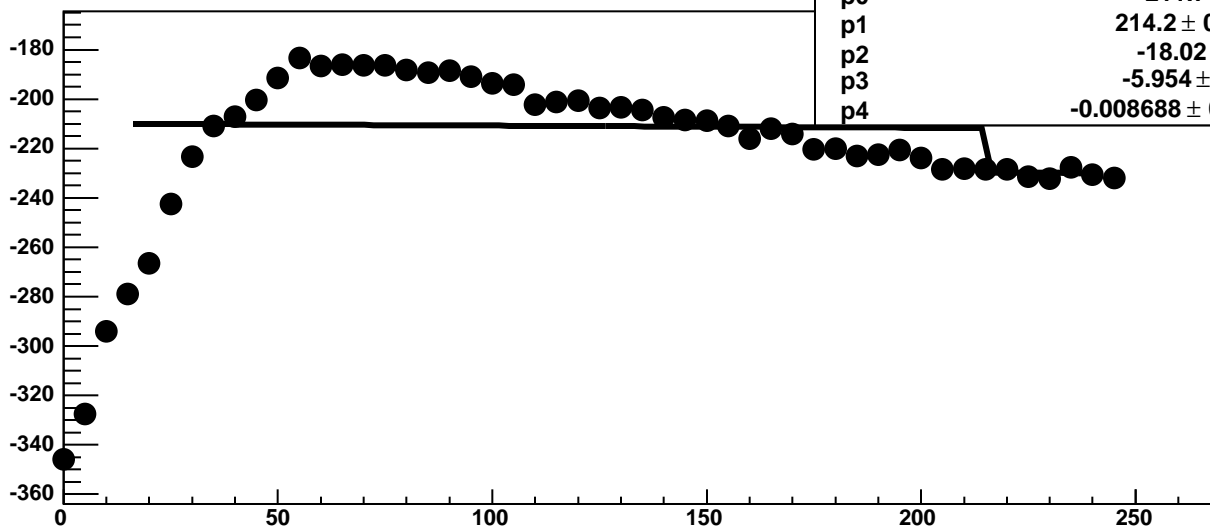
Chip 5, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

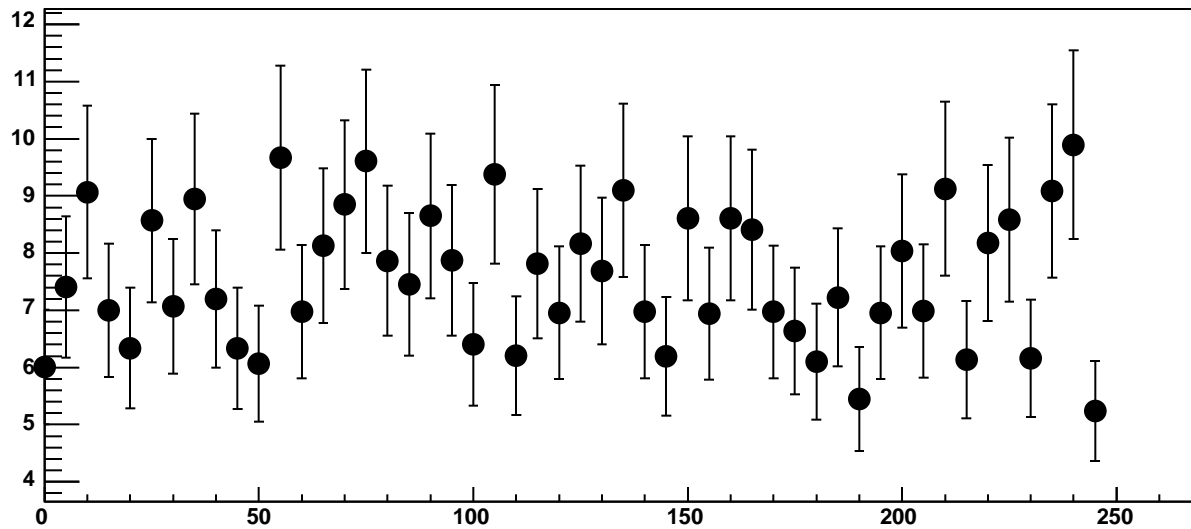


Chip 5, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold

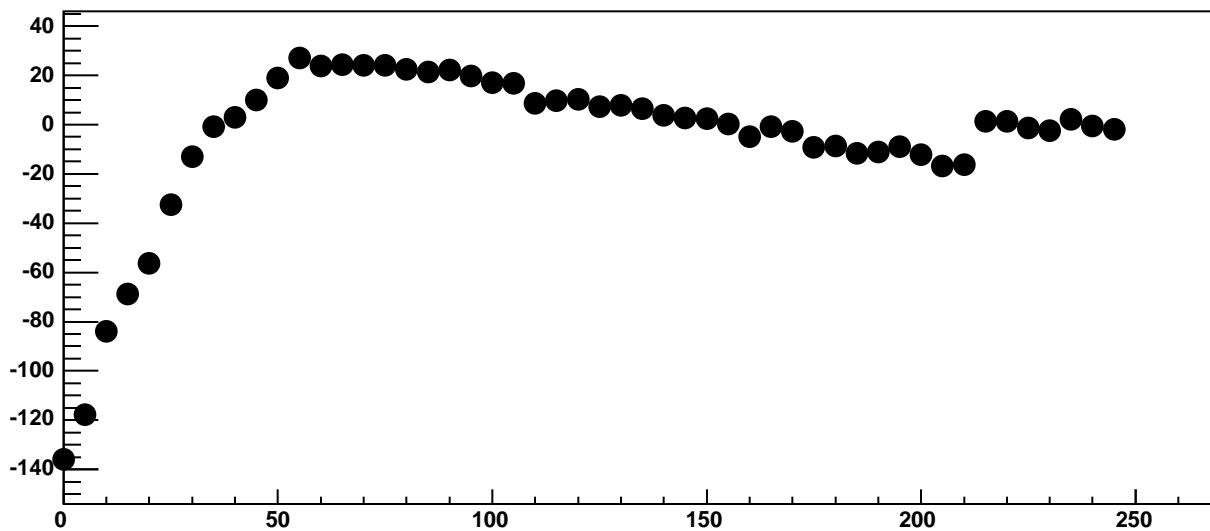


χ^2 / ndf	6095 / 41
p0	-211.7 ± 0.3807
p1	214.2 ± 0.006799
p2	-18.02 ± 0.8408
p3	-5.954 ± 0.08536
p4	-0.008688 ± 0.003421

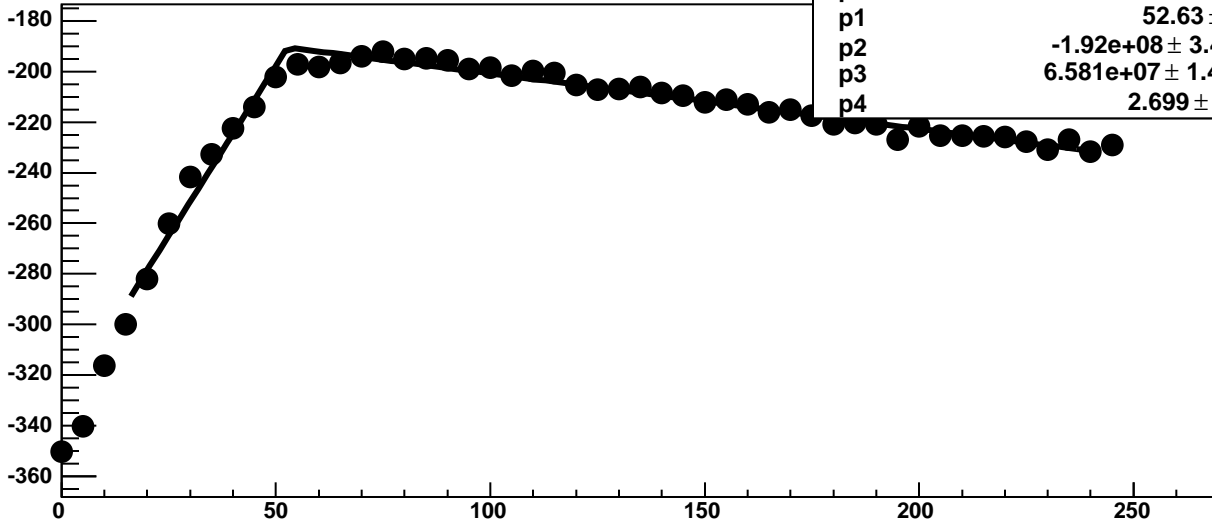
Chip 5, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold

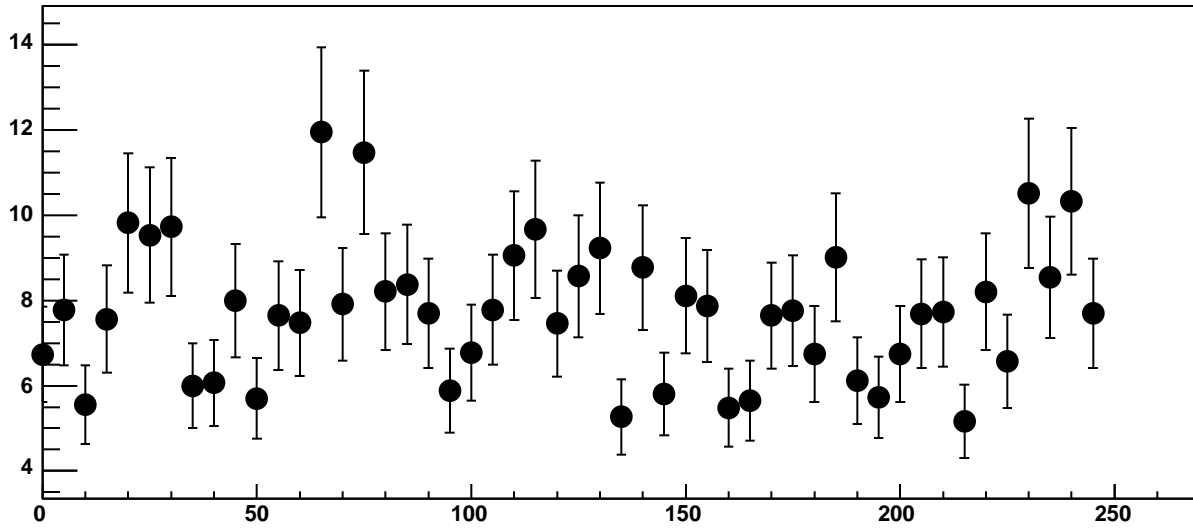


Chip 5, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

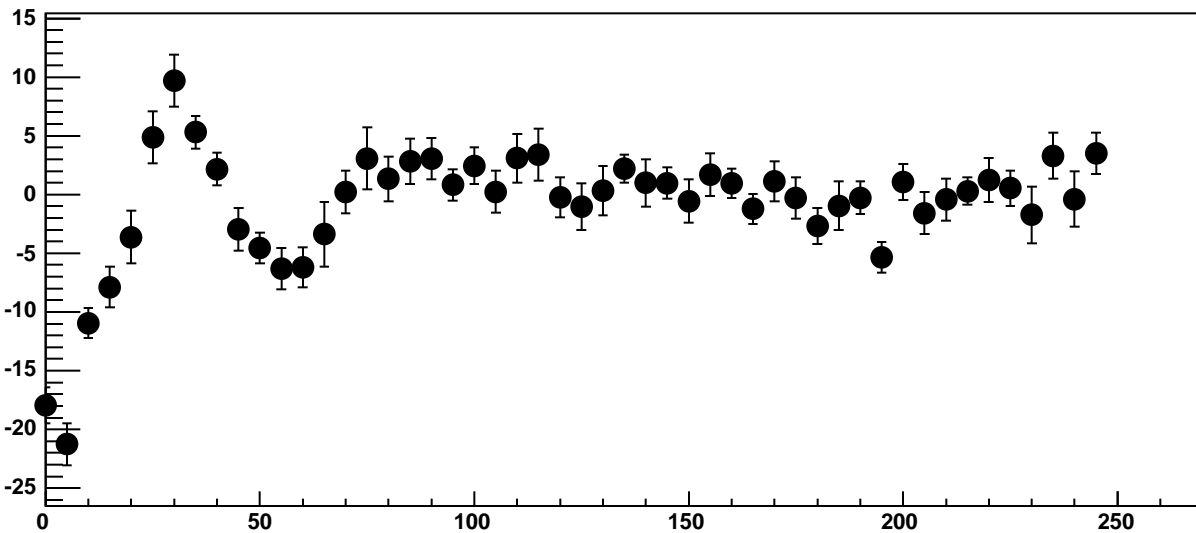


χ^2 / ndf	153.5 / 41
p0	-190.4 ± 0.5546
p1	52.63 ± 0.4153
p2	$-1.92\text{e}+08 \pm 3.412\text{e}+06$
p3	$6.581\text{e}+07 \pm 1.459\text{e}+05$
p4	2.699 ± 0.05148

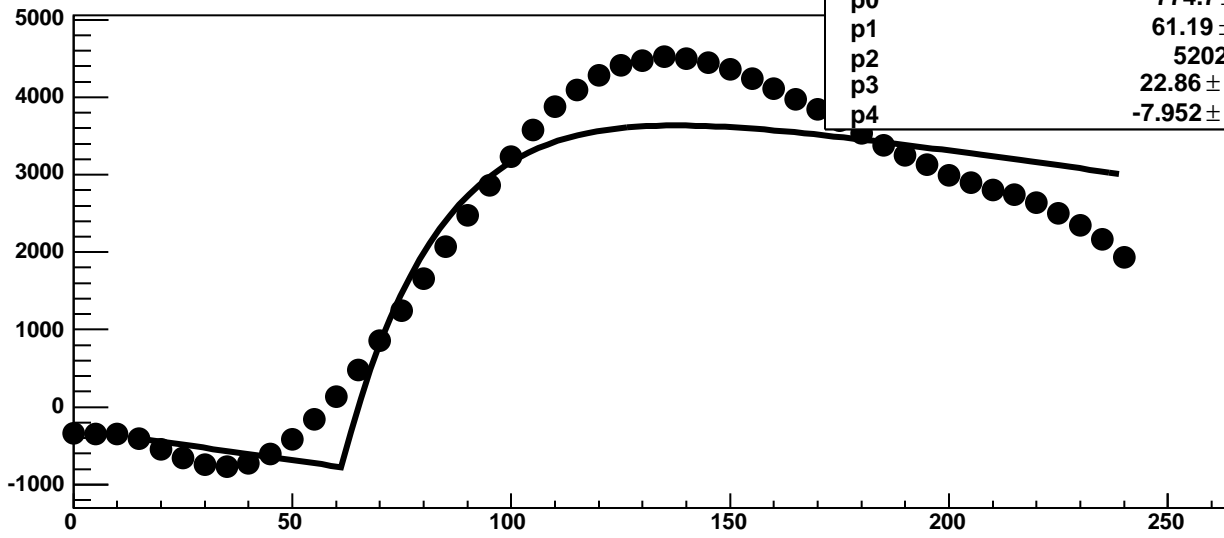
Chip 5, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

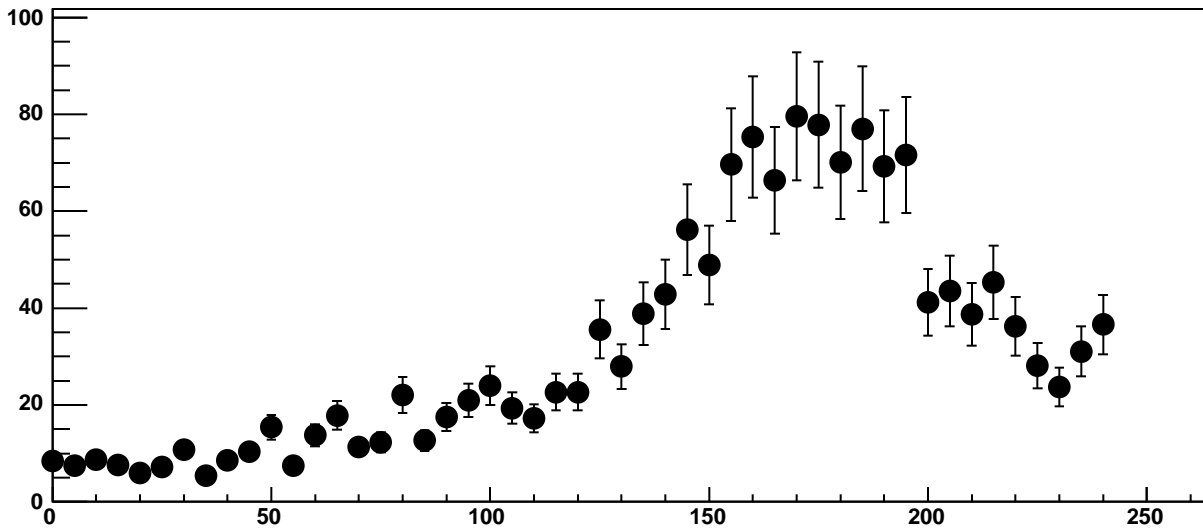


Chip 5, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

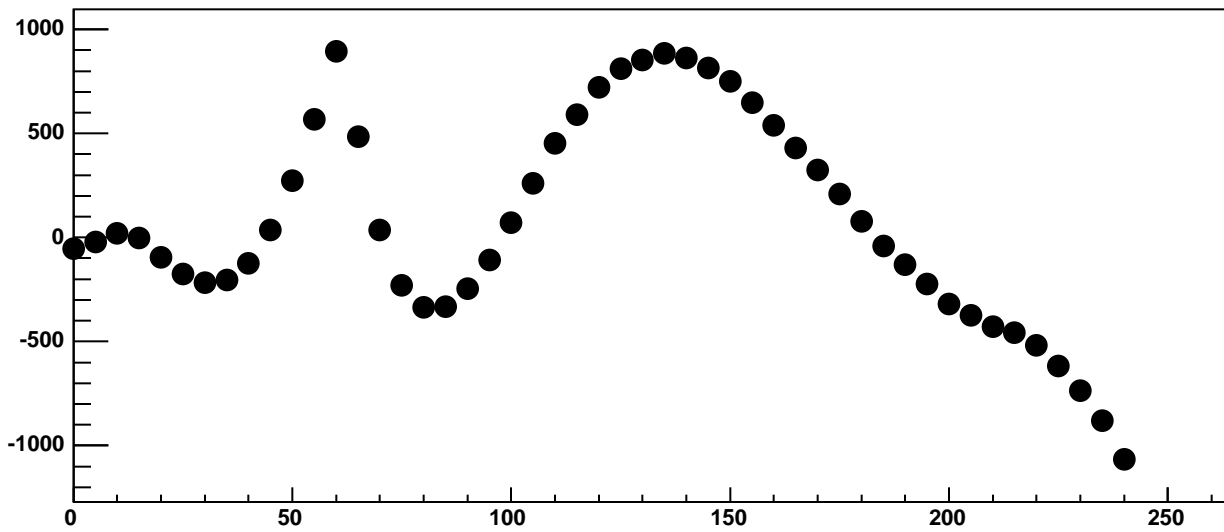


χ^2 / ndf	4.684e+05 / 41
p0	-774.7 ± 0.9813
p1	61.19 ± 0.0174
p2	5202 ± 4.919
p3	22.86 ± 0.03695
p4	-7.952 ± 0.02977

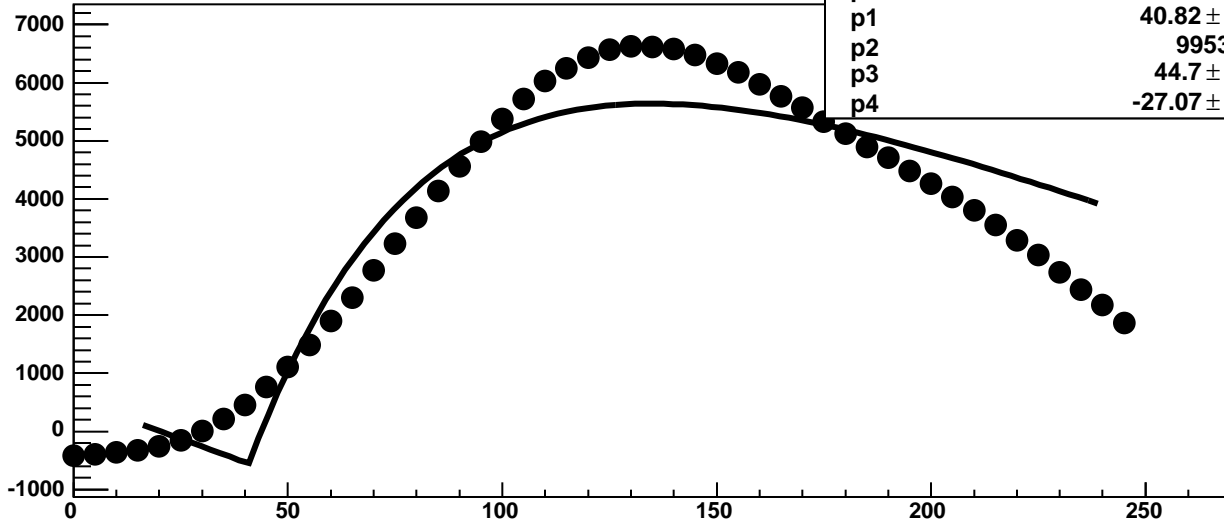
Chip 5, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



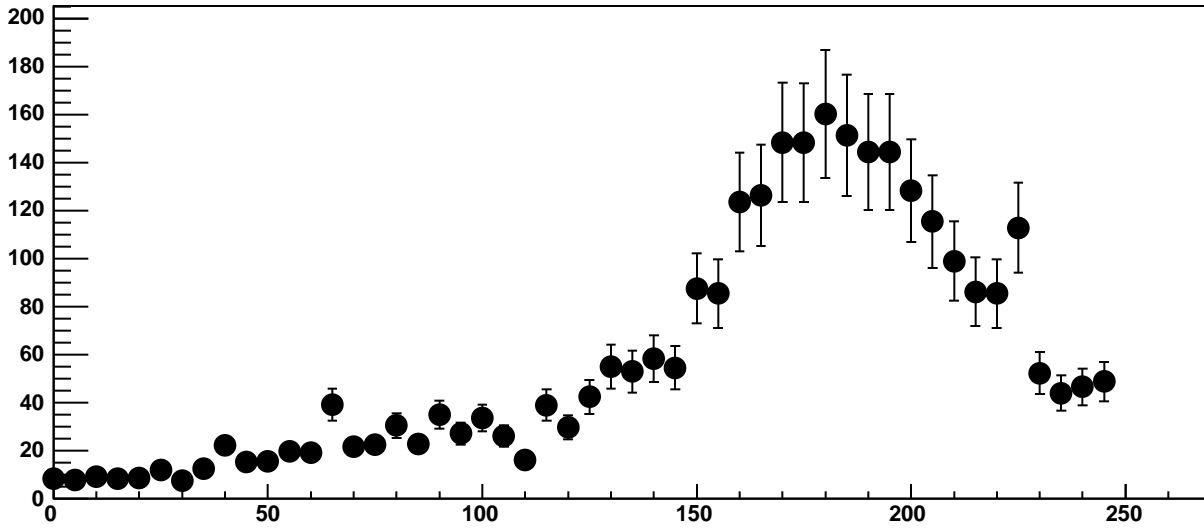
Chip 5, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold



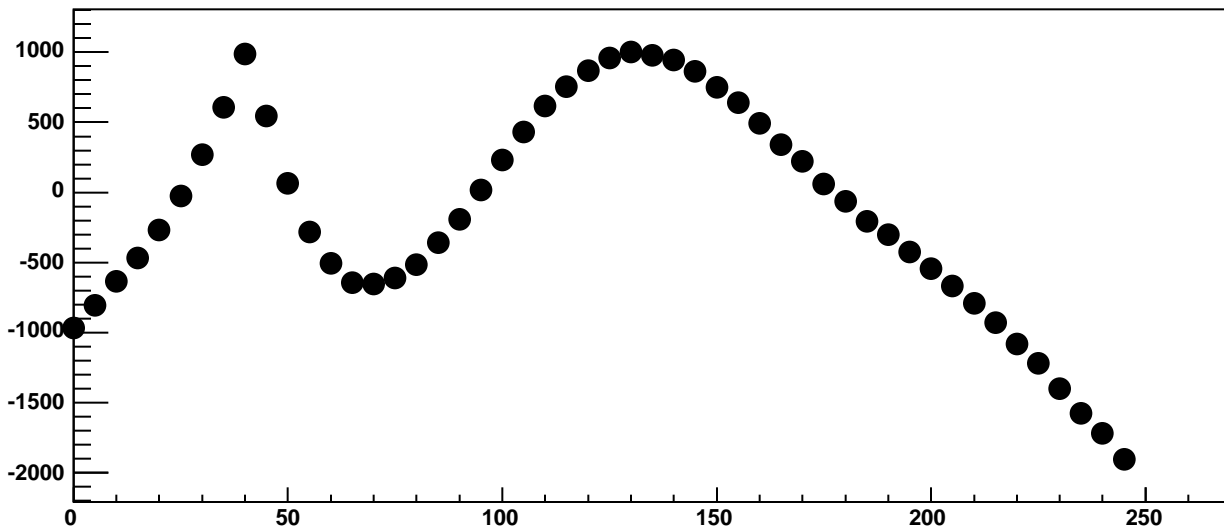
Chip 5, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold



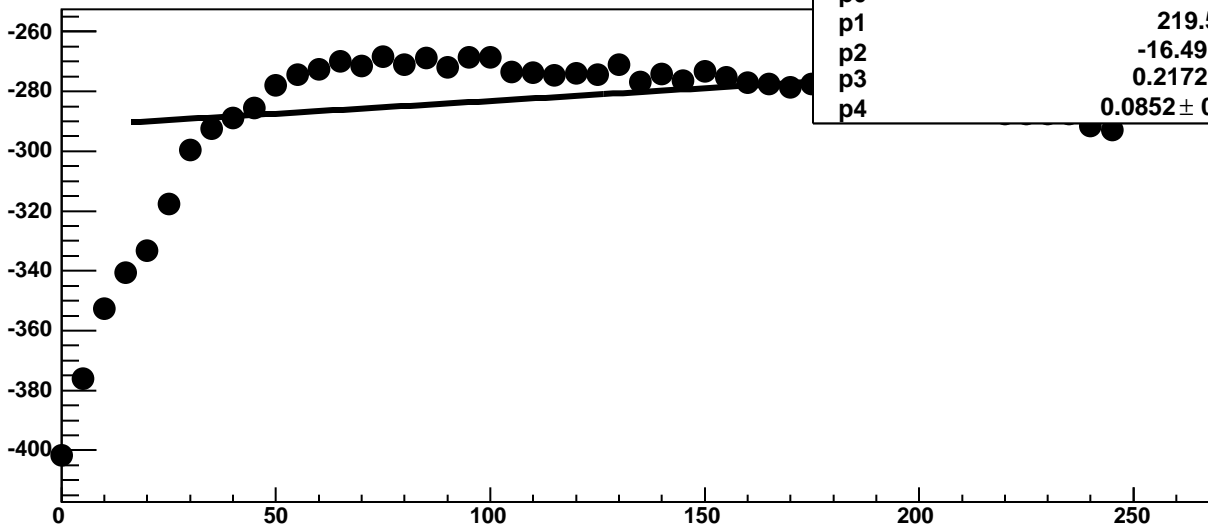
Chip 5, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold

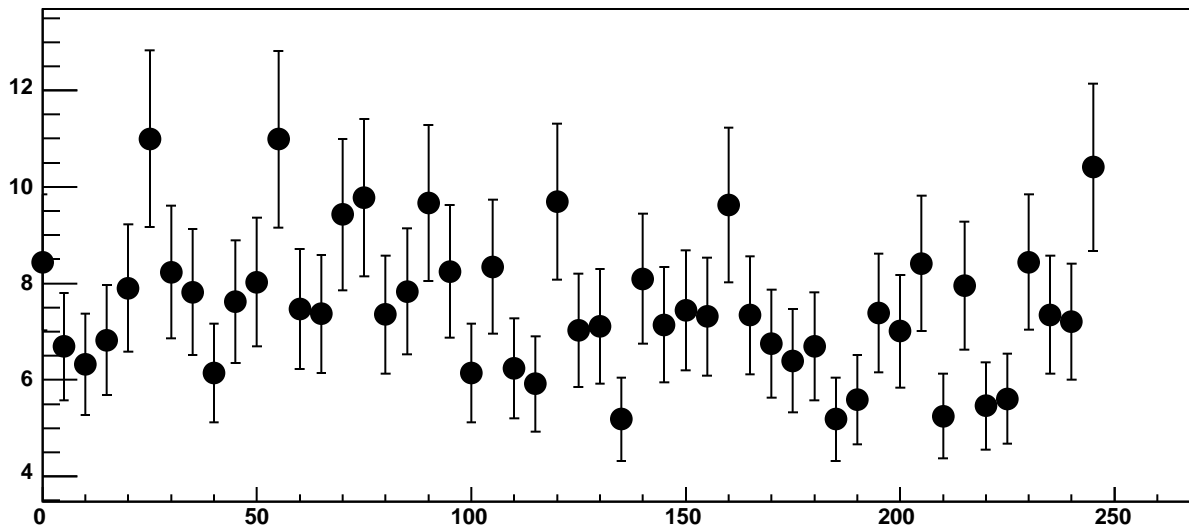


Chip 5, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

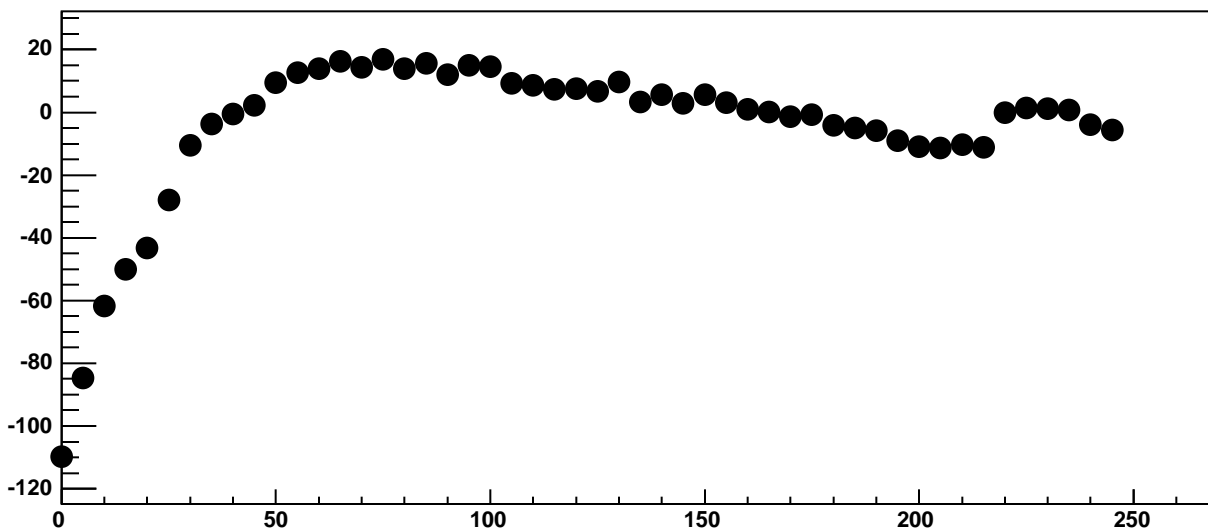


χ^2 / ndf	2864 / 41
p0	-273 ± 0.471
p1	219.5 ± 2.131
p2	-16.49 ± 0.9021
p3	0.2172 ± 0.9641
p4	0.0852 ± 0.003955

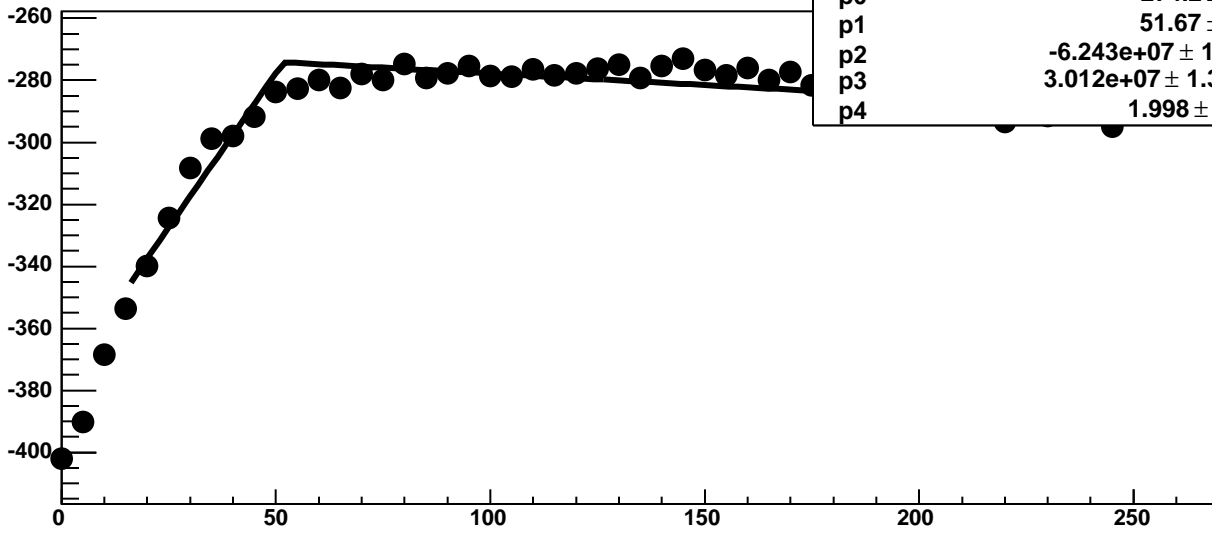
Chip 5, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold

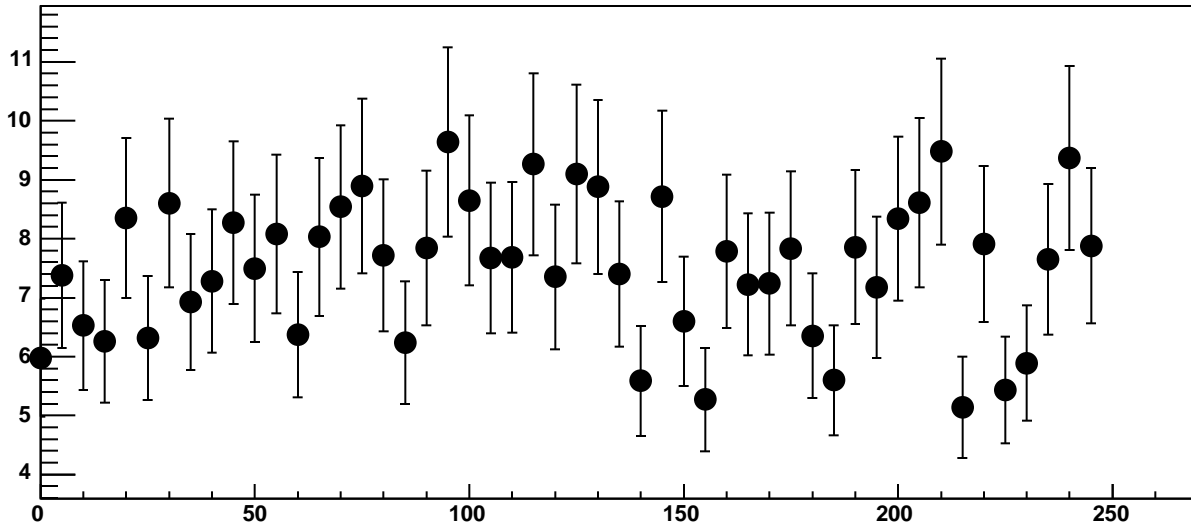


Chip 5, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

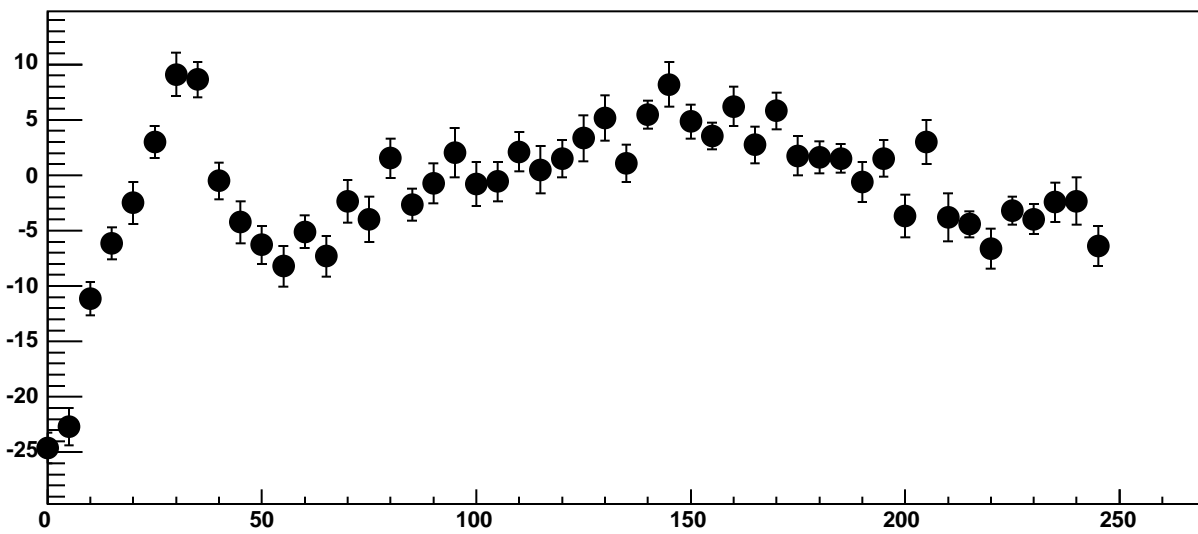


χ^2 / ndf	303.4 / 41
p0	-274.2 ± 0.5514
p1	51.67 ± 0.6355
p2	$-6.243\text{e}+07 \pm 1.55\text{e}+06$
p3	$3.012\text{e}+07 \pm 1.332\text{e}+05$
p4	1.998 ± 0.05078

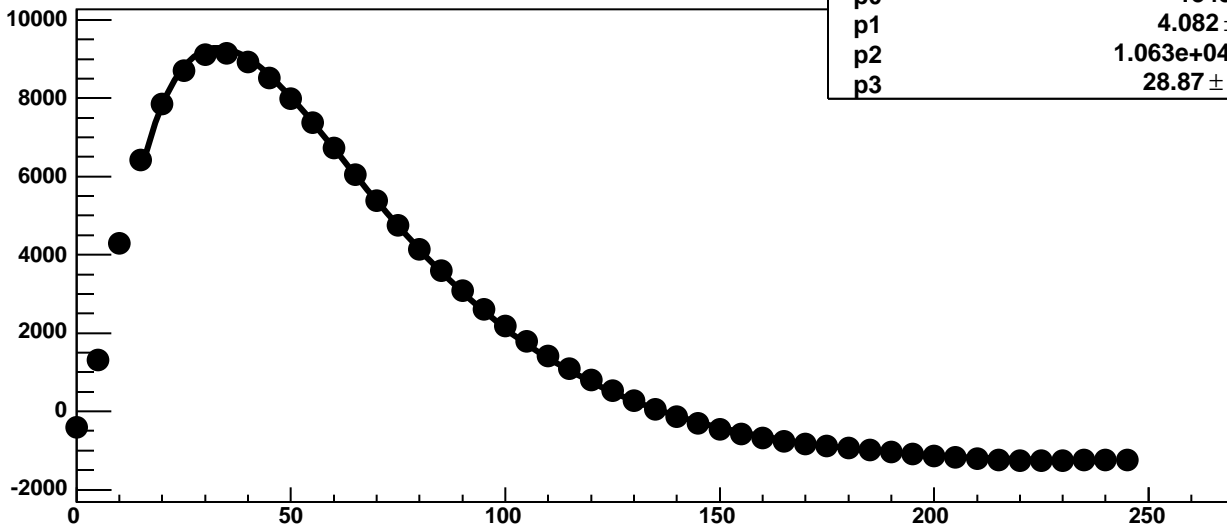
Chip 5, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



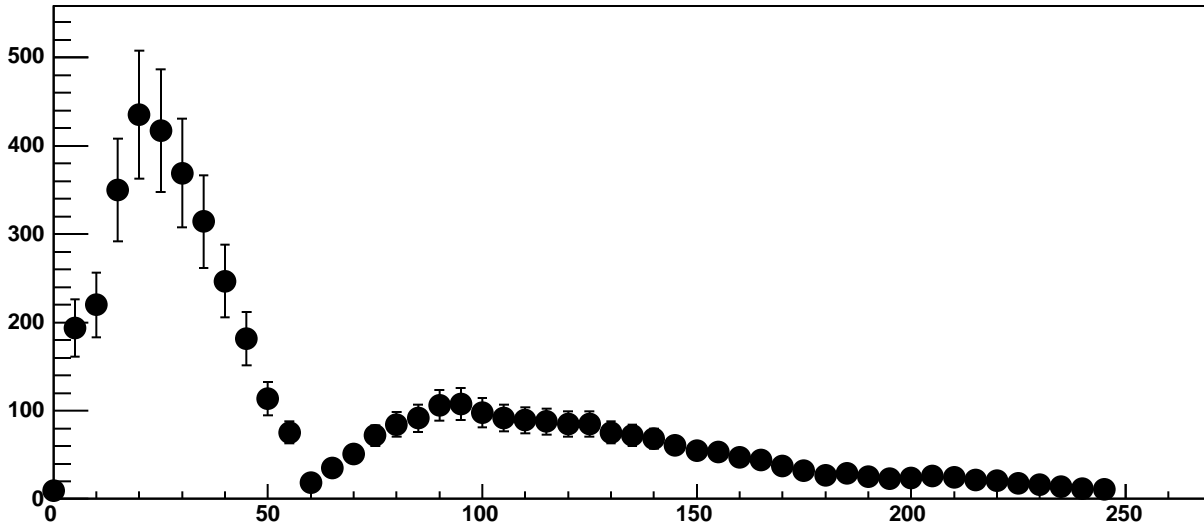
Chip 5, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold



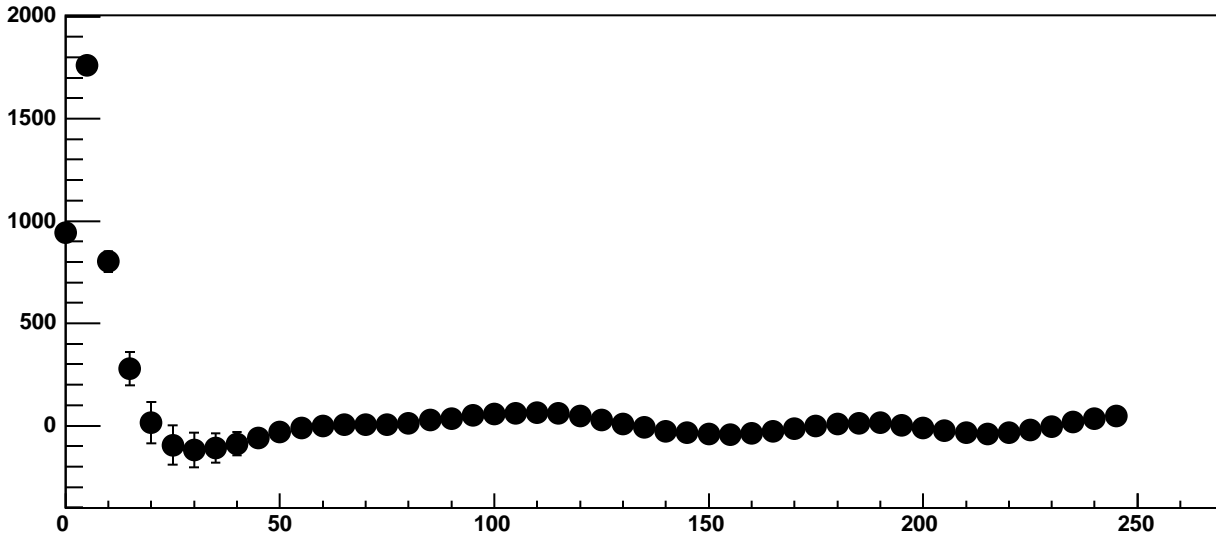
Chip 5, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold



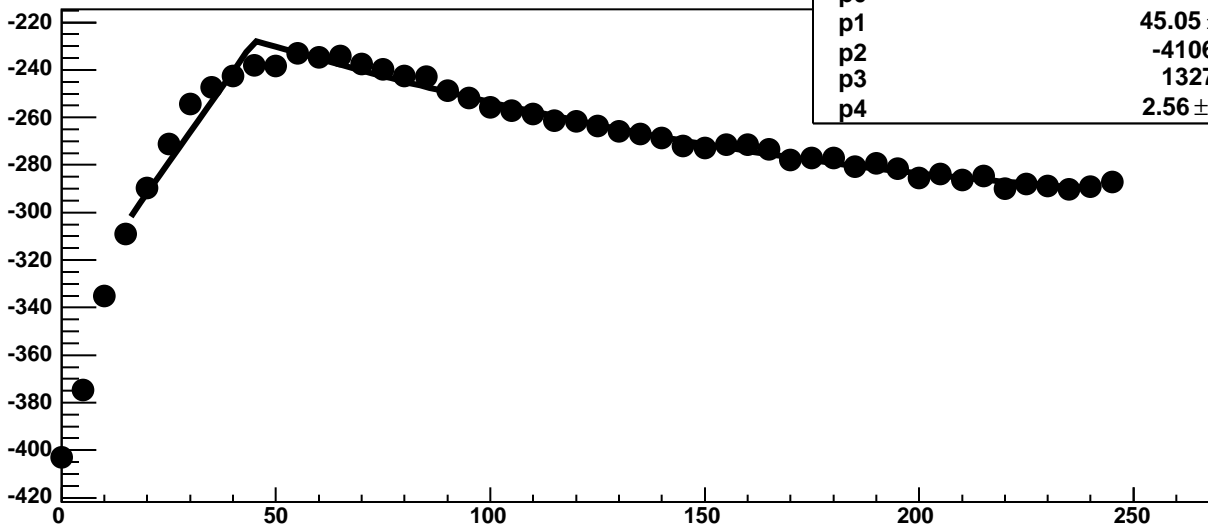
Chip 5, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold

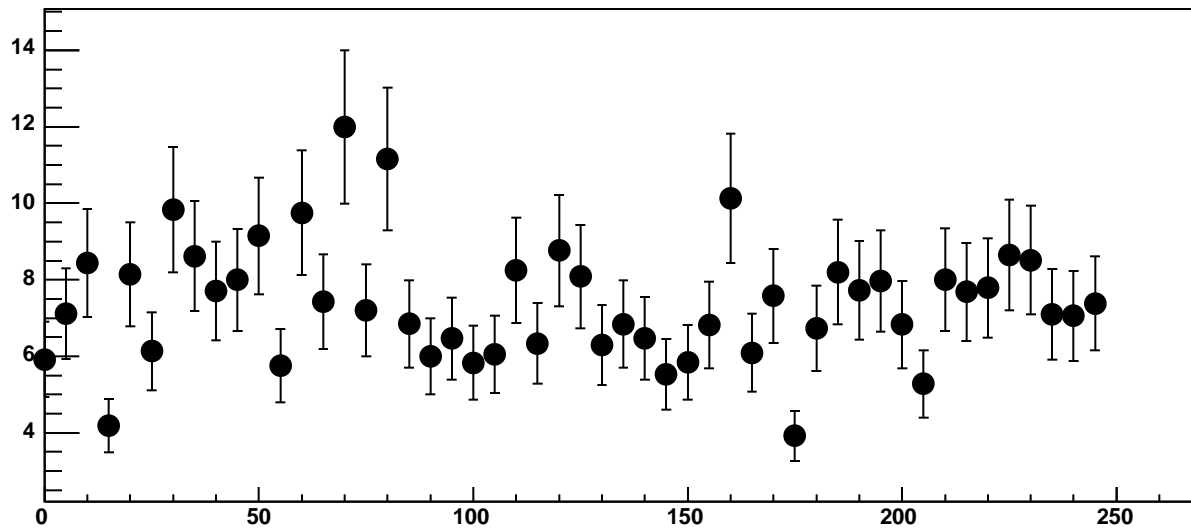


Chip 5, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold

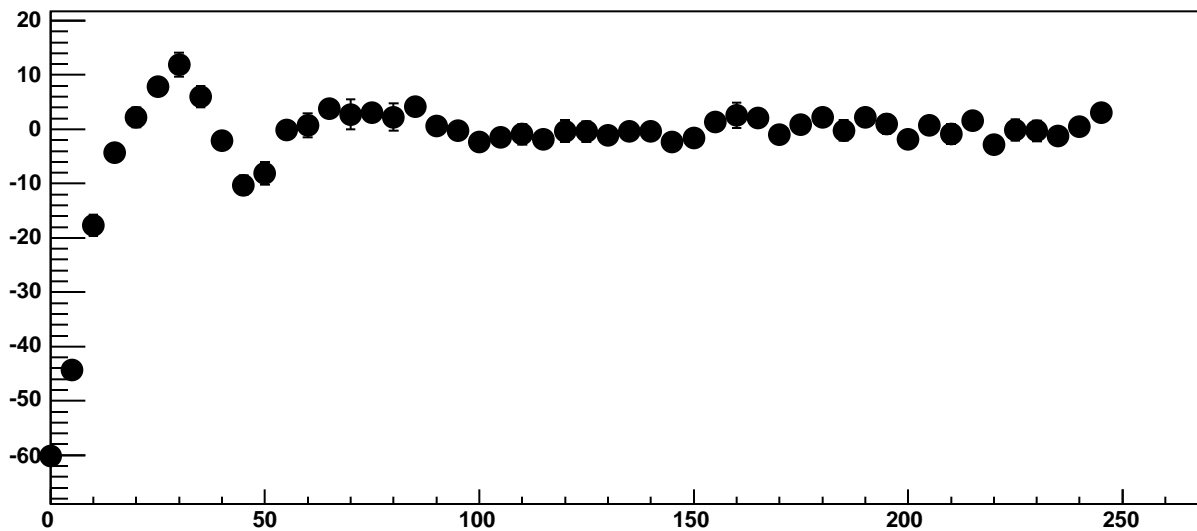


χ^2 / ndf	181.3 / 41
p0	-227.7 ± 0.7202
p1	45.05 ± 0.4575
p2	-4106 ± 309.4
p3	1327 ± 99.88
p4	2.56 ± 0.05403

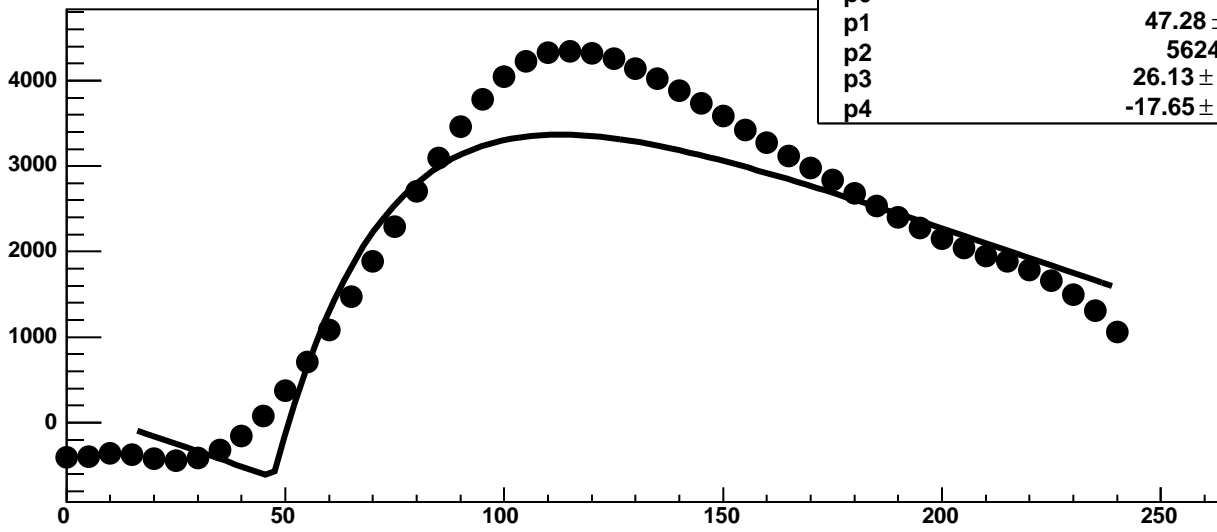
Chip 5, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



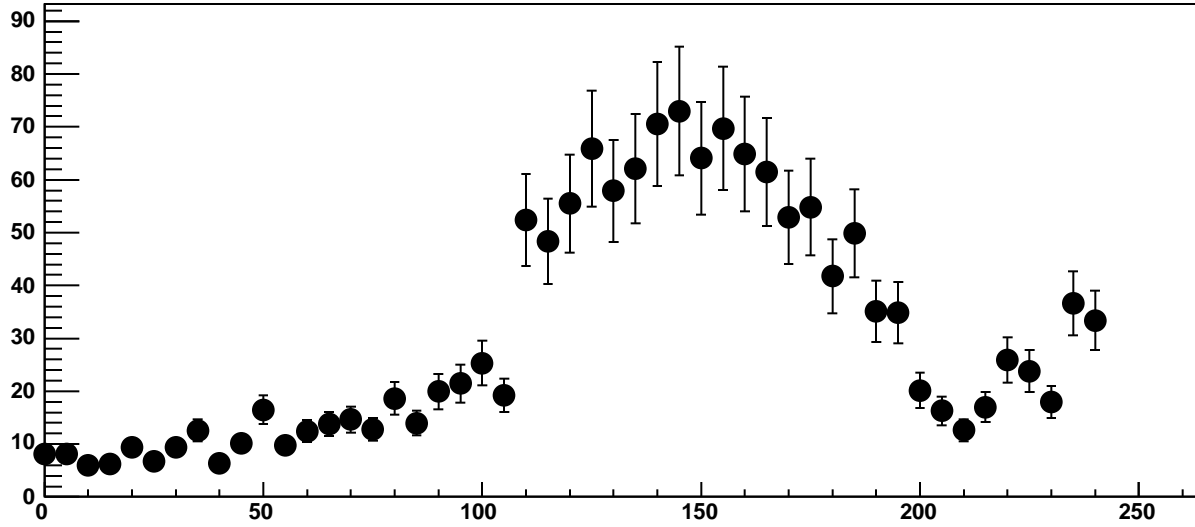
Chip 5, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold



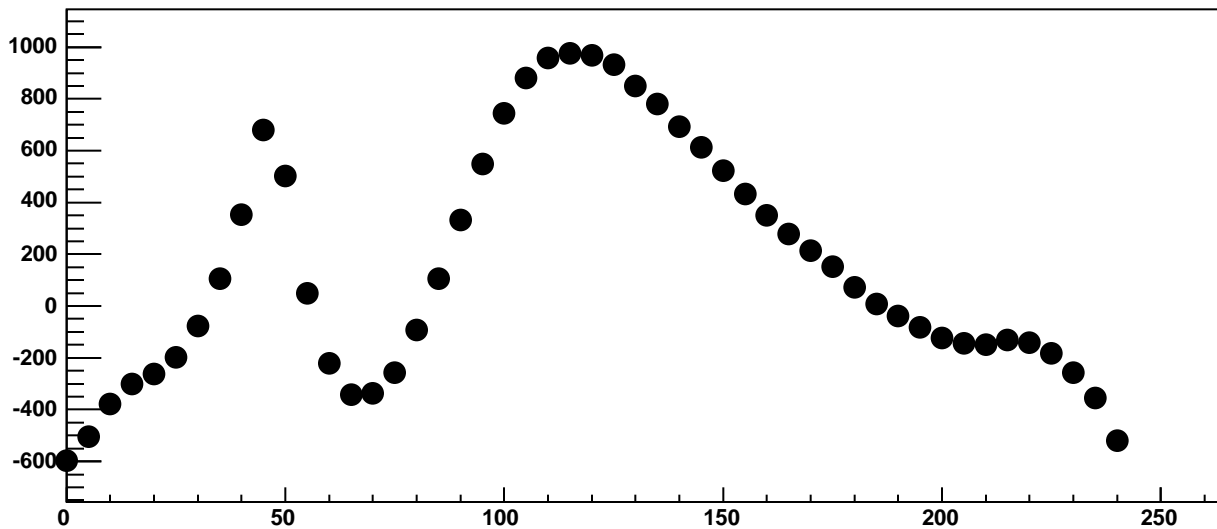
Chip 5, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold



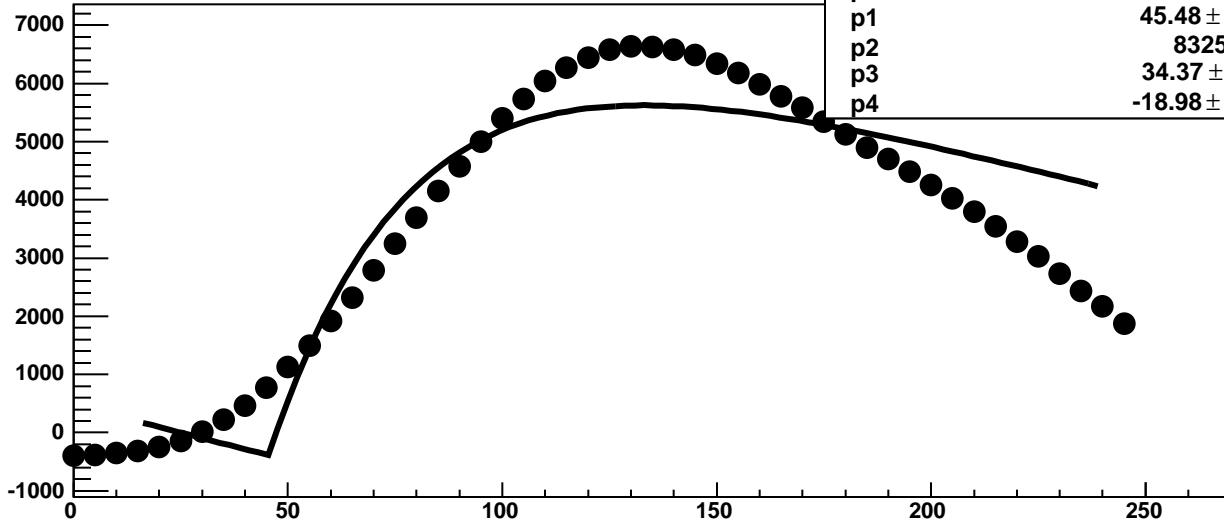
Chip 5, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

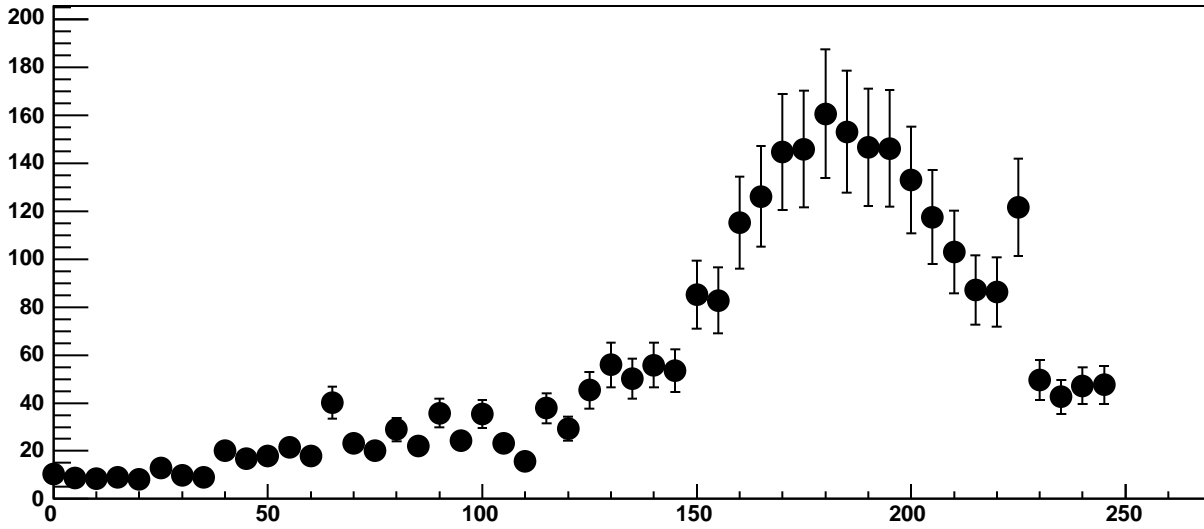


Chip 5, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold

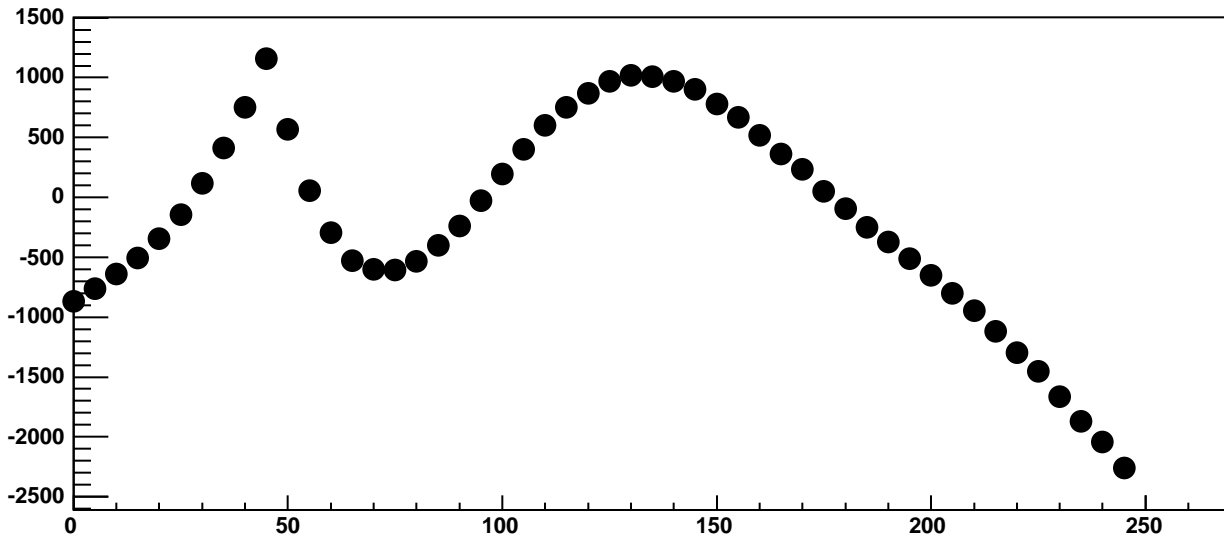


χ^2 / ndf	5.287e+05 / 41
p0	-387.9 ± 1.337
p1	45.48 ± 0.01653
p2	8325 ± 9.698
p3	34.37 ± 0.05201
p4	-18.98 ± 0.05767

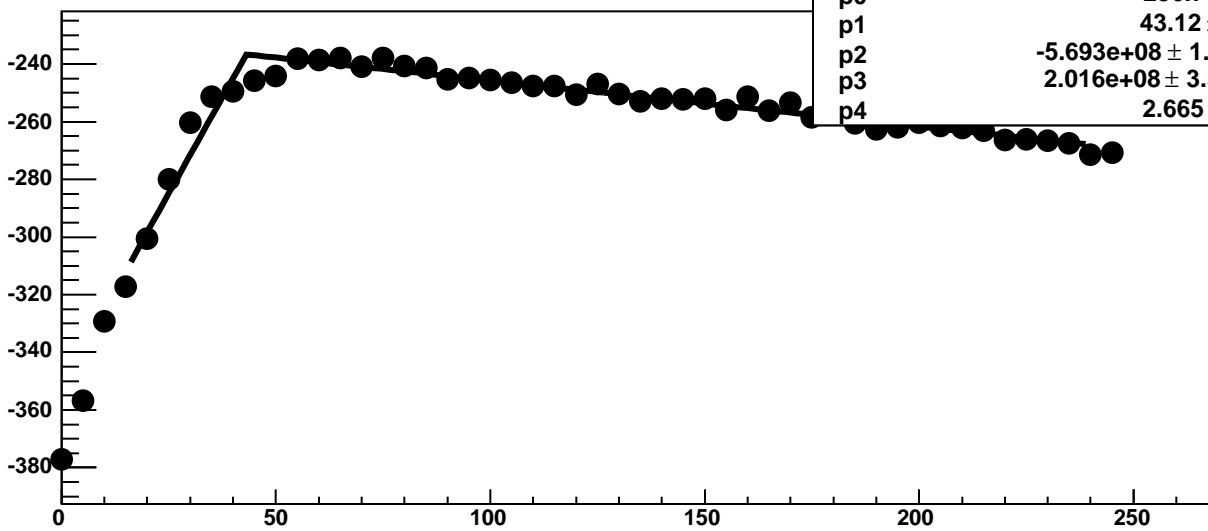
Chip 5, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

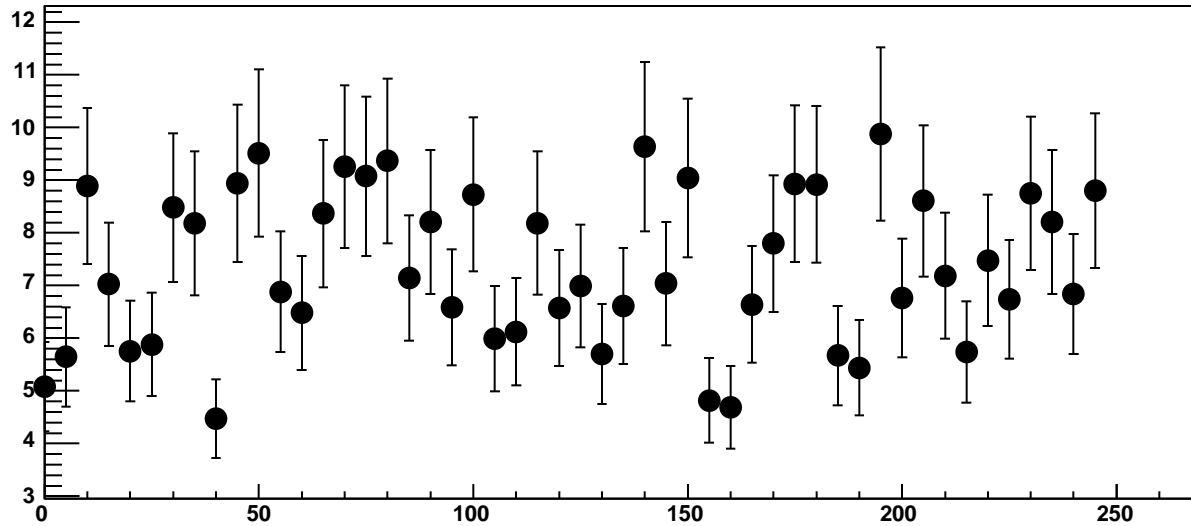


Chip 5, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold

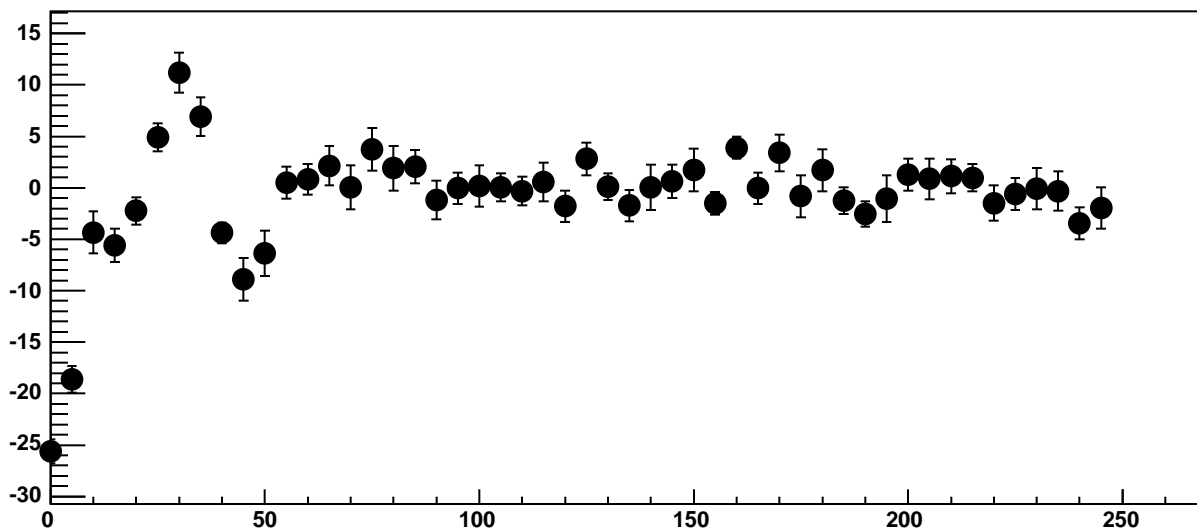


χ^2 / ndf	167.3 / 41
p0	-236.7 ± 0.5272
p1	43.12 ± 0.4355
p2	$-5.693\text{e}+08 \pm 1.266\text{e}+07$
p3	$2.016\text{e}+08 \pm 3.524\text{e}+05$
p4	2.665 ± 0.0628

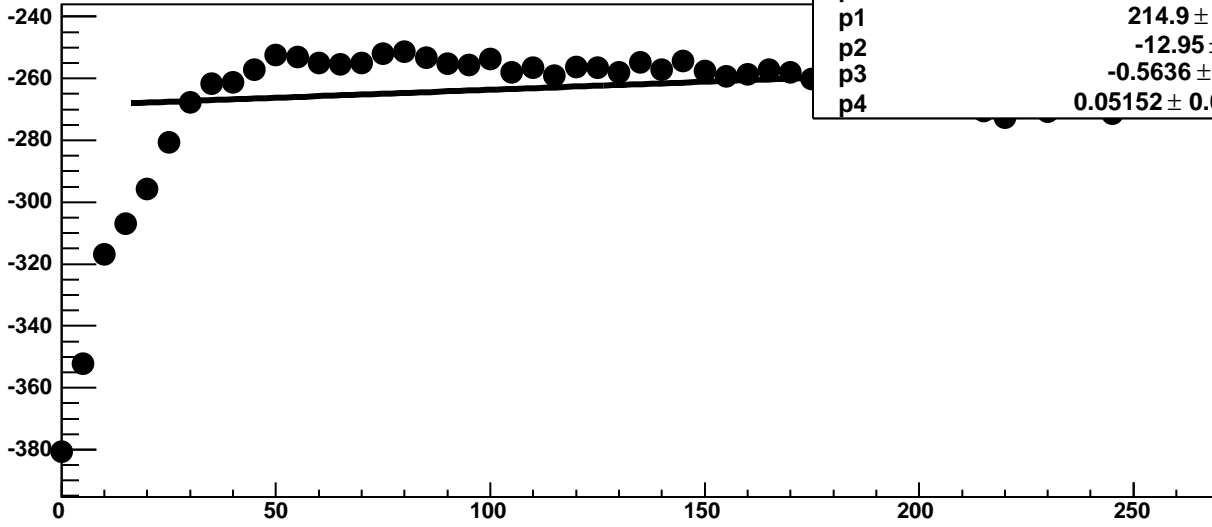
Chip 5, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold

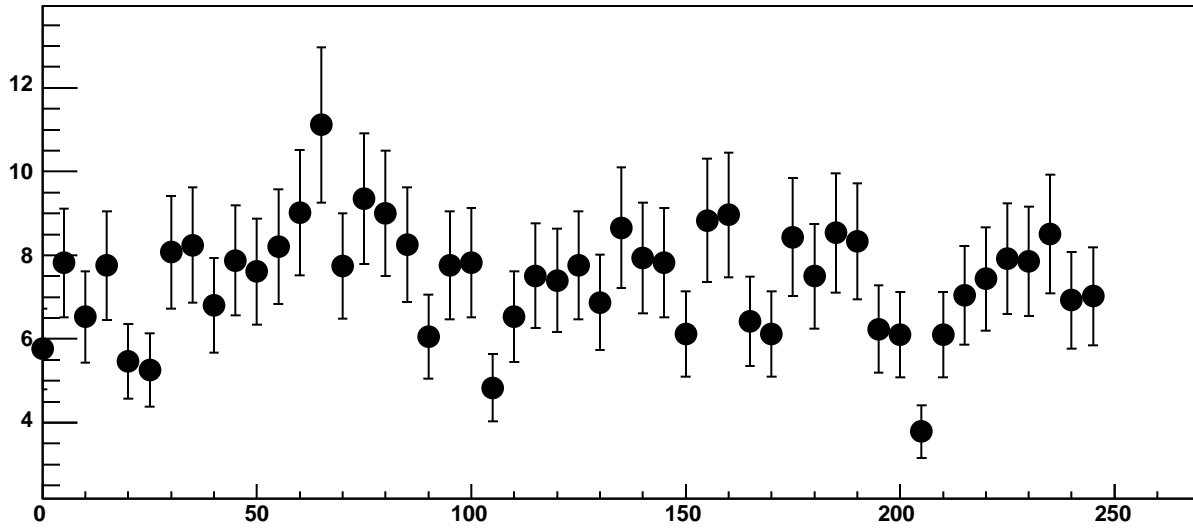


Chip 5, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

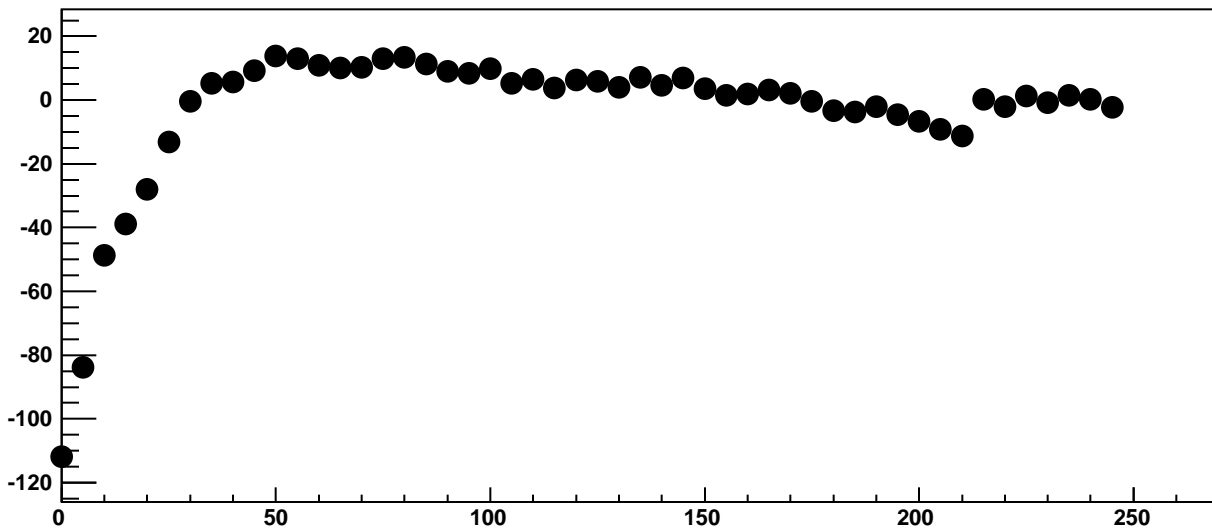


χ^2 / ndf	1909 / 41
p0	-257.7 ± 0.03914
p1	214.9 ± 0.01422
p2	-12.95 ± 0.1162
p3	-0.5636 ± 0.06191
p4	0.05152 ± 0.0003629

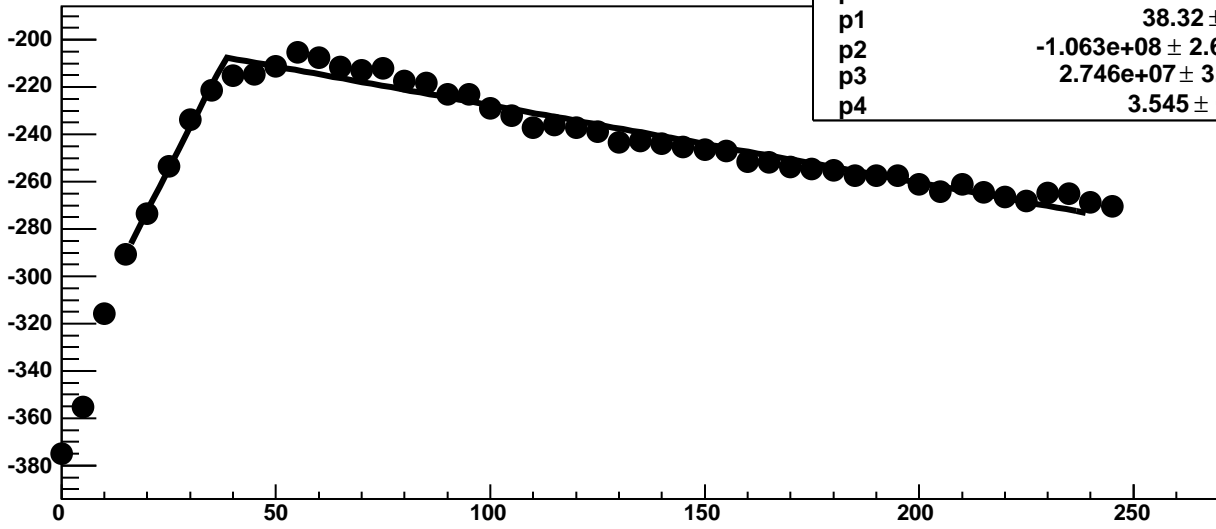
Chip 5, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

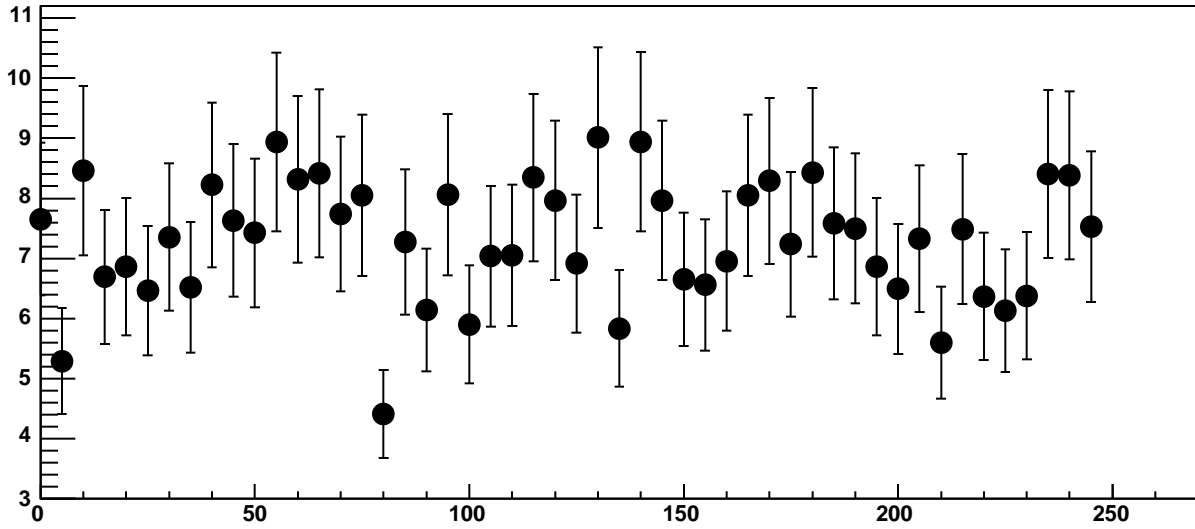


Chip 5, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

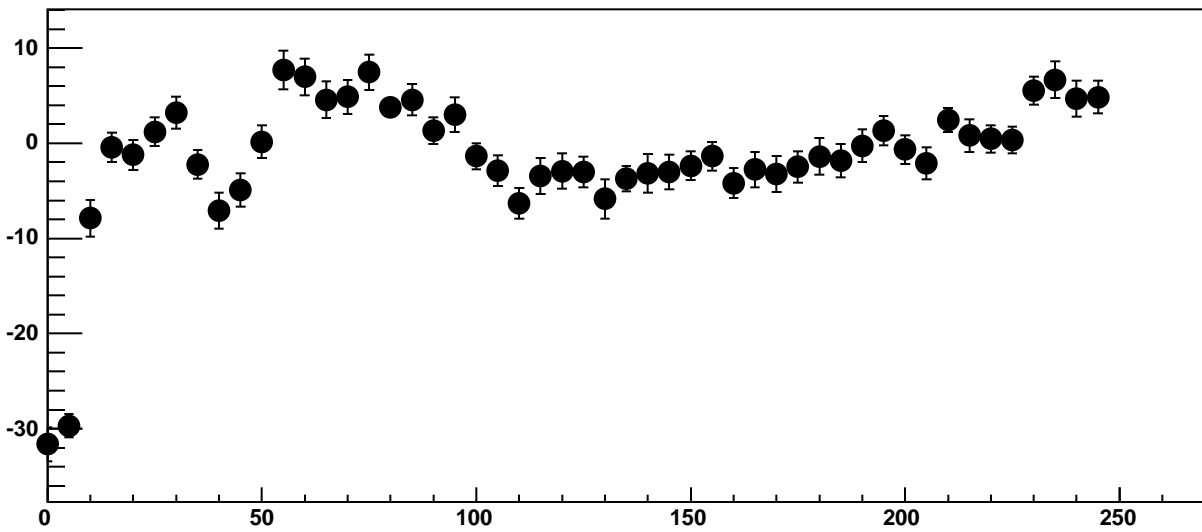


χ^2 / ndf	218.3 / 41
p0	-207.4 ± 0.4892
p1	38.32 ± 0.4042
p2	$-1.063\text{e}+08 \pm 2.624\text{e}+06$
p3	$2.746\text{e}+07 \pm 3.25\text{e}+04$
p4	3.545 ± 0.09733

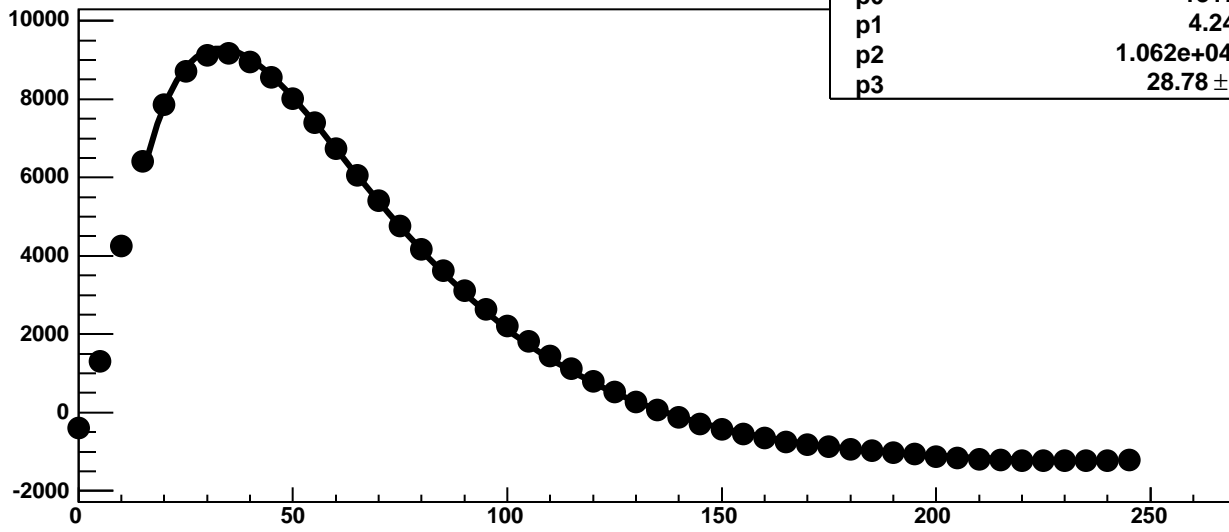
Chip 5, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

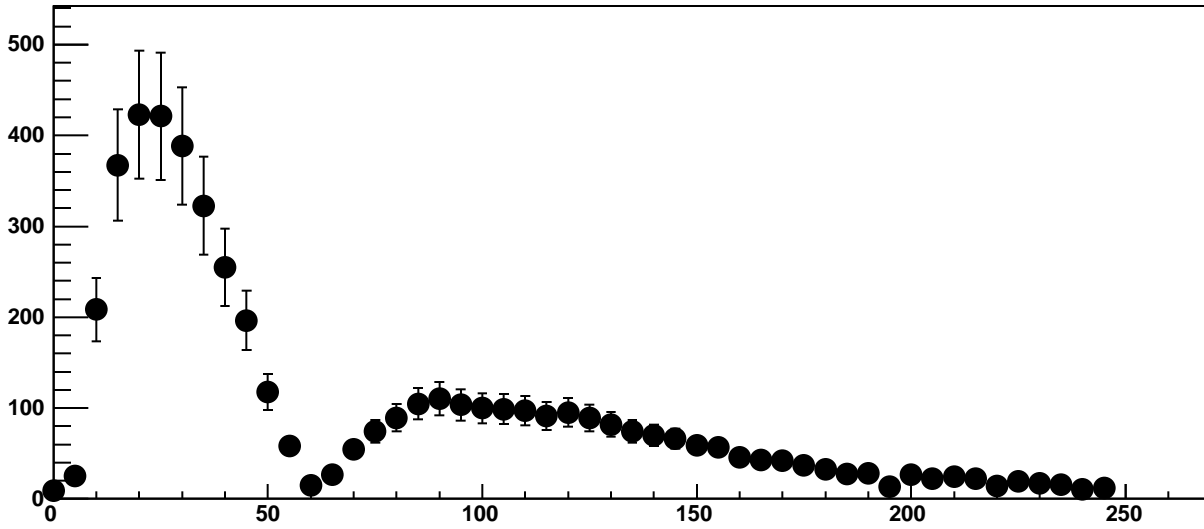


Chip 5, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold

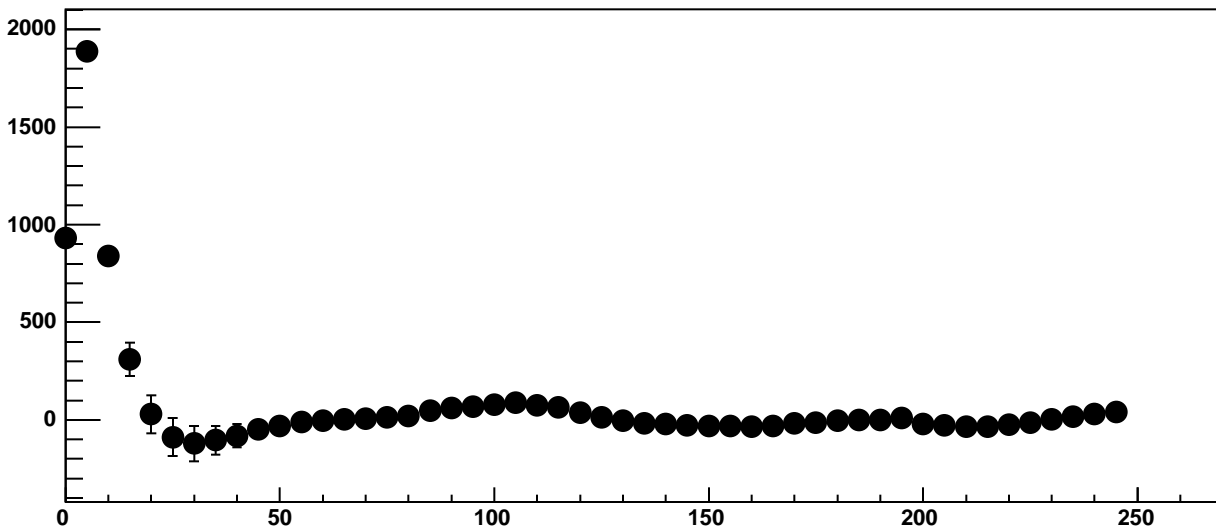


χ^2 / ndf	532.4 / 42
p0	-1317 ± 1.651
p1	4.24 ± 0.131
p2	1.062e+04 ± 17.33
p3	28.78 ± 0.04341

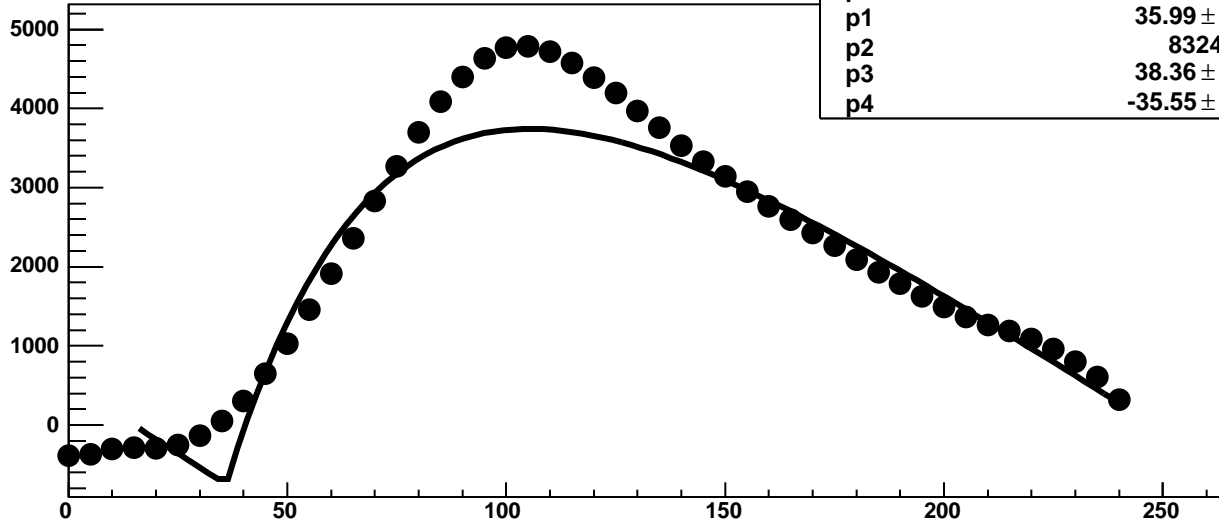
Chip 5, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold

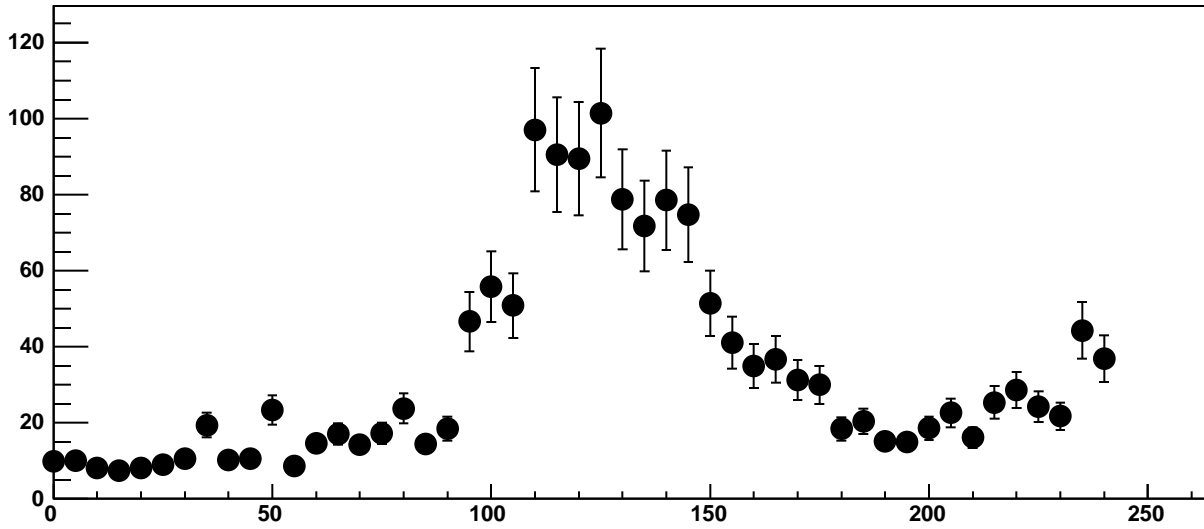


Chip 5, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold

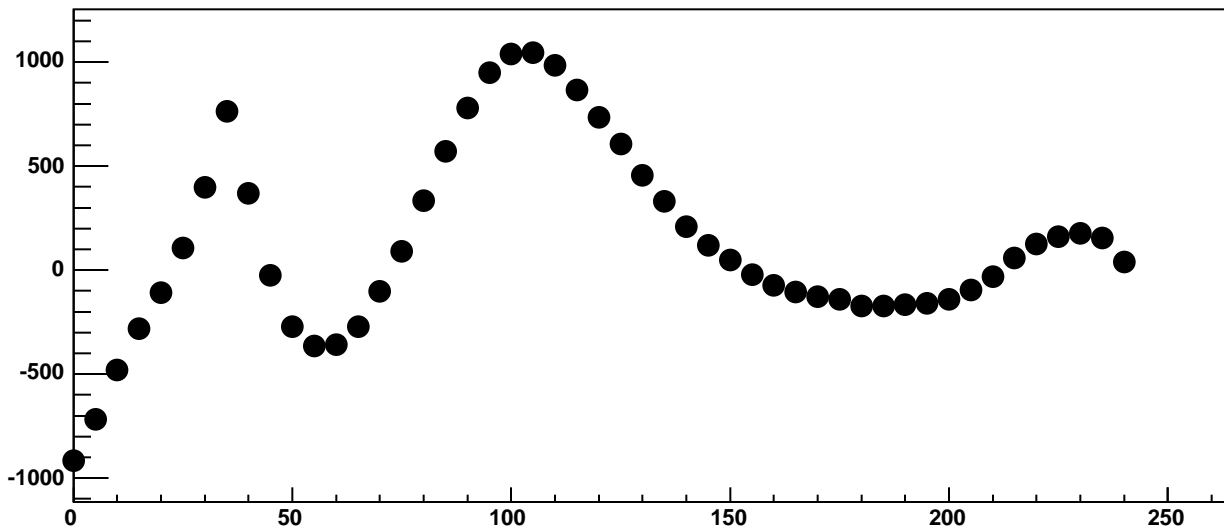


χ^2 / ndf	2.776e+05 / 41
p0	-750.2 ± 1.061
p1	35.99 ± 0.01325
p2	8324 ± 10.63
p3	38.36 ± 0.05744
p4	-35.55 ± 0.05525

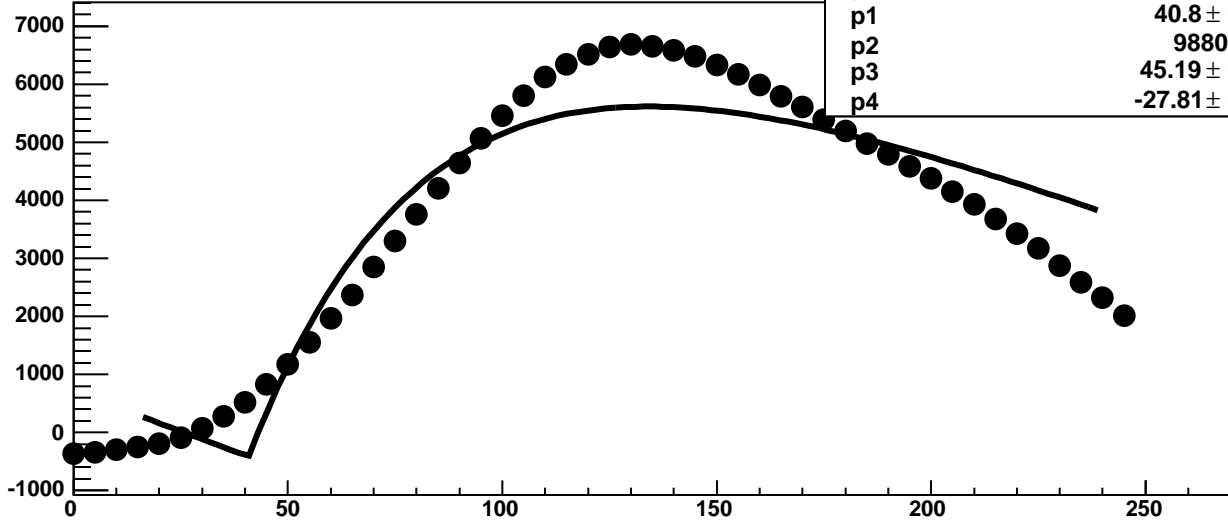
Chip 5, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold

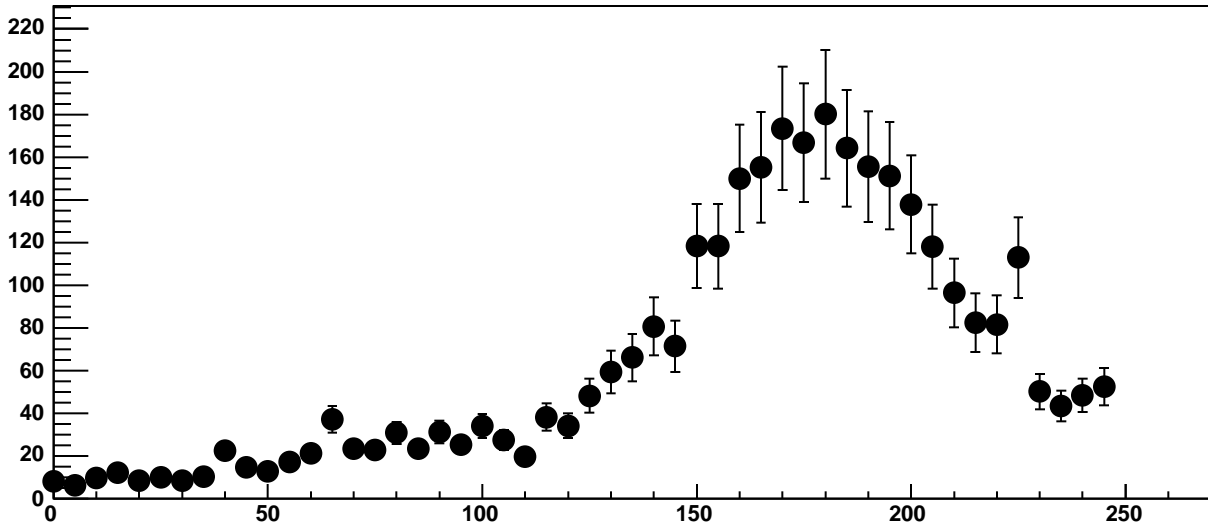


Chip 5, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

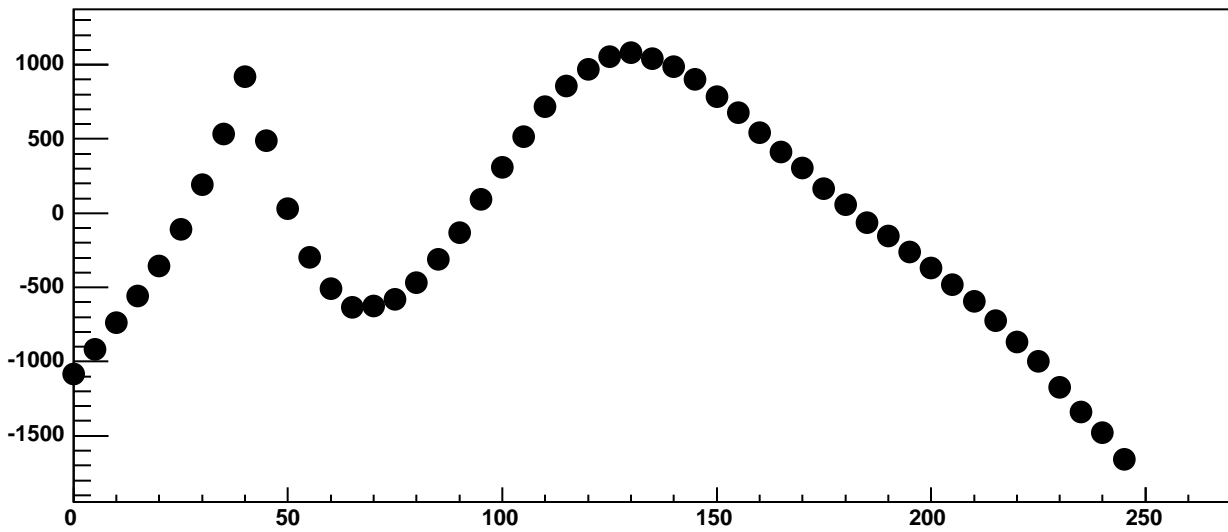


χ^2 / ndf	3.79e+05 / 41
p0	-416.4 ± 1.329
p1	40.8 ± 0.01579
p2	9880 ± 14.72
p3	45.19 ± 0.07608
p4	-27.81 ± 0.07636

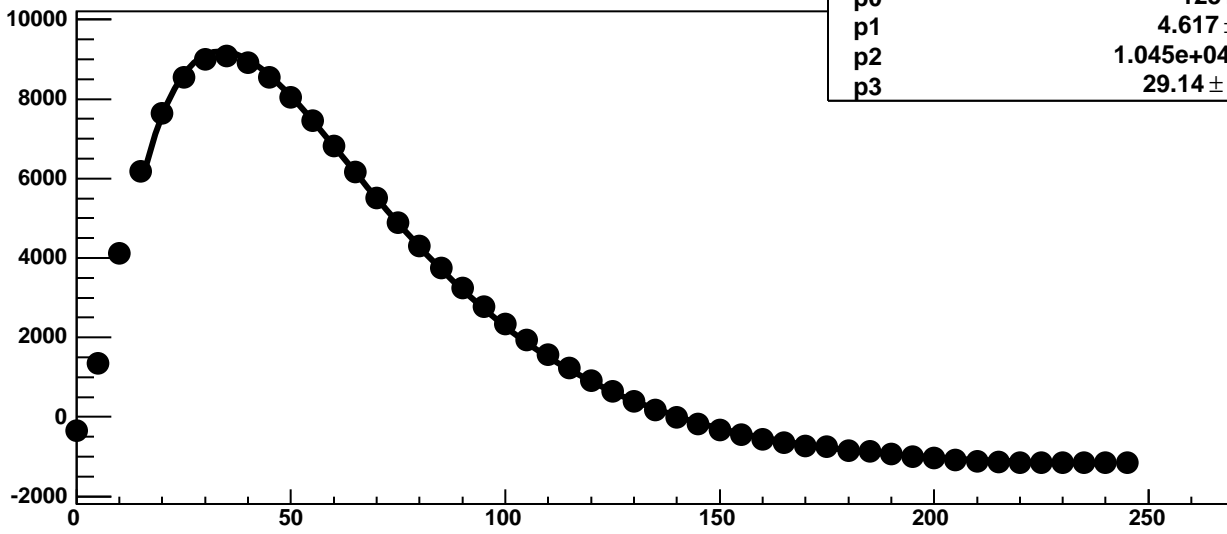
Chip 5, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

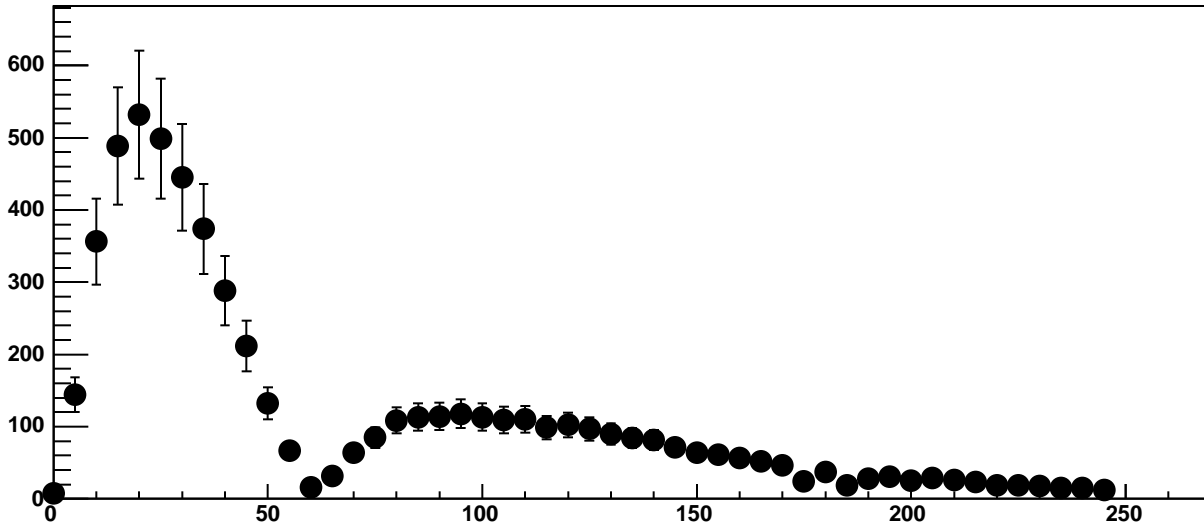


Chip 5, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold

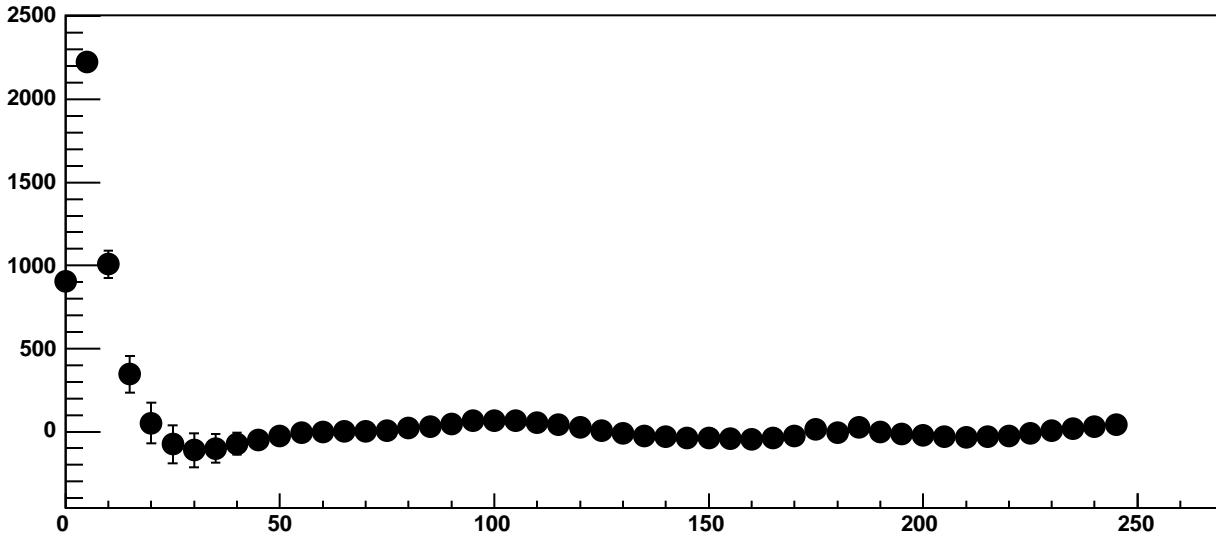


χ^2 / ndf	404.6 / 42
p0	-1251 ± 1.984
p1	4.617 ± 0.1622
p2	$1.045\text{e}+04 \pm 20.34$
p3	29.14 ± 0.04968

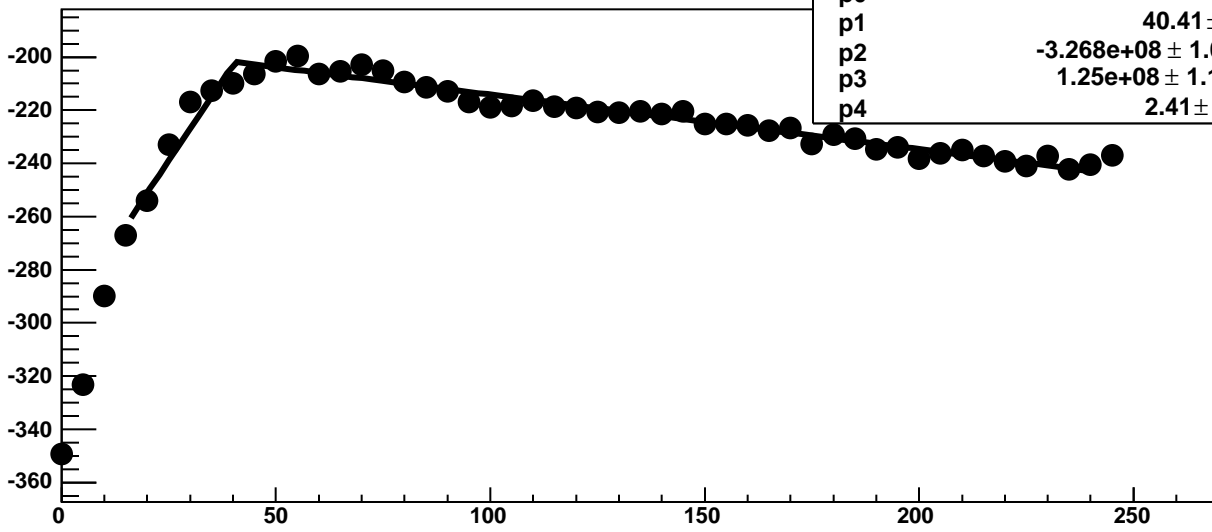
Chip 5, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold

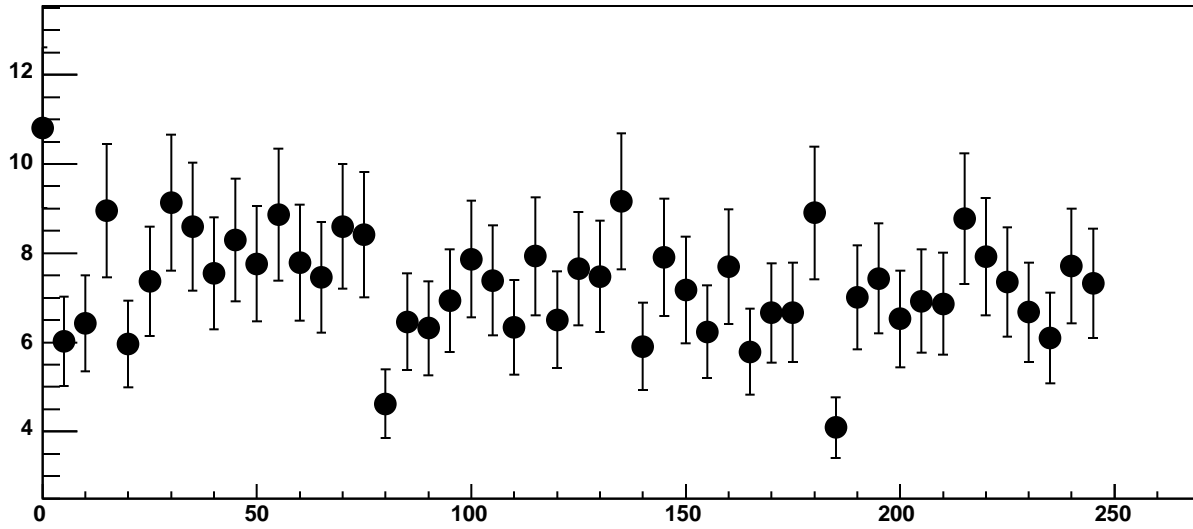


Chip 5, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

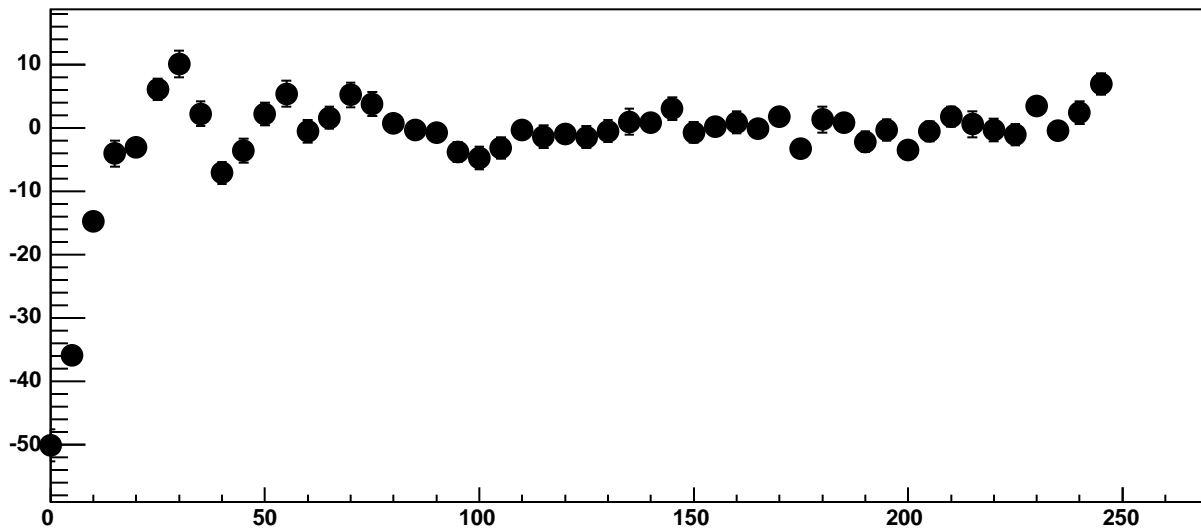


χ^2 / ndf	133.9 / 41
p0	-201.9 ± 0.5024
p1	40.41 ± 0.5524
p2	$-3.268\text{e}+08 \pm 1.056\text{e}+07$
p3	$1.25\text{e}+08 \pm 1.137\text{e}+05$
p4	2.41 ± 0.08454

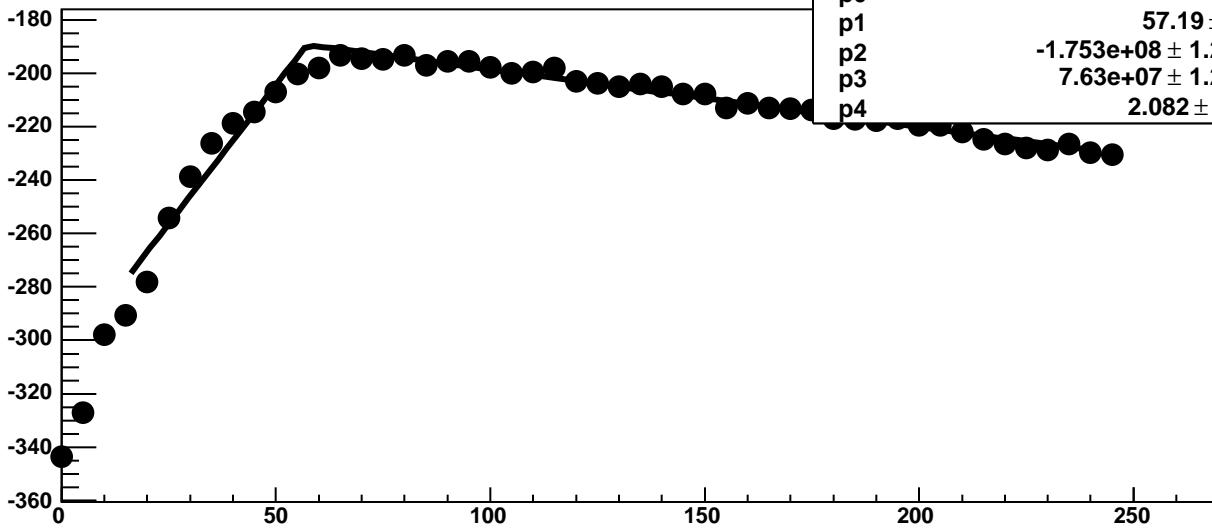
Chip 5, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

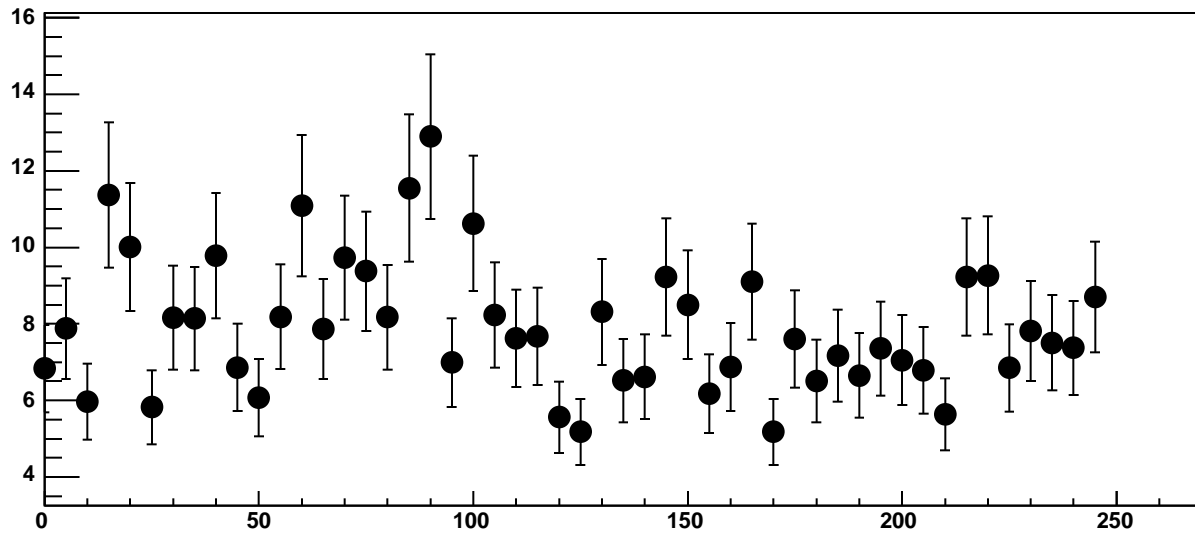


Chip 5, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

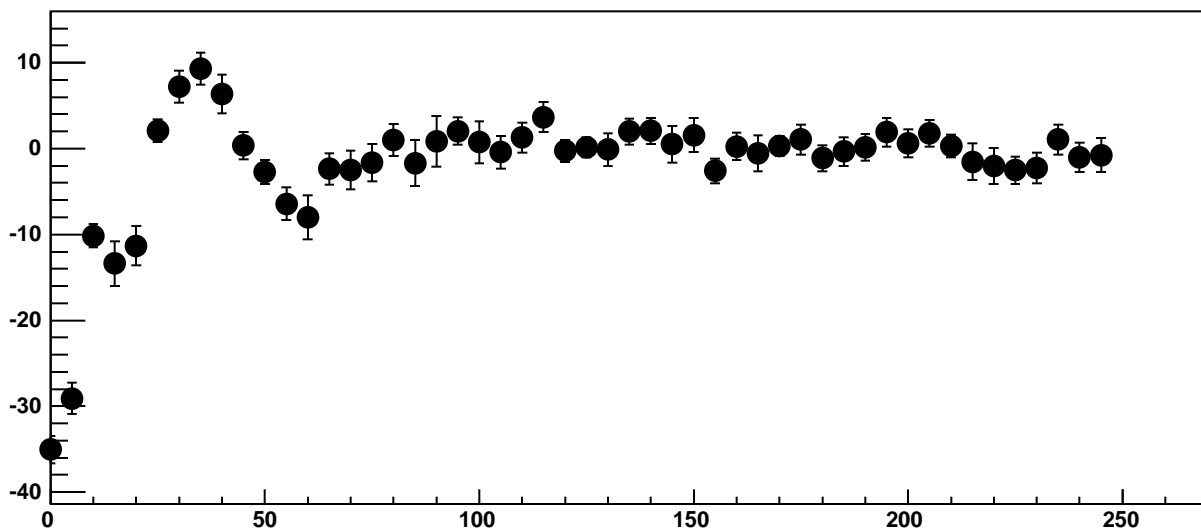


χ^2 / ndf	155.4 / 41
p0	-189.4 ± 0.7028
p1	57.19 ± 0.3823
p2	$-1.753\text{e}+08 \pm 1.274\text{e}+06$
p3	$7.63\text{e}+07 \pm 1.209\text{e}+06$
p4	2.082 ± 0.01567

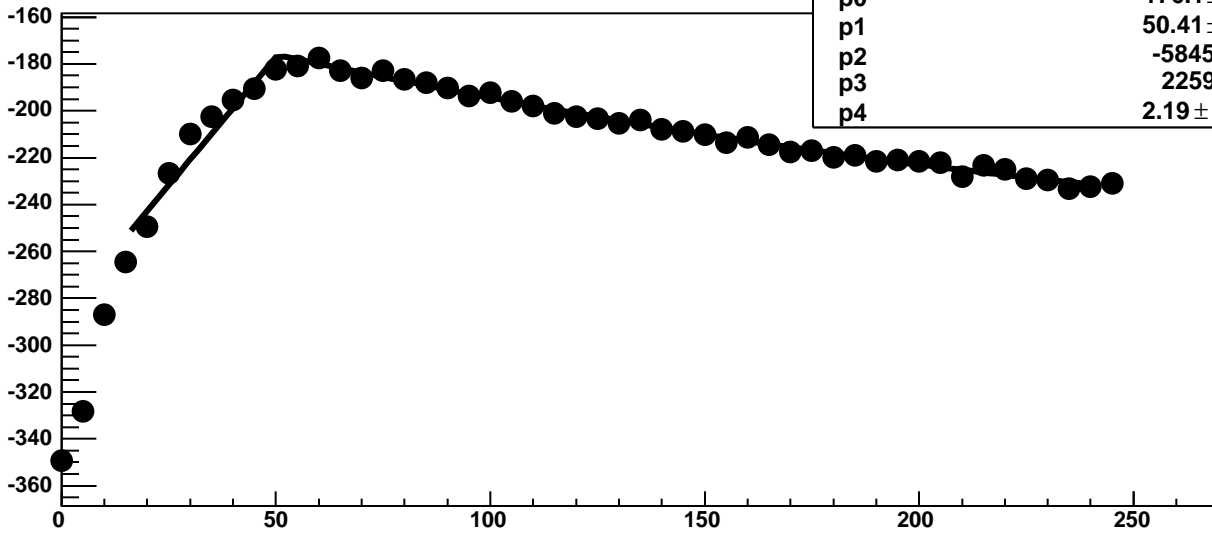
Chip 5, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

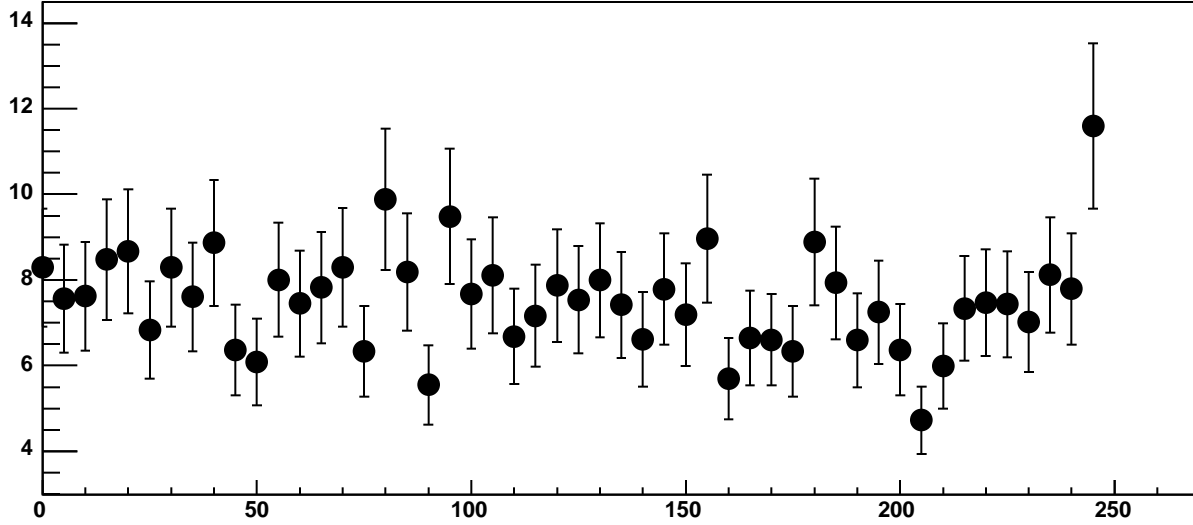


Chip 5, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold

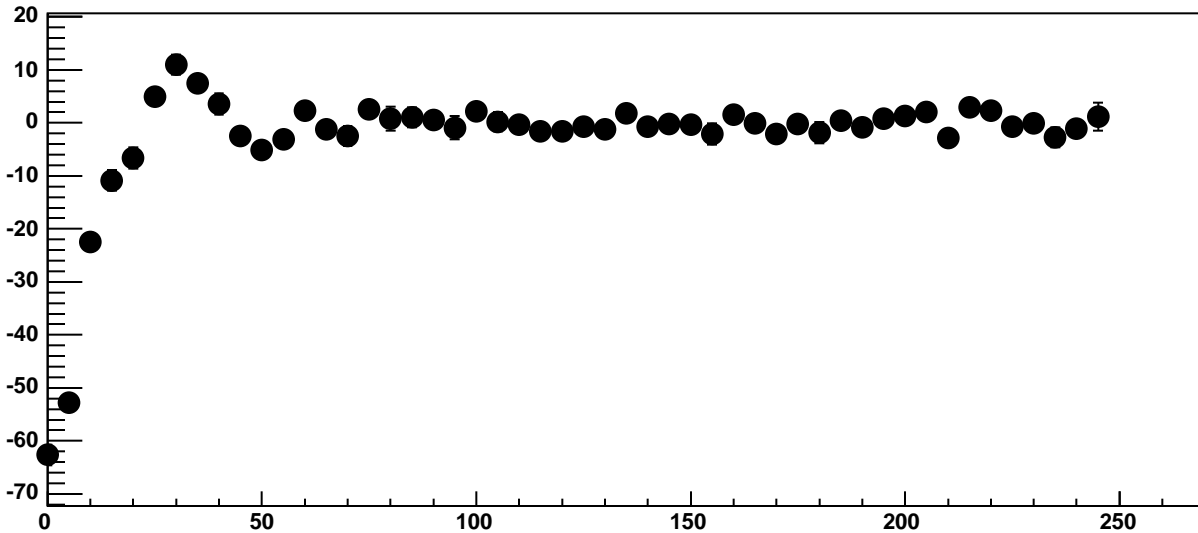


χ^2 / ndf	162.1 / 41
p0	-176.1 ± 0.6486
p1	50.41 ± 0.4665
p2	-5845 ± 705.9
p3	2259 ± 278.7
p4	2.19 ± 0.05114

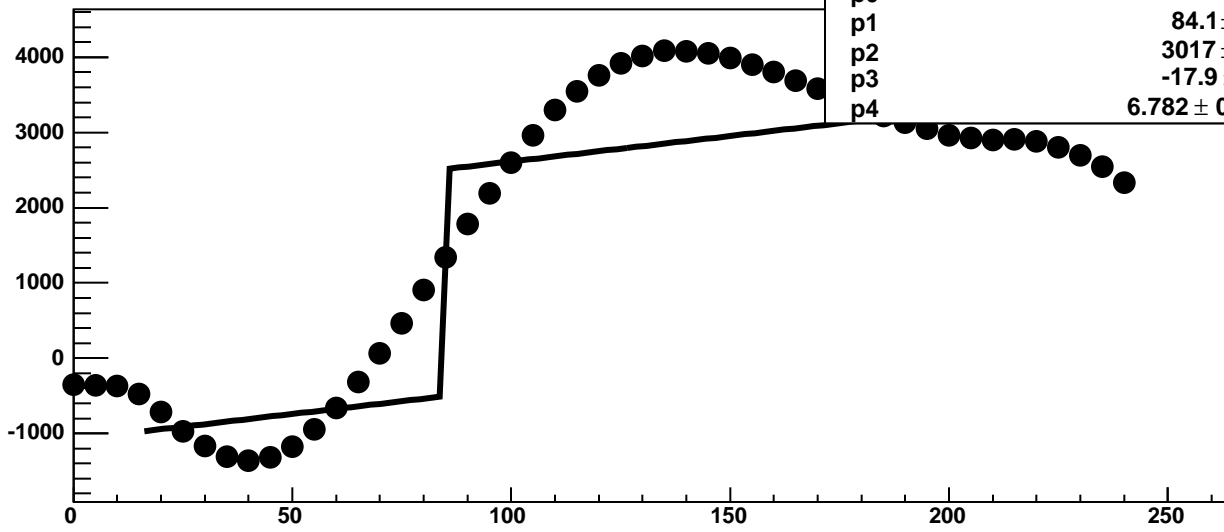
Chip 5, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold

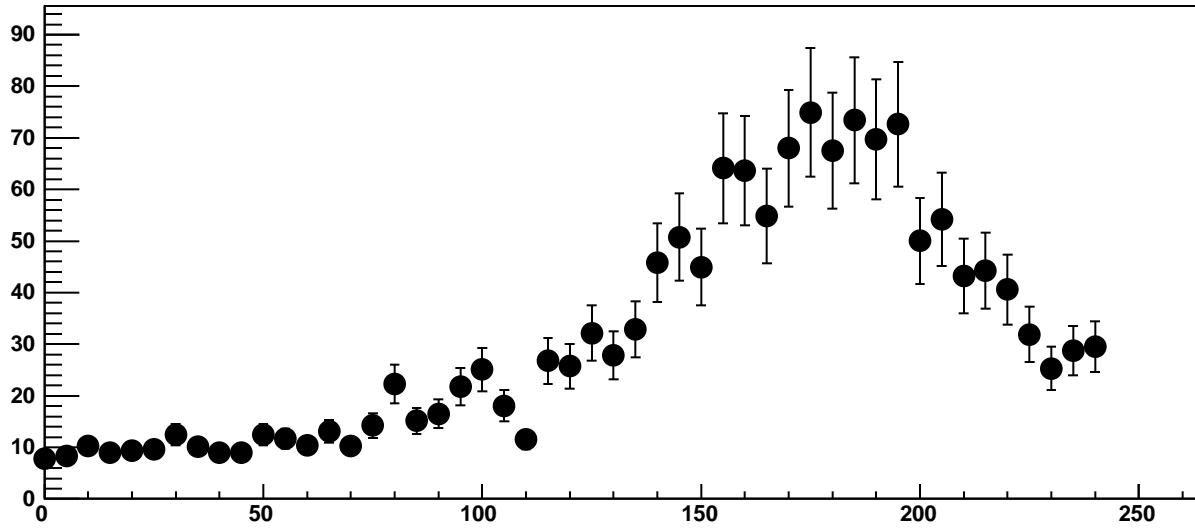


Chip 5, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold

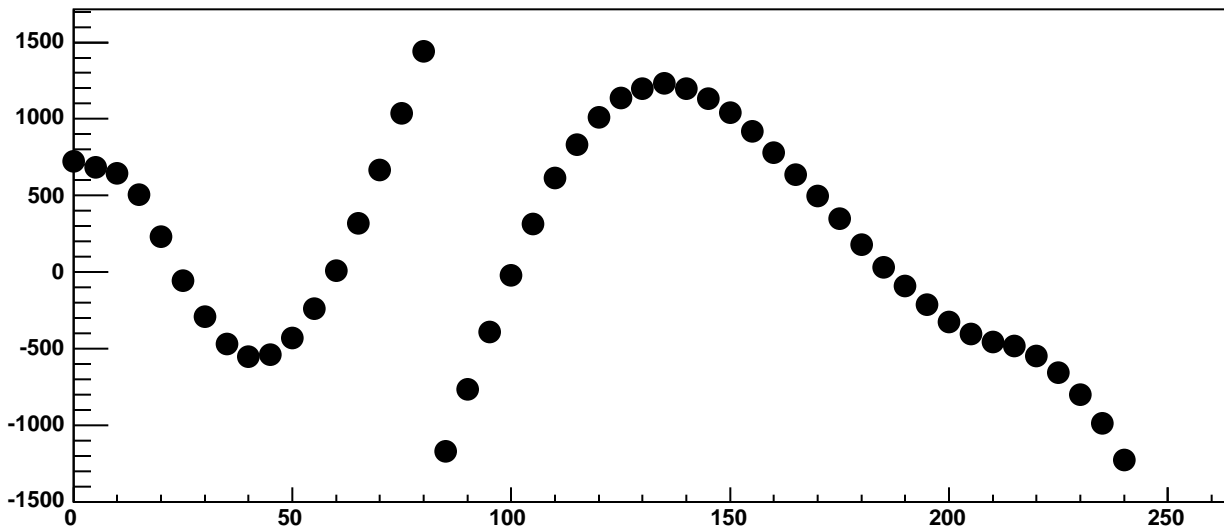


χ^2 / ndf	1.057e+06 / 41
p0	-509.6 ± 0.1914
p1	84.1 ± 0.5322
p2	3017 ± 0.4053
p3	-17.9 ± 0.4381
p4	6.782 ± 0.004352

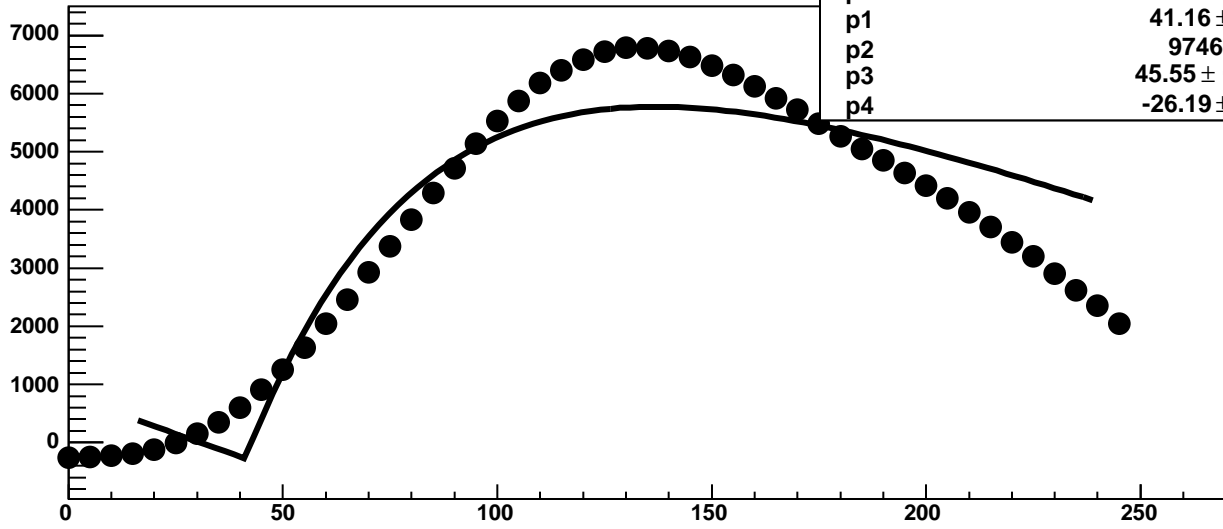
Chip 5, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



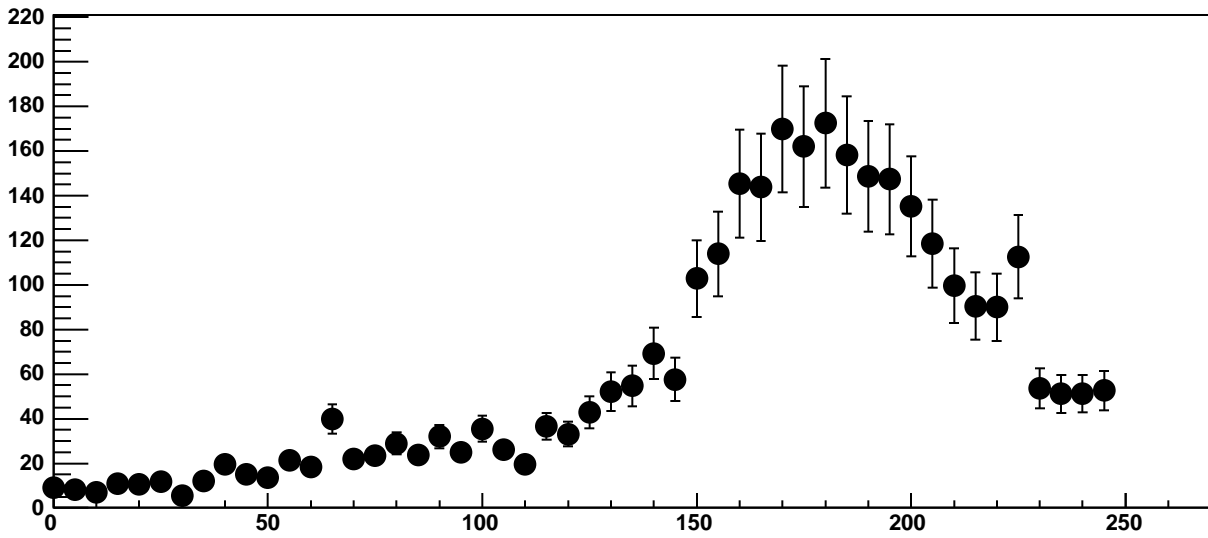
Chip 5, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold



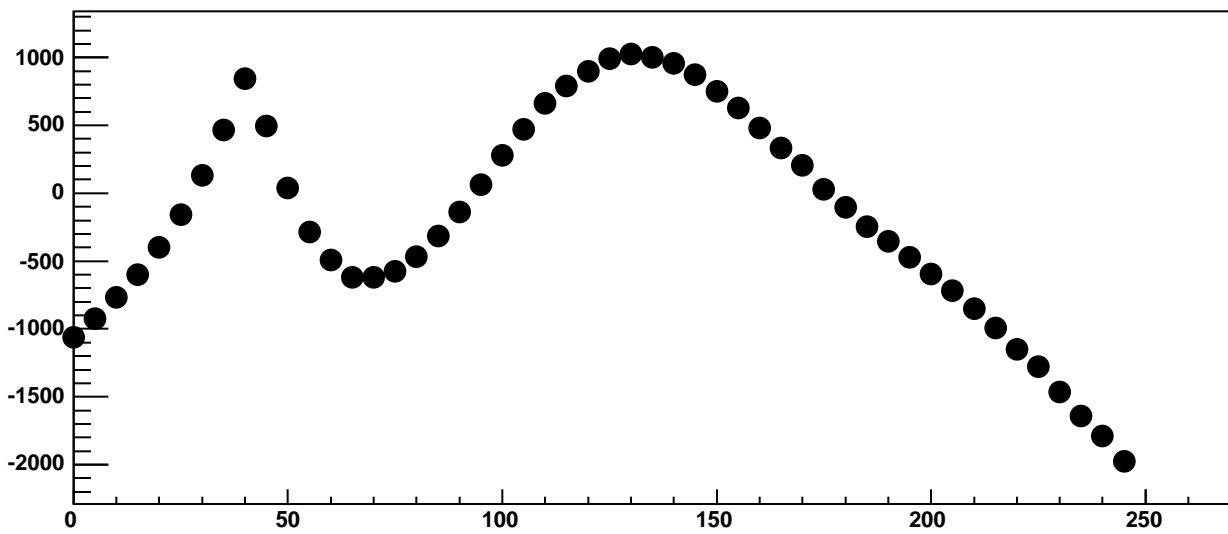
Chip 5, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold



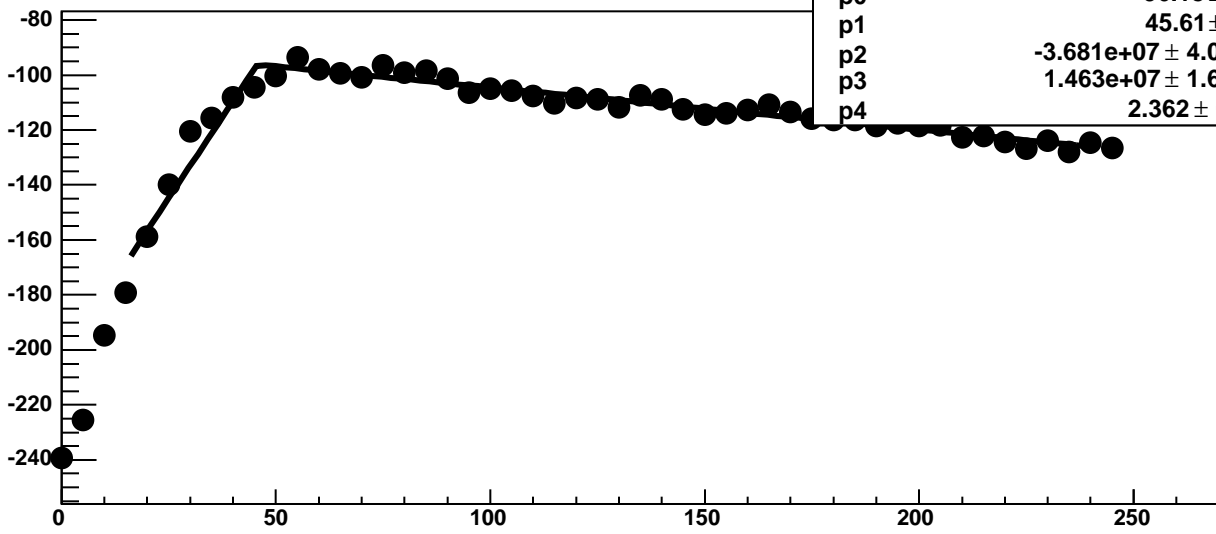
Chip 5, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



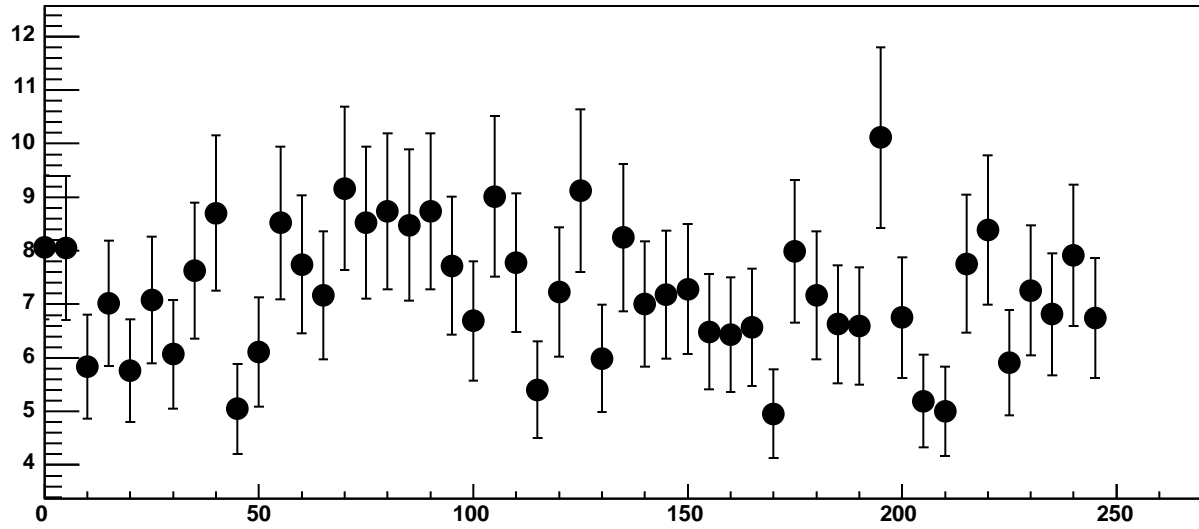
Chip 5, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold



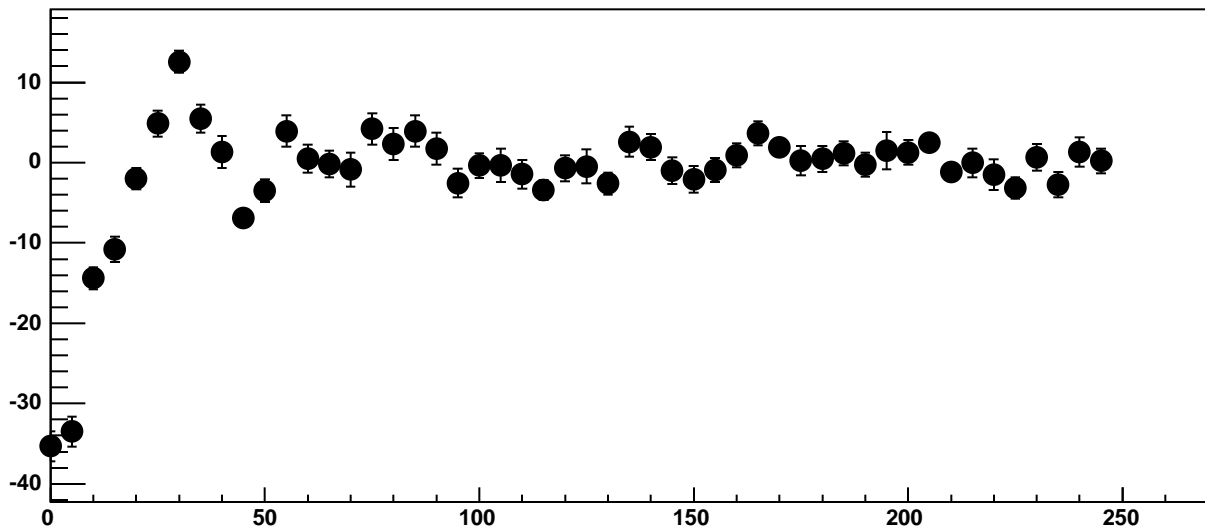
Chip 5, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold



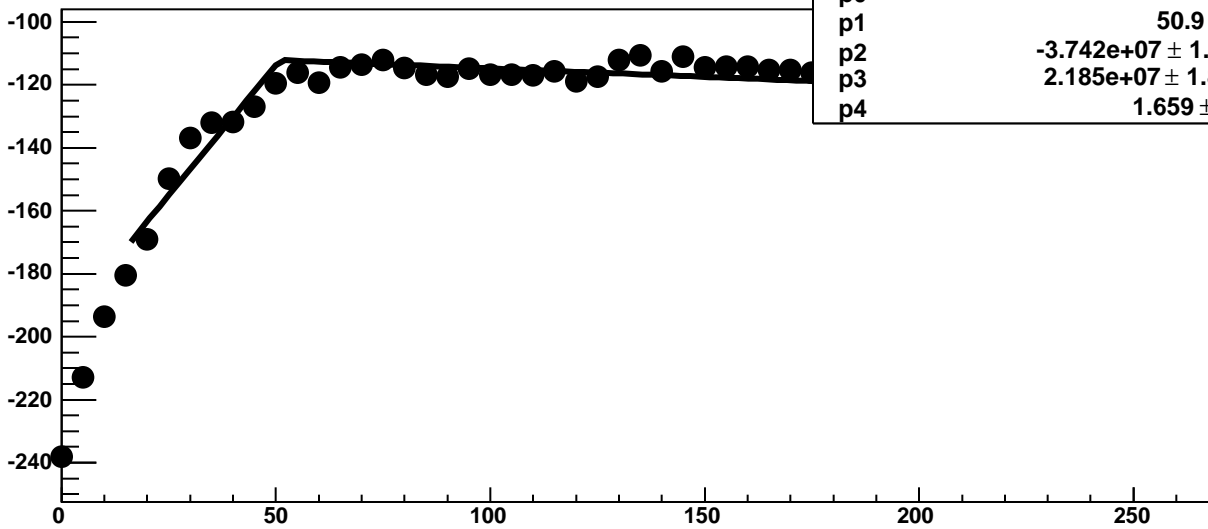
Chip 5, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold

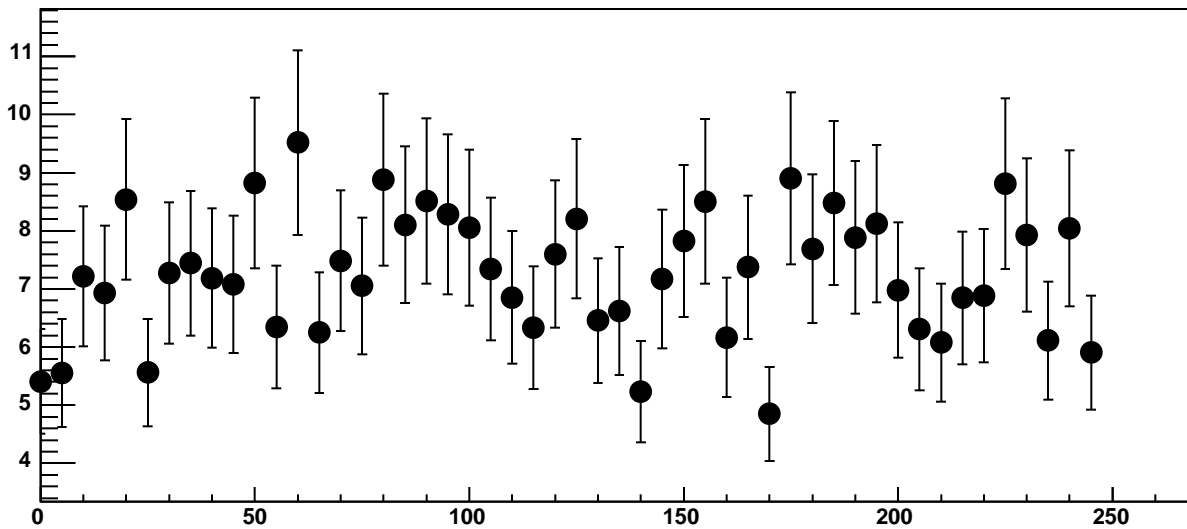


Chip 5, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

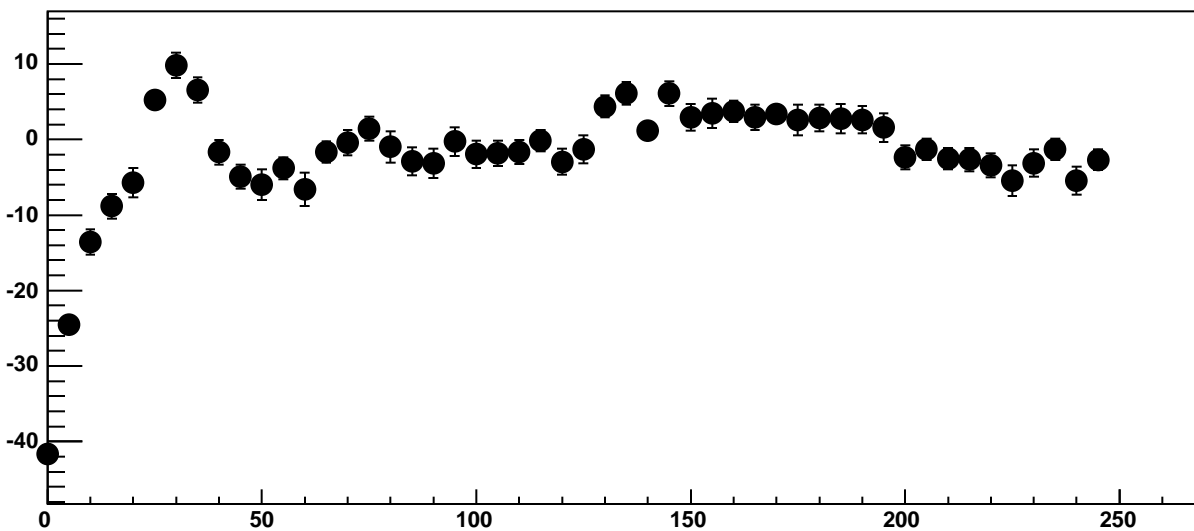


χ^2 / ndf	262.2 / 41
p0	-112.1 ± 0.5373
p1	50.9 ± 0.7653
p2	-3.742e+07 ± 1.188e+06
p3	2.185e+07 ± 1.801e+05
p4	1.659 ± 0.05311

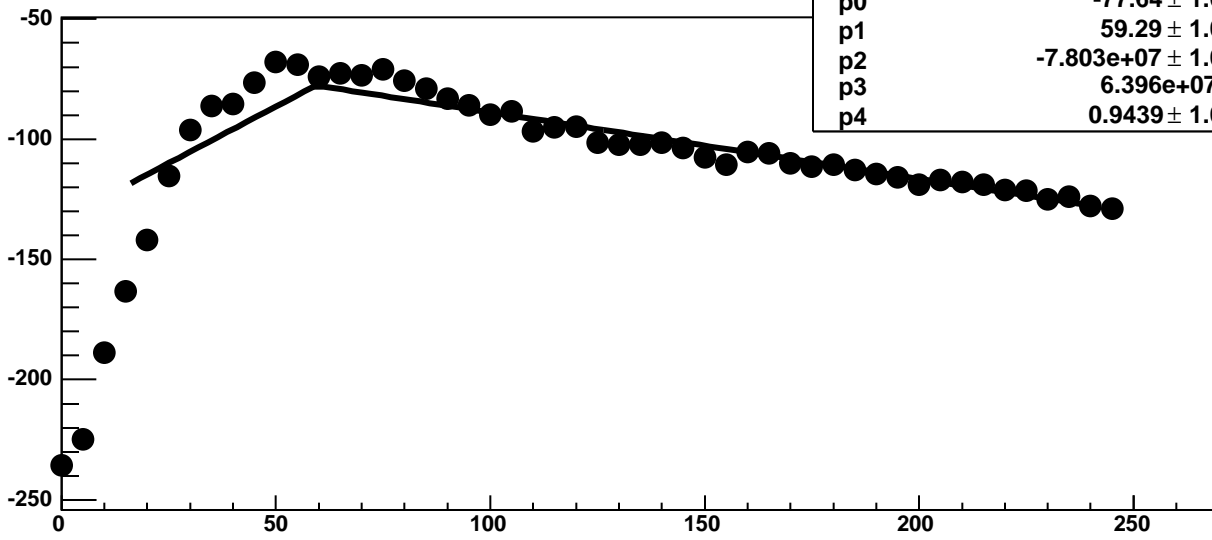
Chip 5, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

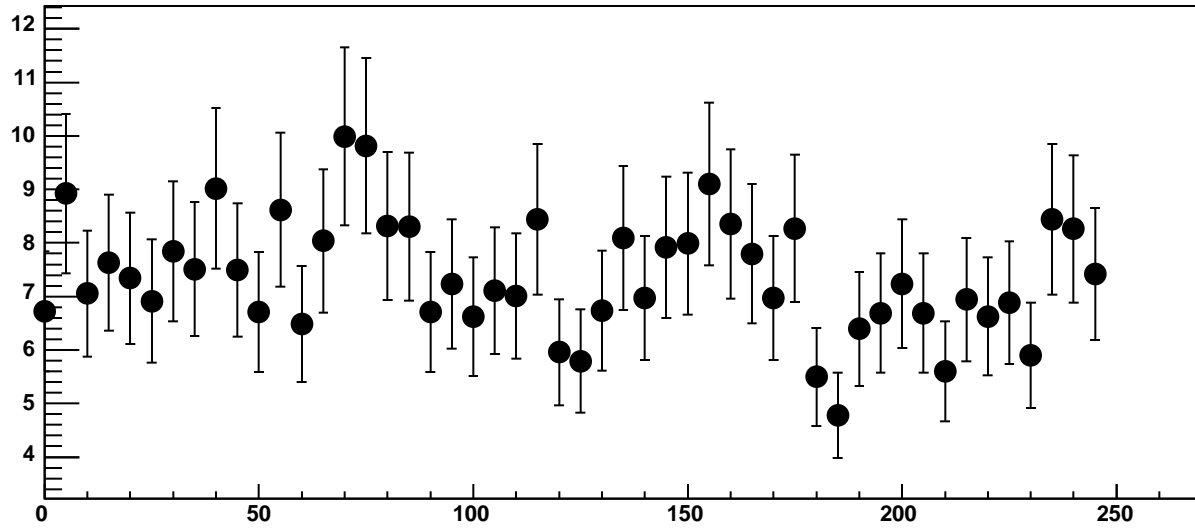


Chip 5, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

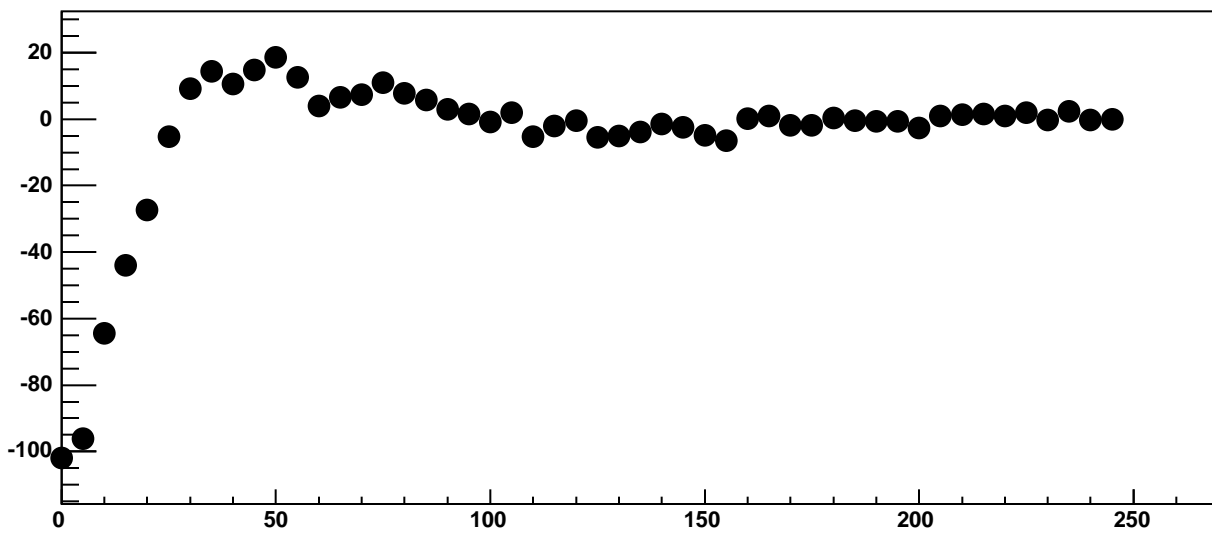


χ^2 / ndf	1443 / 41
p0	$-77.64 \pm 1.089e+10$
p1	$59.29 \pm 1.089e+10$
p2	$-7.803e+07 \pm 1.024e+10$
p3	$6.396e+07 \pm 1599$
p4	$0.9439 \pm 1.089e+10$

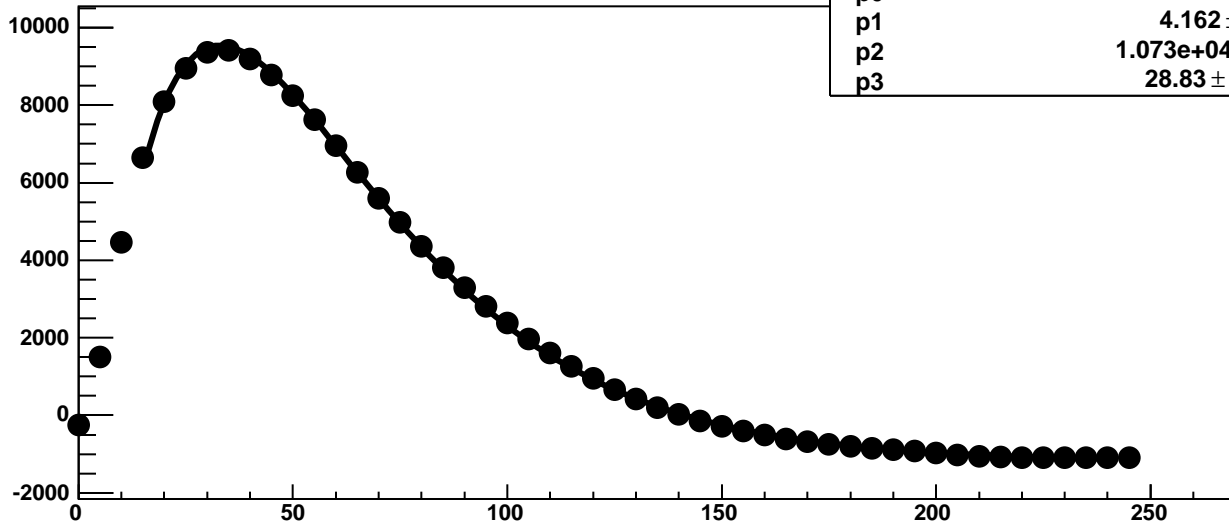
Chip 5, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

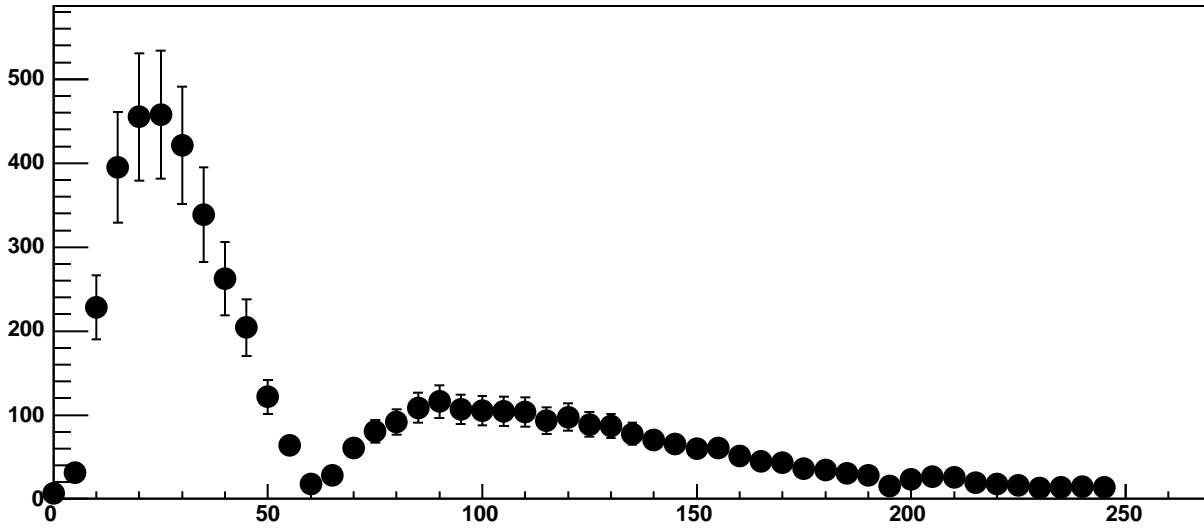


Chip 5, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold

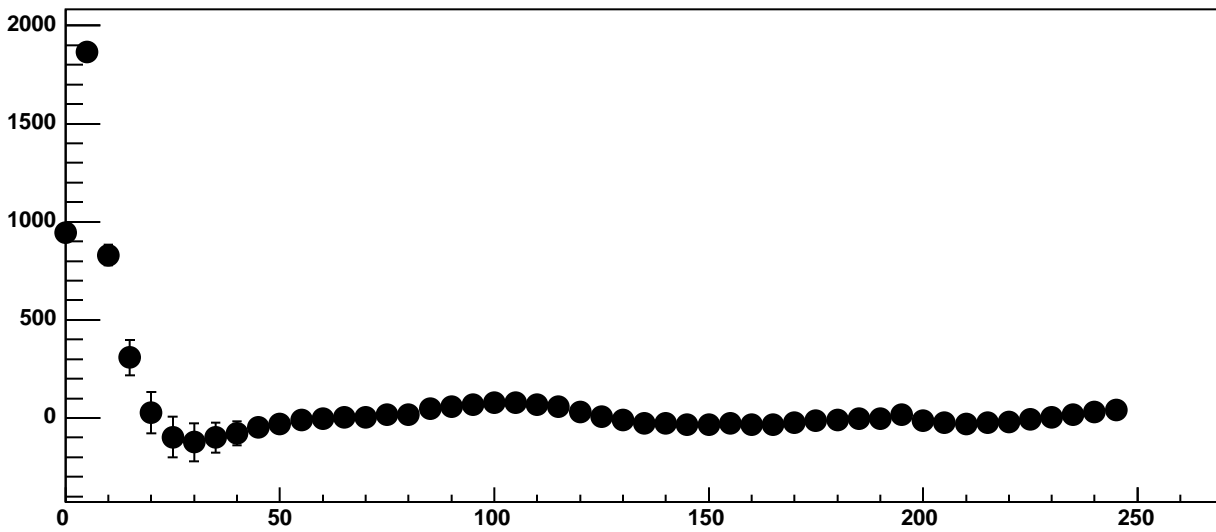


χ^2 / ndf	352.7 / 42
p0	-1188 ± 1.738
p1	4.162 ± 0.1398
p2	1.073e+04 ± 18.65
p3	28.83 ± 0.04549

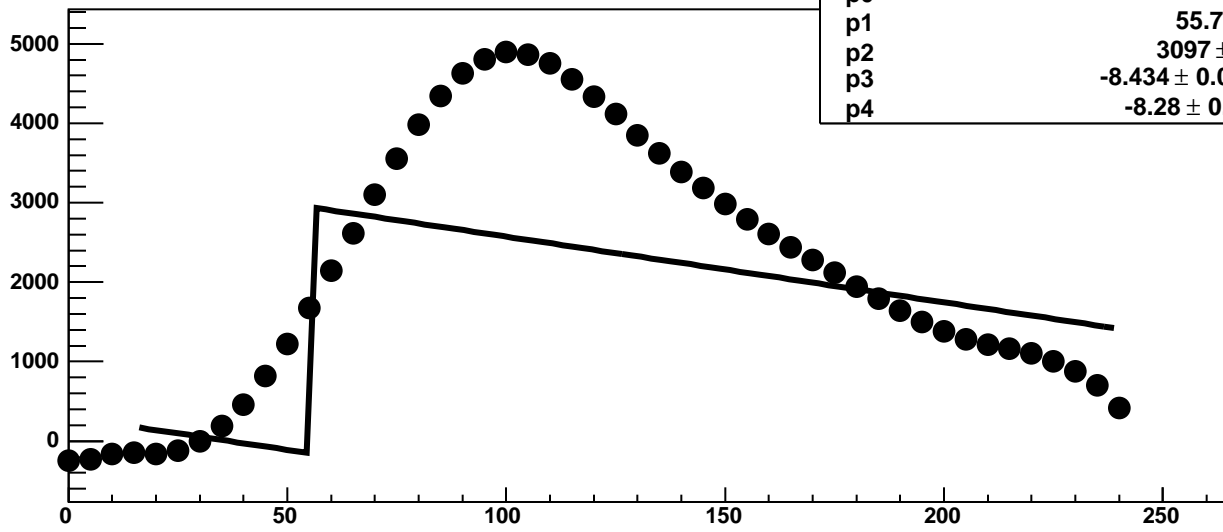
Chip 5, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold

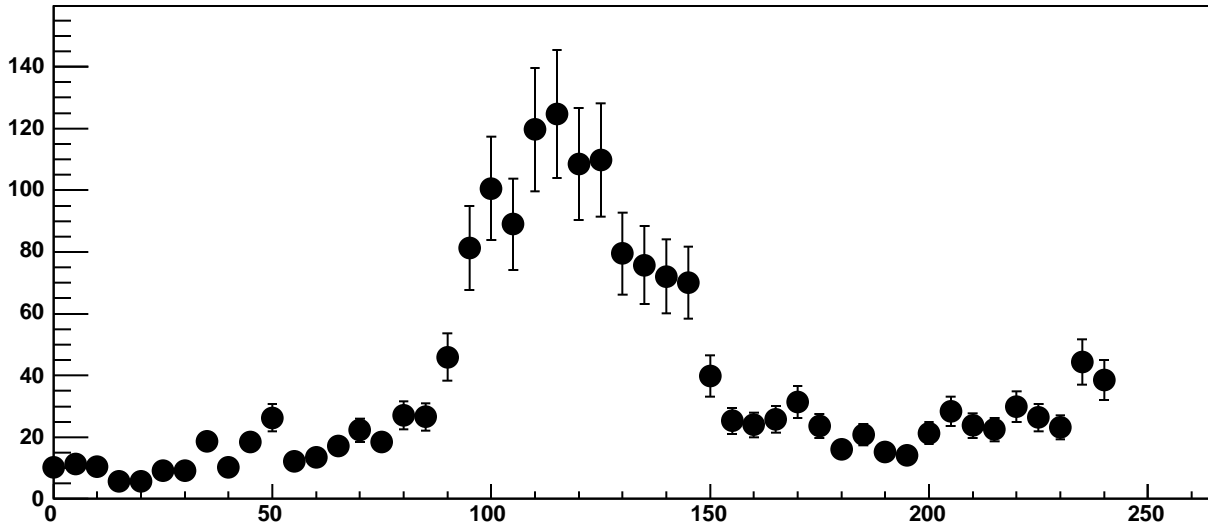


Chip 5, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

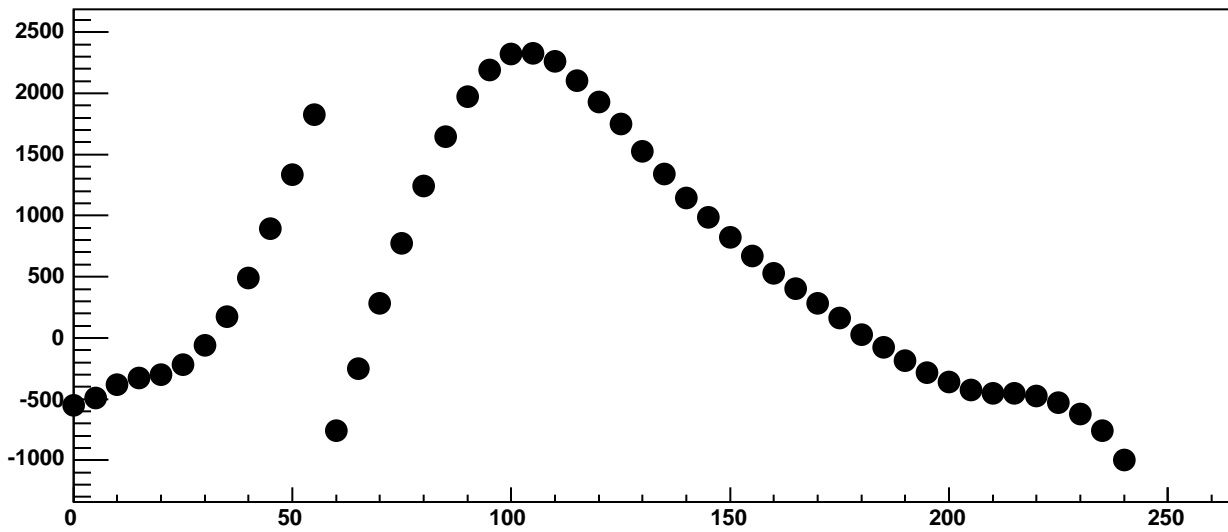


χ^2 / ndf	1.138e+06 / 41
p0	-157 ± 0.2313
p1	55.7 ± 0.028
p2	3097 ± 0.6137
p3	-8.434 ± 0.0002067
p4	-8.28 ± 0.004818

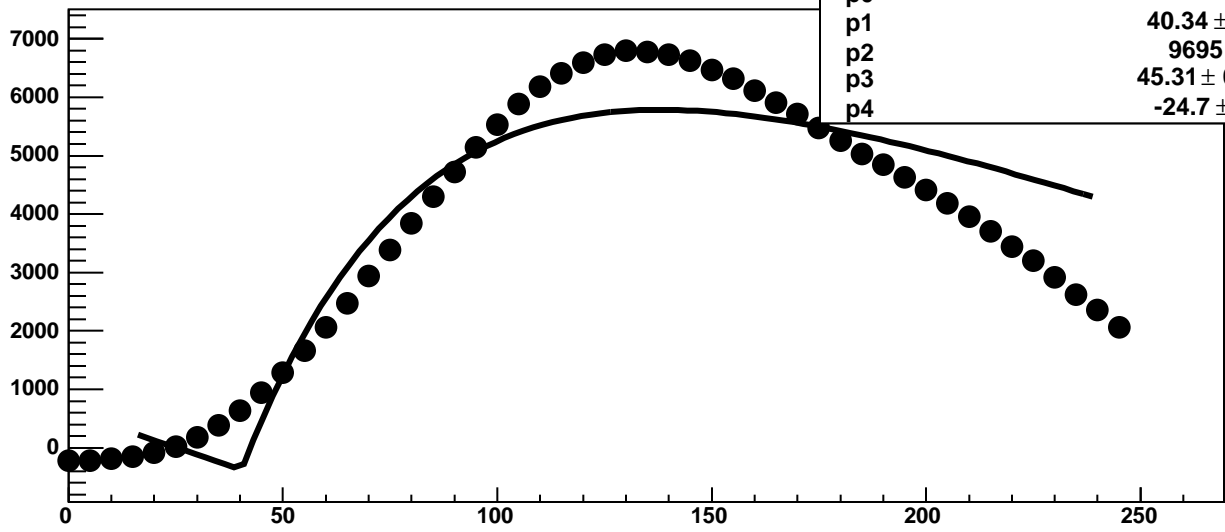
Chip 5, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

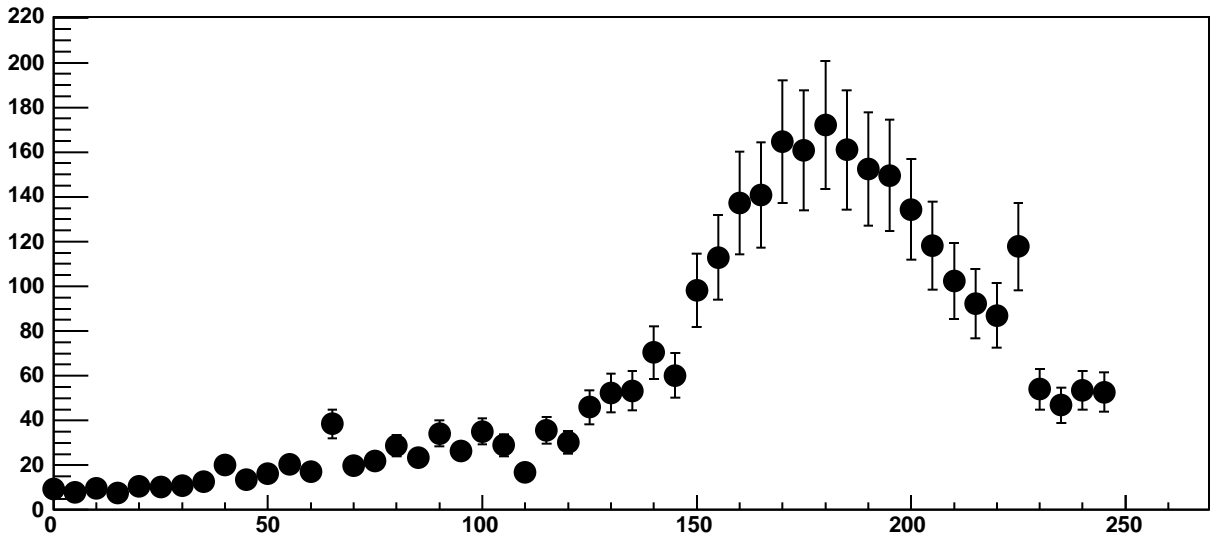


Chip 5, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

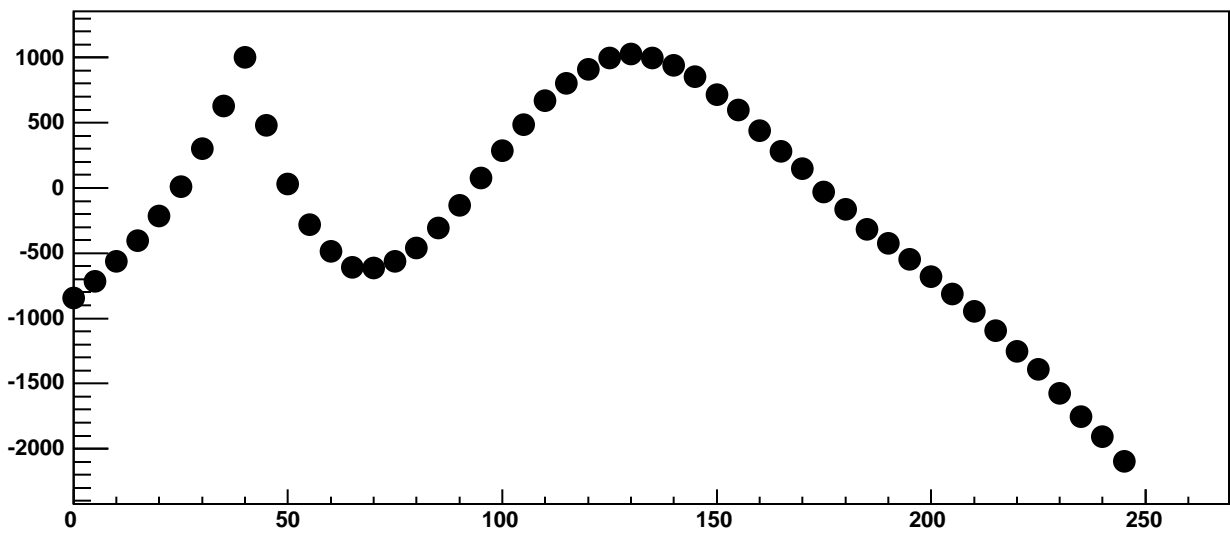


χ^2 / ndf	4.352e+05 / 41
p0	-374.3 ± 1.422
p1	40.34 ± 0.0165
p2	9695 ± 13.75
p3	45.31 ± 0.07182
p4	-24.7 ± 0.0727

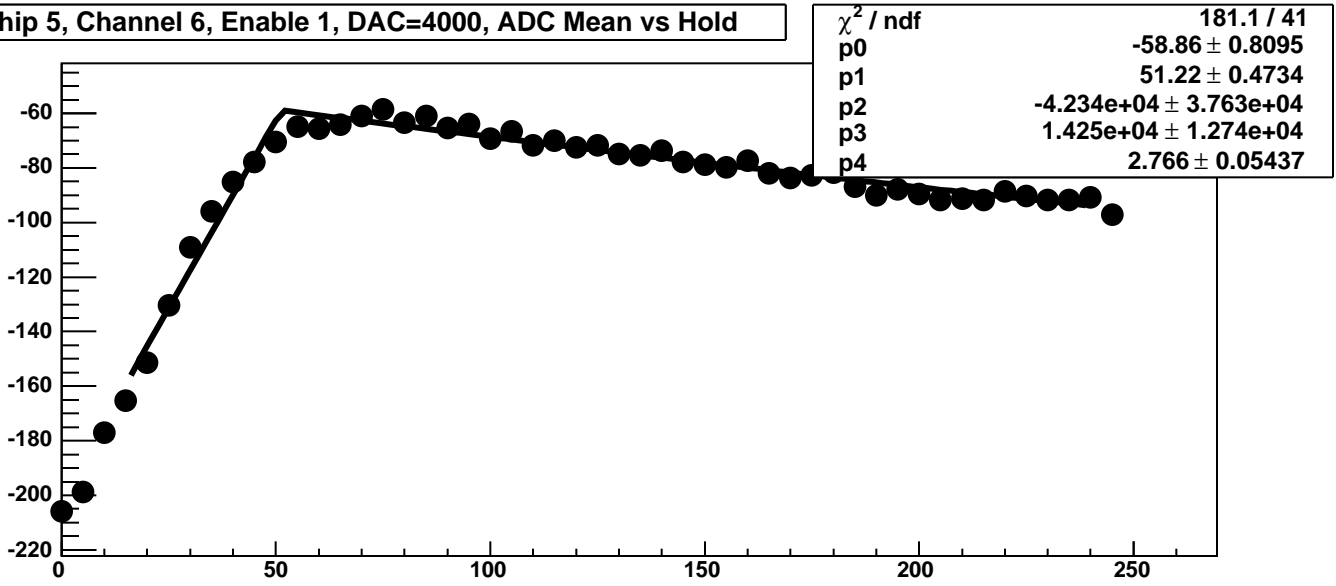
Chip 5, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



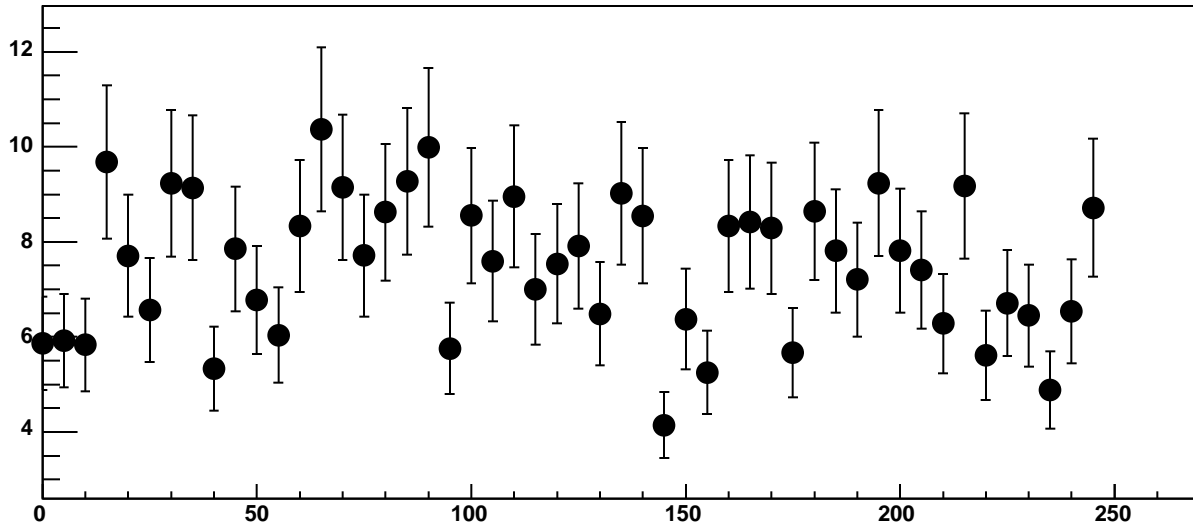
Chip 5, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold



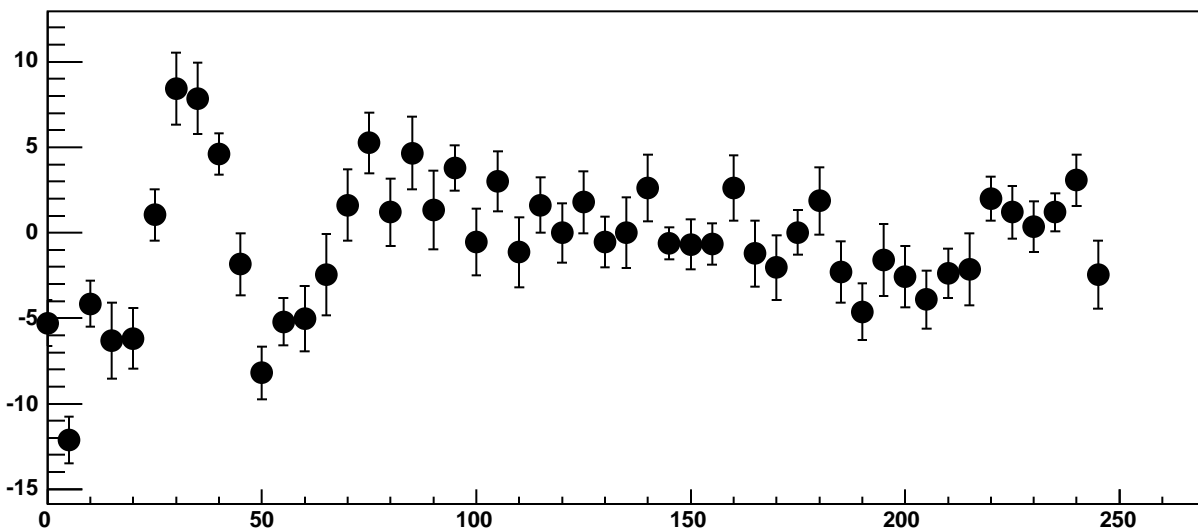
Chip 5, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold



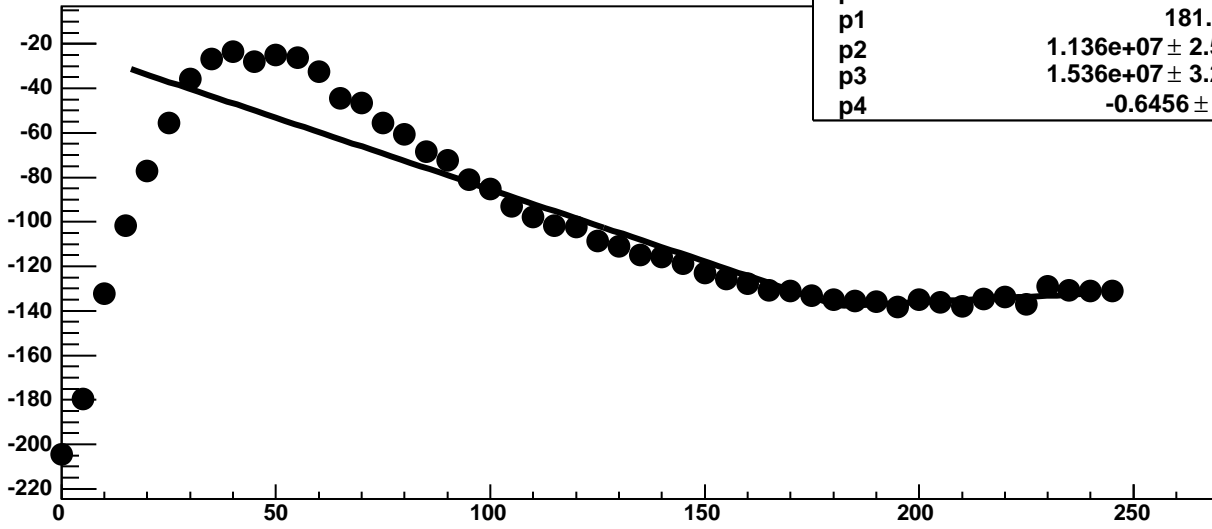
Chip 5, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



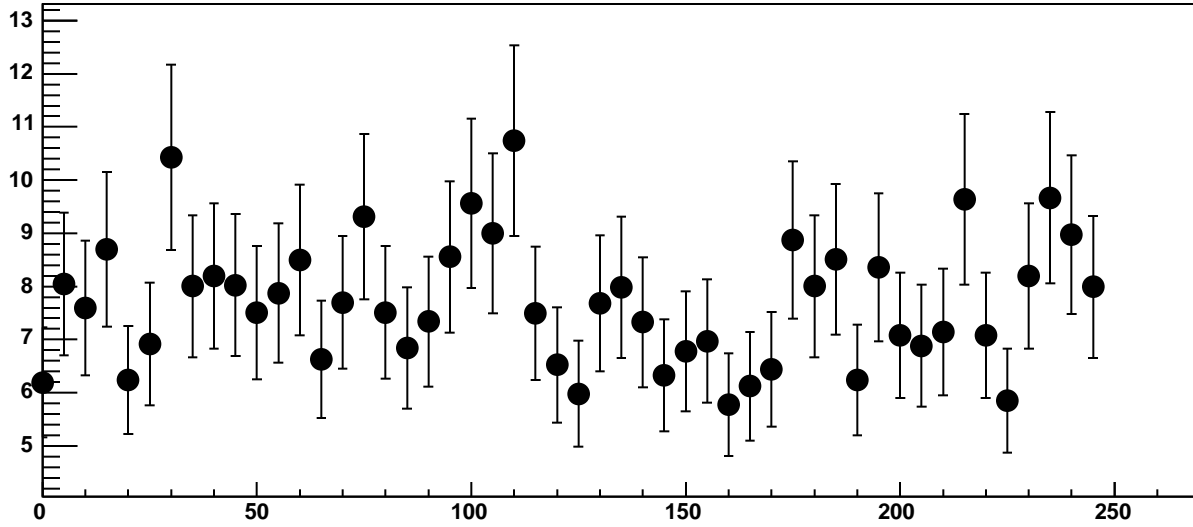
Chip 5, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold



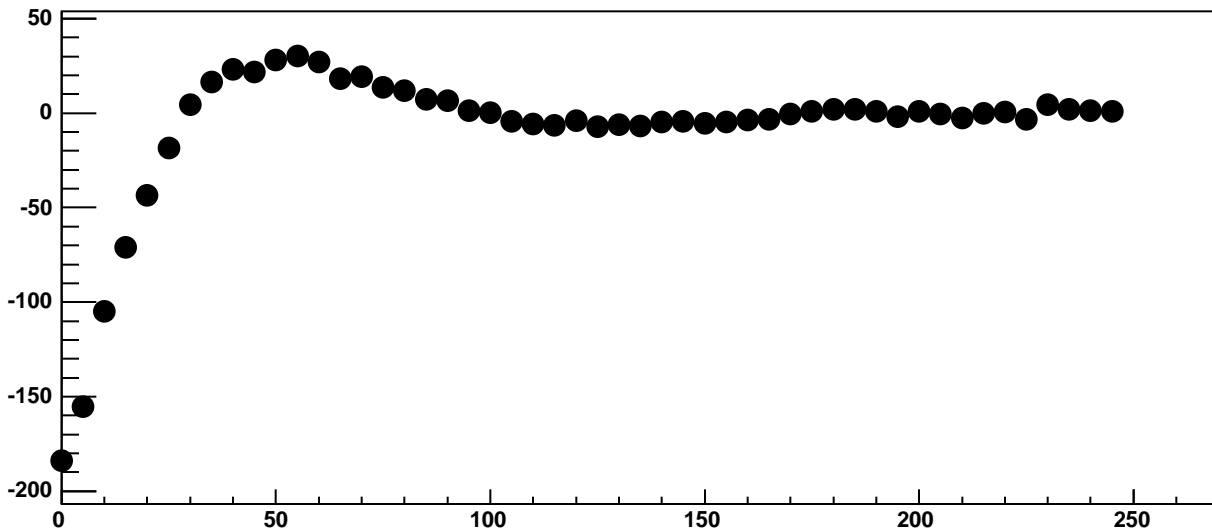
Chip 5, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold



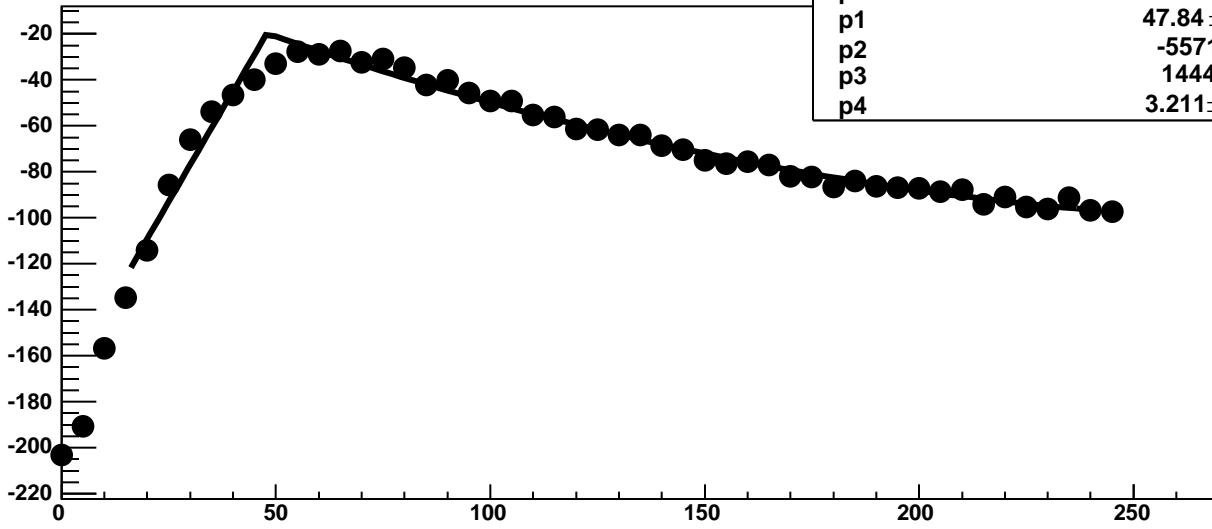
Chip 5, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold

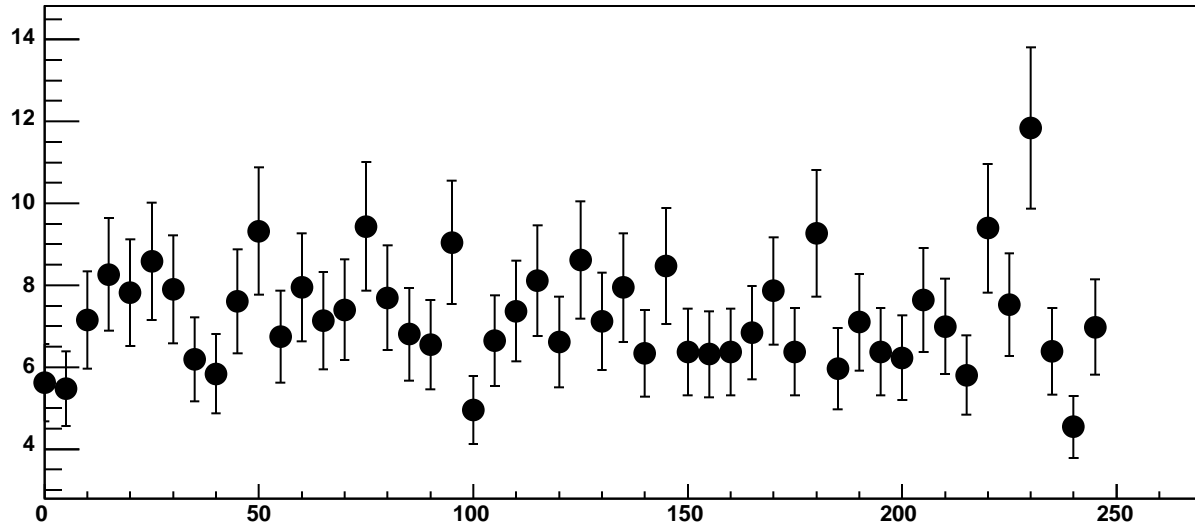


Chip 5, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

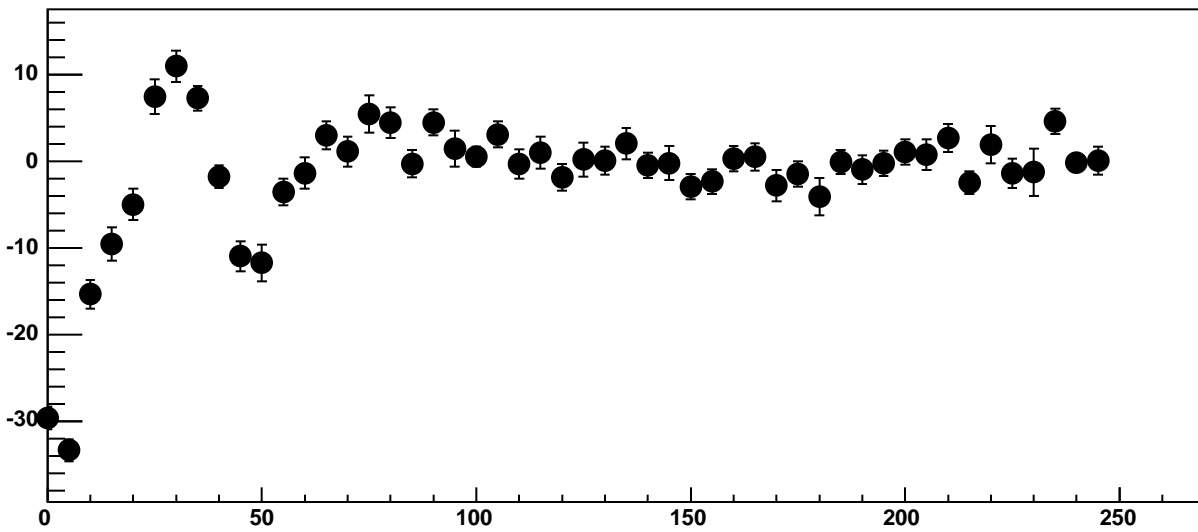


χ^2 / ndf	253.2 / 41
p0	-19.75 ± 0.6827
p1	47.84 ± 0.3618
p2	-5571 ± 356.5
p3	1444 ± 90.04
p4	3.211 ± 0.0644

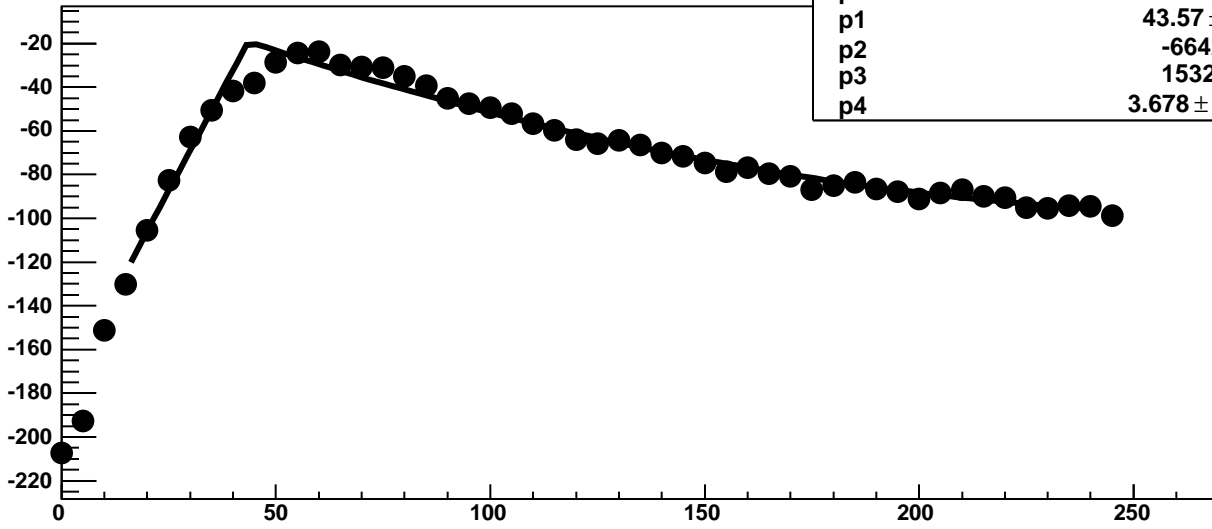
Chip 5, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

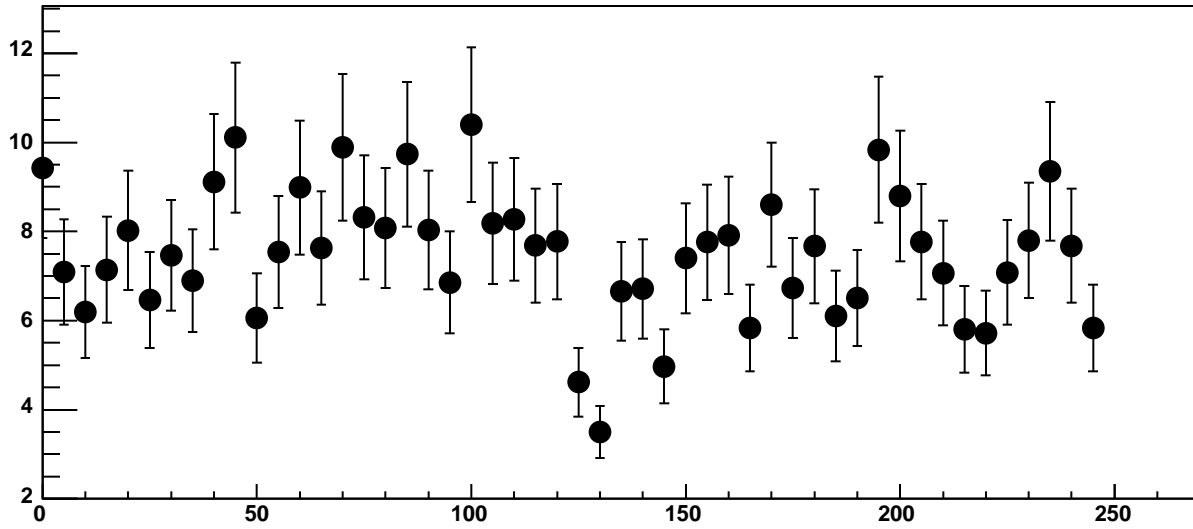


Chip 5, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

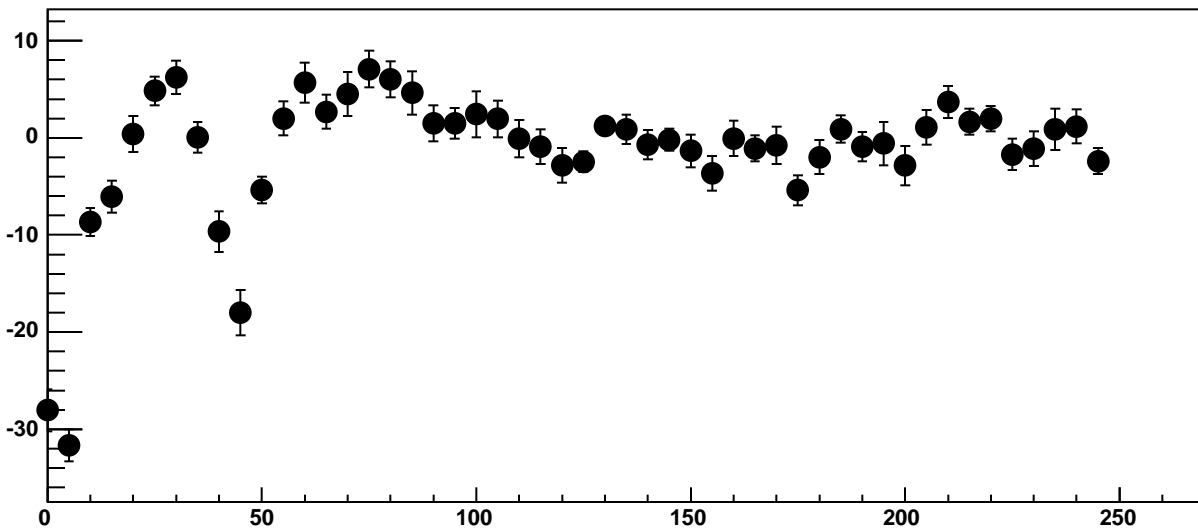


χ^2 / ndf	226.1 / 41
p0	-19.06 ± 0.7631
p1	43.57 ± 0.4173
p2	-6642 ± 486
p3	1532 ± 104.7
p4	3.678 ± 0.08518

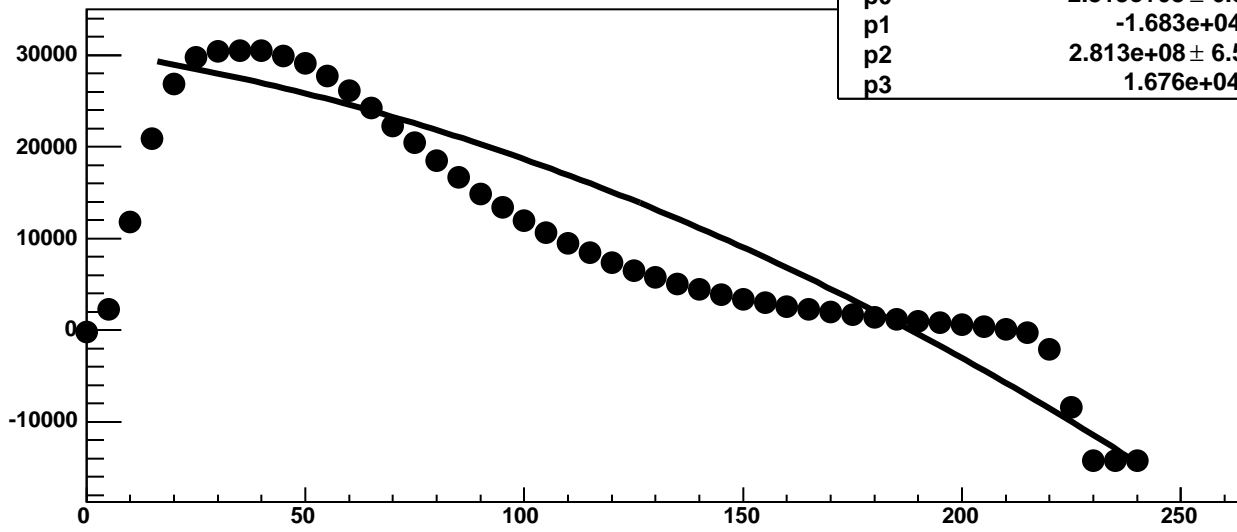
Chip 5, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

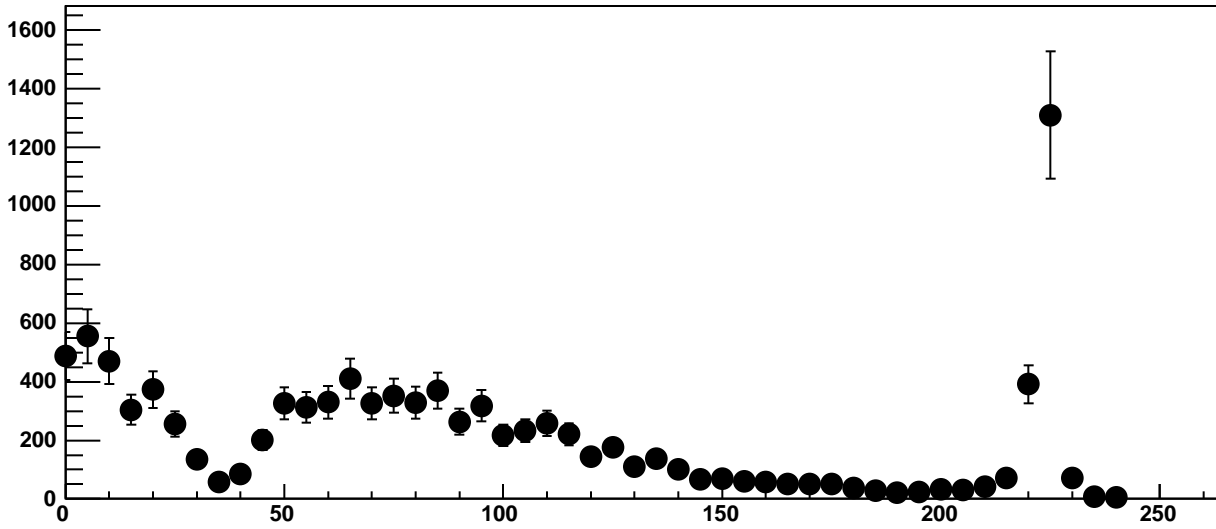


Chip 5, Channel 6, Enable 5!, DAC=4000, ADC Mean vs Hold

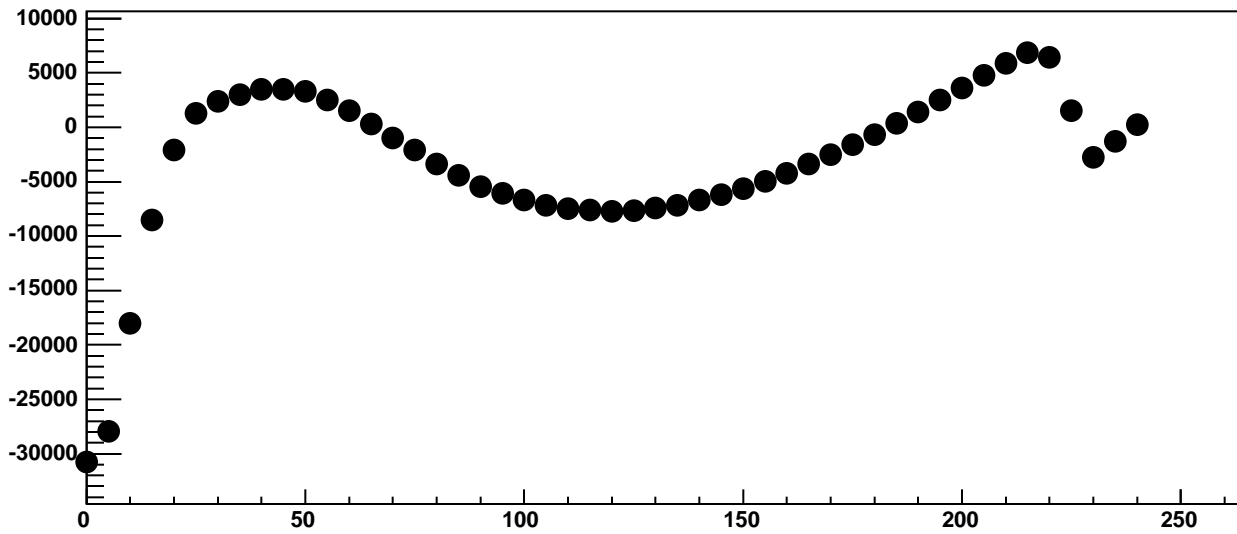


χ^2 / ndf	3.224e+06 / 42
p0	-2.813e+08 ± 6.537e+05
p1	-1.683e+04 ± 23.68
p2	2.813e+08 ± 6.537e+05
p3	1.676e+04 ± 23.54

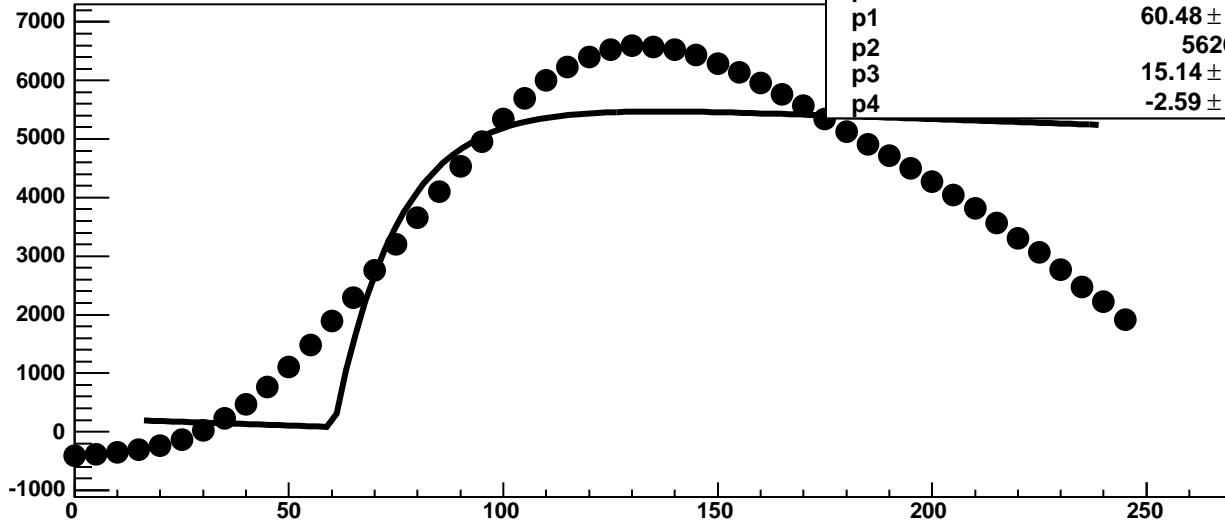
Chip 5, Channel 6, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 6, Enable 5!, DAC=4000, ADC Residuals vs Hold

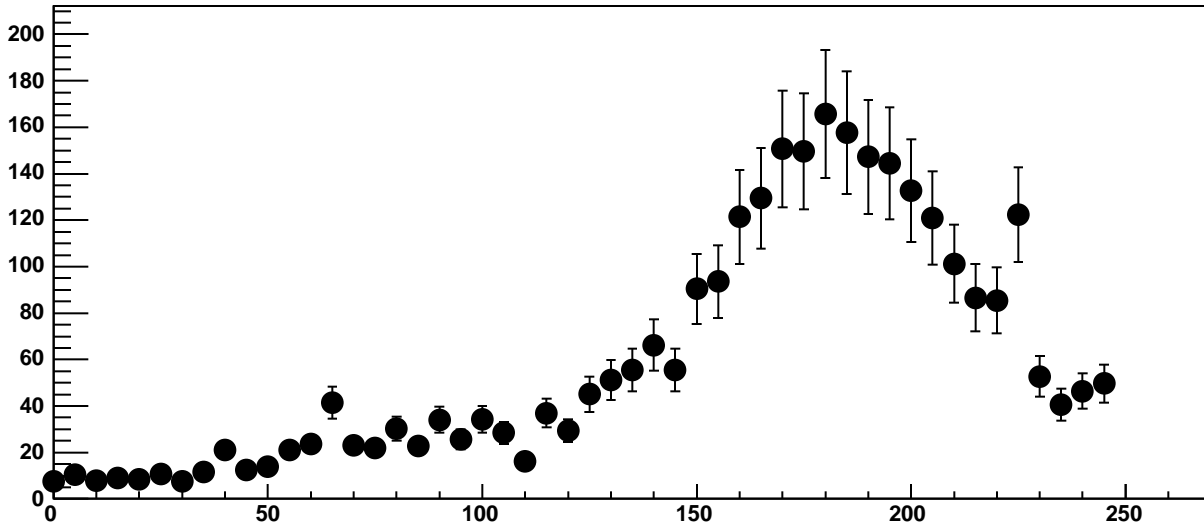


Chip 5, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

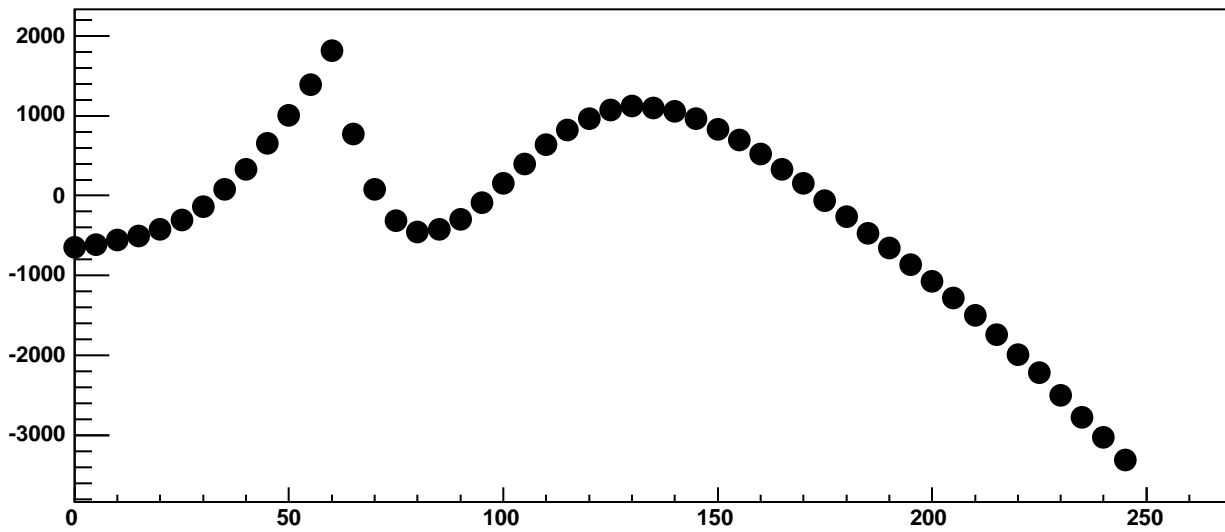


χ^2 / ndf	8.515e+05 / 41
p0	78.67 ± 1.501
p1	60.48 ± 0.02533
p2	5626 ± 5.37
p3	15.14 ± 0.03467
p4	-2.59 ± 0.04095

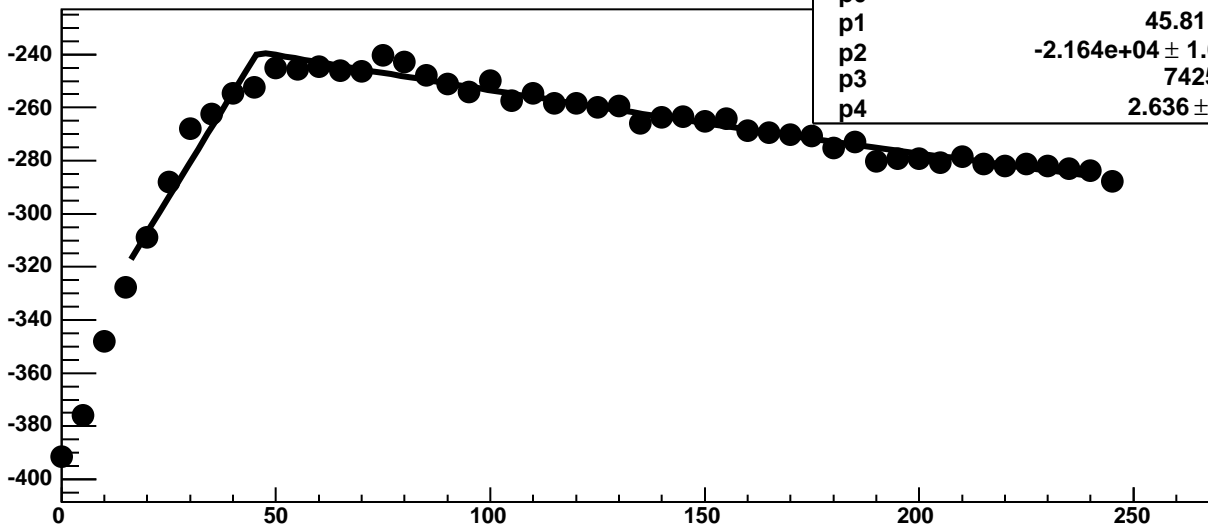
Chip 5, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

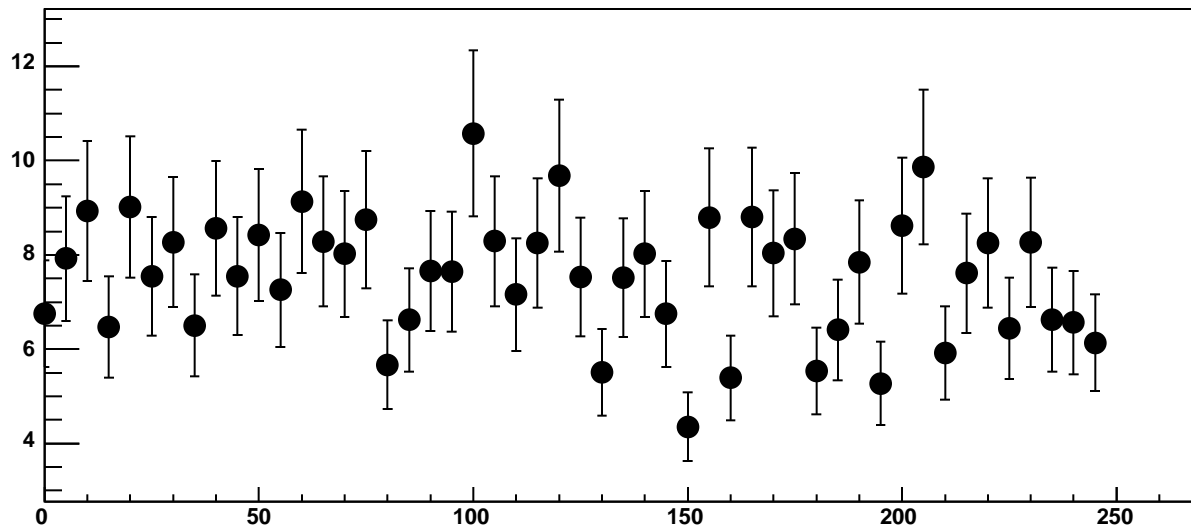


Chip 5, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold

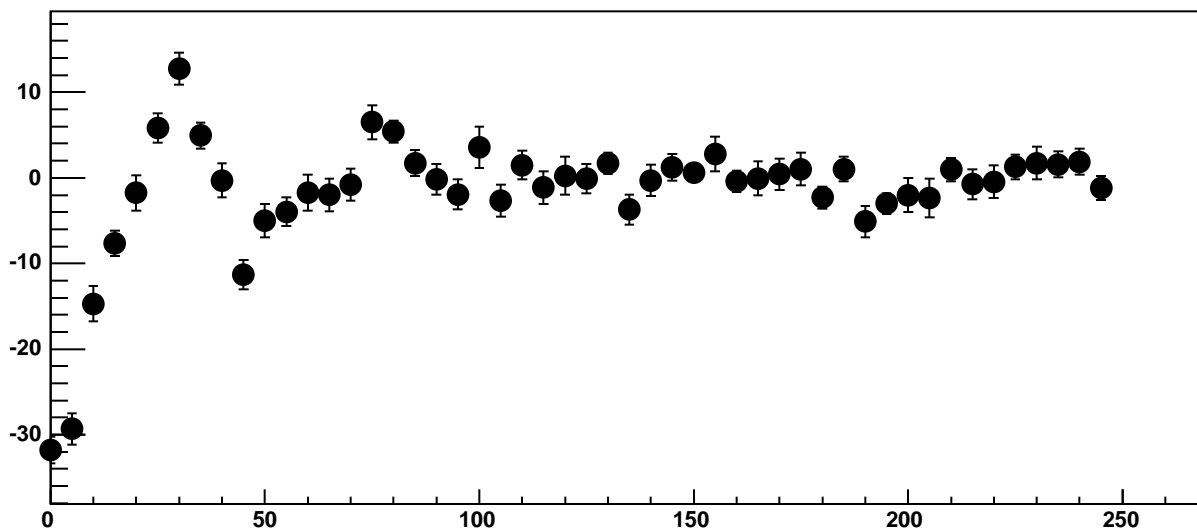


χ^2 / ndf	222 / 41
p0	-239 ± 0.806
p1	45.81 ± 0.5179
p2	$-2.164\text{e}+04 \pm 1.022\text{e}+04$
p3	7425 ± 3541
p4	2.636 ± 0.06398

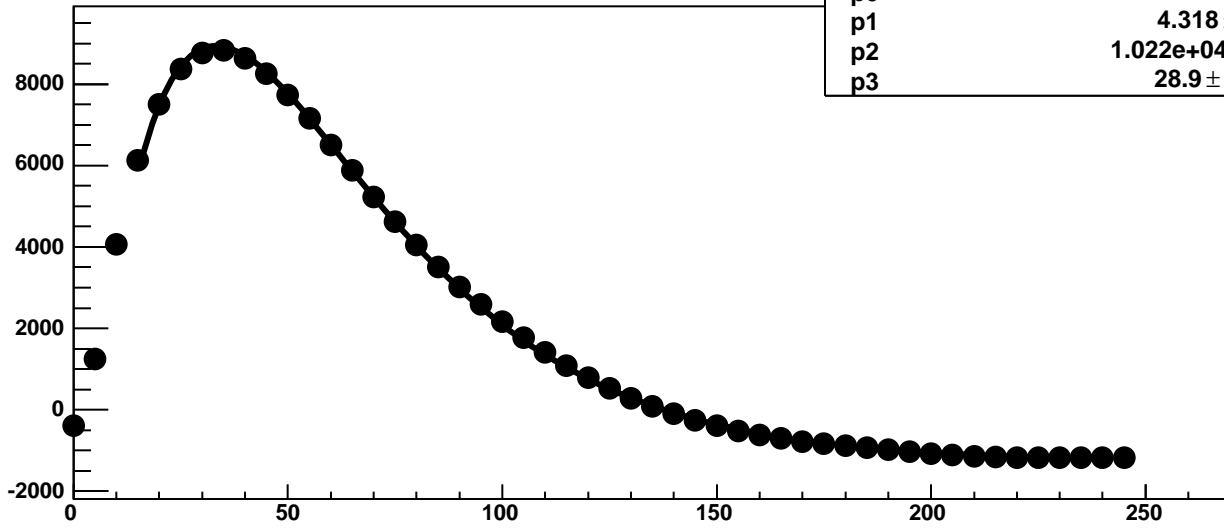
Chip 5, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

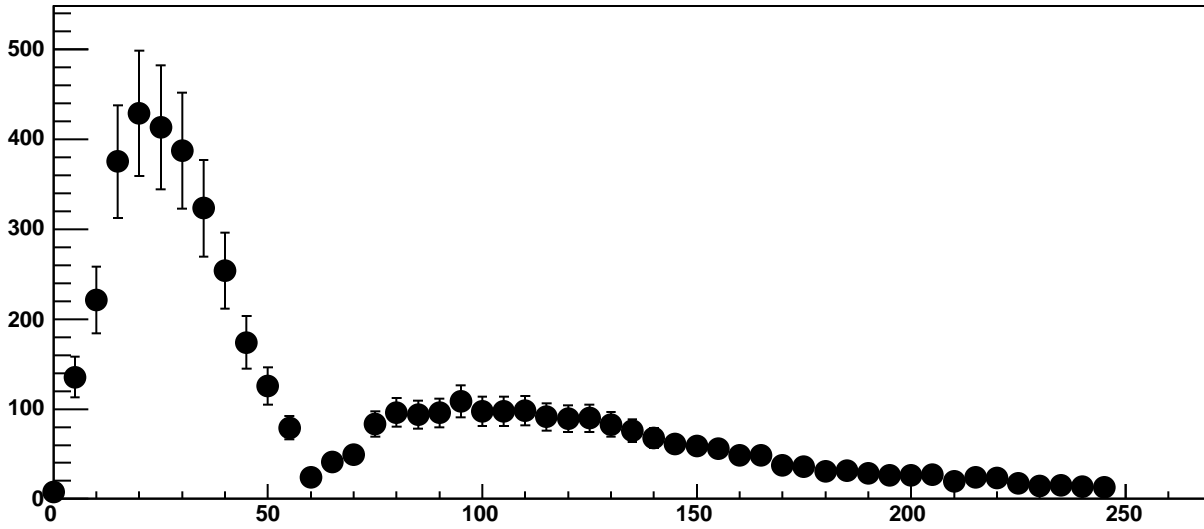


Chip 5, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold

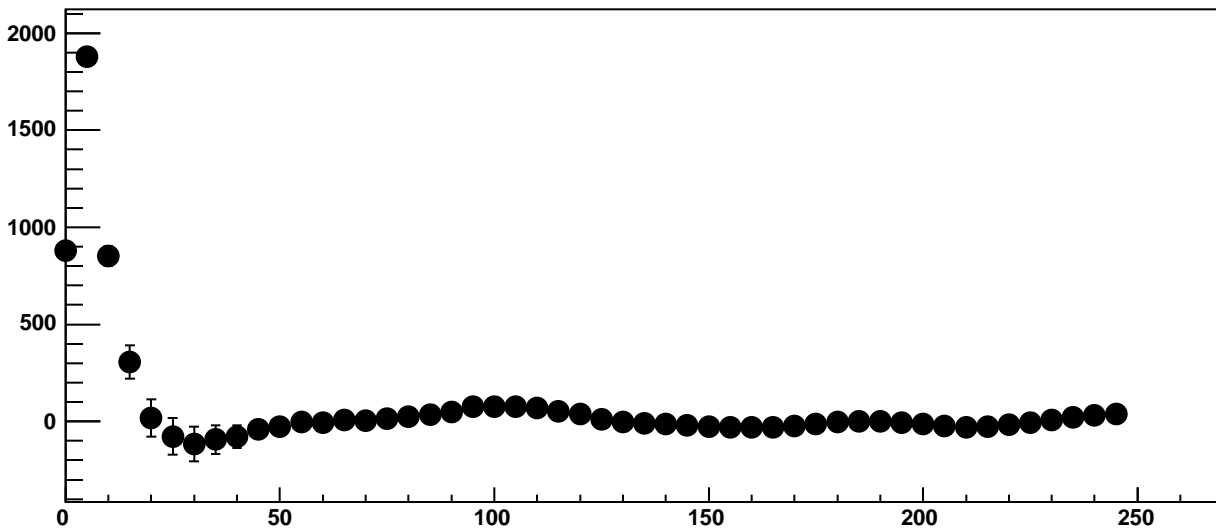


χ^2 / ndf	361.4 / 42
p0	-1271 ± 1.791
p1	4.318 ± 0.1381
p2	$1.022\text{e}+04 \pm 17.63$
p3	28.9 ± 0.04662

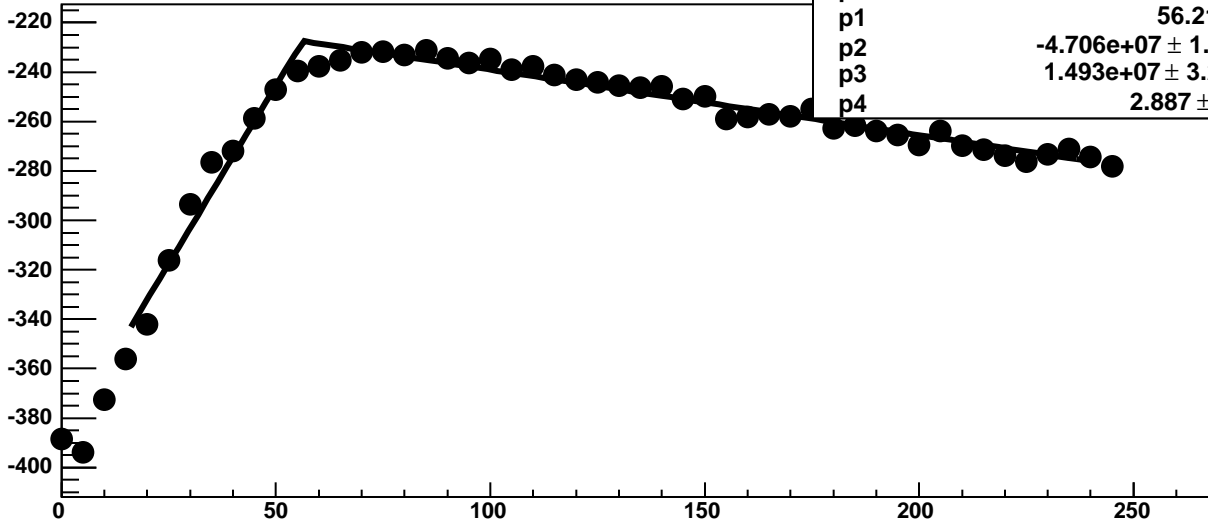
Chip 5, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold



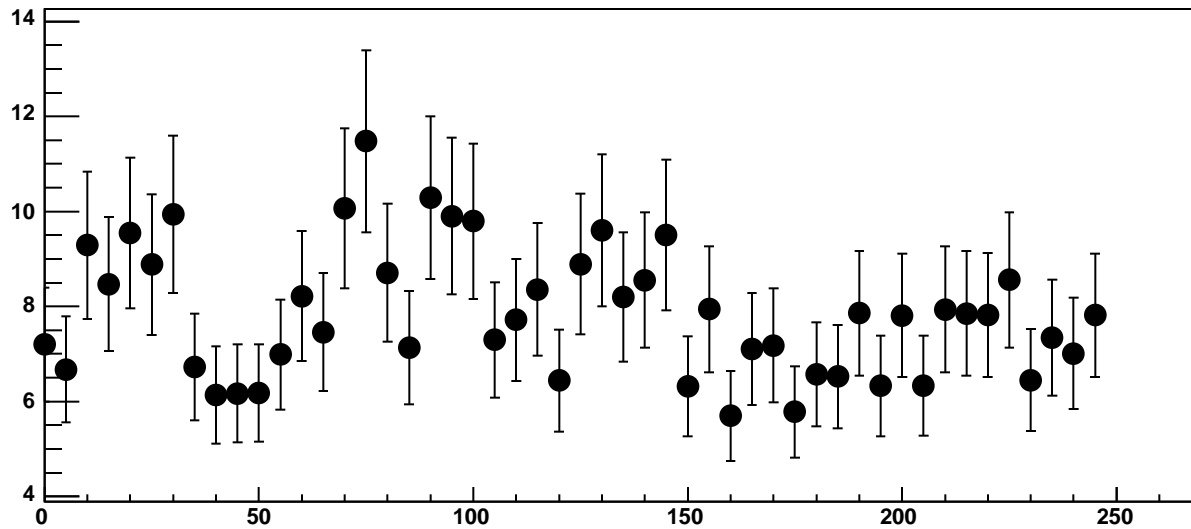
Chip 5, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold



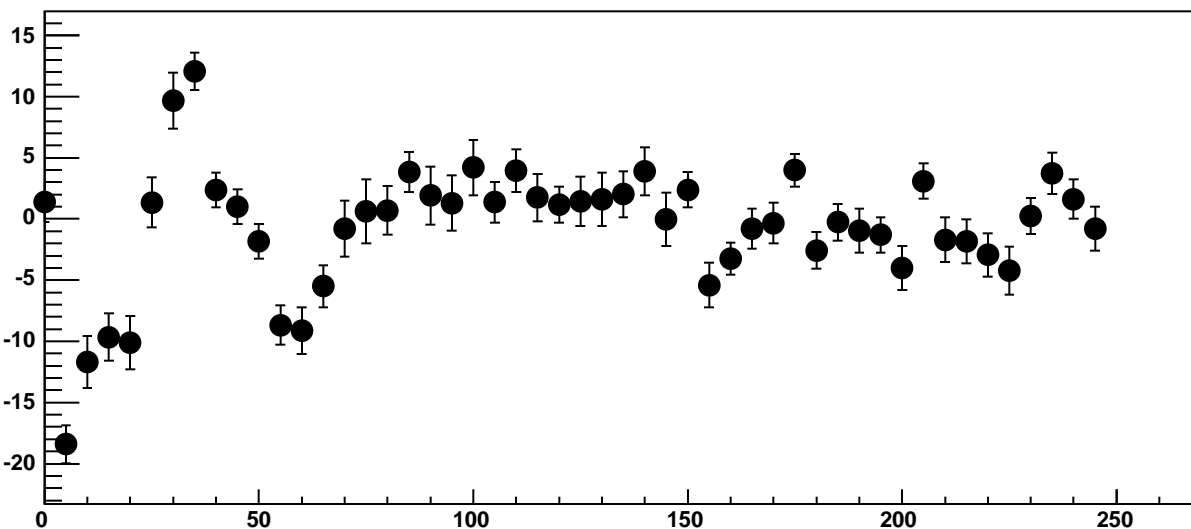
Chip 5, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold



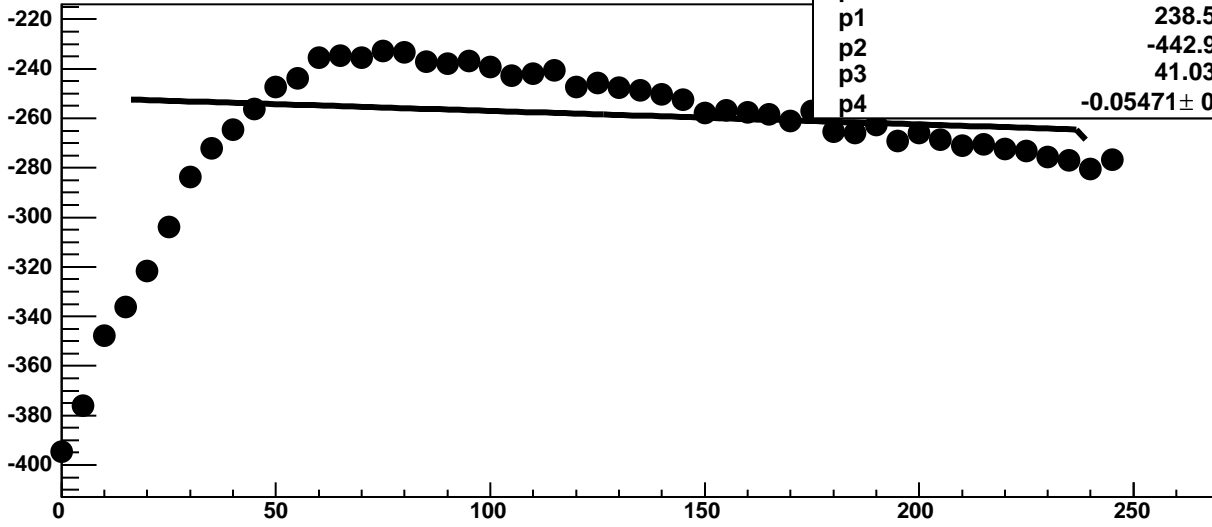
Chip 5, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



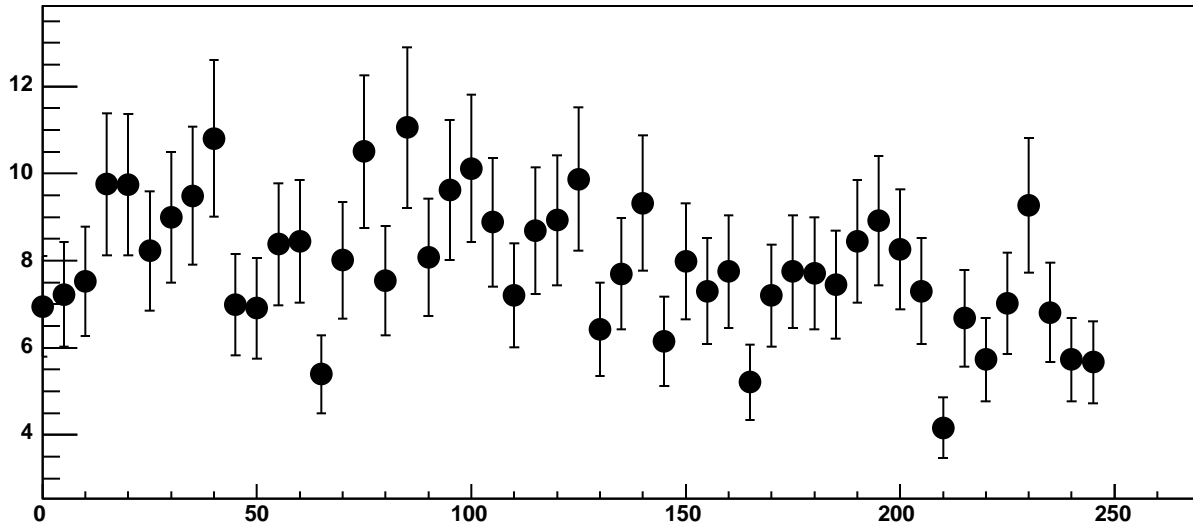
Chip 5, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold



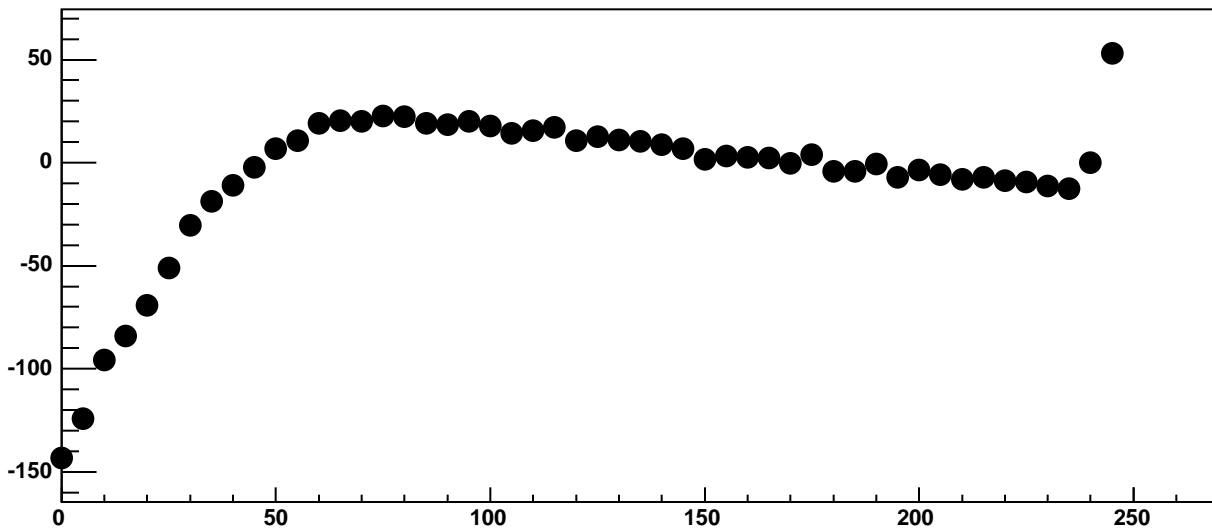
Chip 5, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold



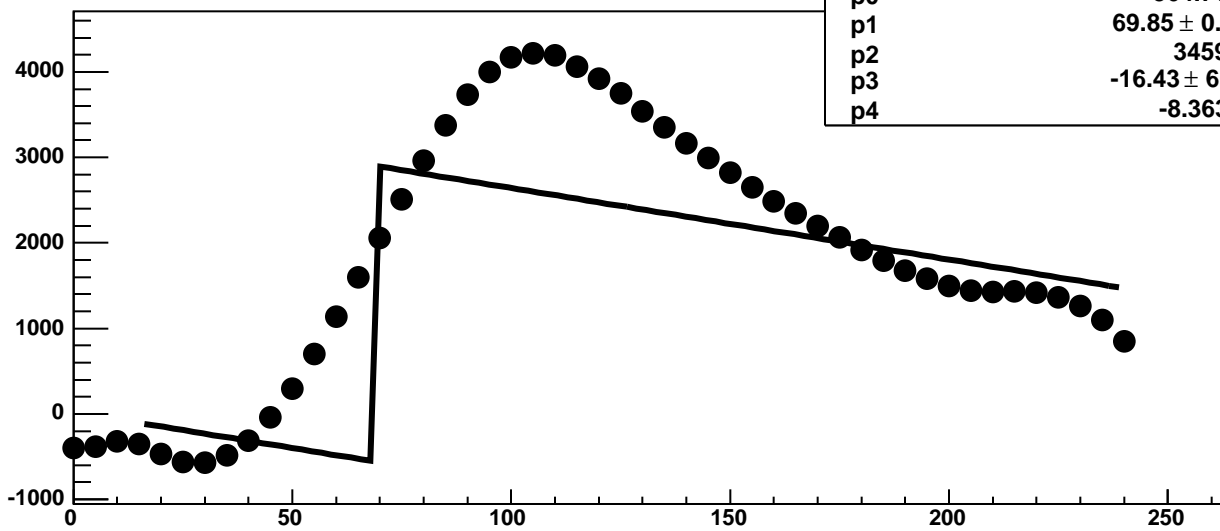
Chip 5, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

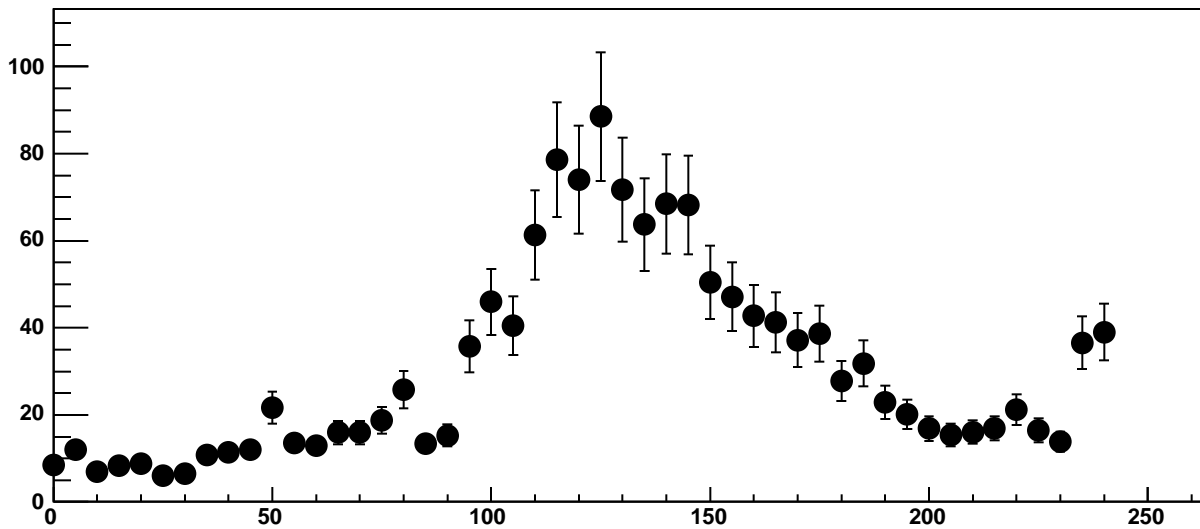


Chip 5, Channel 7, Enable 5, DAC=4000, ADC Mean vs Hold

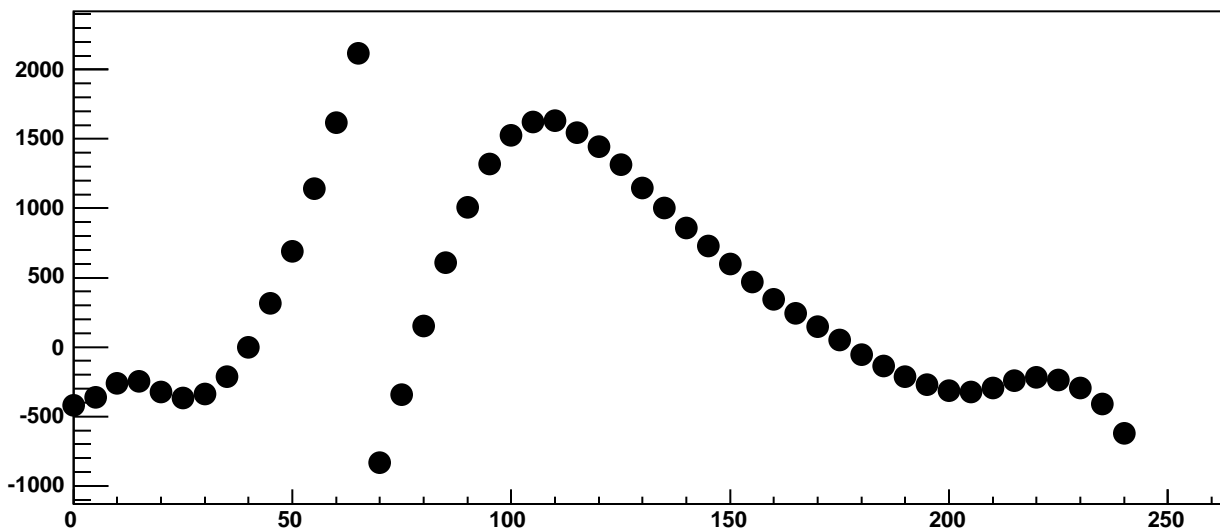


χ^2 / ndf	1.337e+06 / 41
p0	-564.7 ± 0.8776
p1	69.85 ± 0.0003539
p2	3459 ± 2.419
p3	-16.43 ± 6.746e-06
p4	-8.363 ± 0.015

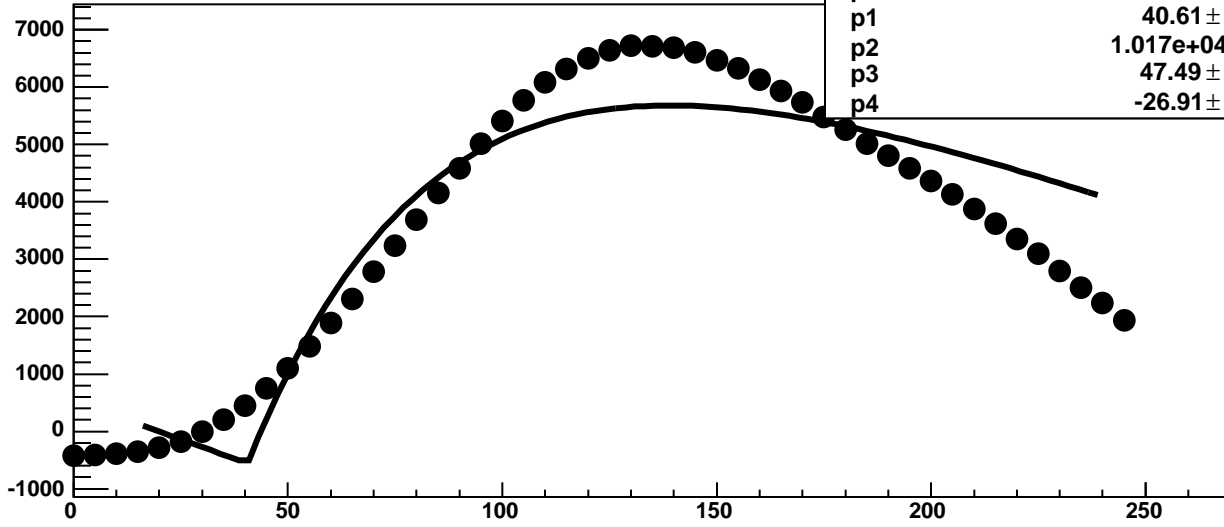
Chip 5, Channel 7, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 7, Enable 5, DAC=4000, ADC Residuals vs Hold

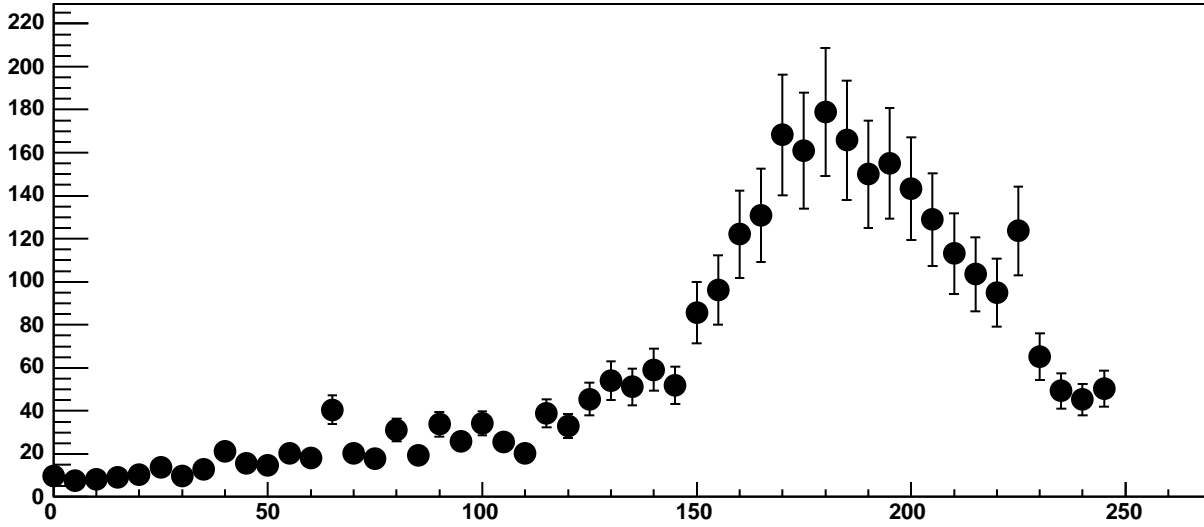


Chip 5, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

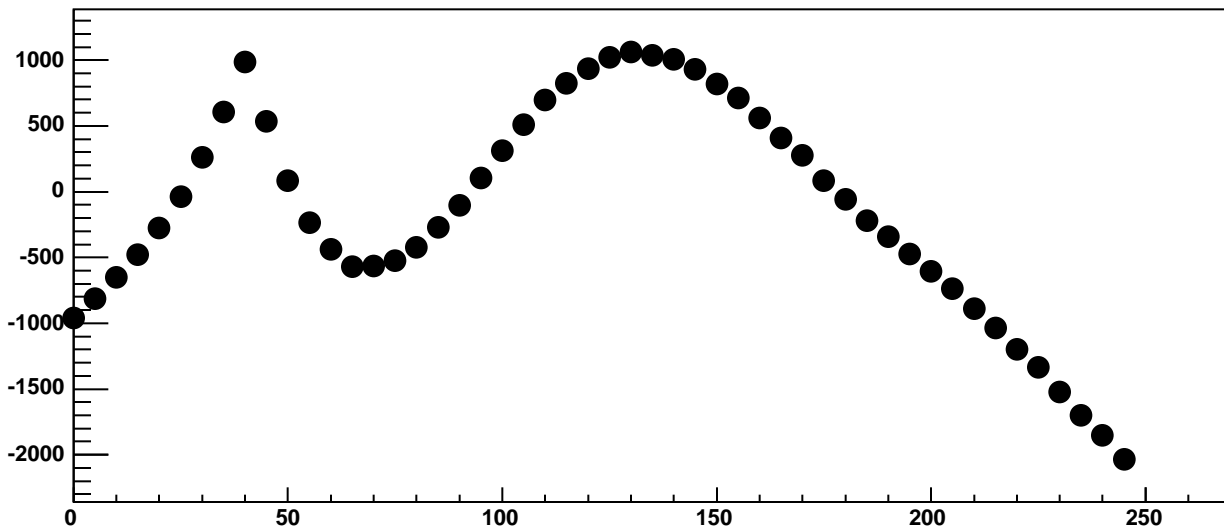


χ^2 / ndf	4.131e+05 / 41
p0	-558.1 ± 1.459
p1	40.61 ± 0.01548
p2	1.017e+04 ± 15.07
p3	47.49 ± 0.07385
p4	-26.91 ± 0.07789

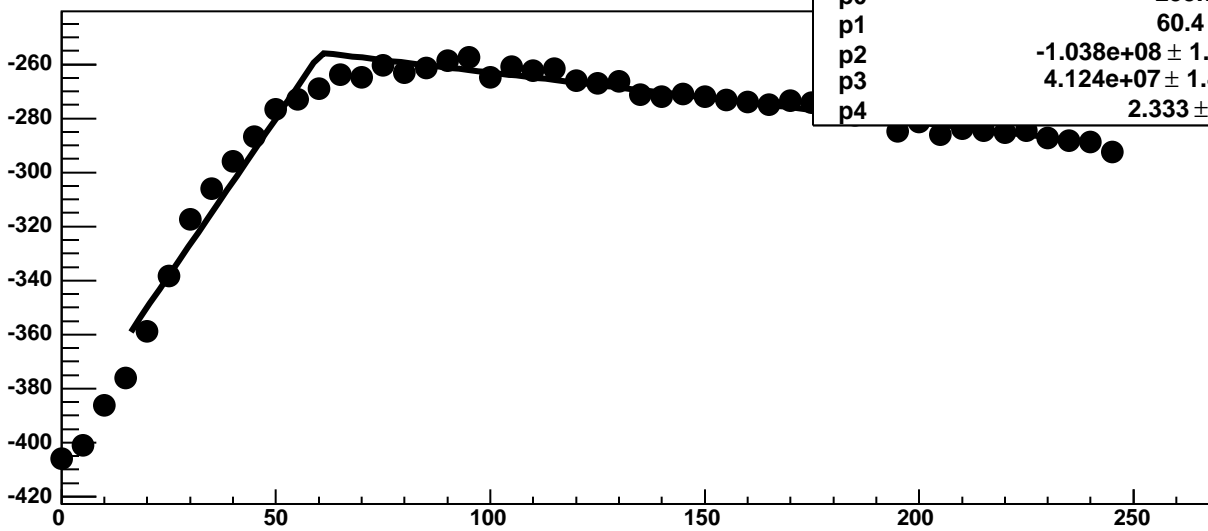
Chip 5, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

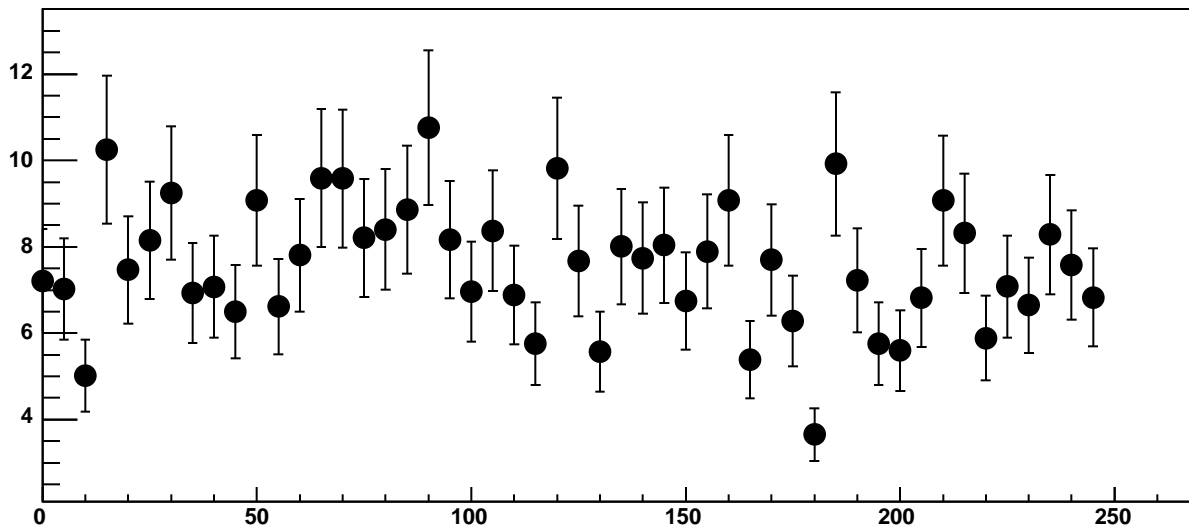


Chip 5, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

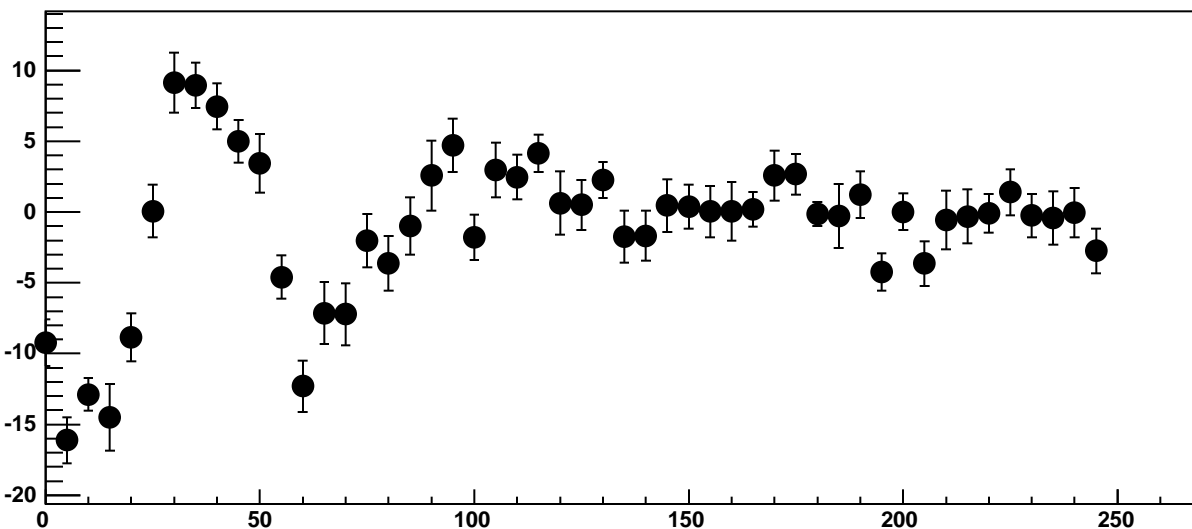


χ^2 / ndf	284 / 41
p0	-255.7 ± 0.588
p1	60.4 ± 0.4809
p2	$-1.038\text{e}+08 \pm 1.707\text{e}+06$
p3	$4.124\text{e}+07 \pm 1.468\text{e}+05$
p4	2.333 ± 0.04055

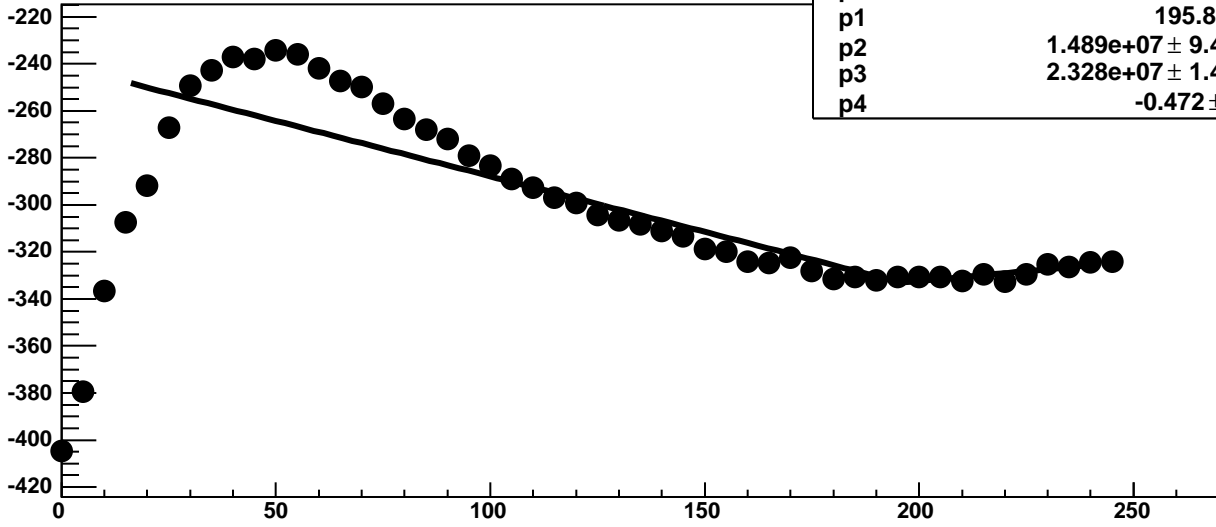
Chip 5, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

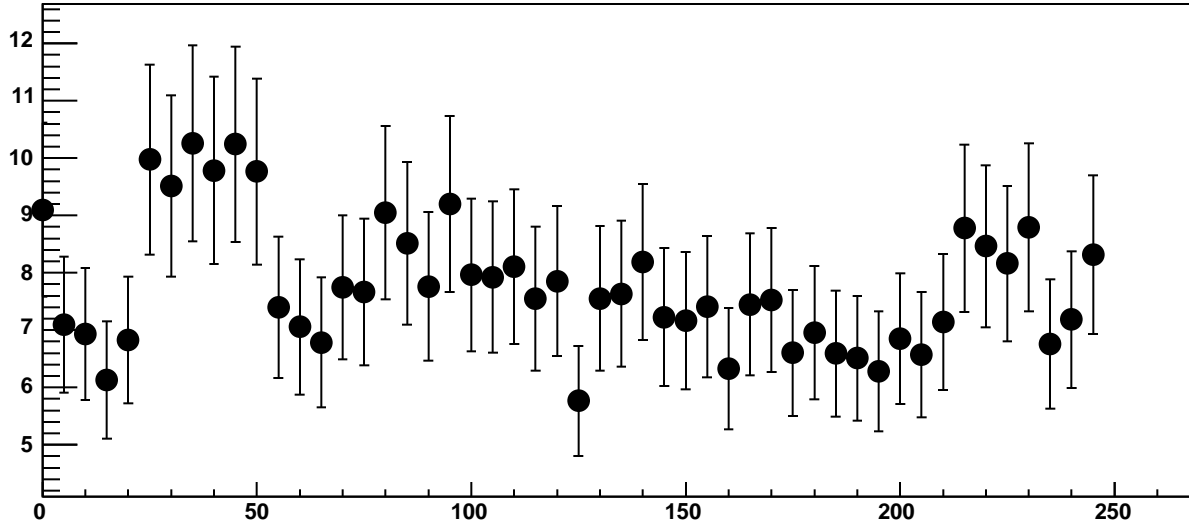


Chip 5, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold

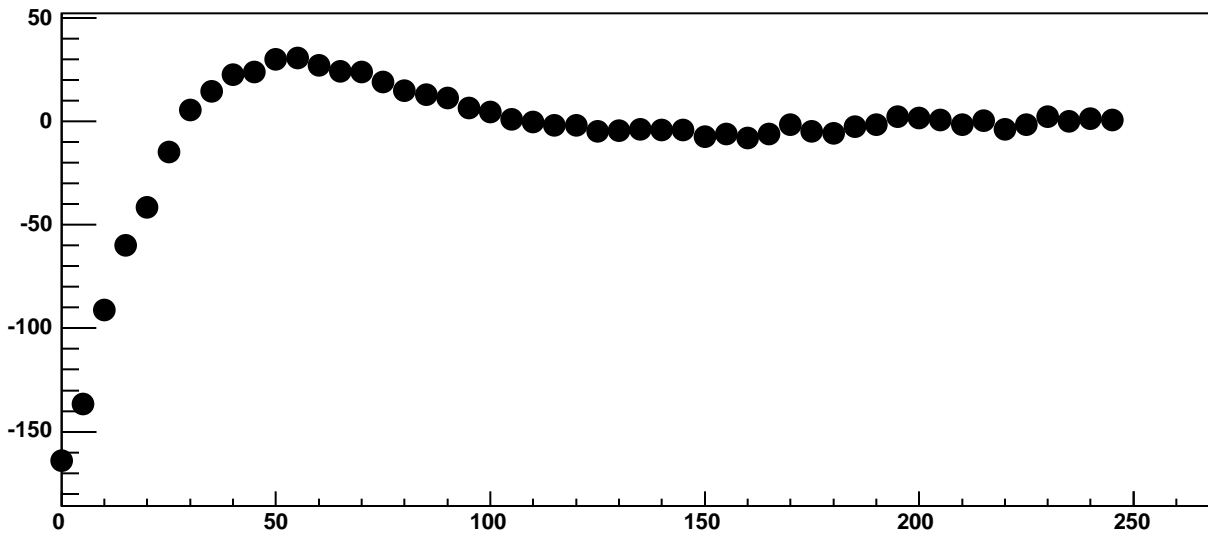


χ^2 / ndf	4473 / 41
p0	-333 ± 0.8472
p1	195.8 ± 1.946
p2	1.489e+07 ± 9.426e+06
p3	2.328e+07 ± 1.425e+07
p4	-0.472 ± 0.0052

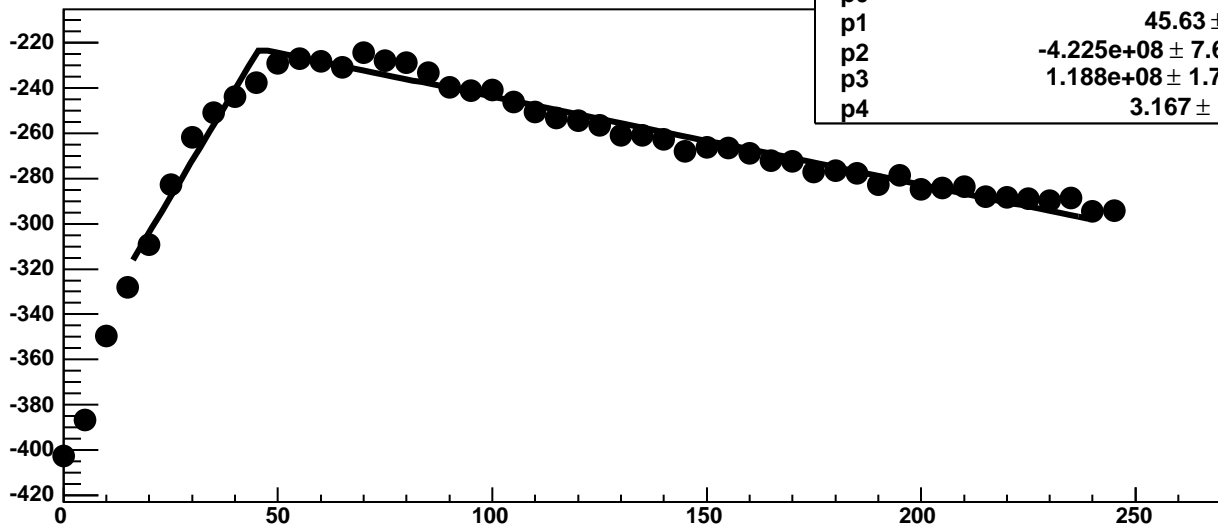
Chip 5, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold

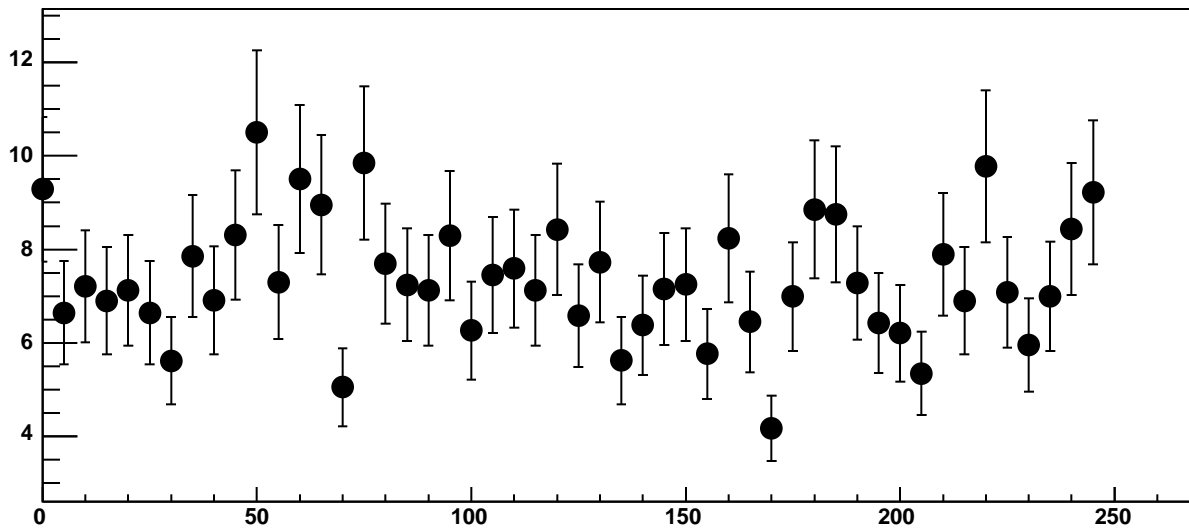


Chip 5, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

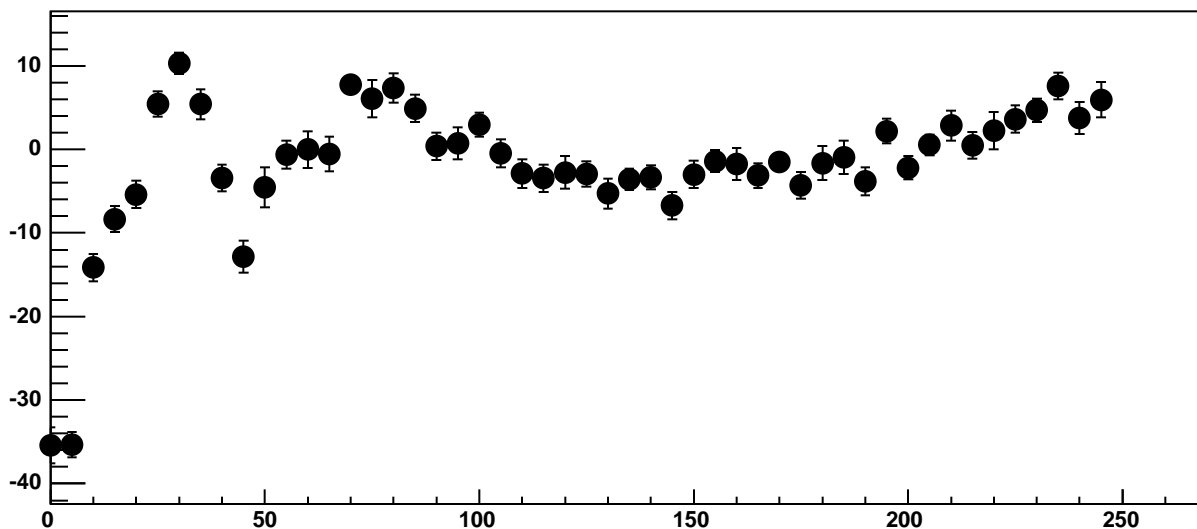


χ^2 / ndf	388.3 / 41
p0	-222.7 ± 0.5141
p1	45.63 ± 0.3732
p2	$-4.225\text{e}+08 \pm 7.644\text{e}+06$
p3	$1.188\text{e}+08 \pm 1.777\text{e}+05$
p4	3.167 ± 0.06373

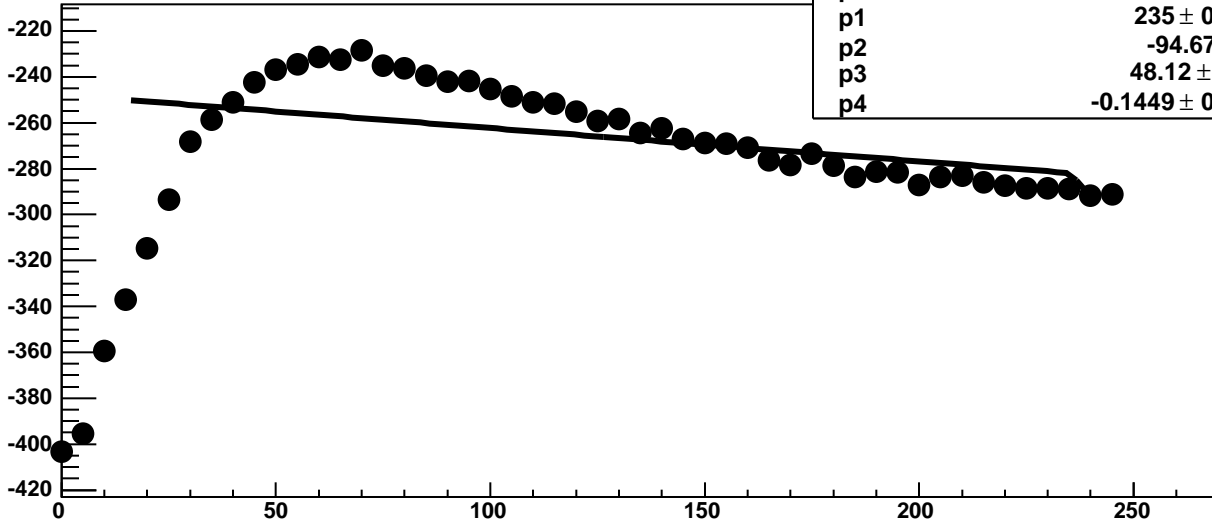
Chip 5, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

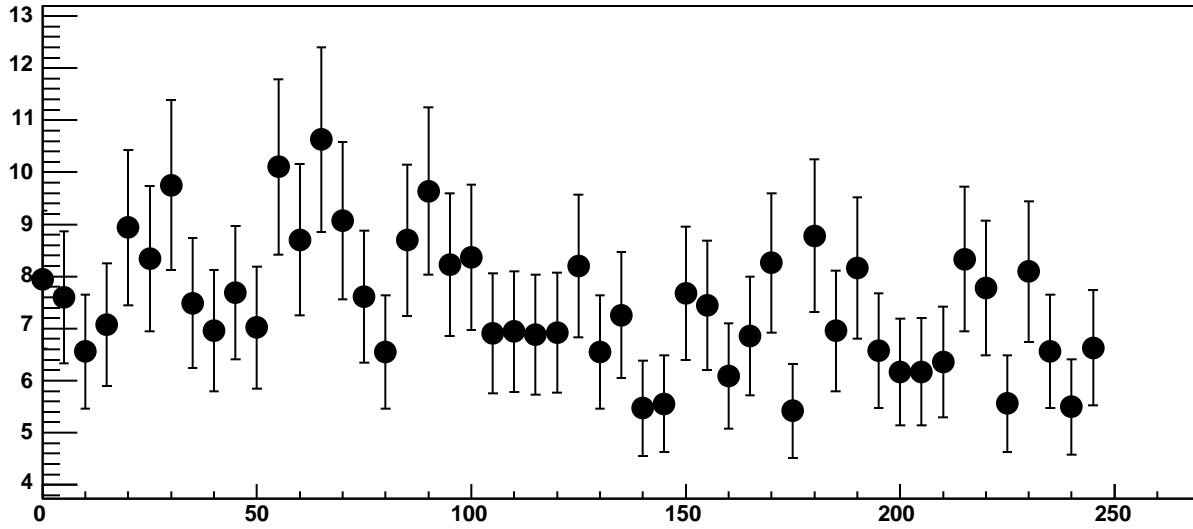


Chip 5, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

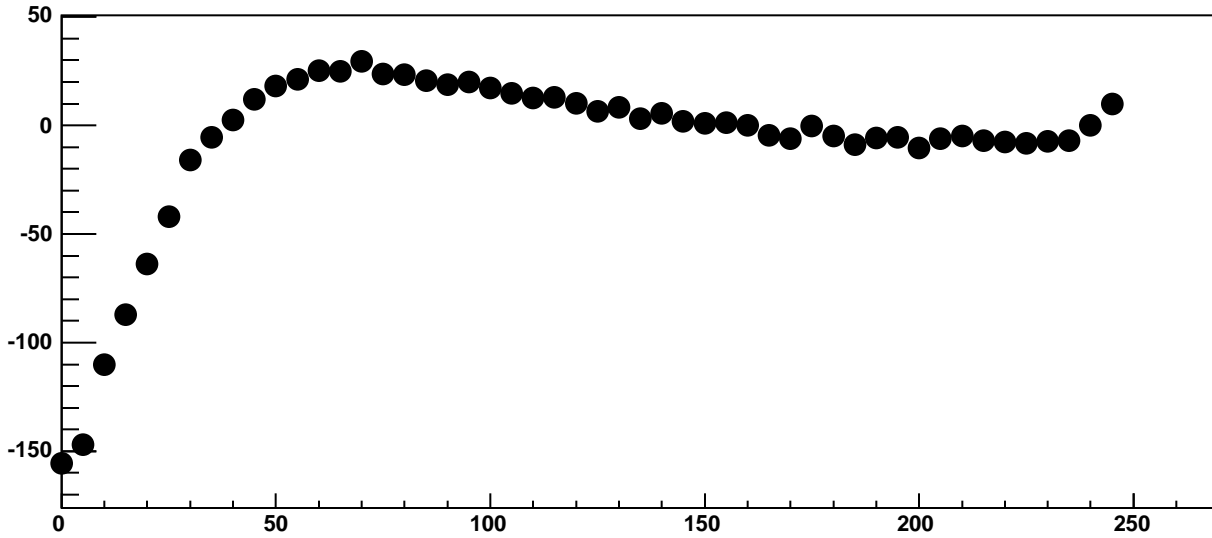


χ^2 / ndf	6520 / 41
p0	-281.9 ± 0.4646
p1	235 ± 0.001338
p2	-94.67 ± 13.78
p3	48.12 ± 0.09731
p4	-0.1449 ± 0.003905

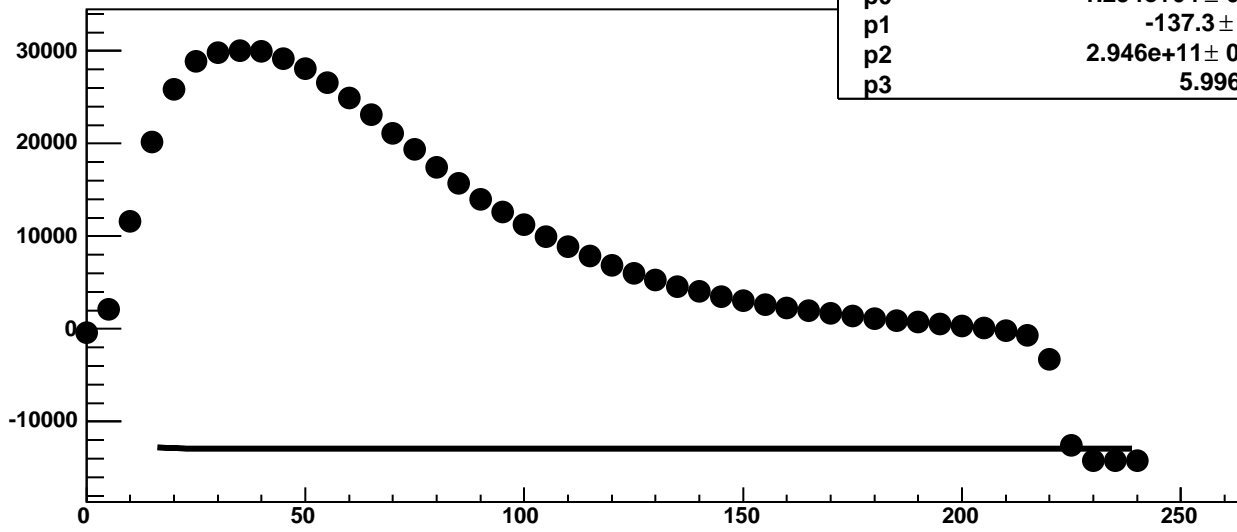
Chip 5, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

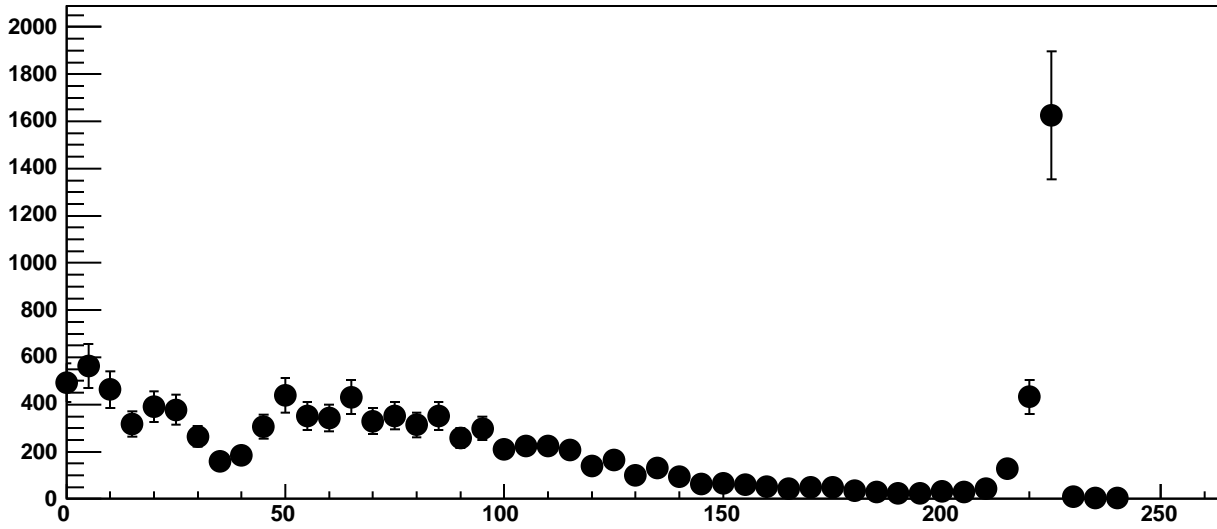


Chip 5, Channel 8, Enable 5!, DAC=4000, ADC Mean vs Hold

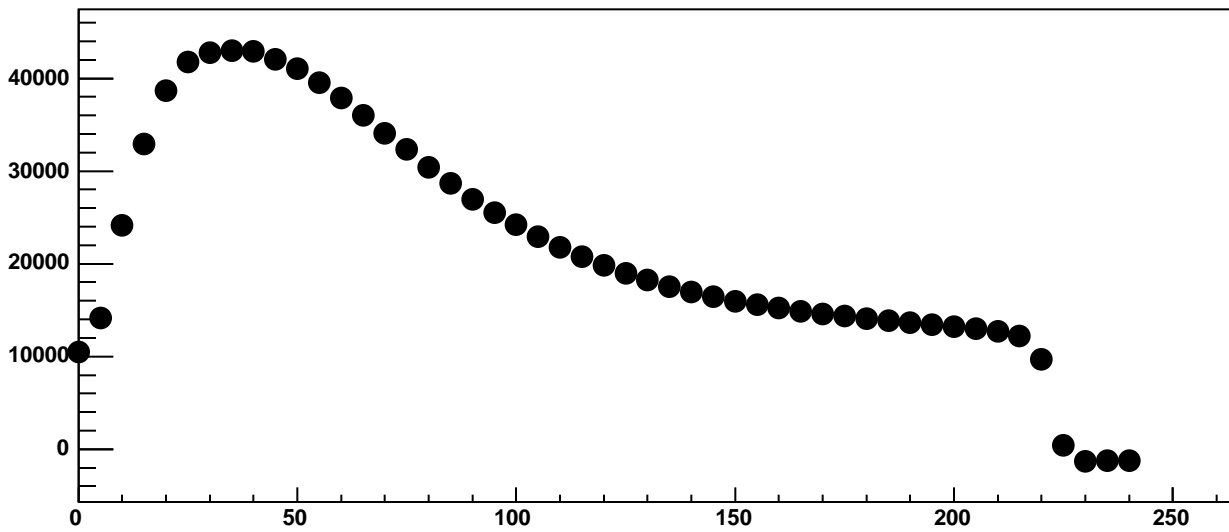


χ^2 / ndf	5.153e+07 / 42
p0	-1.294e+04 ± 0.001488
p1	-137.3 ± 0.03199
p2	2.946e+11 ± 0.001488
p3	5.996 ± 8.879

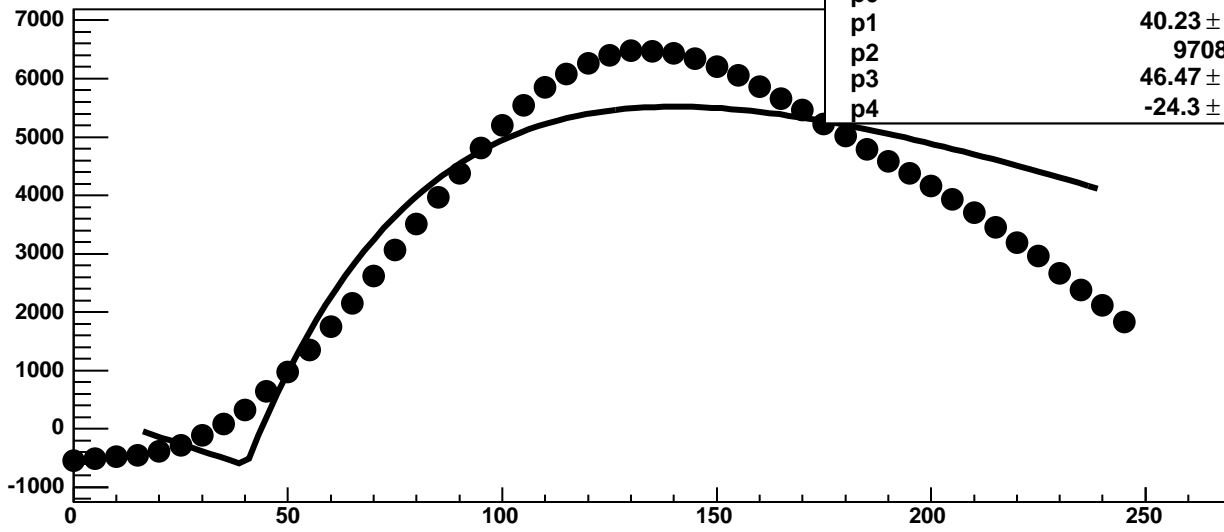
Chip 5, Channel 8, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 8, Enable 5!, DAC=4000, ADC Residuals vs Hold

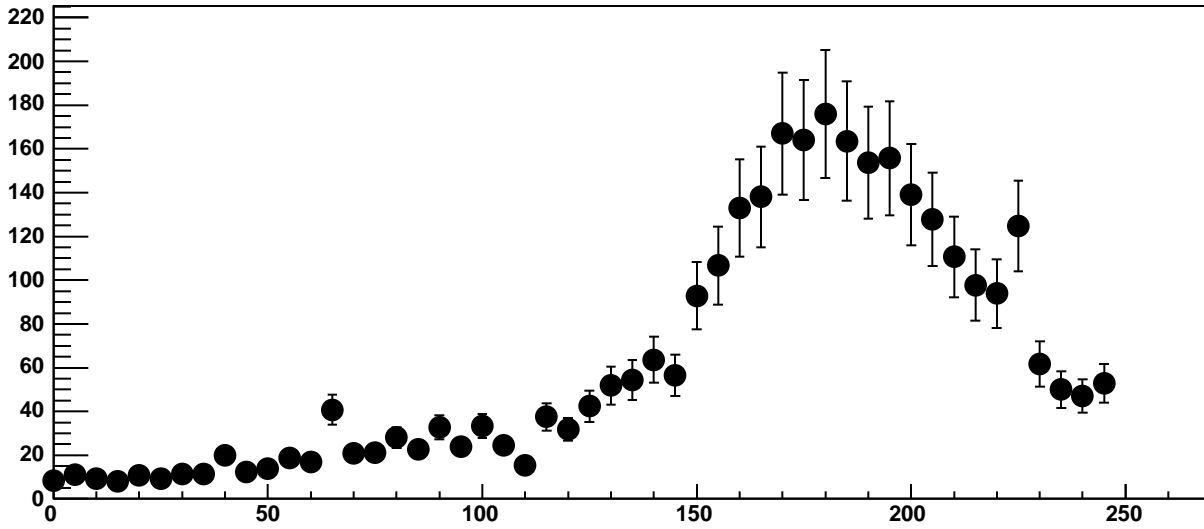


Chip 5, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold

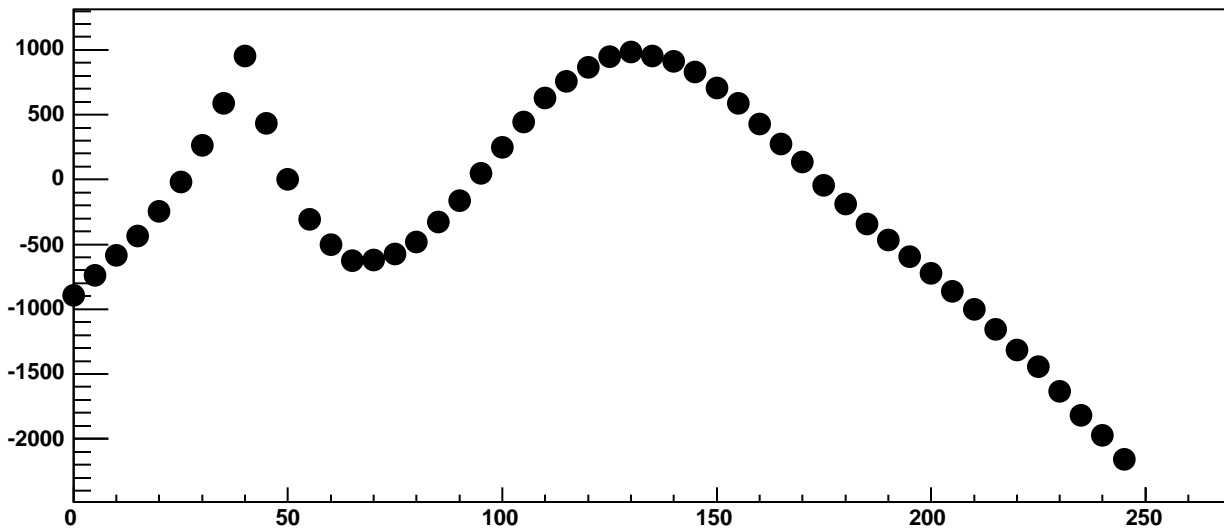


χ^2 / ndf	4.407e+05 / 41
p0	-629.1 ± 1.379
p1	40.23 ± 0.01557
p2	9708 ± 13.87
p3	46.47 ± 0.07333
p4	-24.3 ± 0.07326

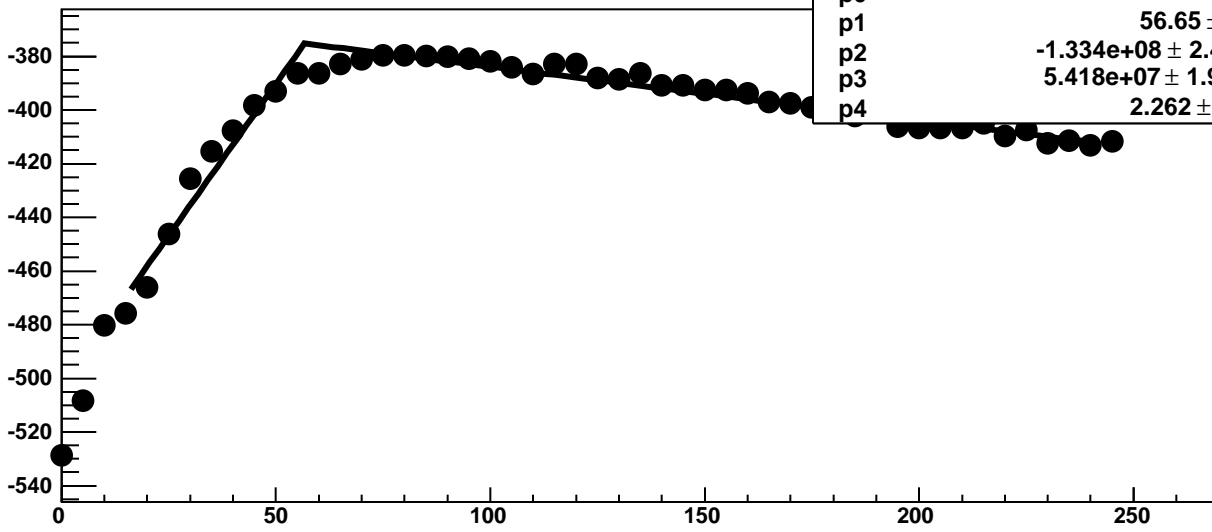
Chip 5, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold

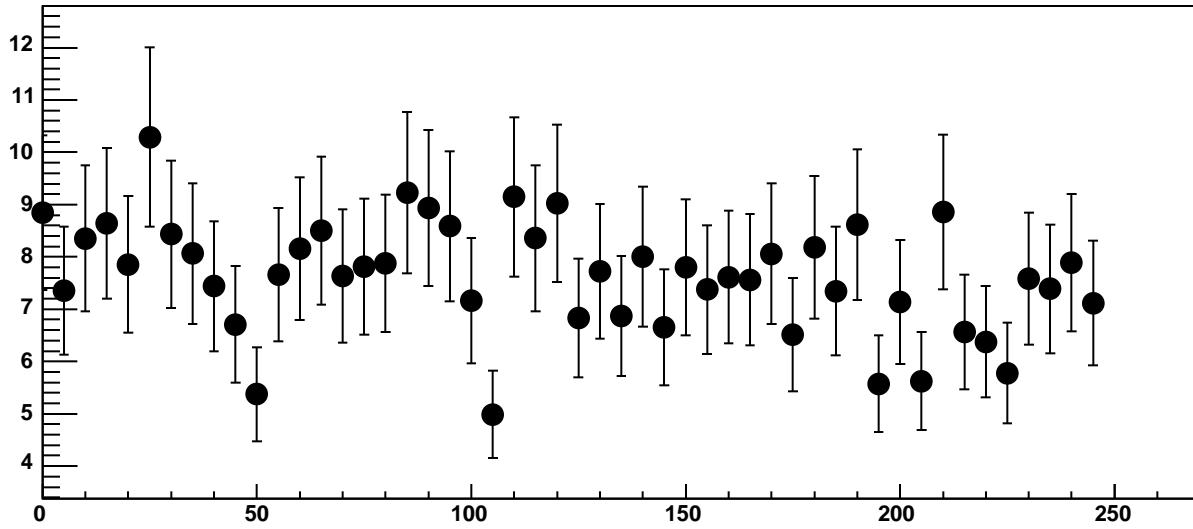


Chip 5, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

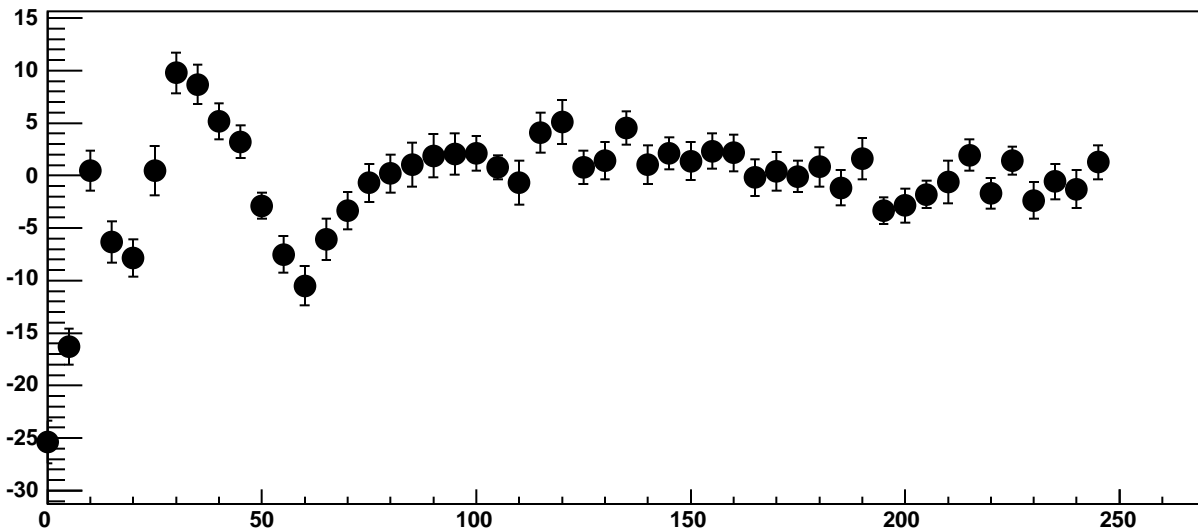


χ^2 / ndf	209.4 / 41
p0	-375.2 ± 0.544
p1	56.65 ± 0.4736
p2	$-1.334\text{e}+08 \pm 2.499\text{e}+06$
p3	$5.418\text{e}+07 \pm 1.999\text{e}+05$
p4	2.262 ± 0.04491

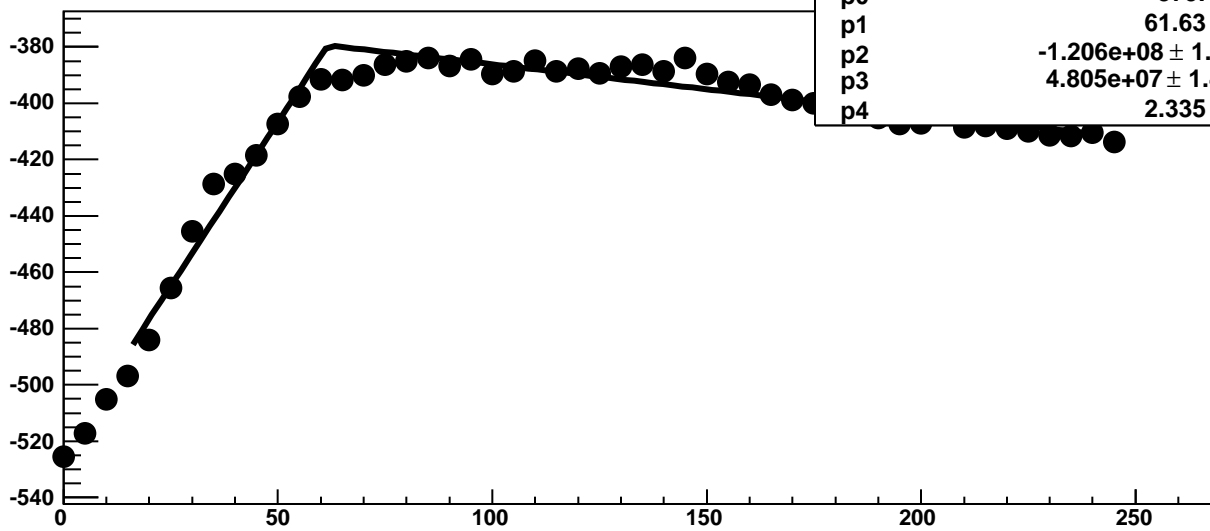
Chip 5, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

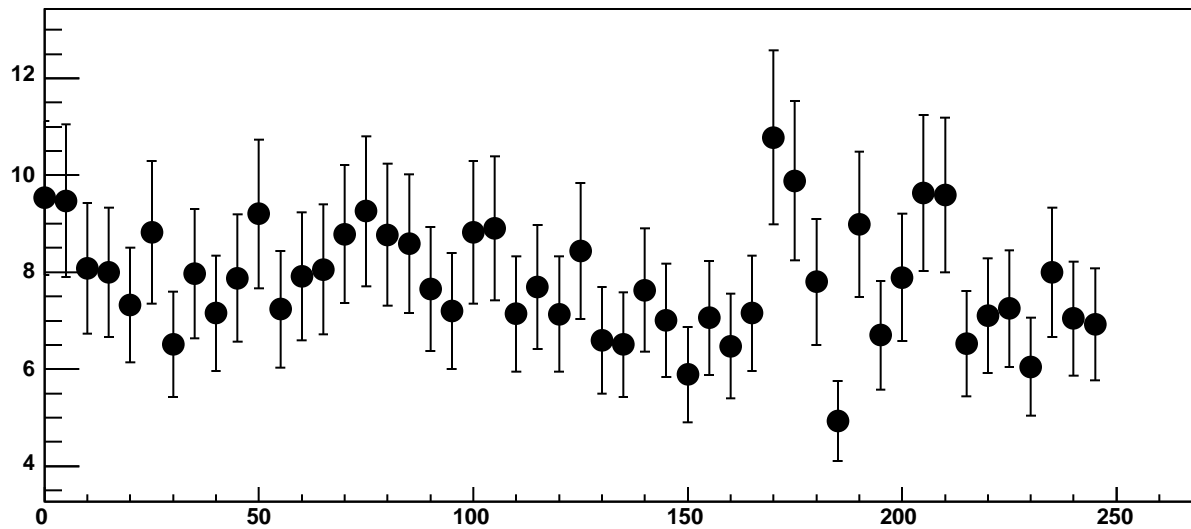


Chip 5, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

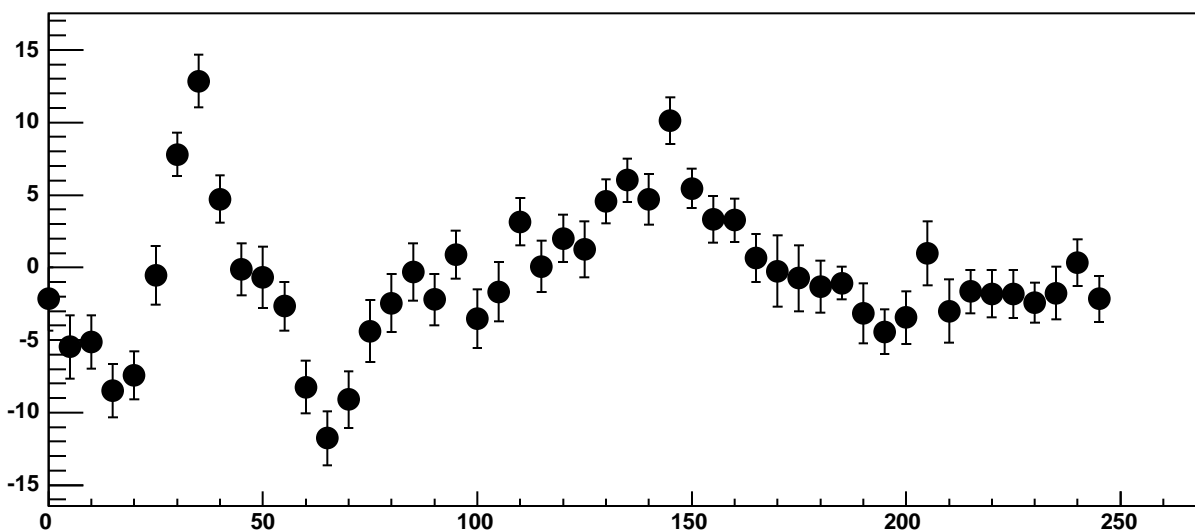


χ^2 / ndf	352.6 / 41
p0	-379.4 ± 0.572
p1	61.63 ± 0.5032
p2	$-1.206\text{e}+08 \pm 1.924\text{e}+06$
p3	$4.805\text{e}+07 \pm 1.456\text{e}+05$
p4	2.335 ± 0.0392

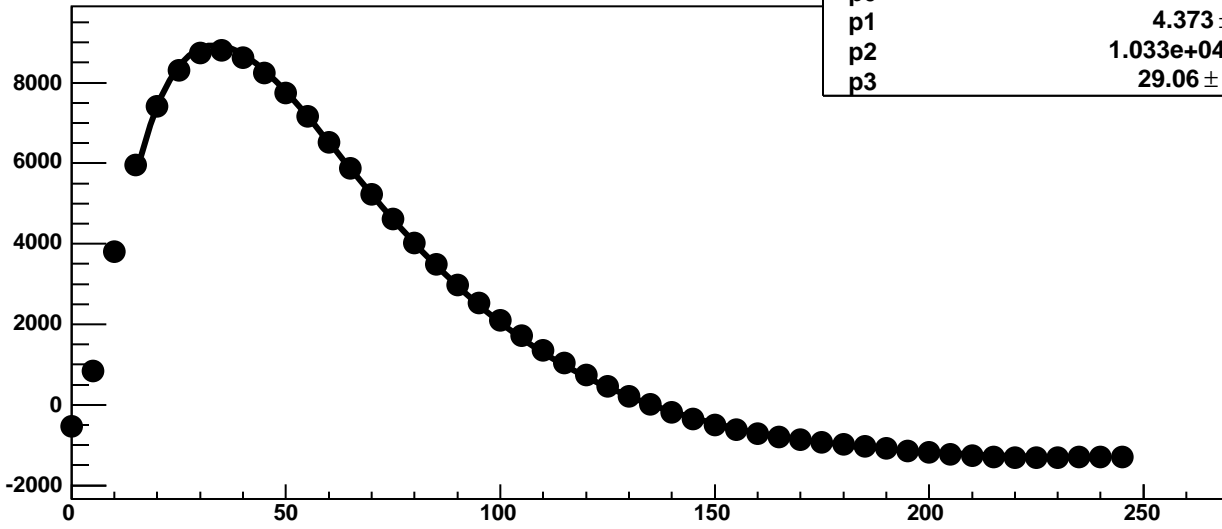
Chip 5, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



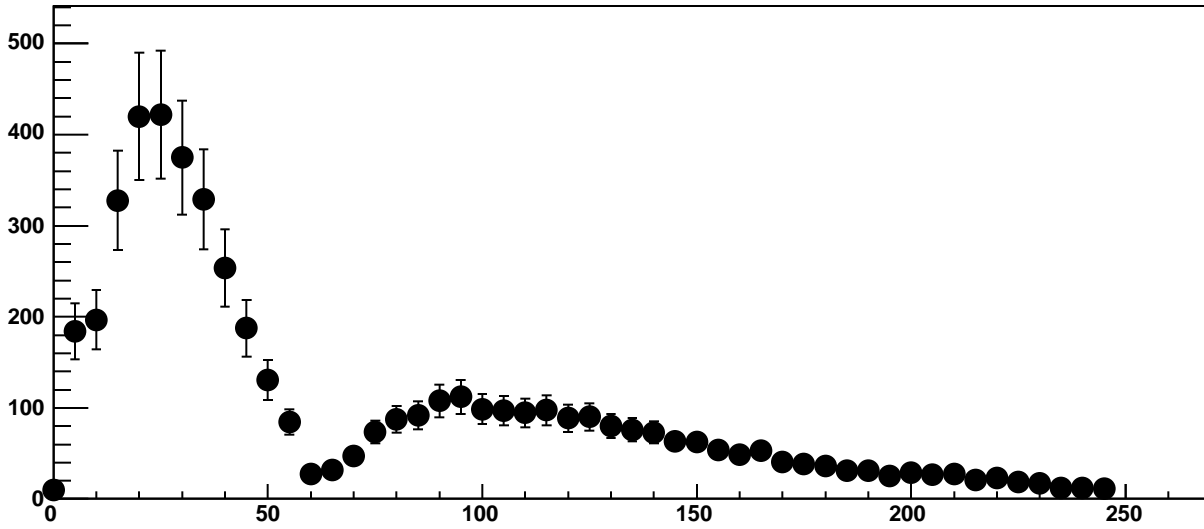
Chip 5, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold



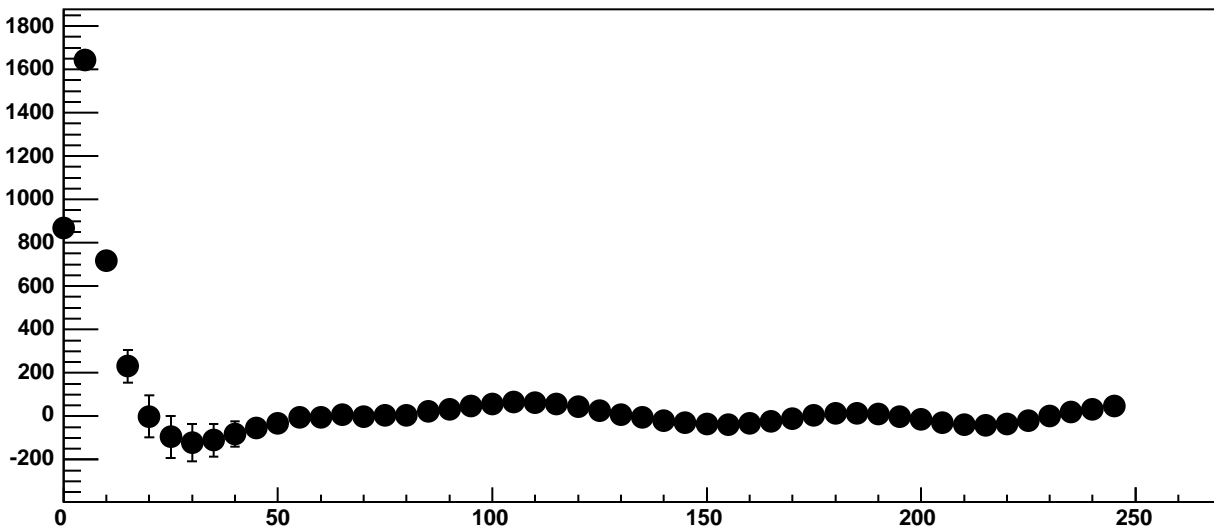
Chip 5, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold



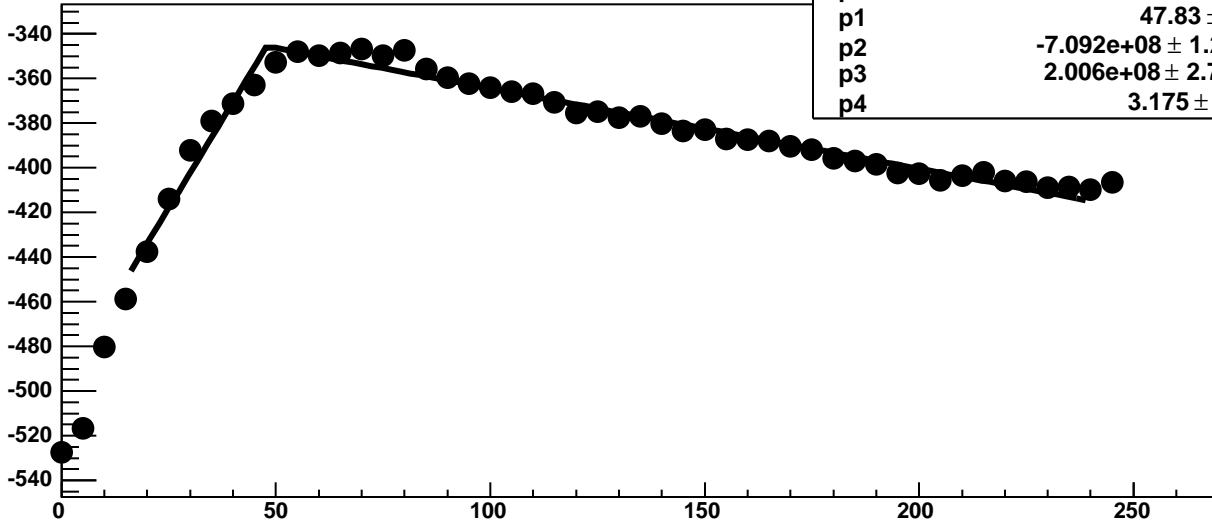
Chip 5, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold

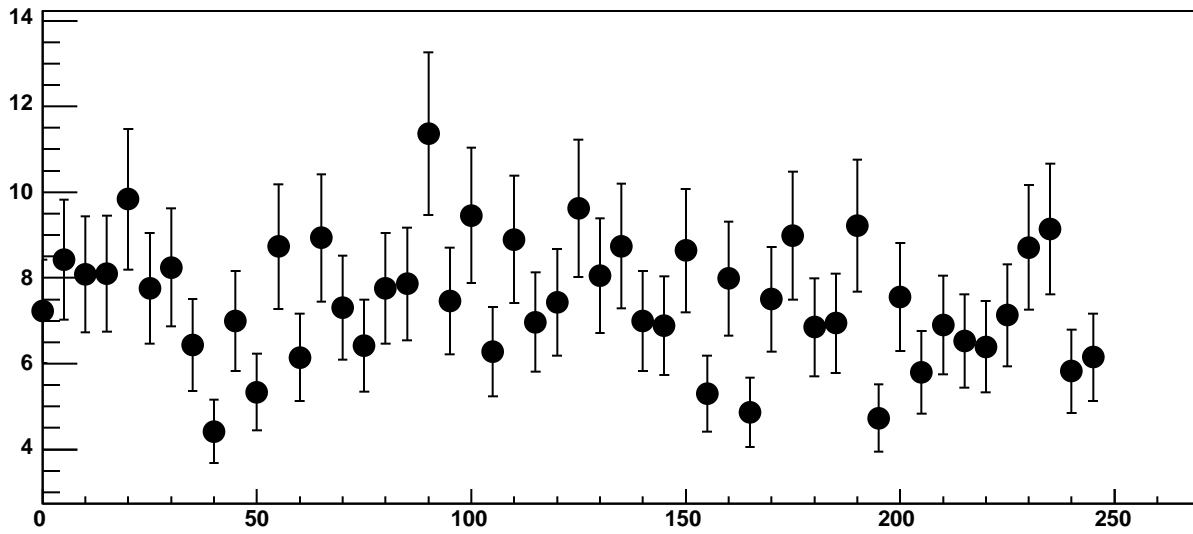


Chip 5, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

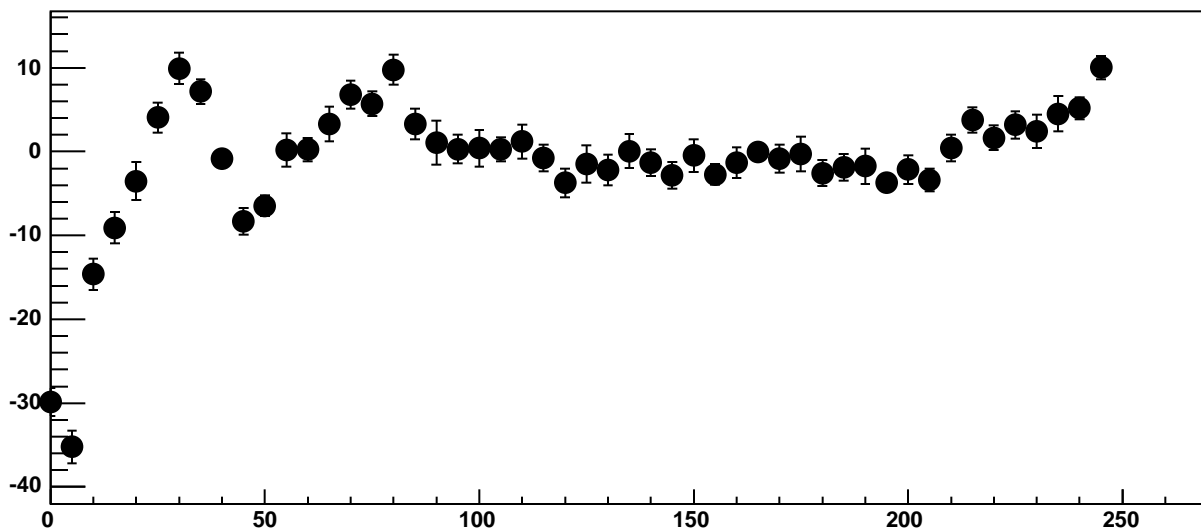


χ^2 / ndf	280 / 41
p0	-345.6 ± 0.4845
p1	47.83 ± 0.3396
p2	$-7.092\text{e}+08 \pm 1.264\text{e}+07$
p3	$2.006\text{e}+08 \pm 2.769\text{e}+05$
p4	3.175 ± 0.06268

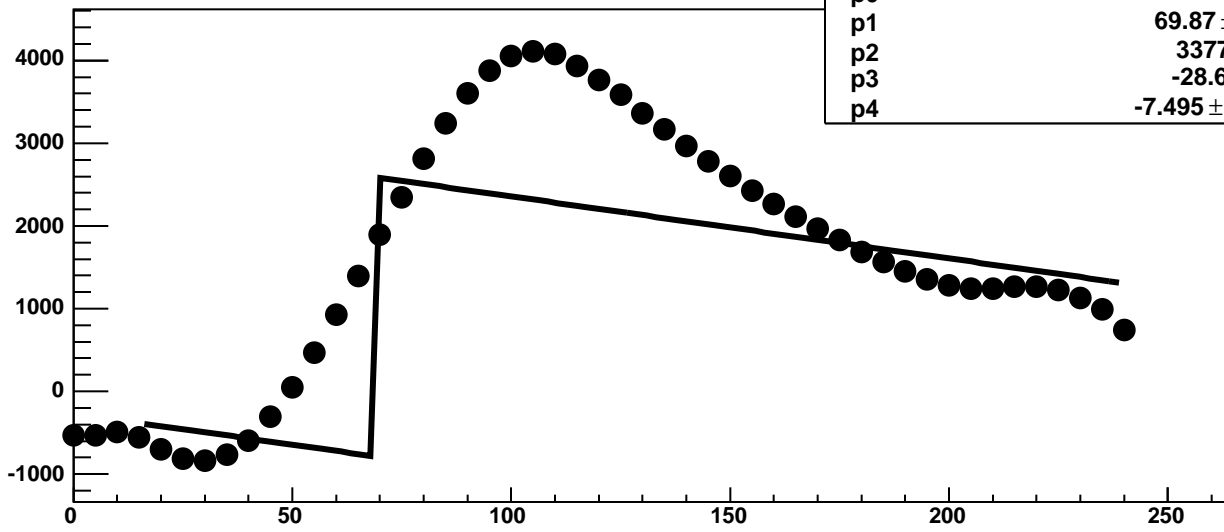
Chip 5, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

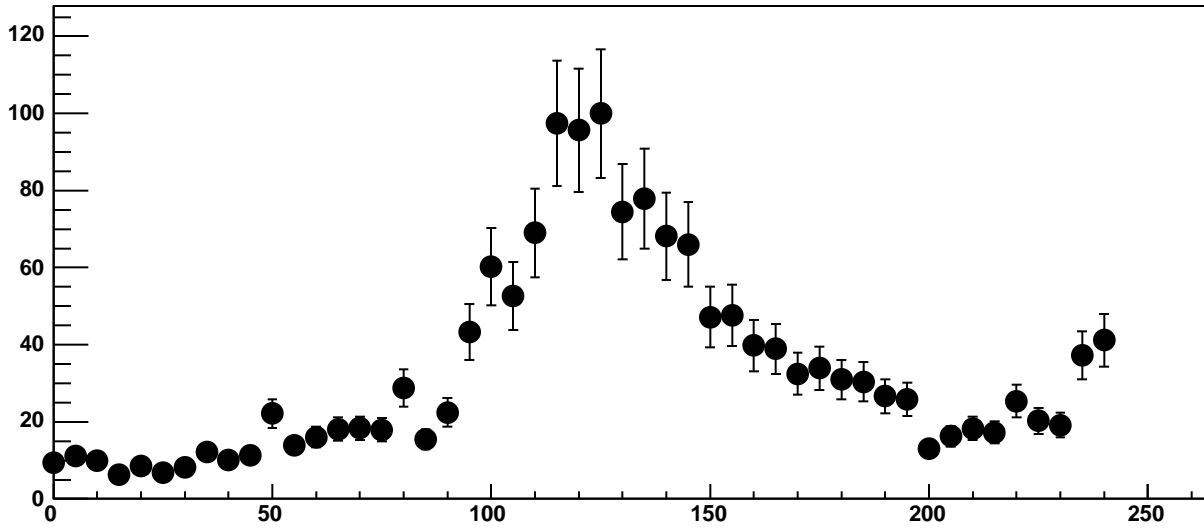


Chip 5, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold

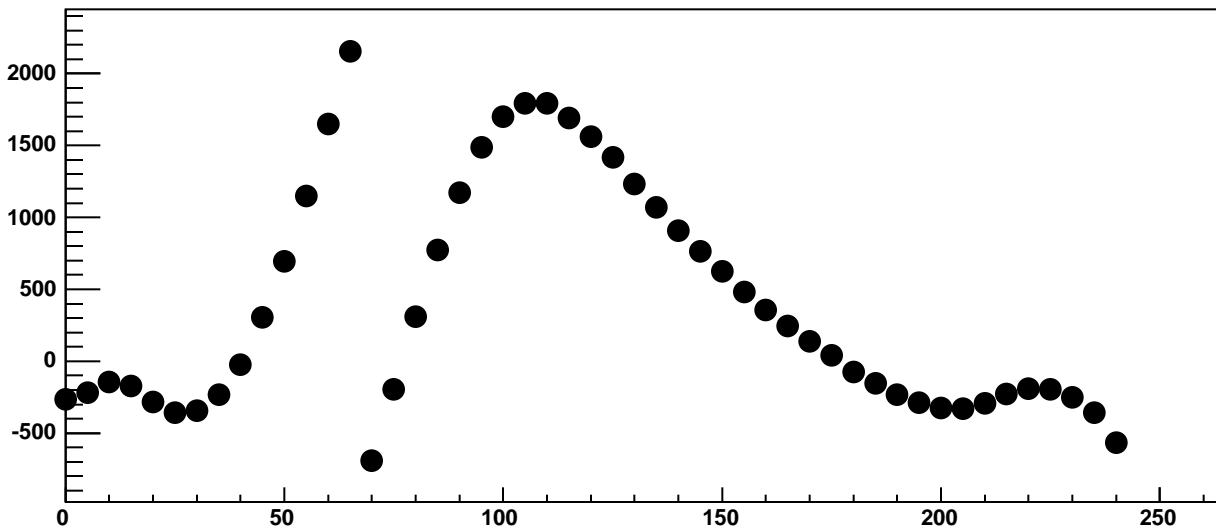


χ^2 / ndf	1.042e+06 / 41
p0	-794 ± 6.403
p1	69.87 ± 0.6793
p2	3377 ± 2.051
p3	-28.6 ± 1.617
p4	-7.495 ± 0.05131

Chip 5, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold

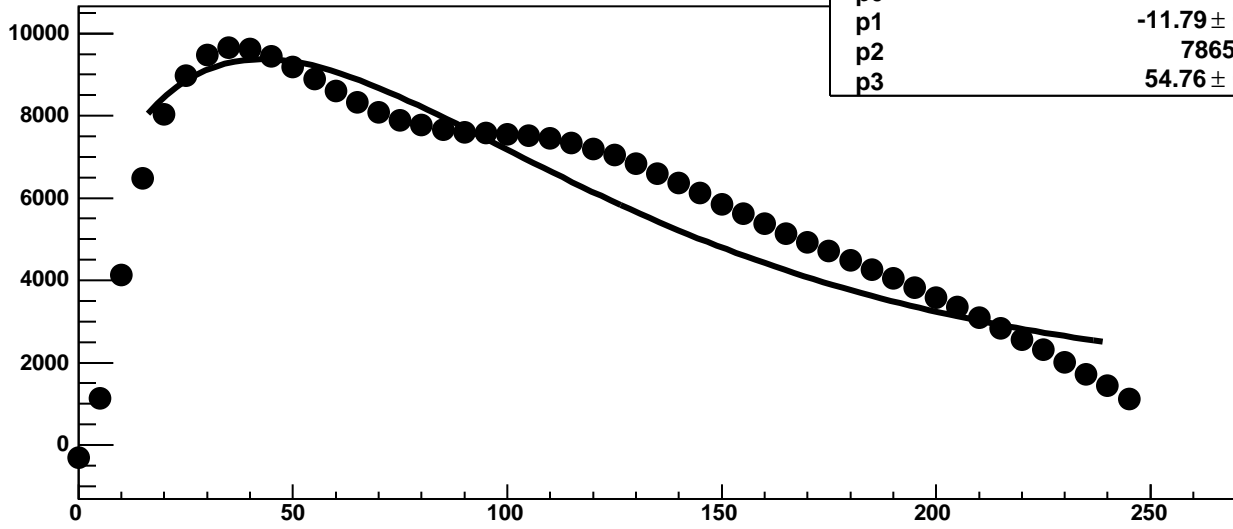


Chip 5, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold

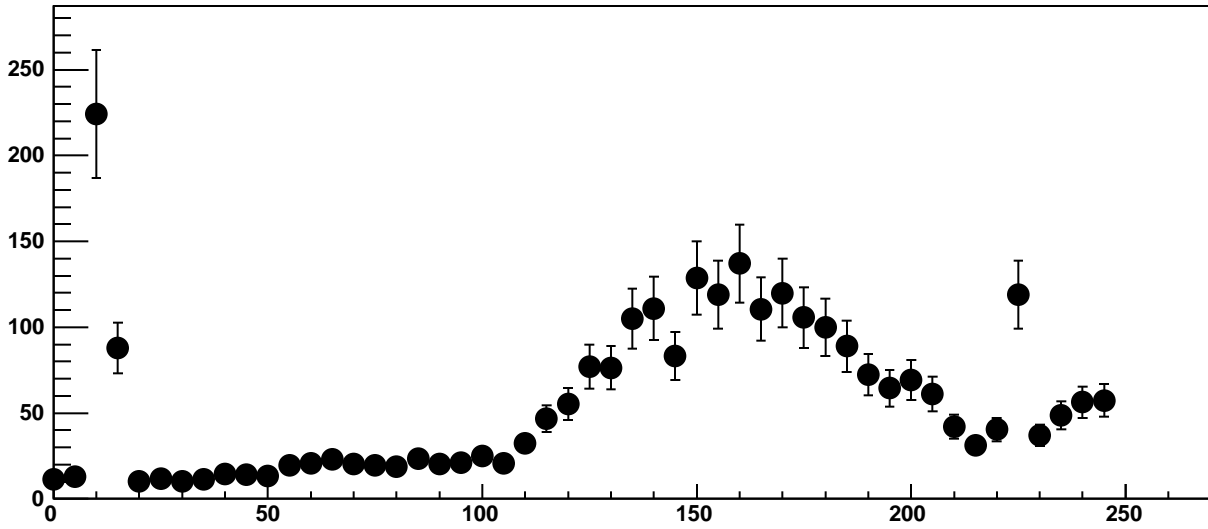


Chip 5, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold

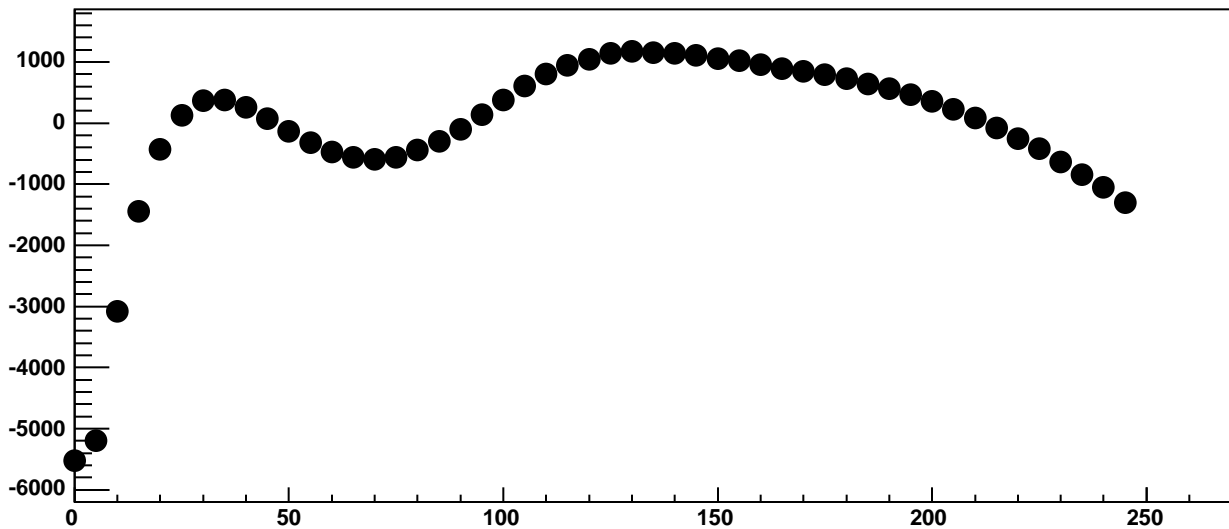
χ^2 / ndf	2.591e+05 / 42
p0	1509 ± 8.35
p1	-11.79 ± 0.07735
p2	7865 ± 7.781
p3	54.76 ± 0.07646



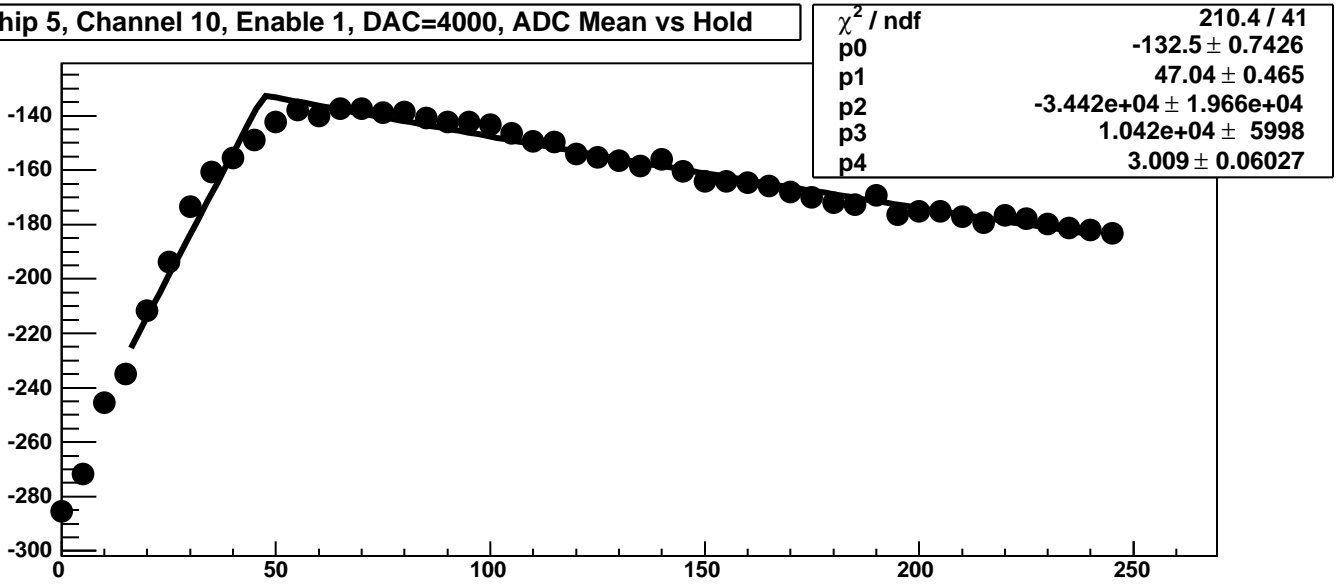
Chip 5, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold



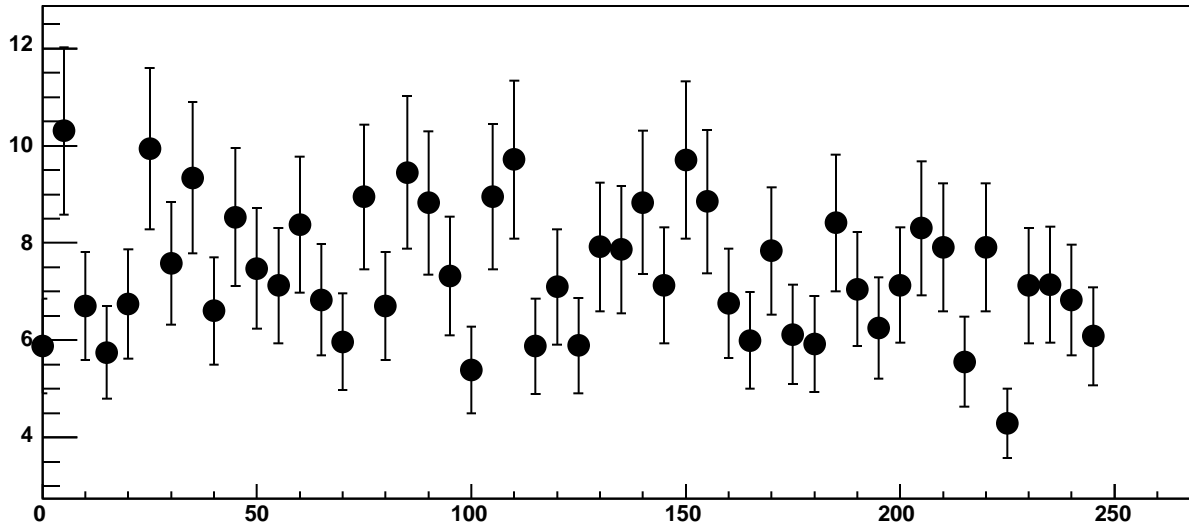
Chip 5, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold



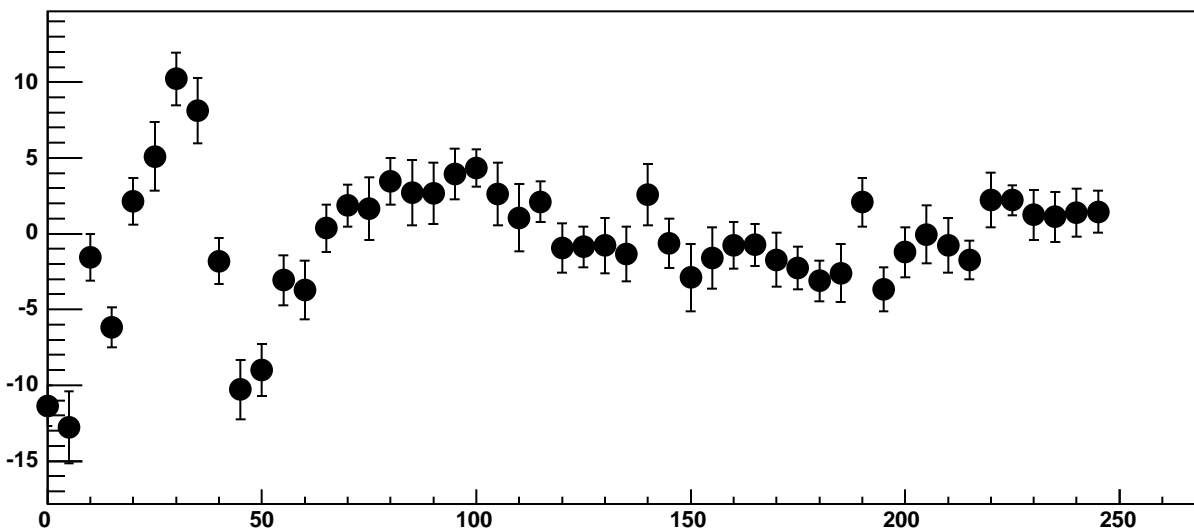
Chip 5, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold



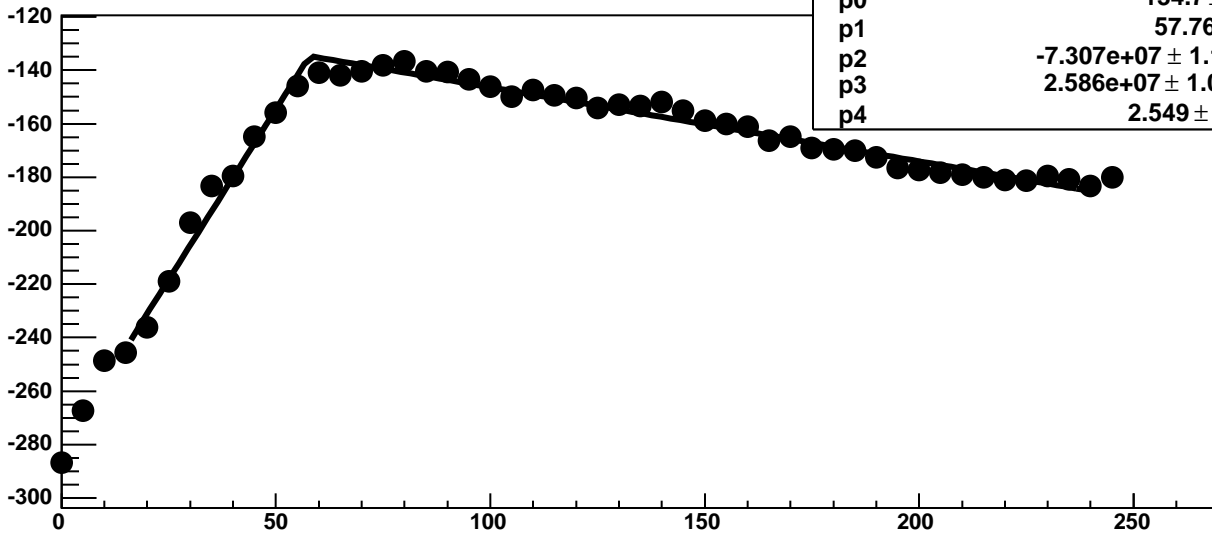
Chip 5, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

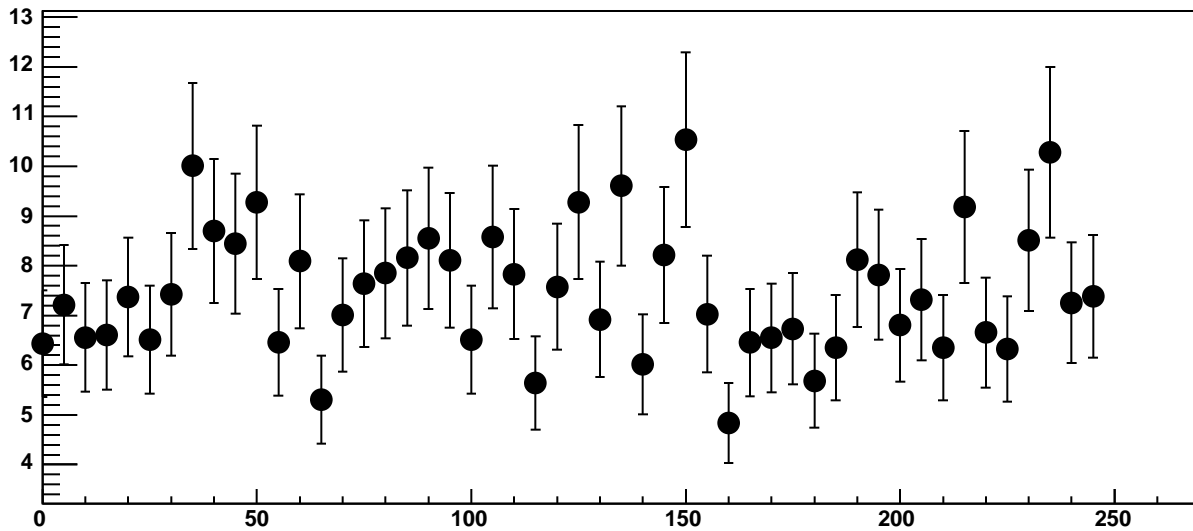


Chip 5, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

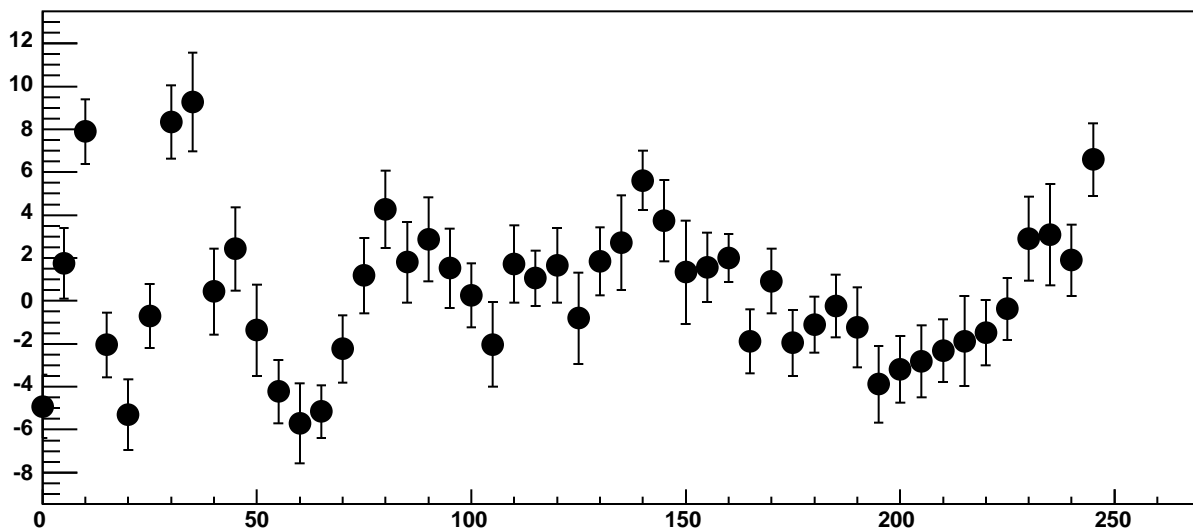


χ^2 / ndf	159.6 / 41
p0	-134.7 ± 0.5037
p1	57.76 ± 0.456
p2	$-7.307\text{e}+07 \pm 1.125\text{e}+06$
p3	$2.586\text{e}+07 \pm 1.033\text{e}+05$
p4	2.549 ± 0.04189

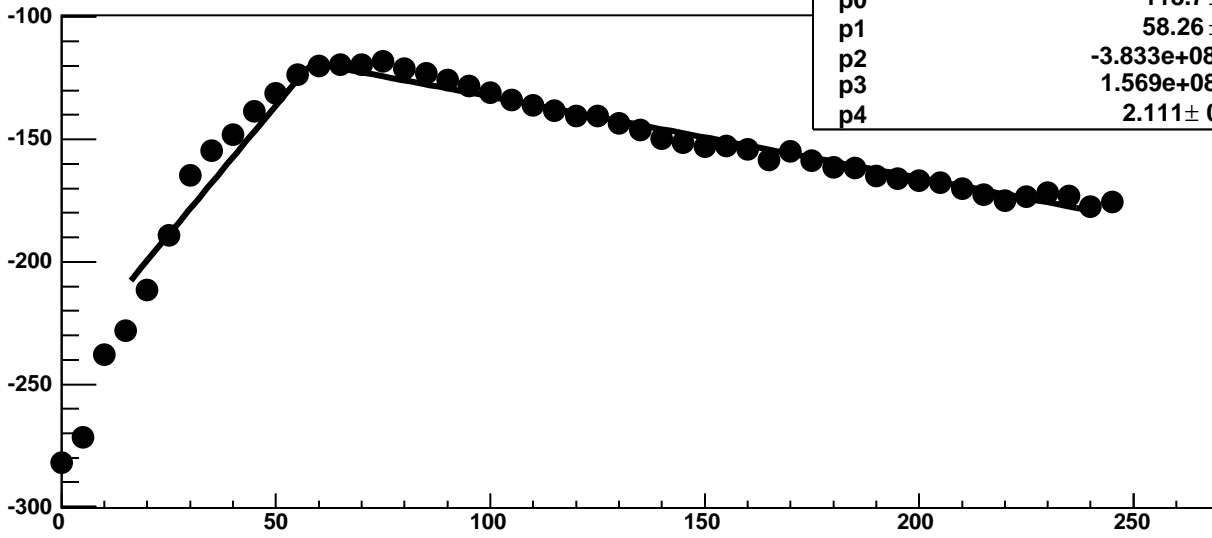
Chip 5, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

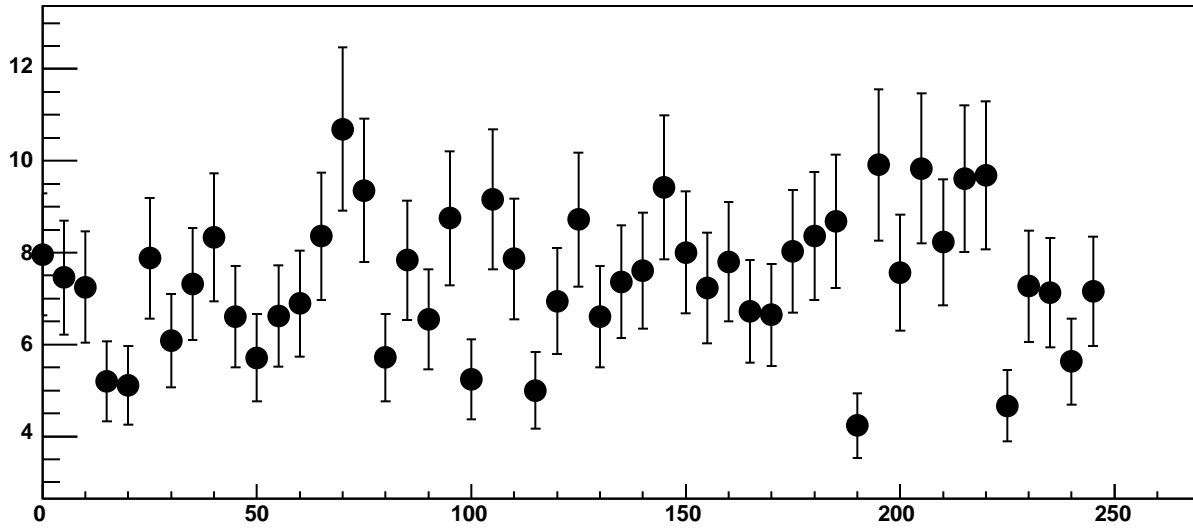


Chip 5, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold

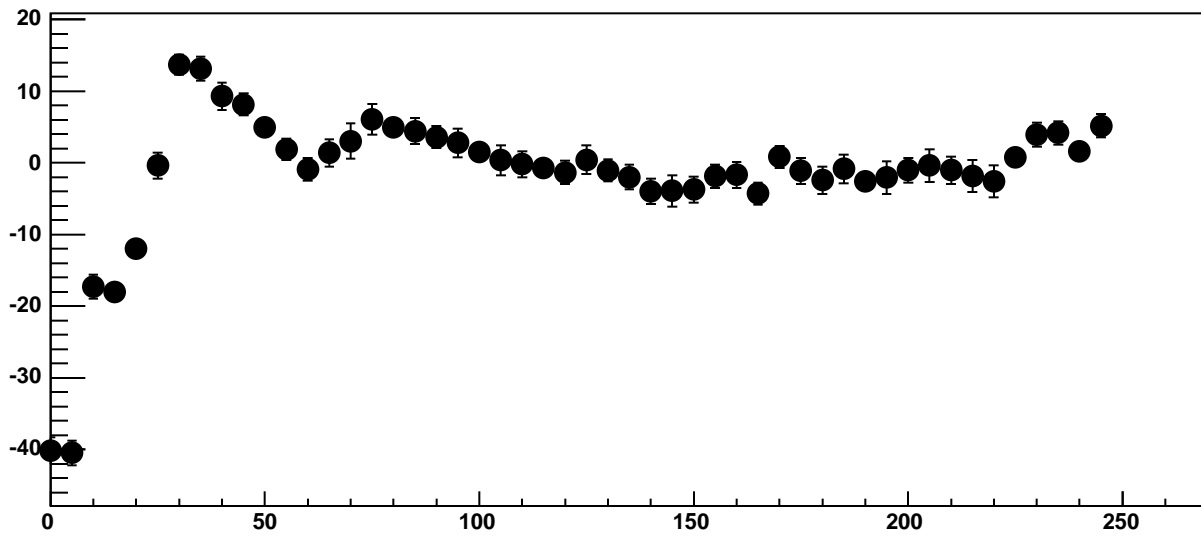


χ^2 / ndf	650.4 / 41
p0	-118.7 ± 0.4518
p1	58.26 ± 0.3288
p2	$-3.833\text{e}+08 \pm 1.414$
p3	$1.569\text{e}+08 \pm 1.414$
p4	2.111 ± 0.004911

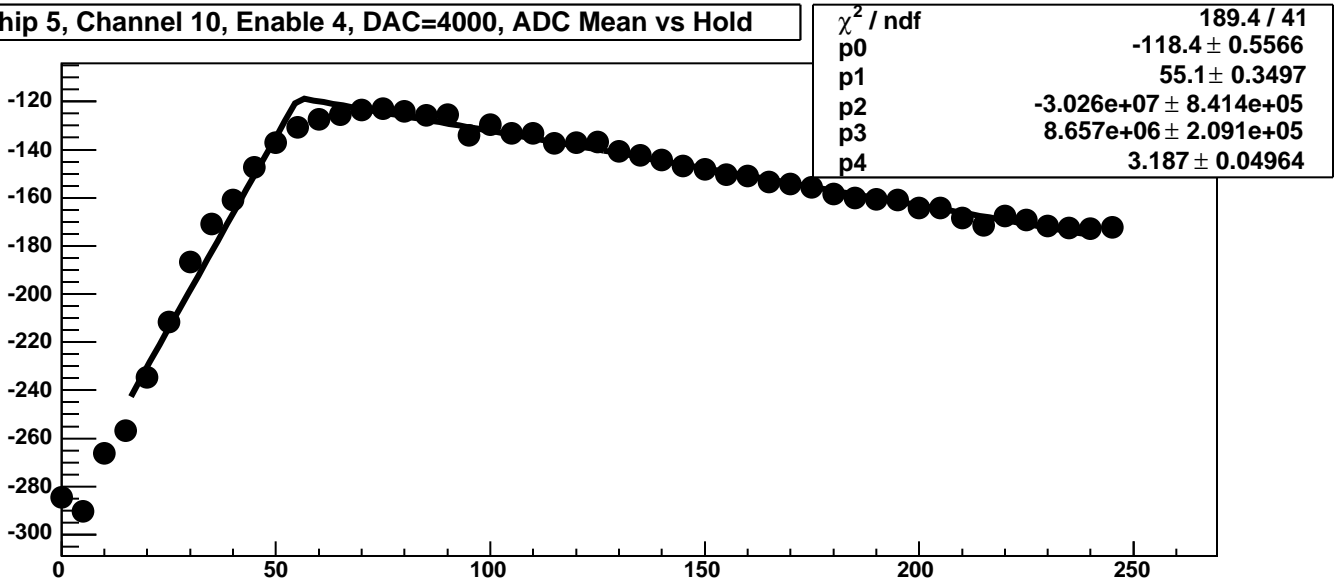
Chip 5, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold



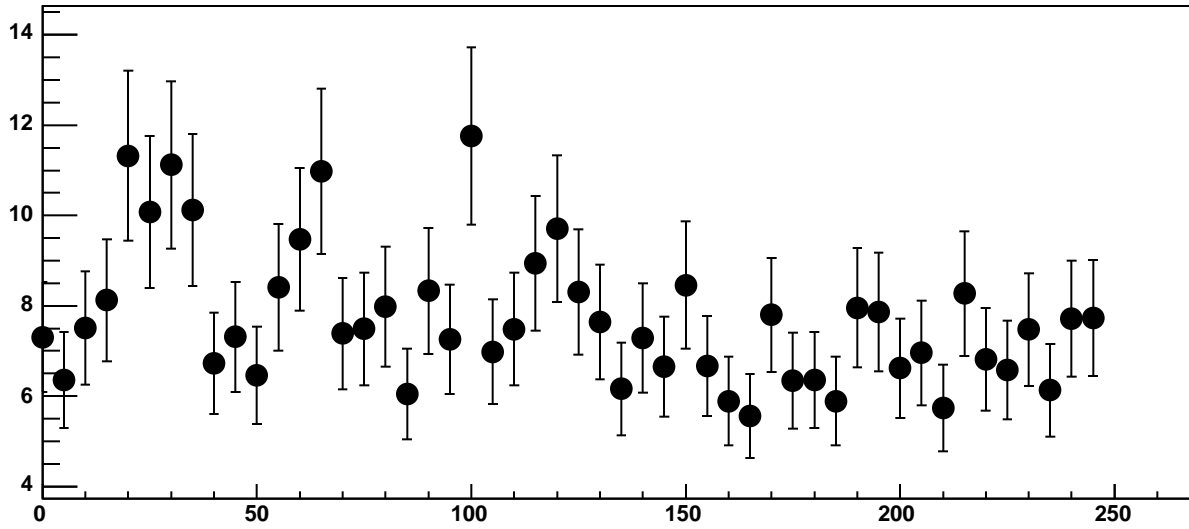
Chip 5, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold



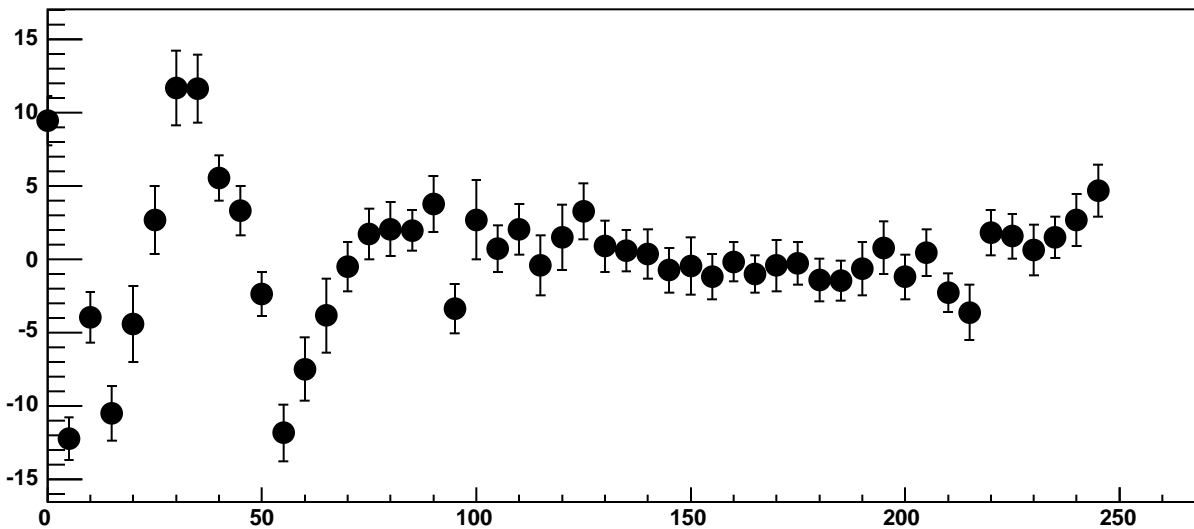
Chip 5, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold



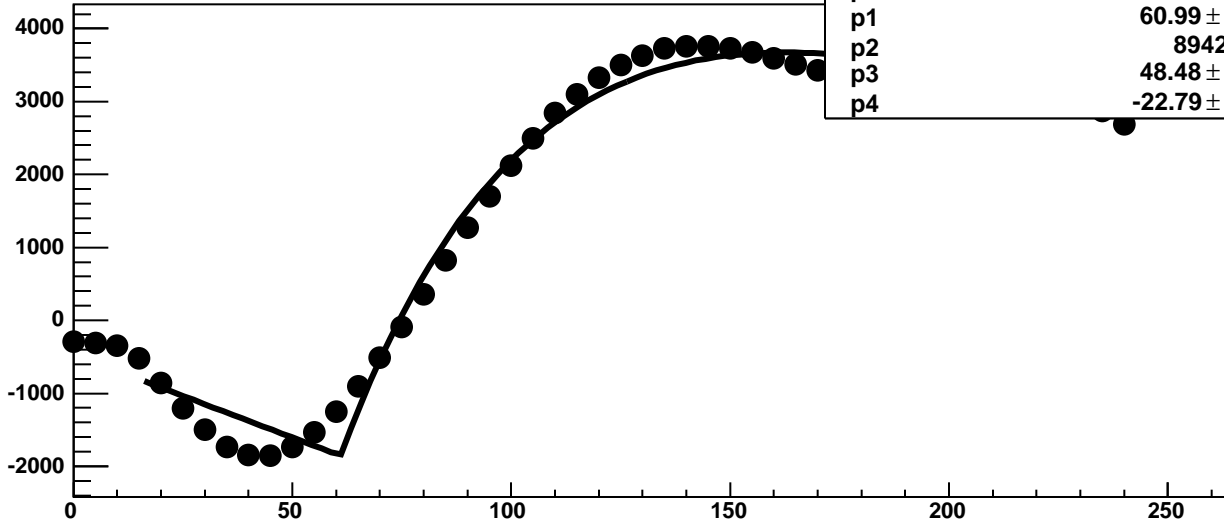
Chip 5, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



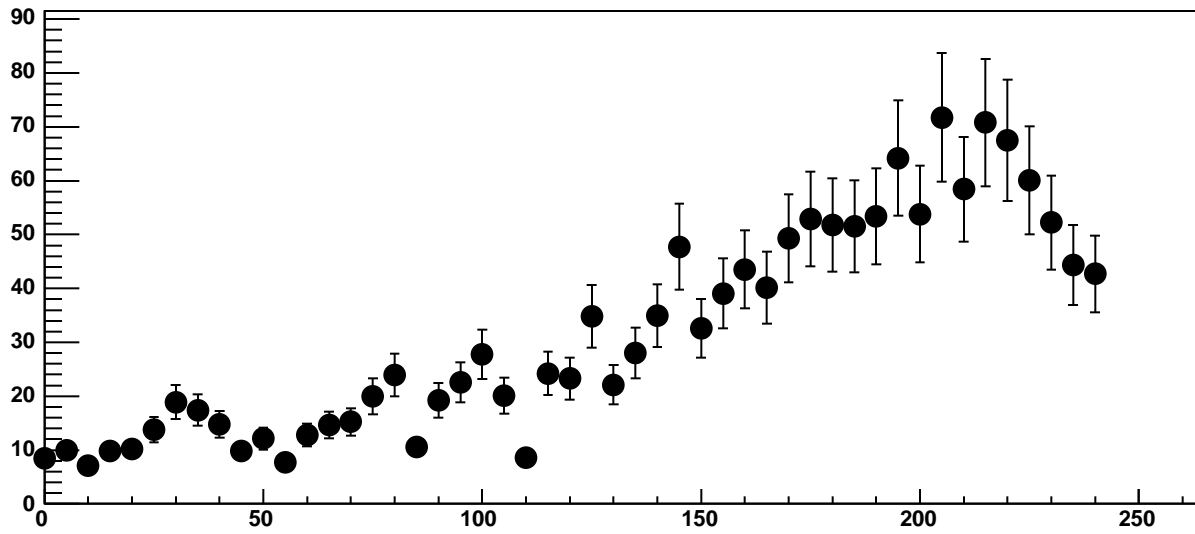
Chip 5, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold



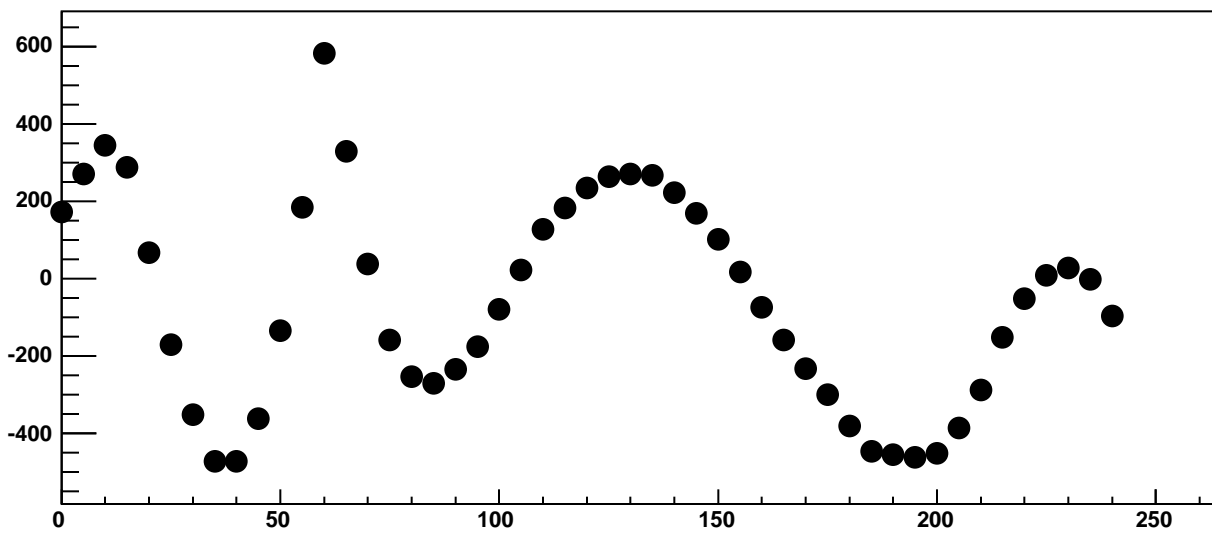
Chip 5, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold



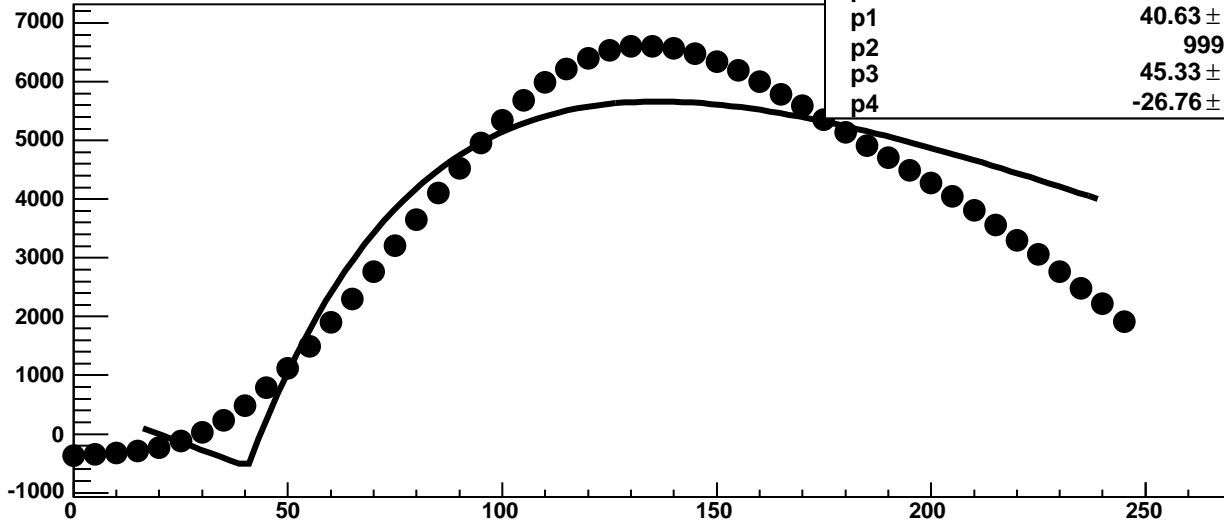
Chip 5, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

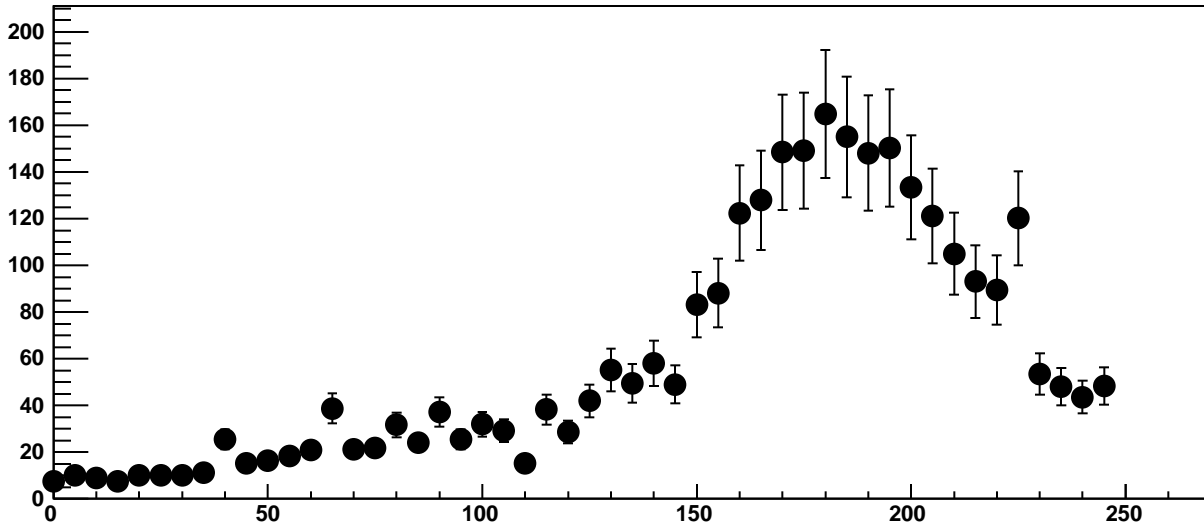


Chip 5, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold

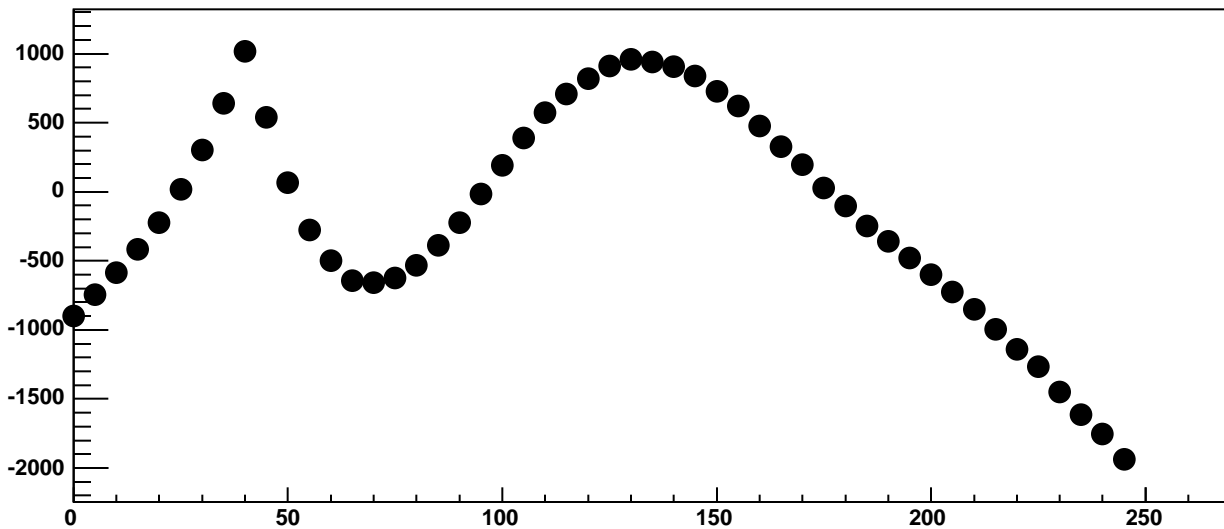


χ^2 / ndf	4.318e+05 / 41
p0	-557 ± 1.402
p1	40.63 ± 0.01654
p2	9991 ± 13.7
p3	45.33 ± 0.07076
p4	-26.76 ± 0.07175

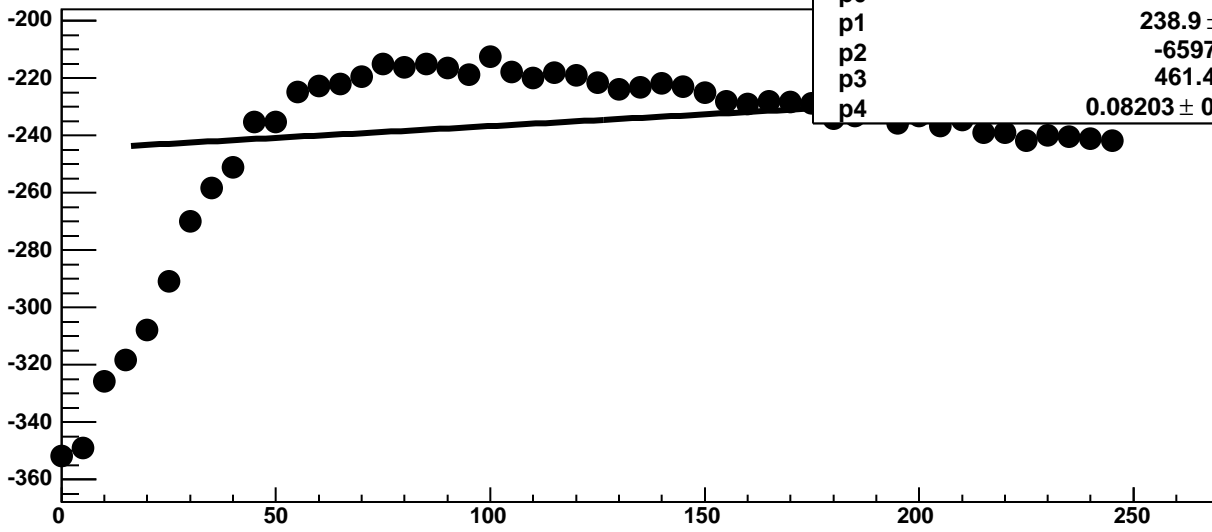
Chip 5, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold

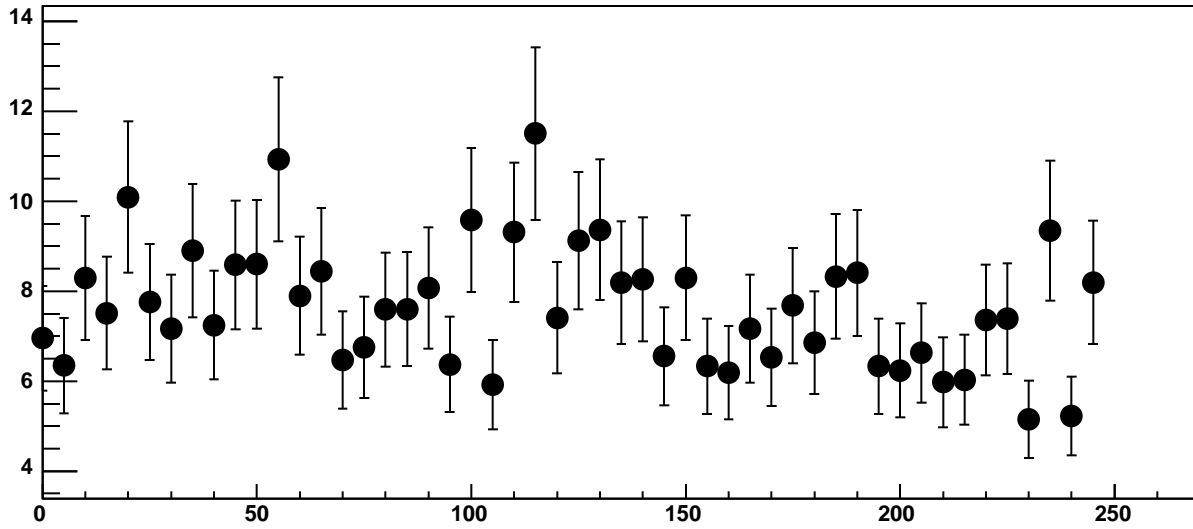


Chip 5, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

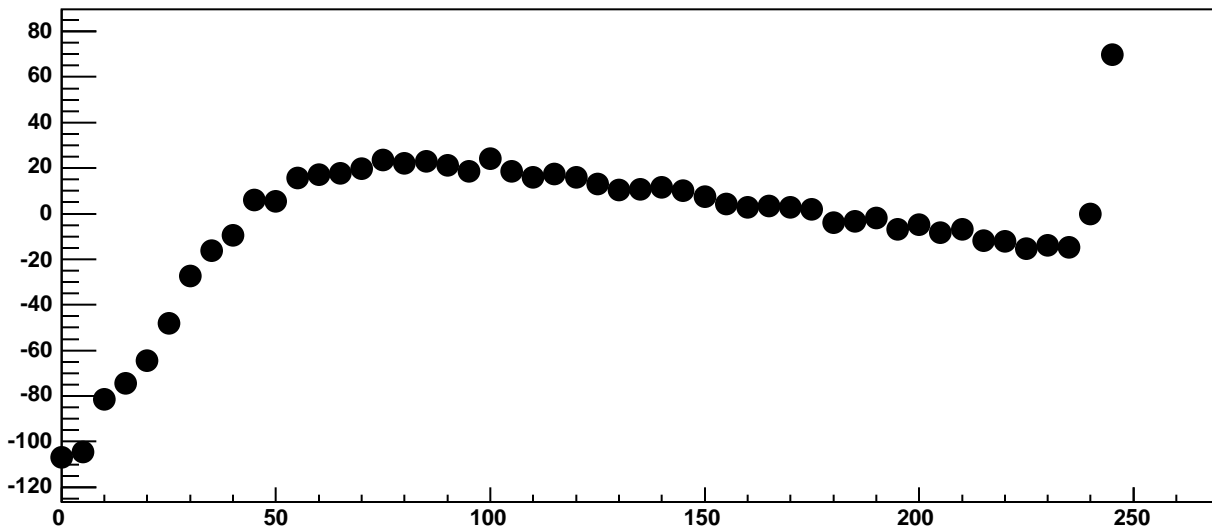


χ^2 / ndf	6217 / 41
p0	-225.3 ± 0.4812
p1	238.9 ± 0.8546
p2	-6597 ± 5087
p3	461.4 ± 291.4
p4	0.08203 ± 0.003864

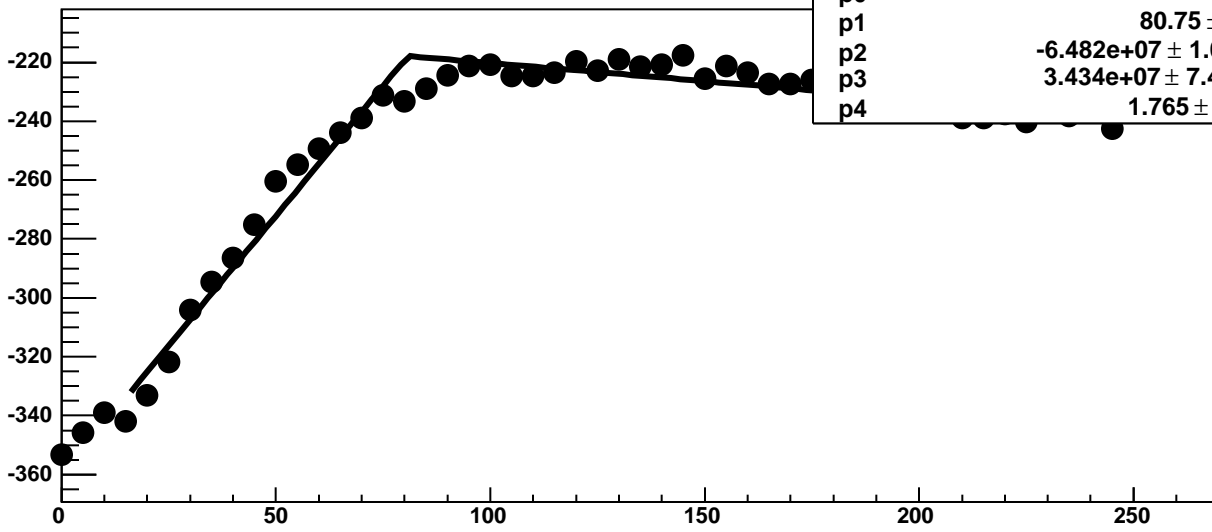
Chip 5, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold

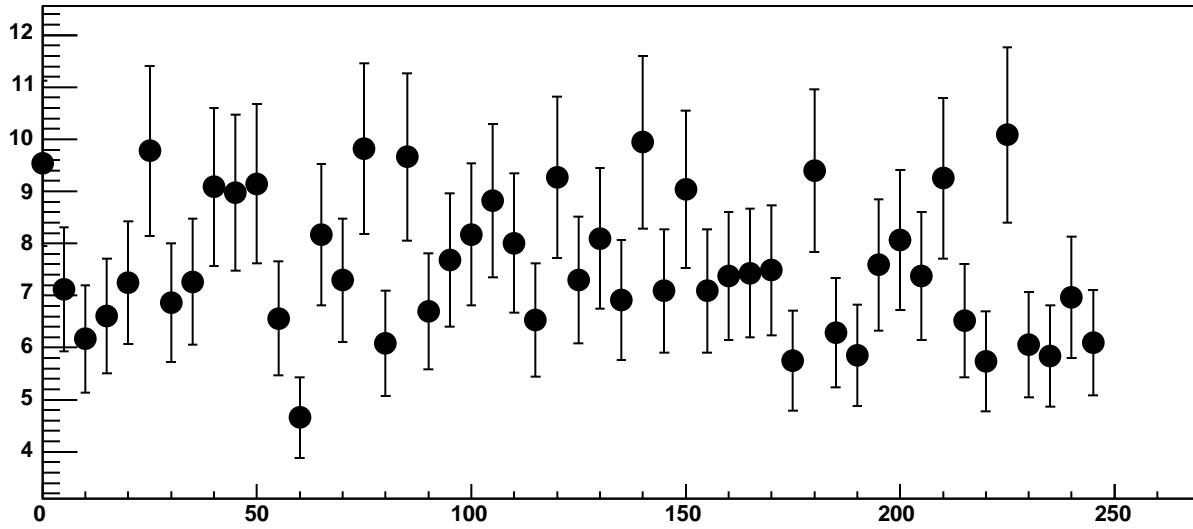


Chip 5, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

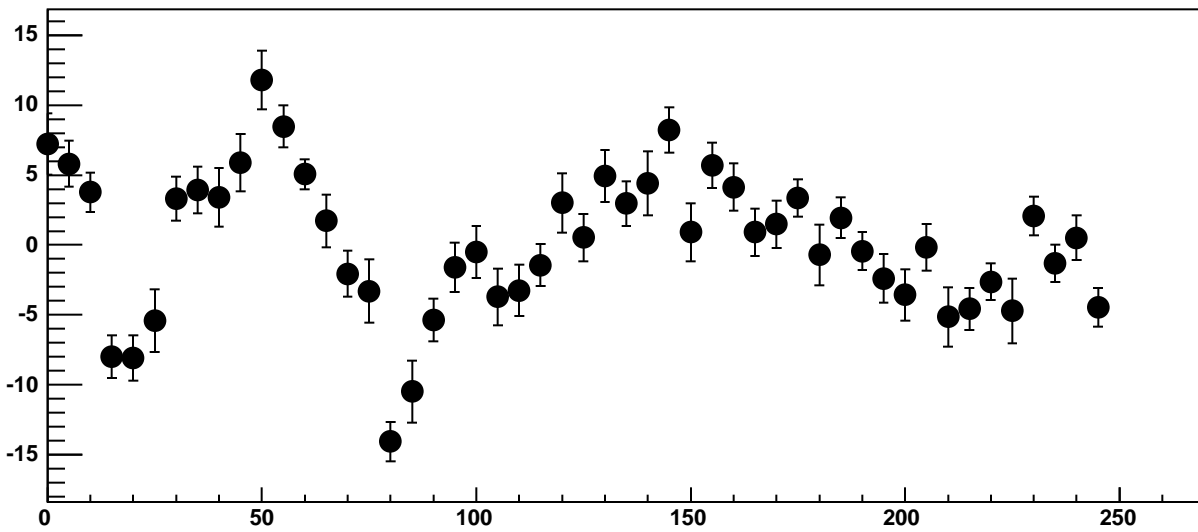


χ^2 / ndf	416.8 / 41
p0	-217.9 ± 0.5722
p1	80.75 ± 0.4655
p2	$-6.482\text{e}+07 \pm 1.006\text{e}+06$
p3	$3.434\text{e}+07 \pm 7.483\text{e}+05$
p4	1.765 ± 0.01776

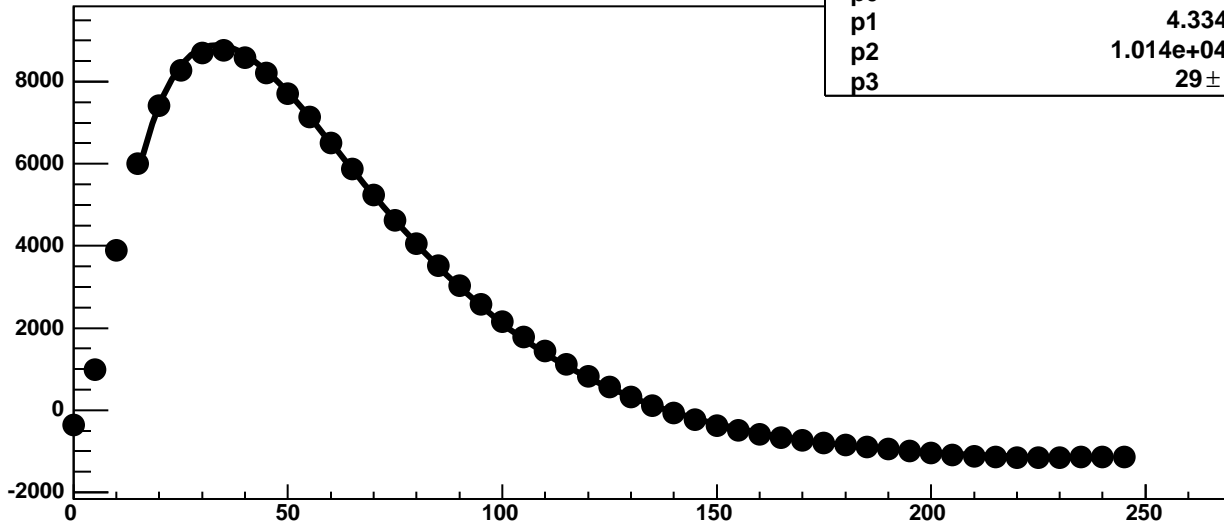
Chip 5, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



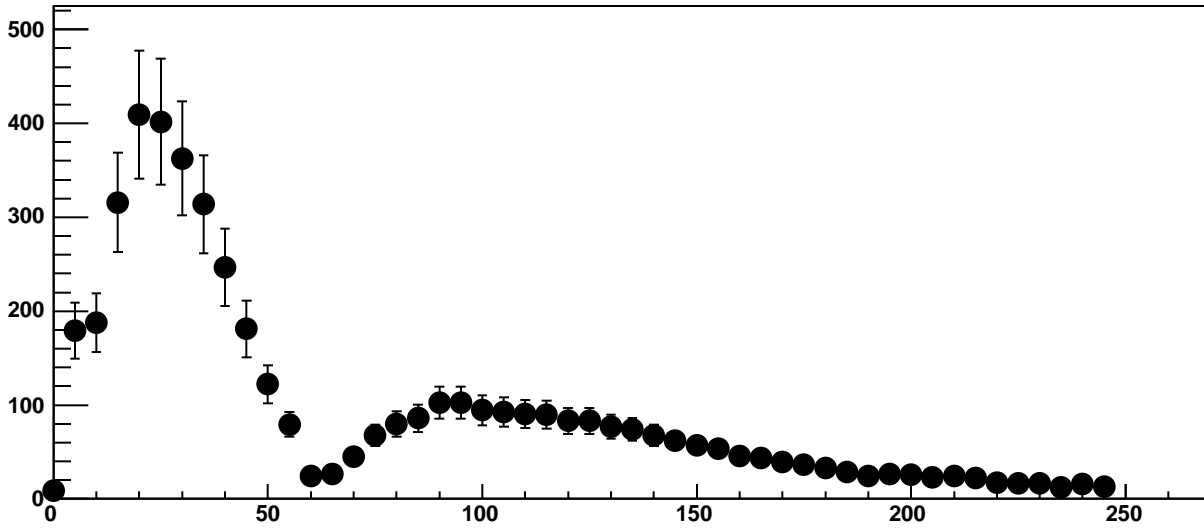
Chip 5, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold



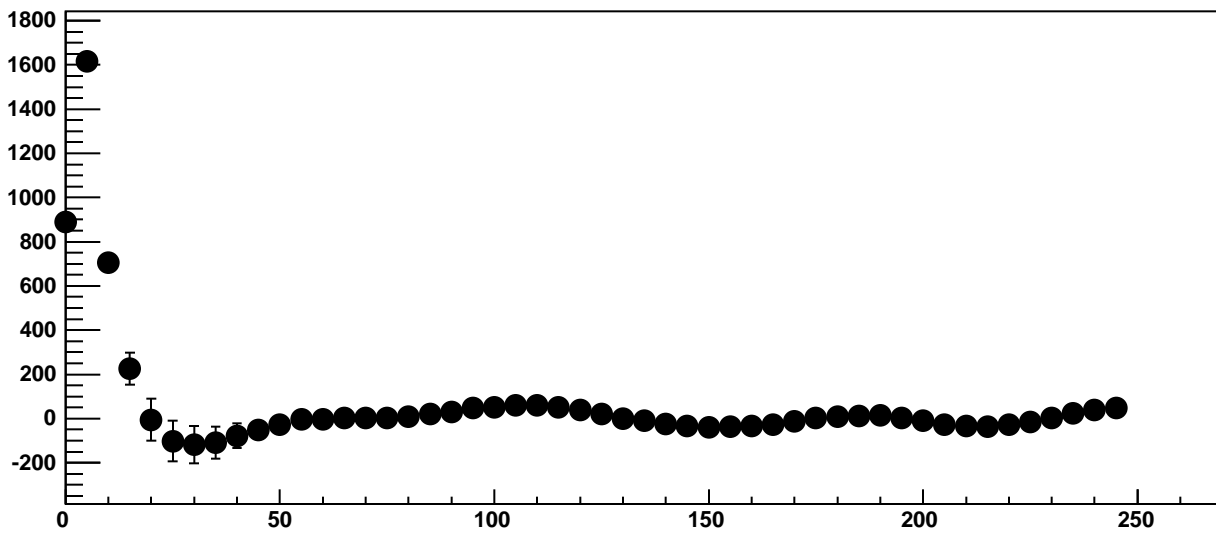
Chip 5, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold



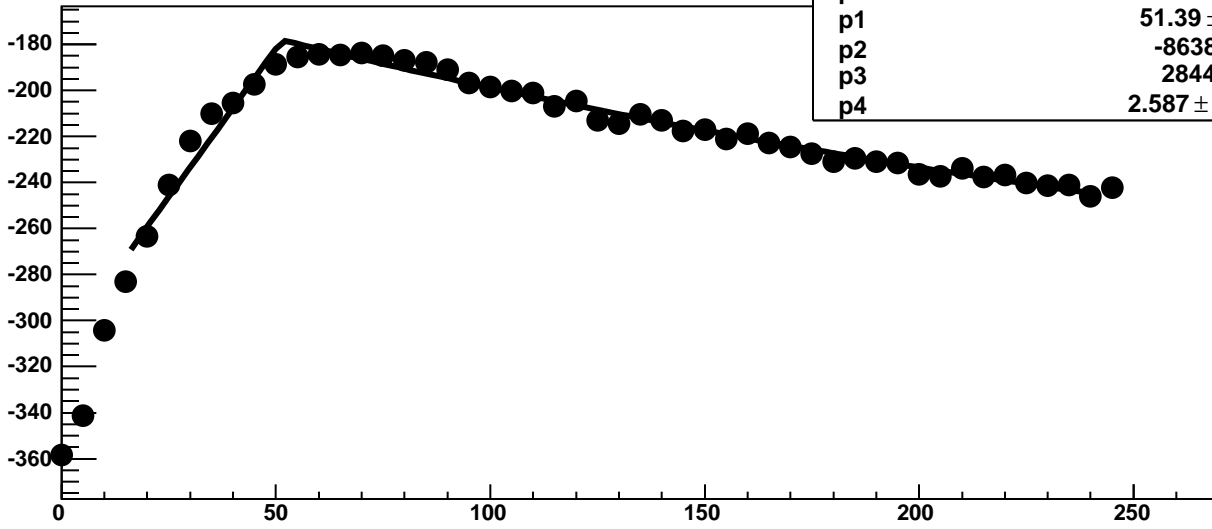
Chip 5, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold

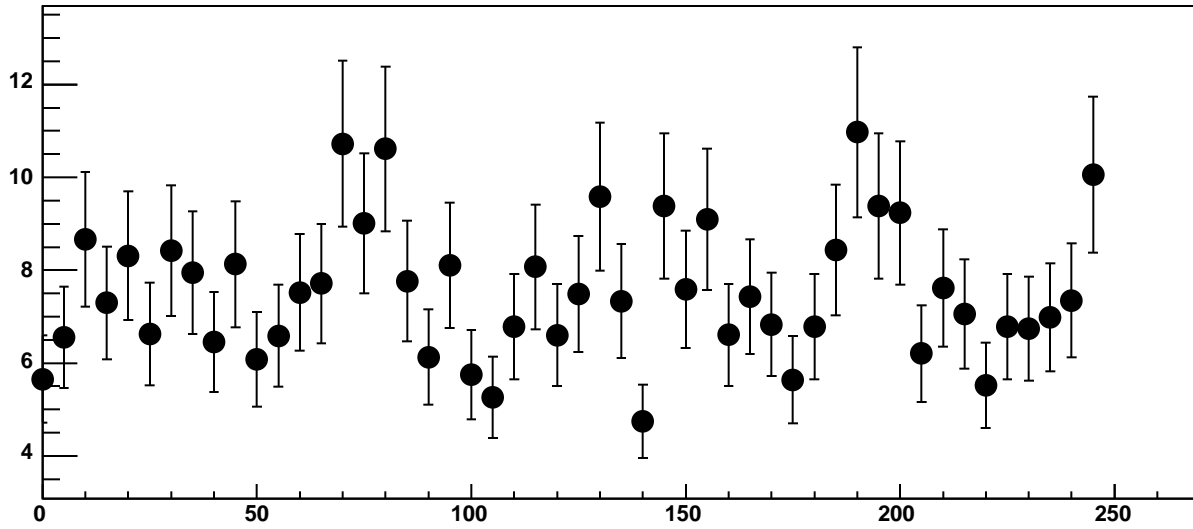


Chip 5, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

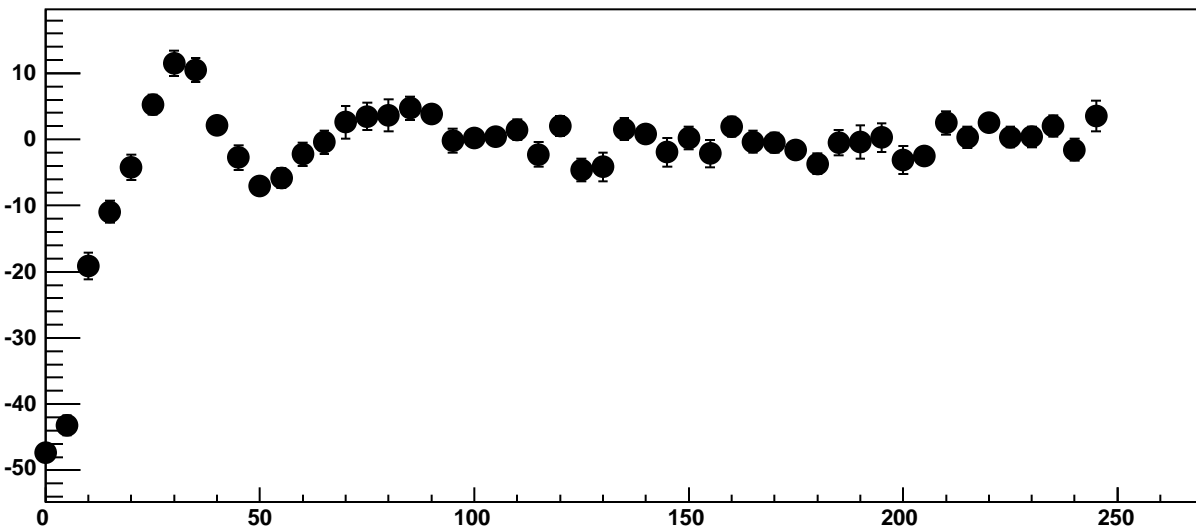


χ^2 / ndf	236.3 / 41
p0	-178.1 ± 0.6108
p1	51.39 ± 0.4026
p2	-8638 ± 943.5
p3	2844 ± 316.8
p4	2.587 ± 0.04954

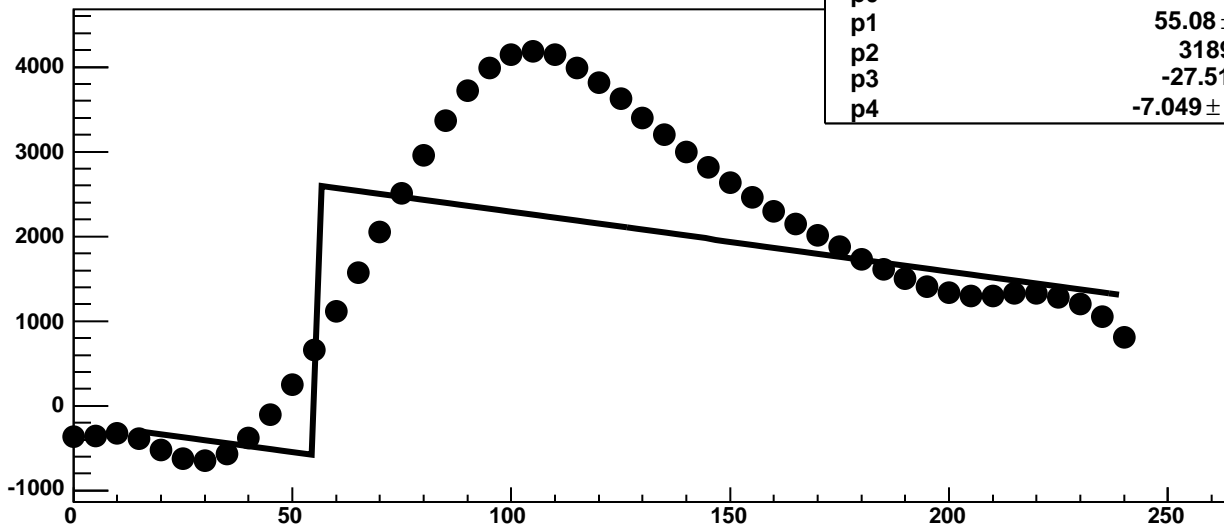
Chip 5, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

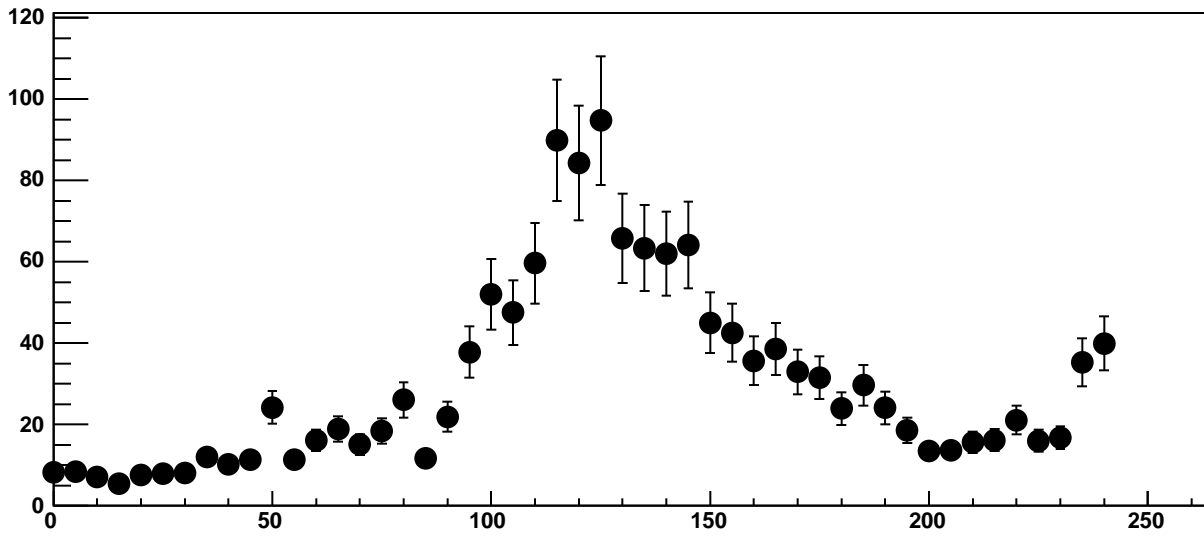


Chip 5, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold

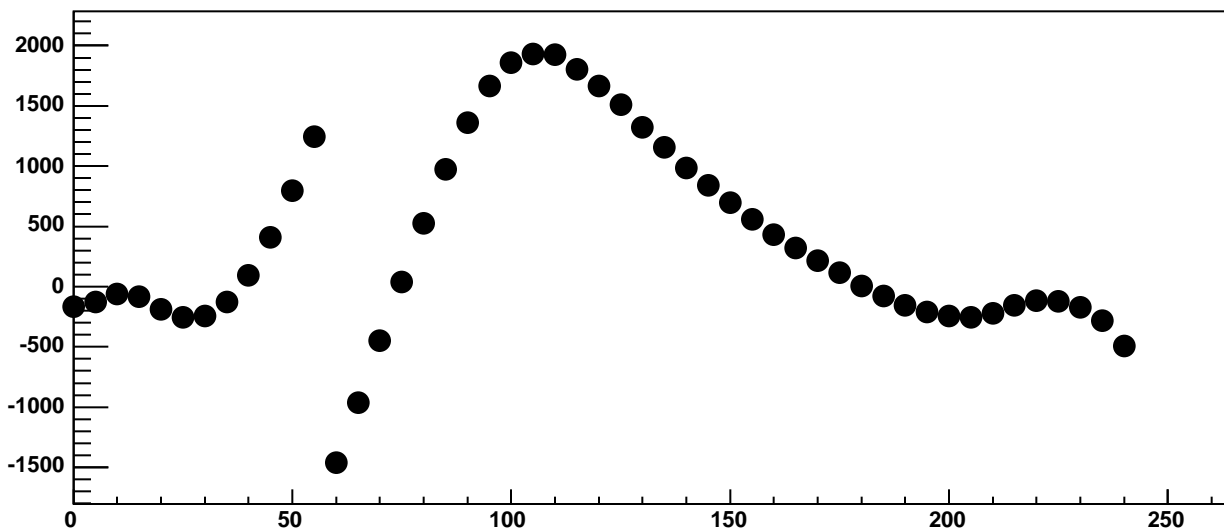


χ^2 / ndf	9.591e+05 / 41
p0	-582 ± 5.236
p1	55.08 ± 0.7548
p2	3189 ± 0.811
p3	-27.51 ± 1.269
p4	-7.049 ± 0.03414

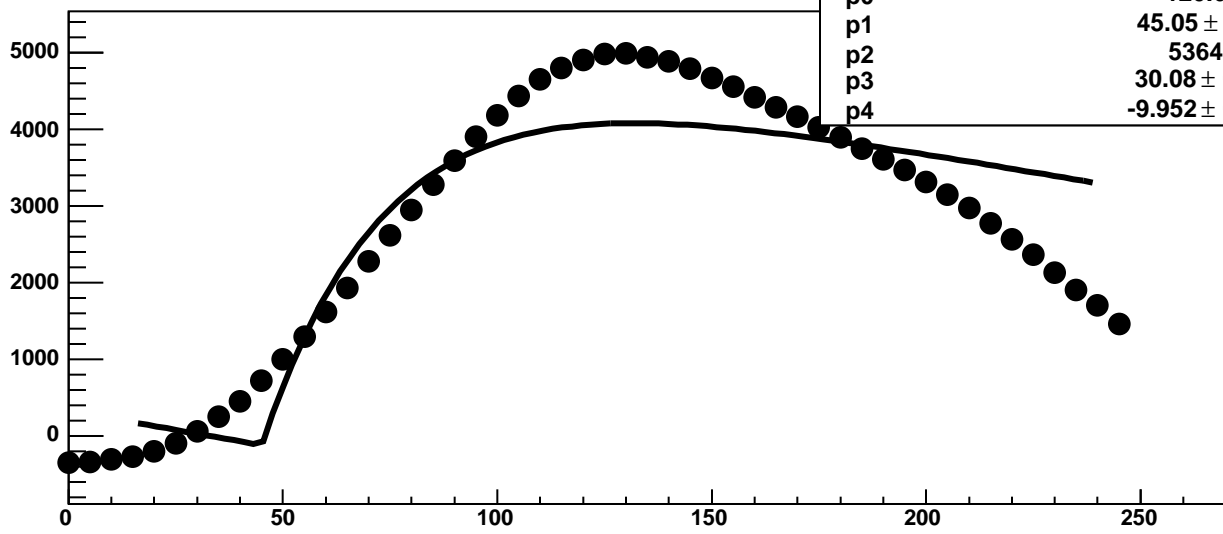
Chip 5, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

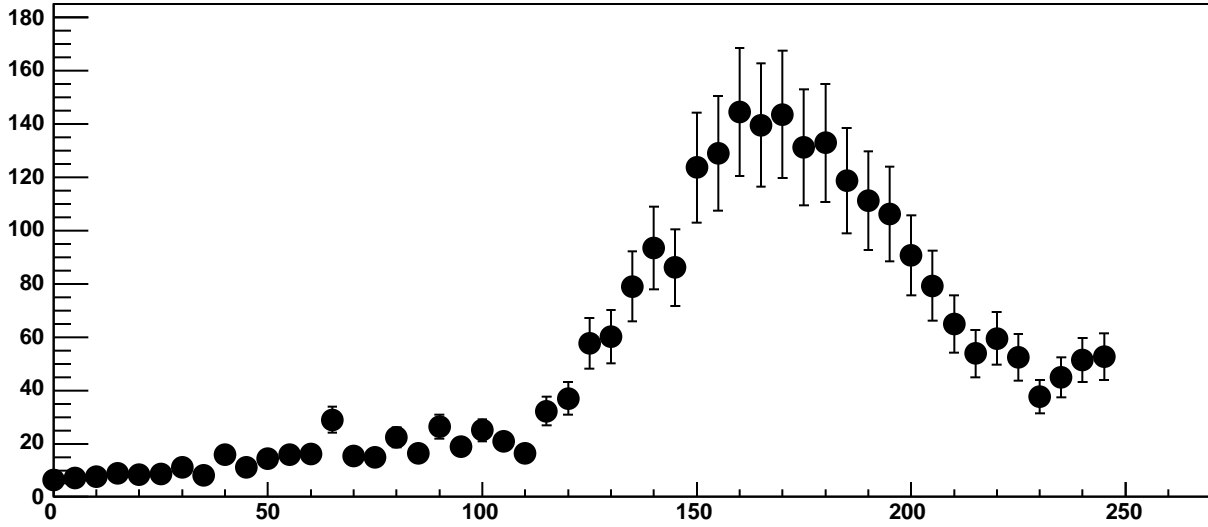


Chip 5, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

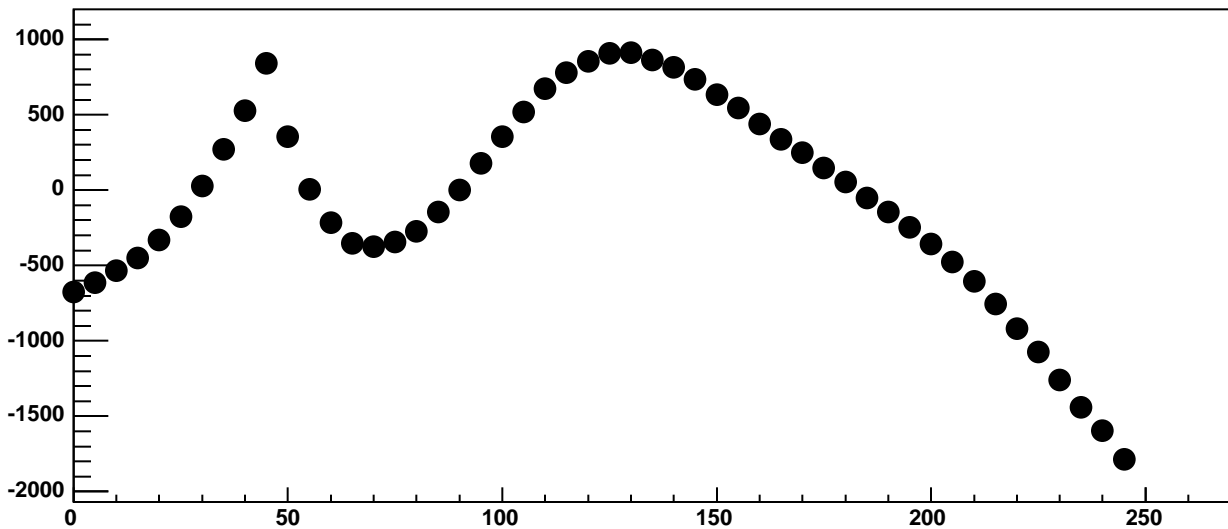


χ^2 / ndf	4.449e+05 / 41
p0	-120.6 ± 1.091
p1	45.05 ± 0.02117
p2	5364 ± 7.082
p3	30.08 ± 0.05762
p4	-9.952 ± 0.04463

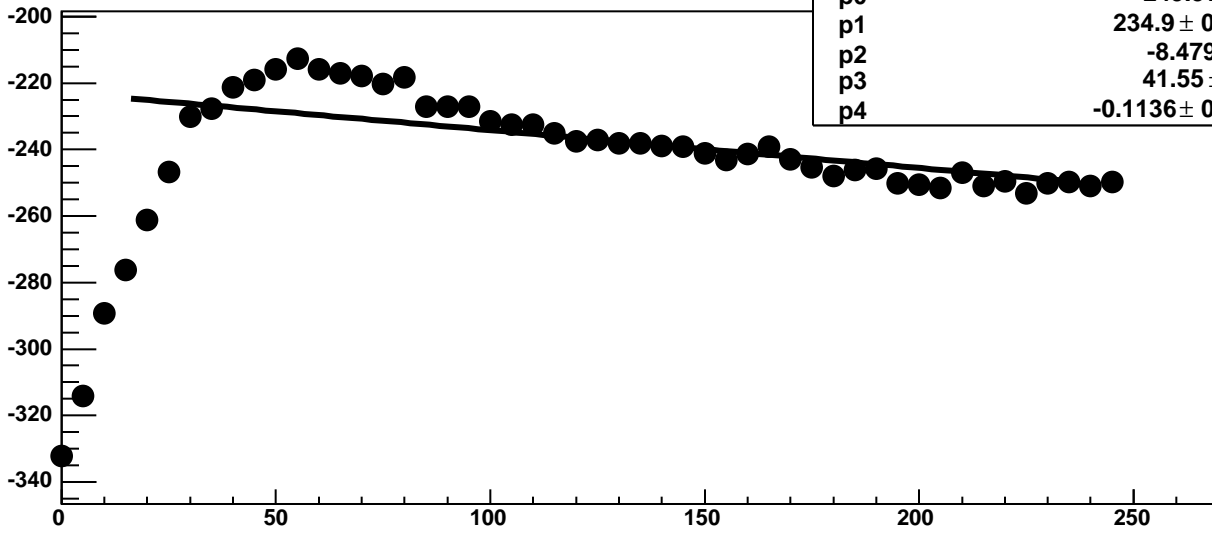
Chip 5, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

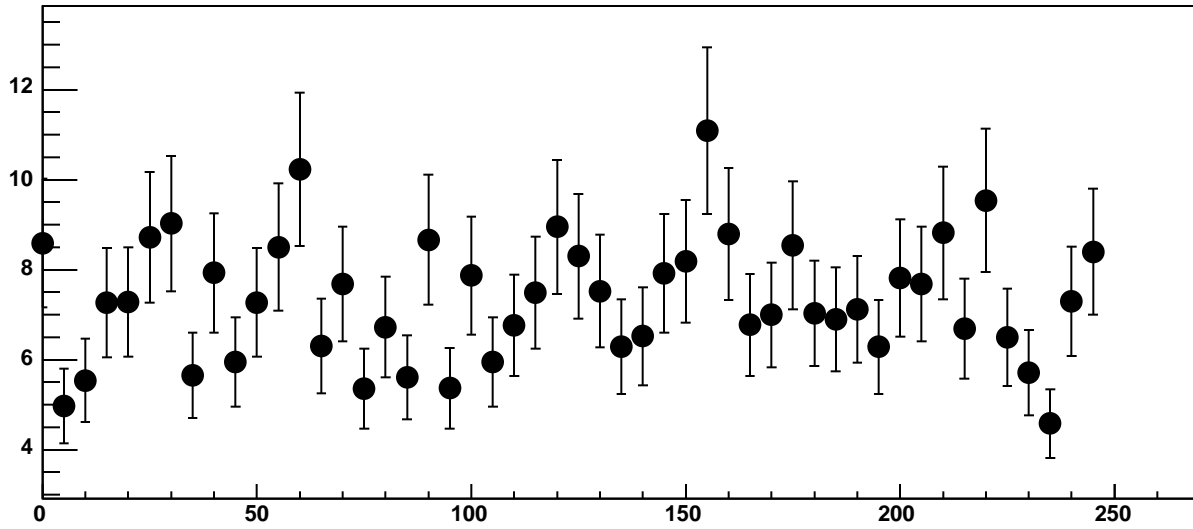


Chip 5, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold

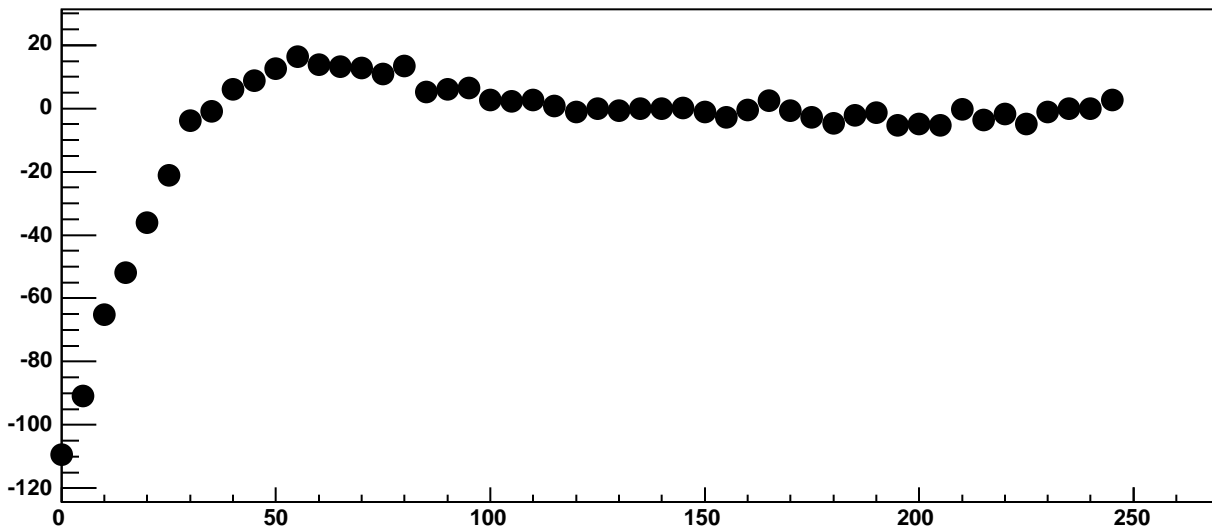


χ^2 / ndf	2182 / 41
p0	-249.5 ± 0.4587
p1	234.9 ± 0.001316
p2	-8.479 ± 15.07
p3	41.55 ± 0.1143
p4	-0.1136 ± 0.003616

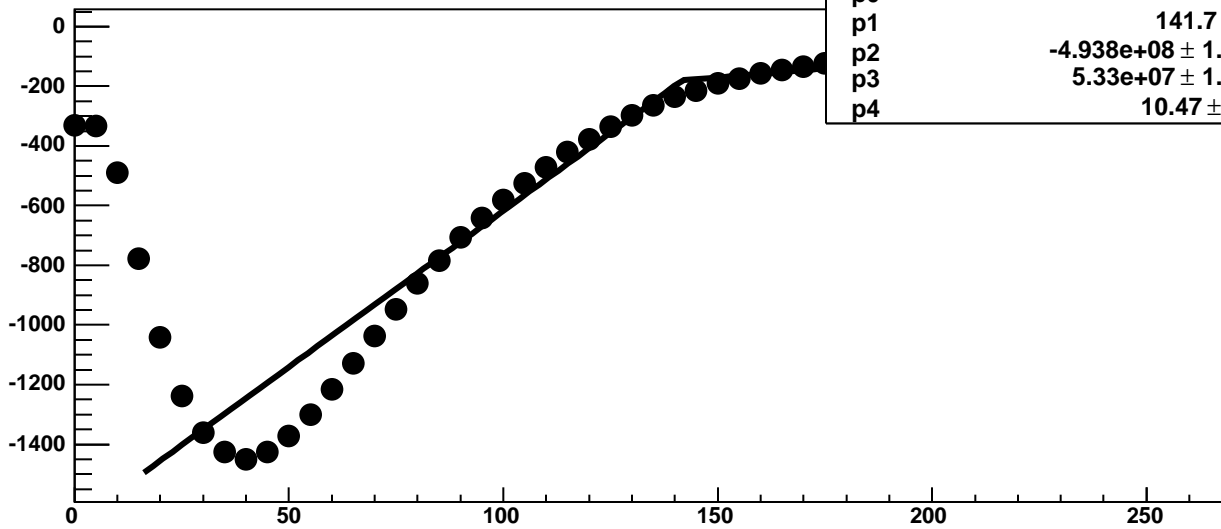
Chip 5, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold

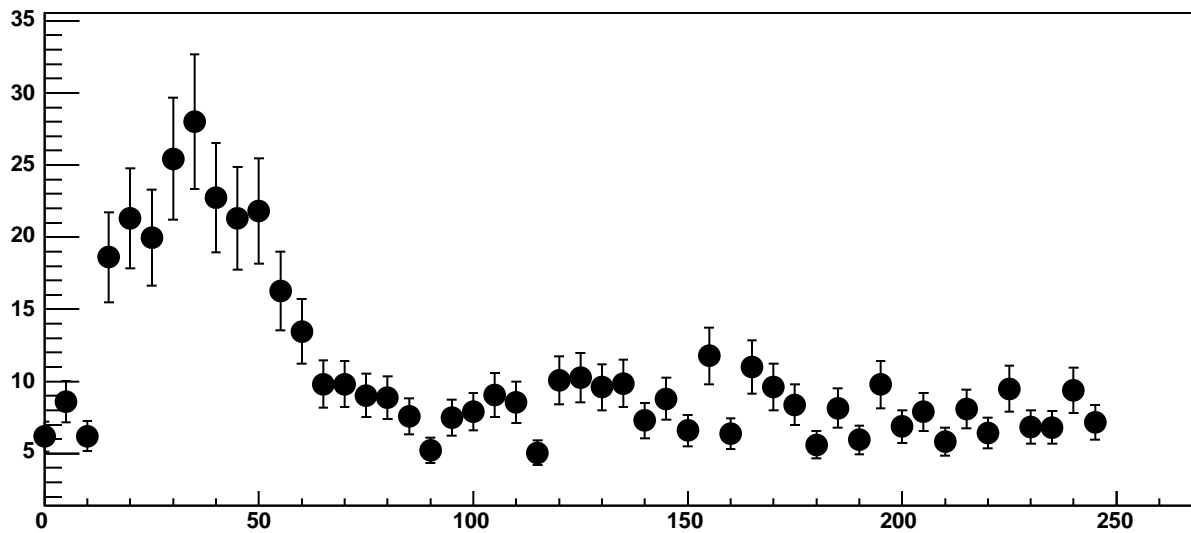


Chip 5, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

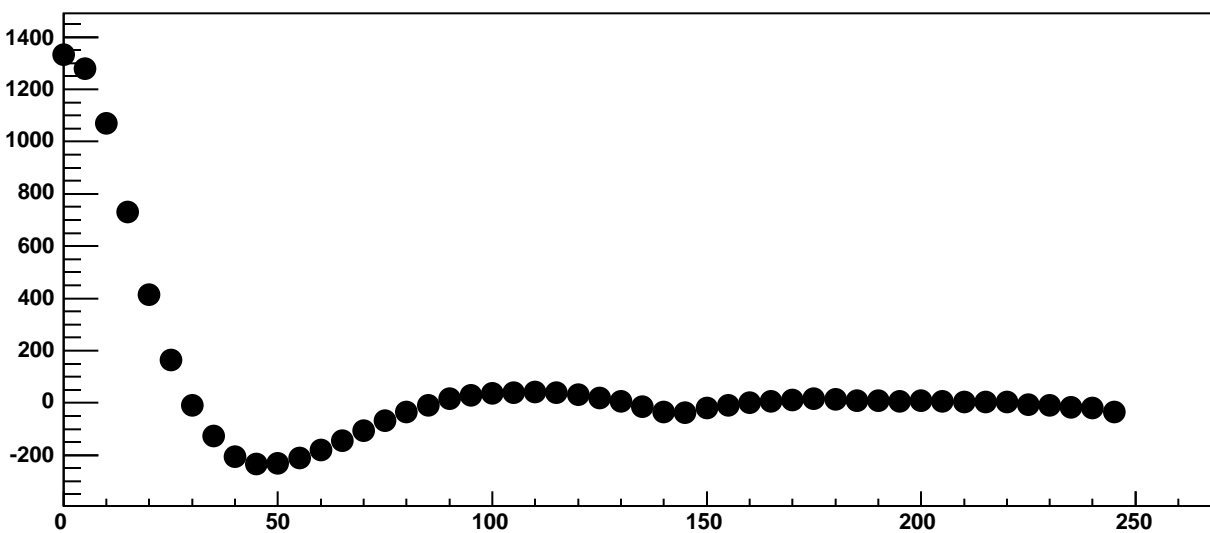


χ^2 / ndf	6.346e+04 / 41
p0	-180.4 ± 0.9248
p1	141.7 ± 0.1269
p2	-4.938e+08 ± 1.713e+07
p3	5.33e+07 ± 1.823e+06
p4	10.47 ± 0.01667

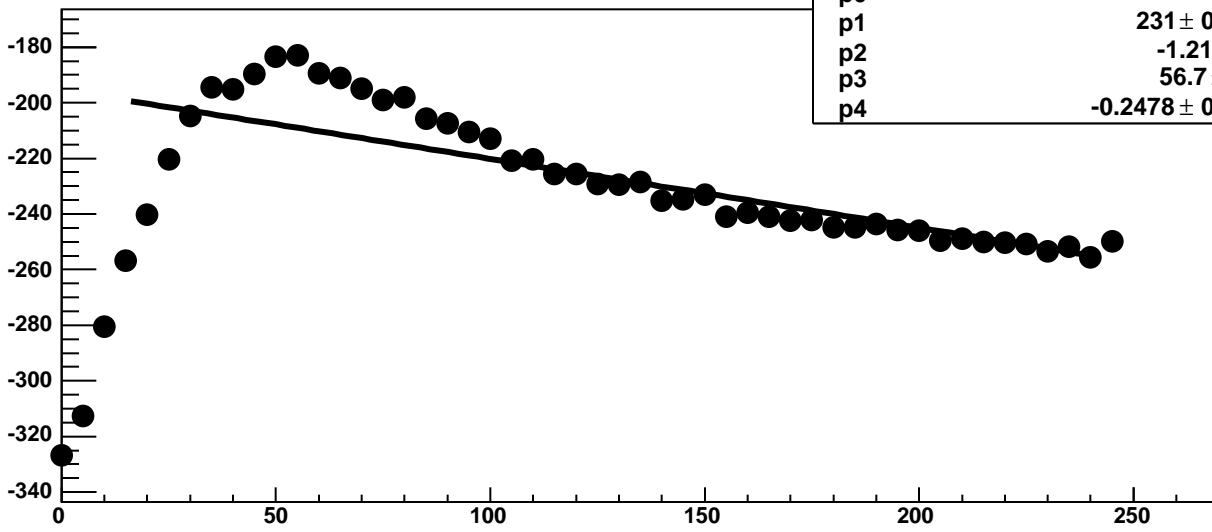
Chip 5, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

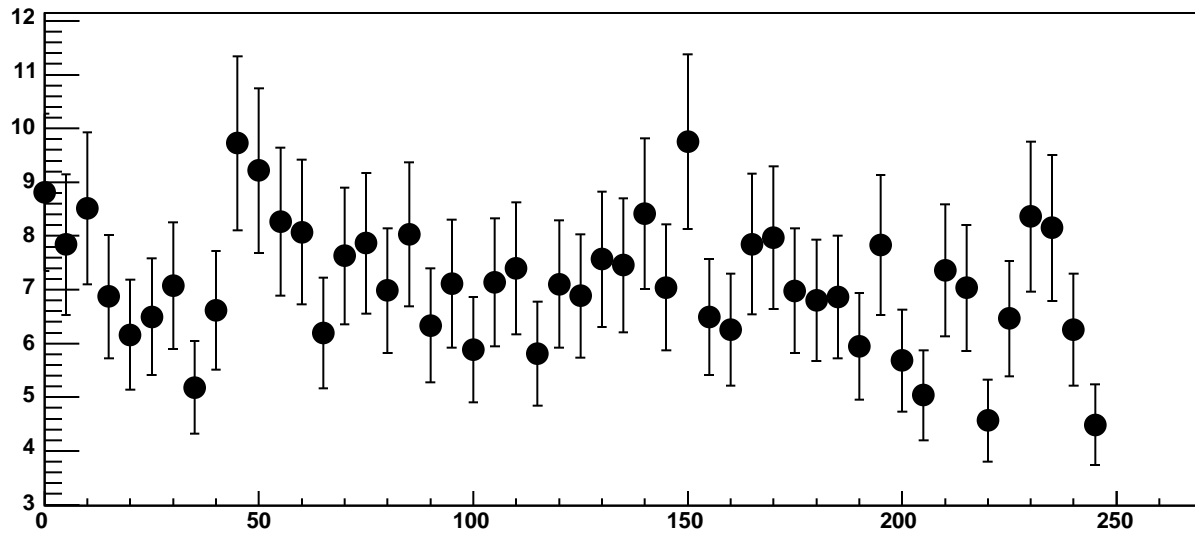


Chip 5, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

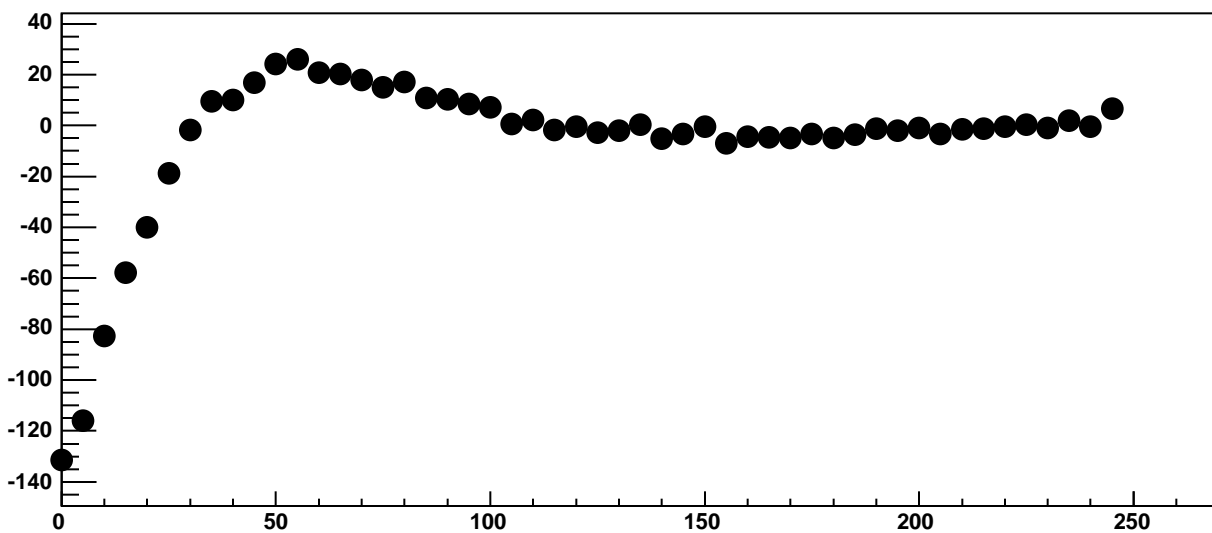


χ^2 / ndf	3643 / 41
p0	-252.6 ± 0.439
p1	231 ± 0.008356
p2	-1.218 ± 9.91
p3	56.7 ± 0.0991
p4	-0.2478 ± 0.003558

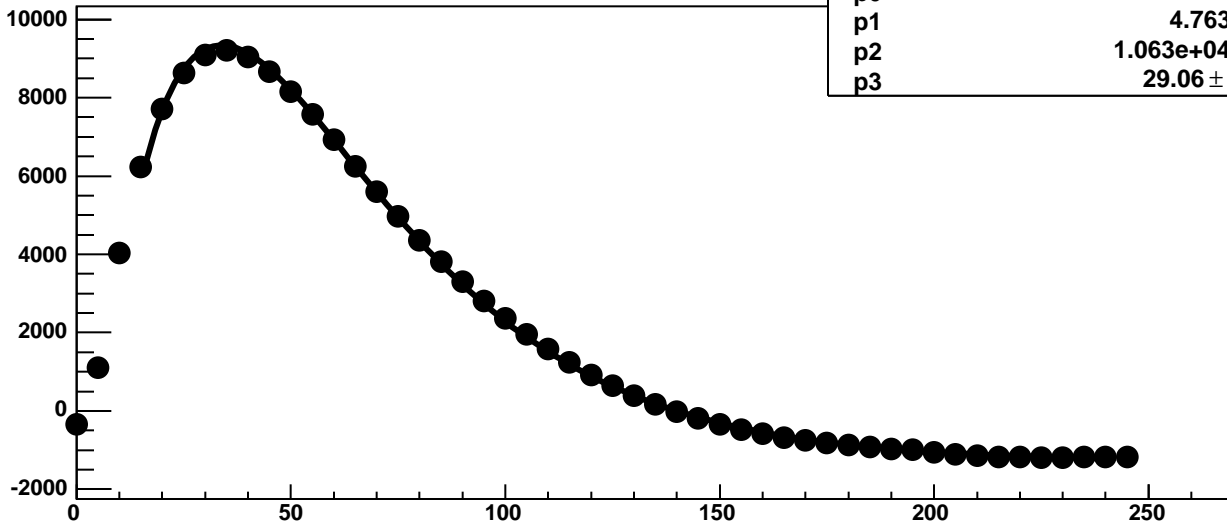
Chip 5, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

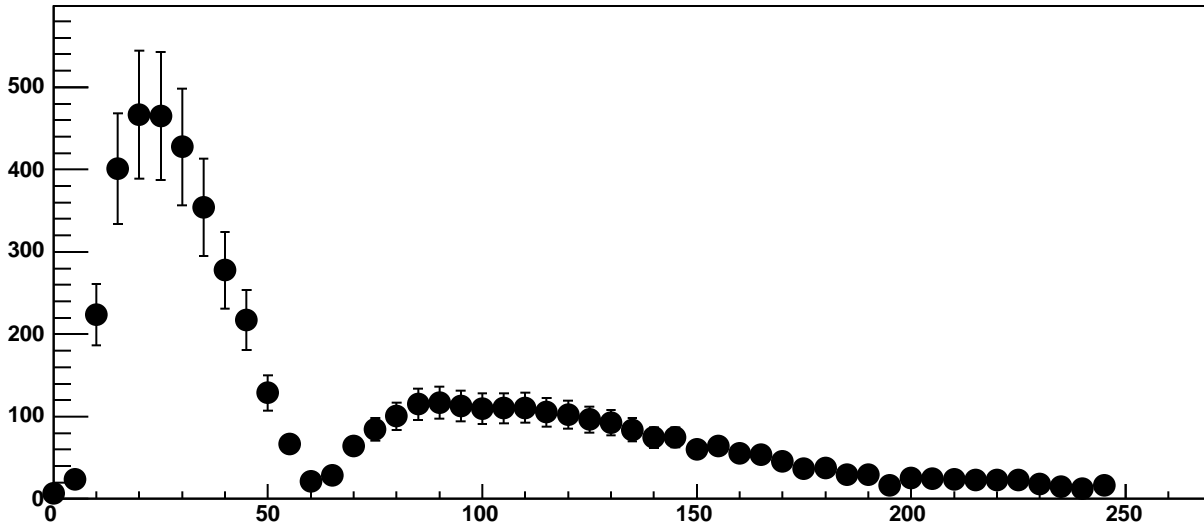


Chip 5, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold

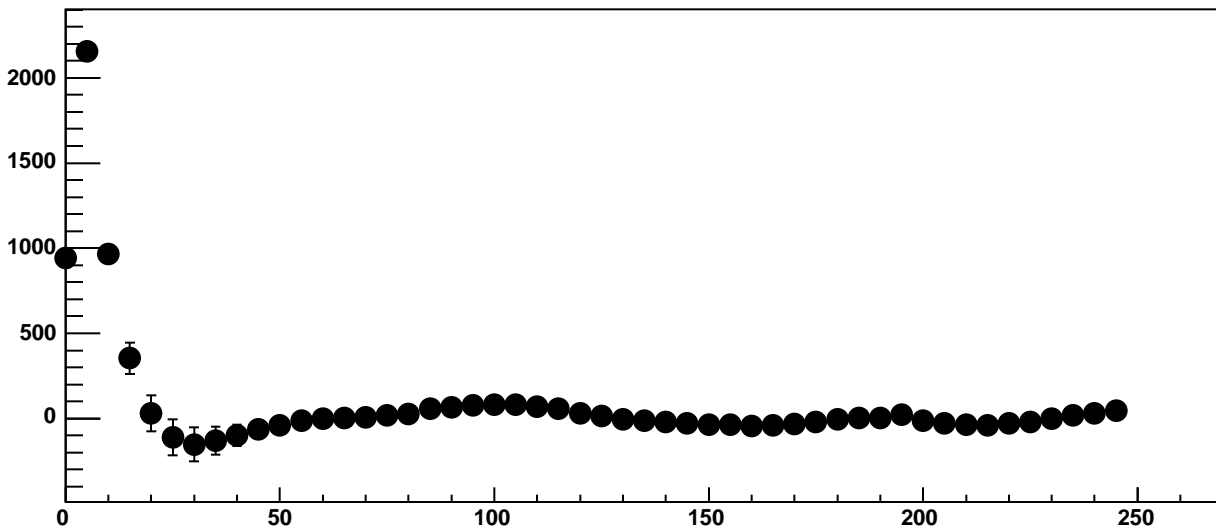


χ^2 / ndf	498.9 / 42
p0	-1283 ± 1.903
p1	4.763 ± 0.139
p2	1.063e+04 ± 18.22
p3	29.06 ± 0.04759

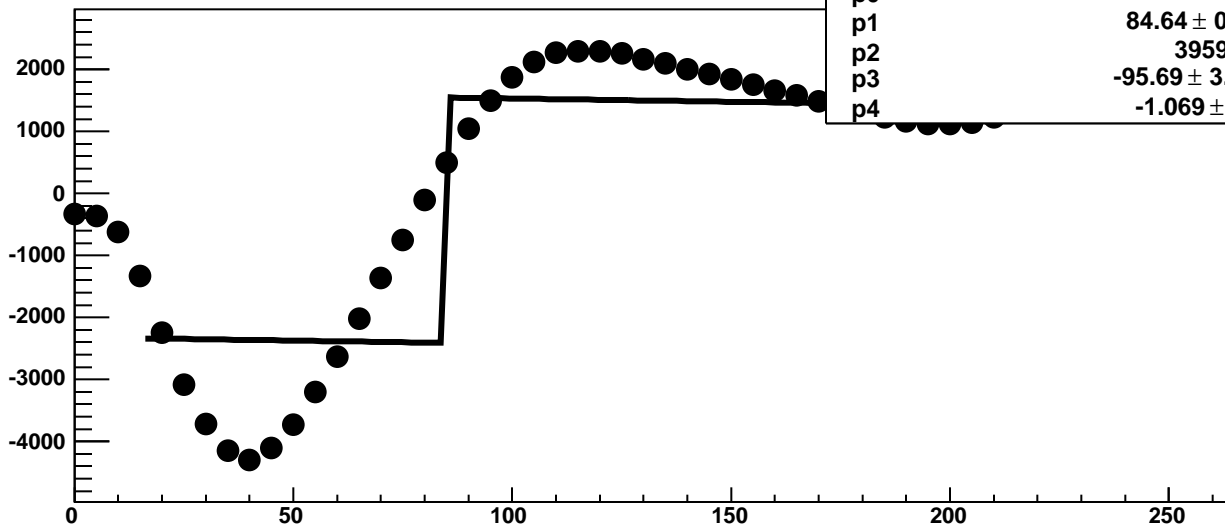
Chip 5, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold



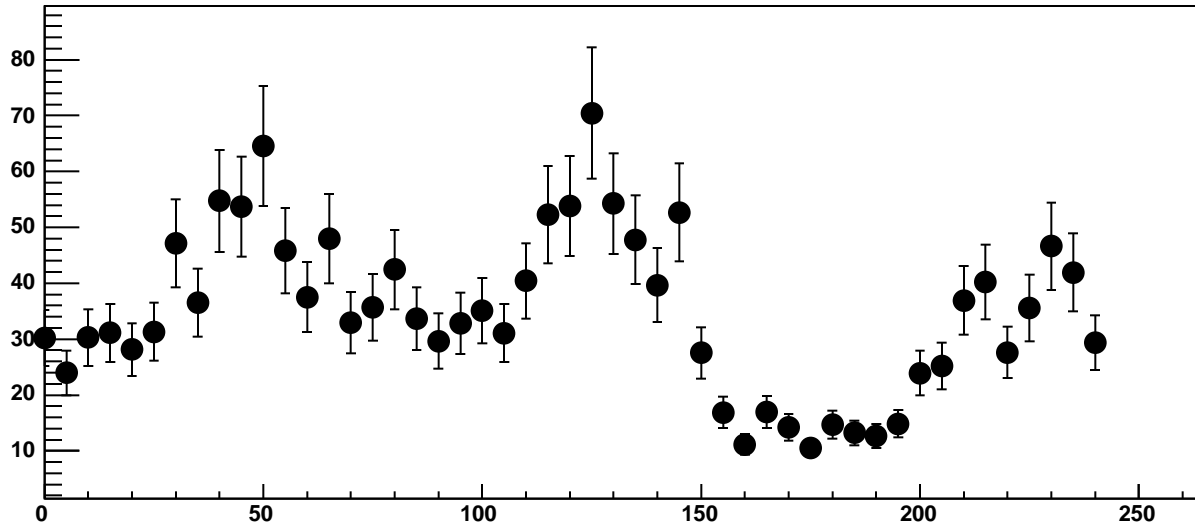
Chip 5, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold



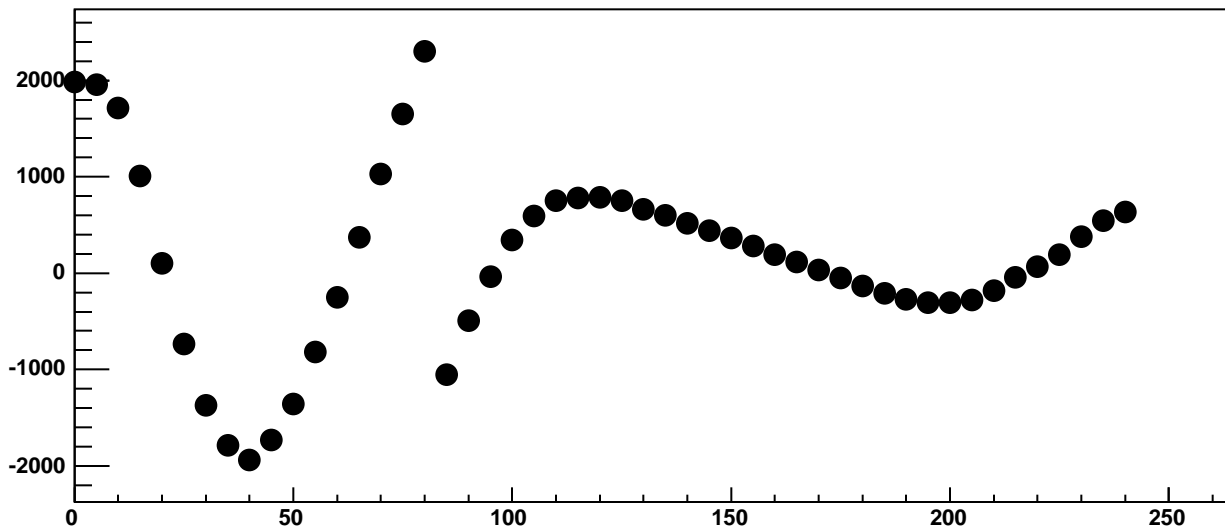
Chip 5, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold



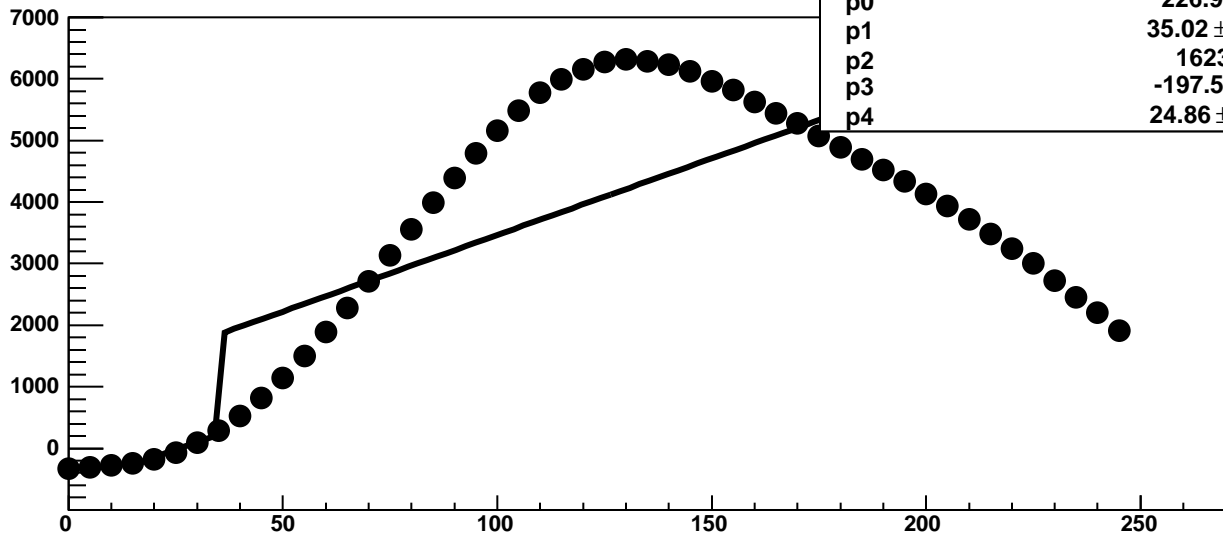
Chip 5, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold

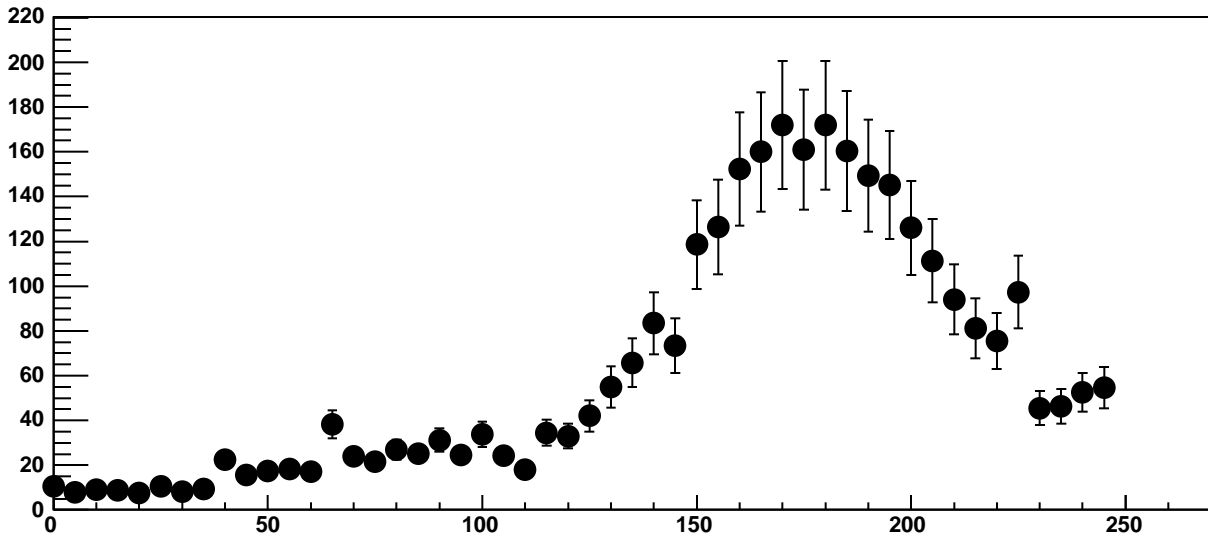


Chip 5, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold

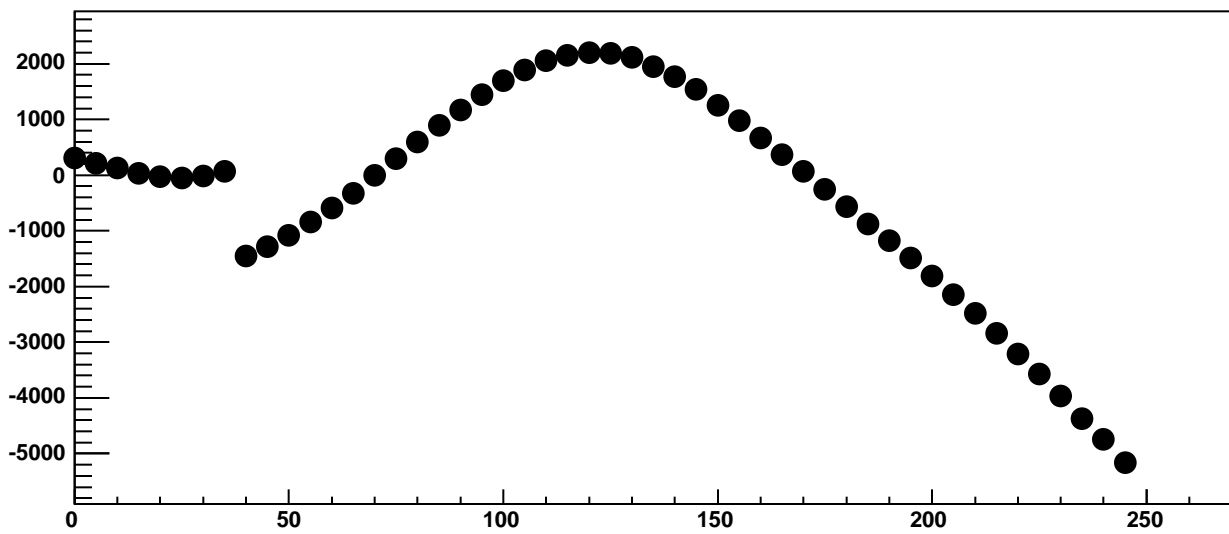


χ^2 / ndf	1.748e+06 / 41
p0	226.9 ± 2.908
p1	35.02 ± 0.1486
p2	1623 ± 8.65
p3	-197.5 ± 13.34
p4	24.86 ± 0.1512

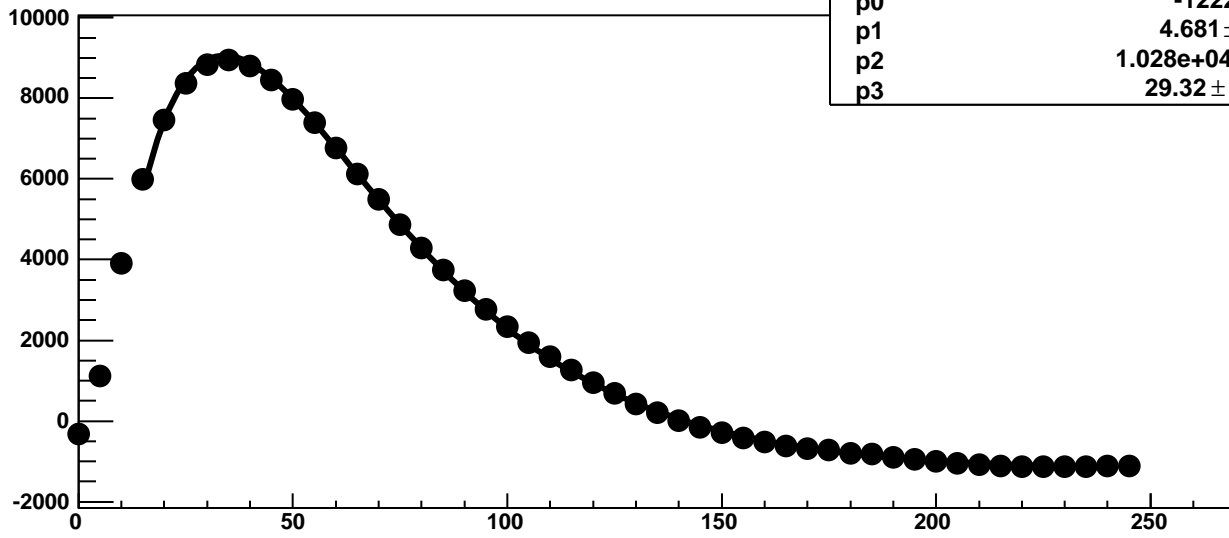
Chip 5, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold

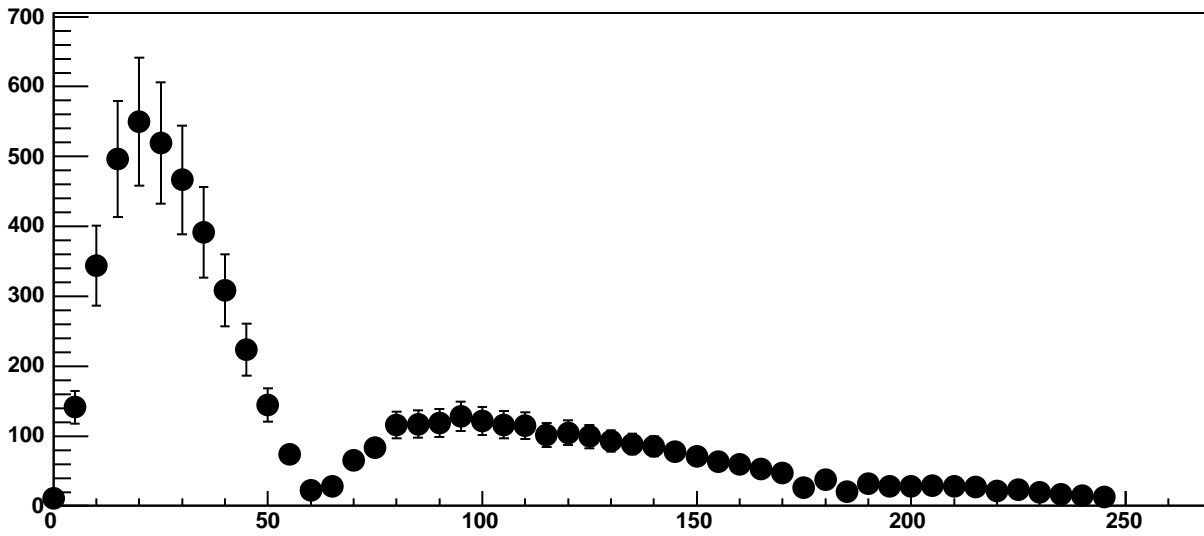


Chip 5, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold

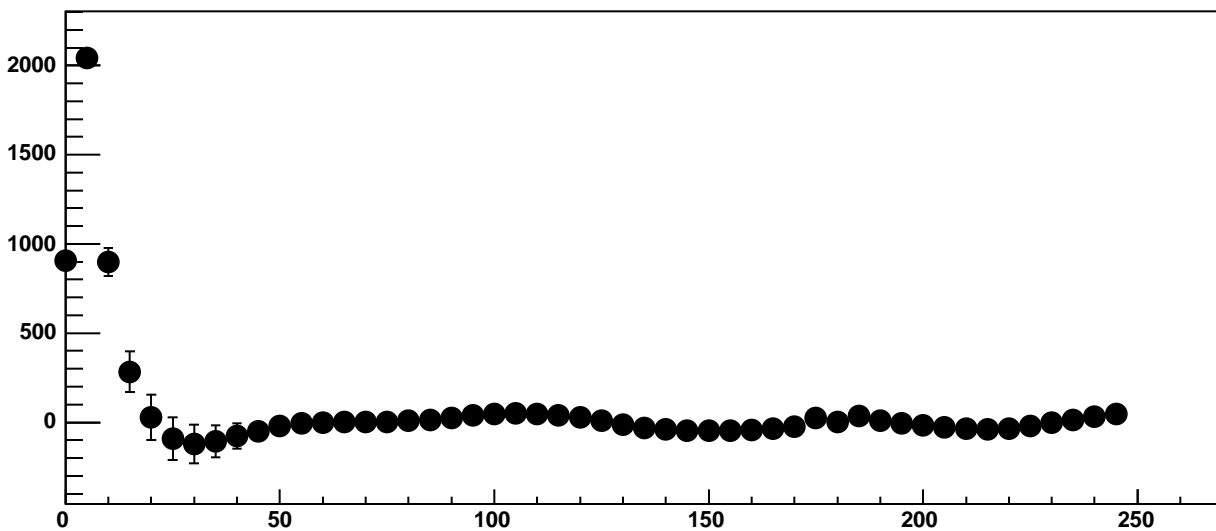


χ^2 / ndf	430.1 / 42
p0	-1222 ± 2.121
p1	4.681 ± 0.1694
p2	$1.028e+04 \pm 20.92$
p3	29.32 ± 0.05308

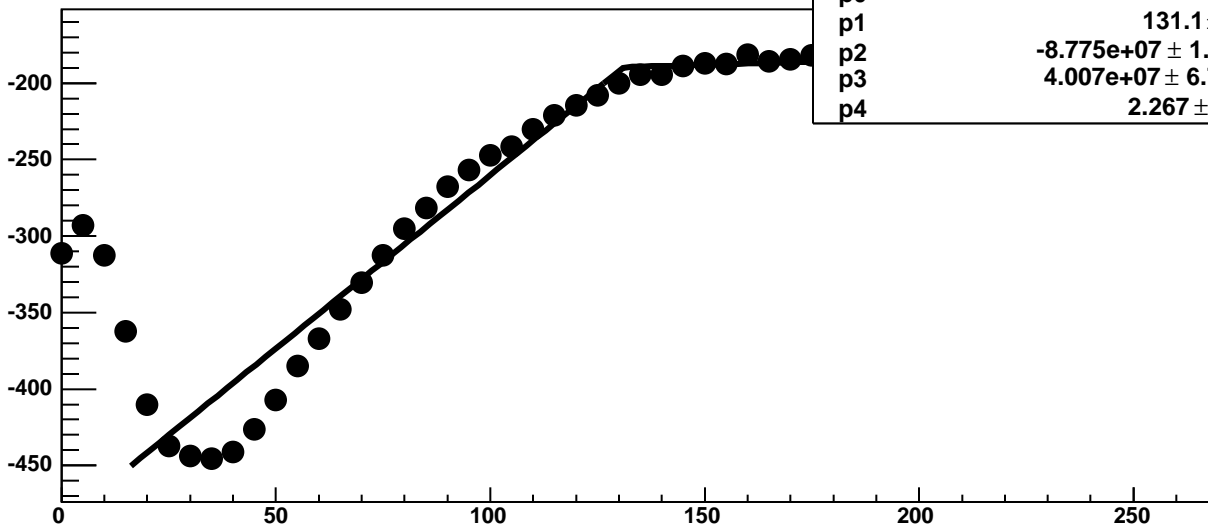
Chip 5, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold

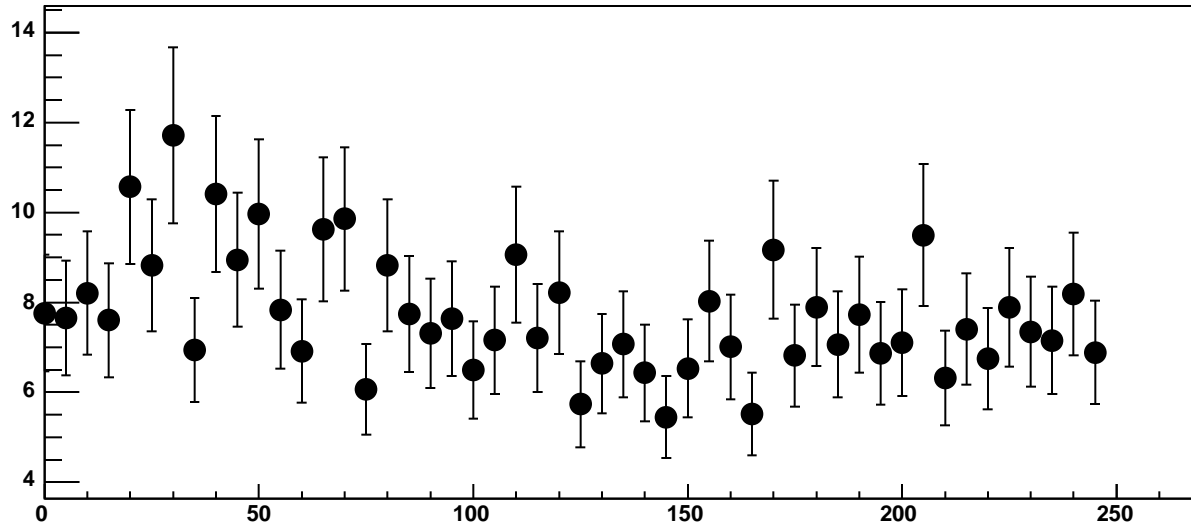


Chip 5, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

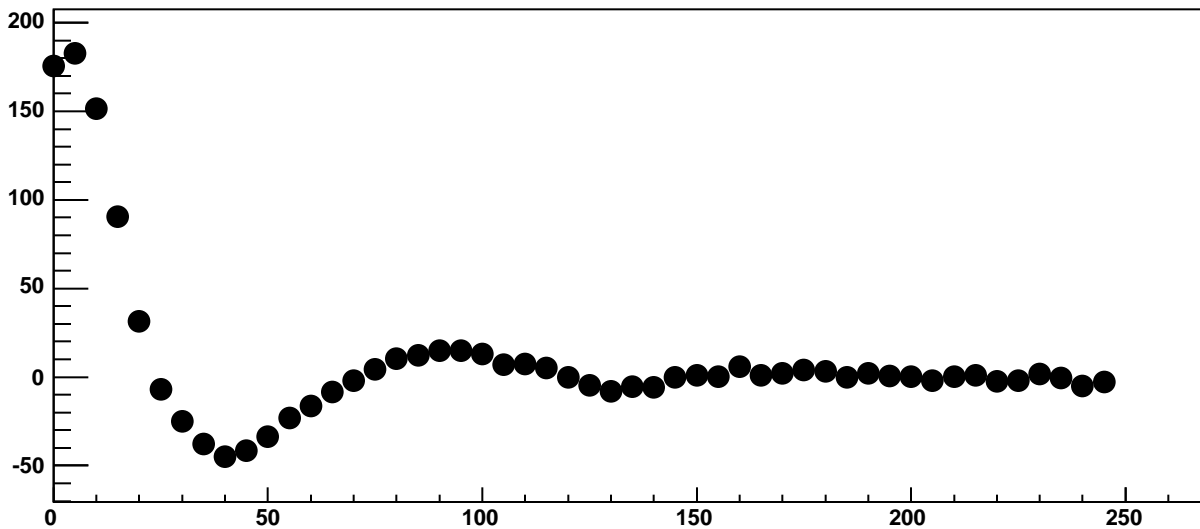


χ^2 / ndf	5256 / 41
p0	-189.5 ± 0.691
p1	131.1 ± 0.4275
p2	$-8.775\text{e}+07 \pm 1.634\text{e}+06$
p3	$4.007\text{e}+07 \pm 6.762\text{e}+05$
p4	2.267 ± 0.01057

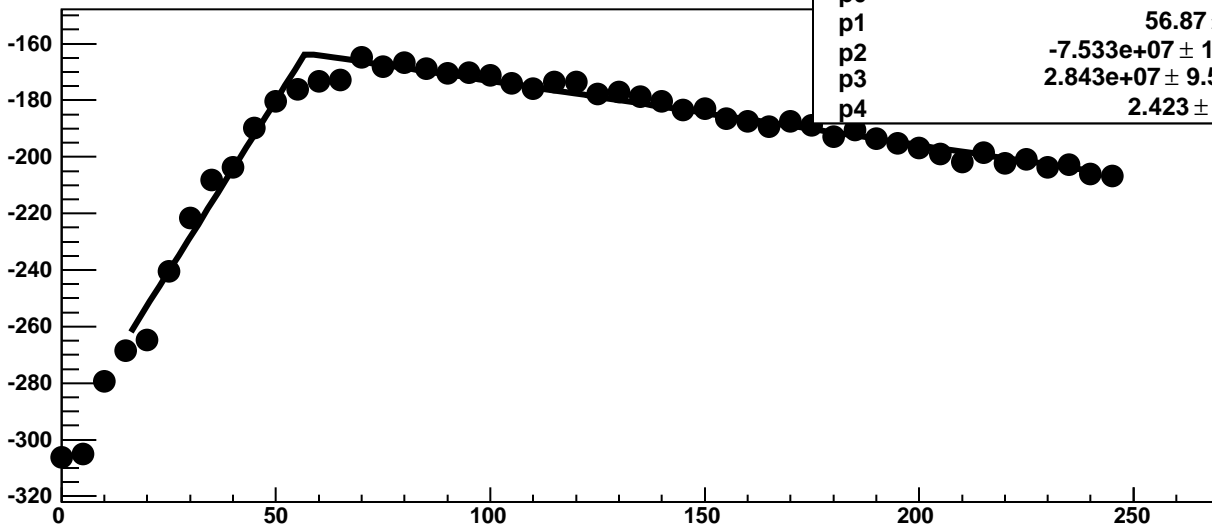
Chip 5, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

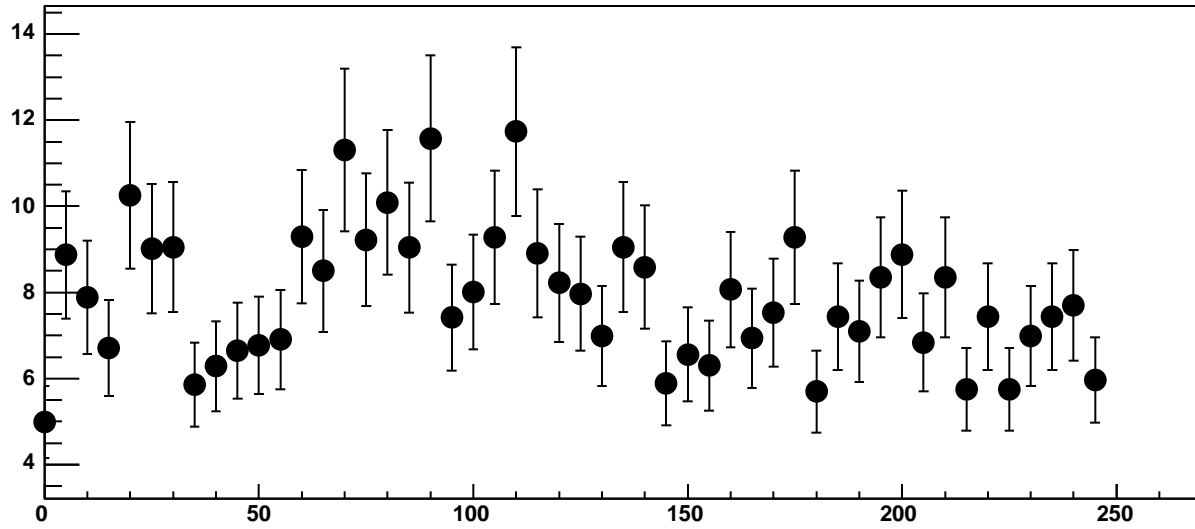


Chip 5, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

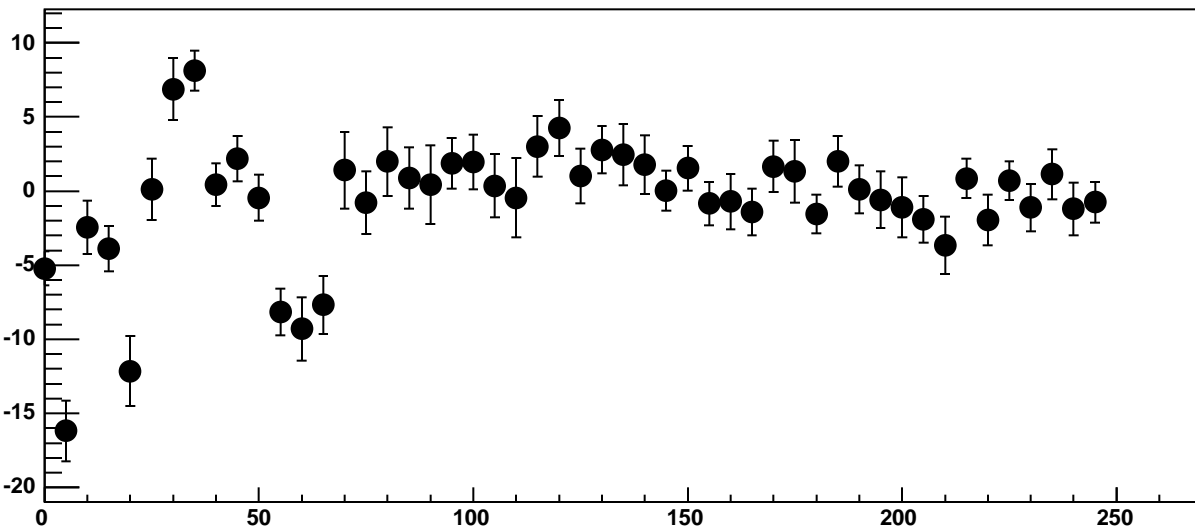


χ^2 / ndf	175.8 / 41
p0	-163.3 ± 0.6099
p1	56.87 ± 0.4631
p2	-7.533e+07 ± 1.27e+06
p3	2.843e+07 ± 9.513e+04
p4	2.423 ± 0.04335

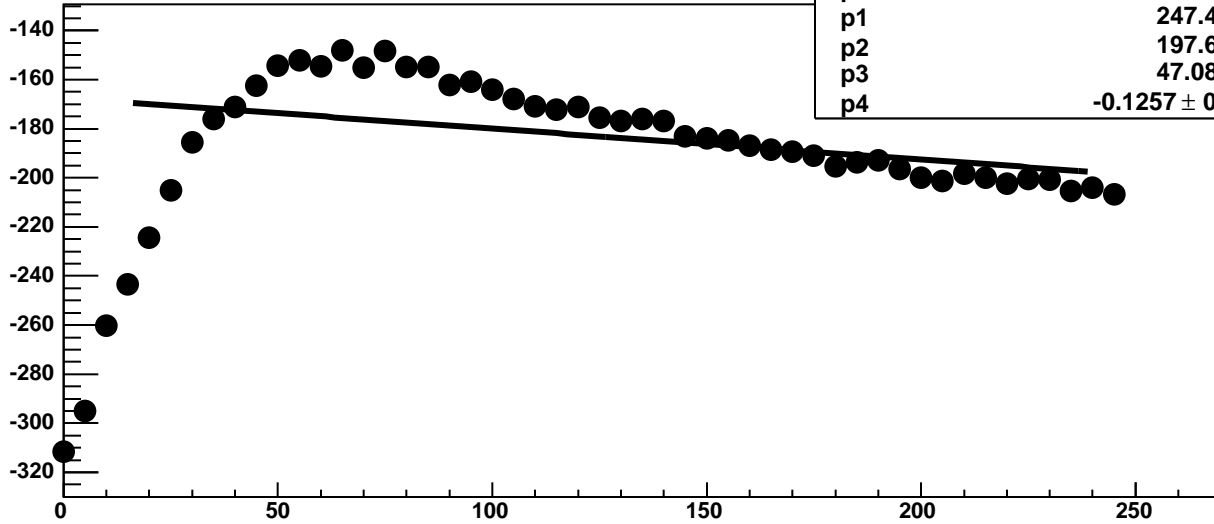
Chip 5, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

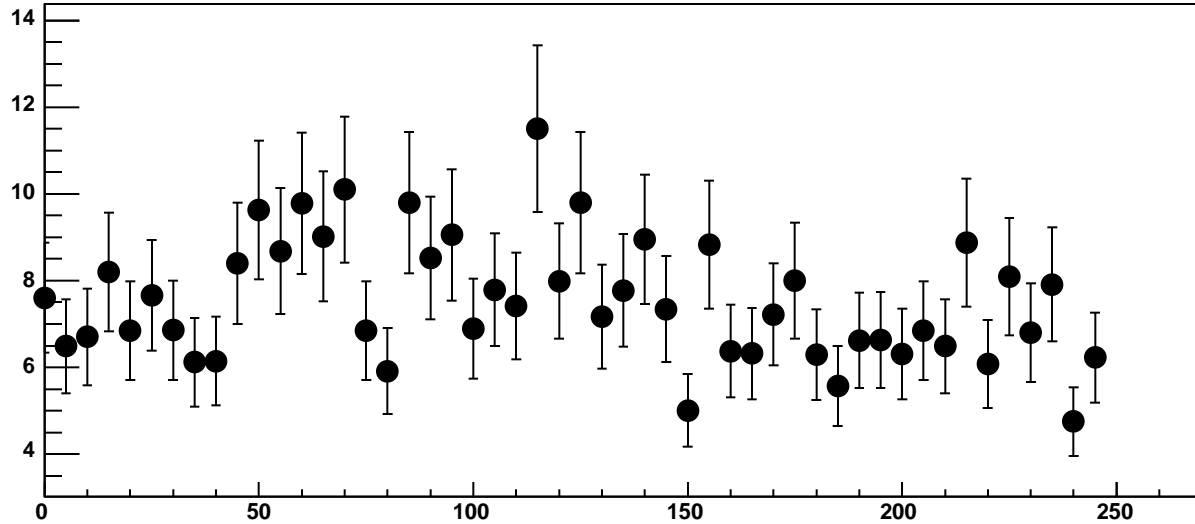


Chip 5, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold

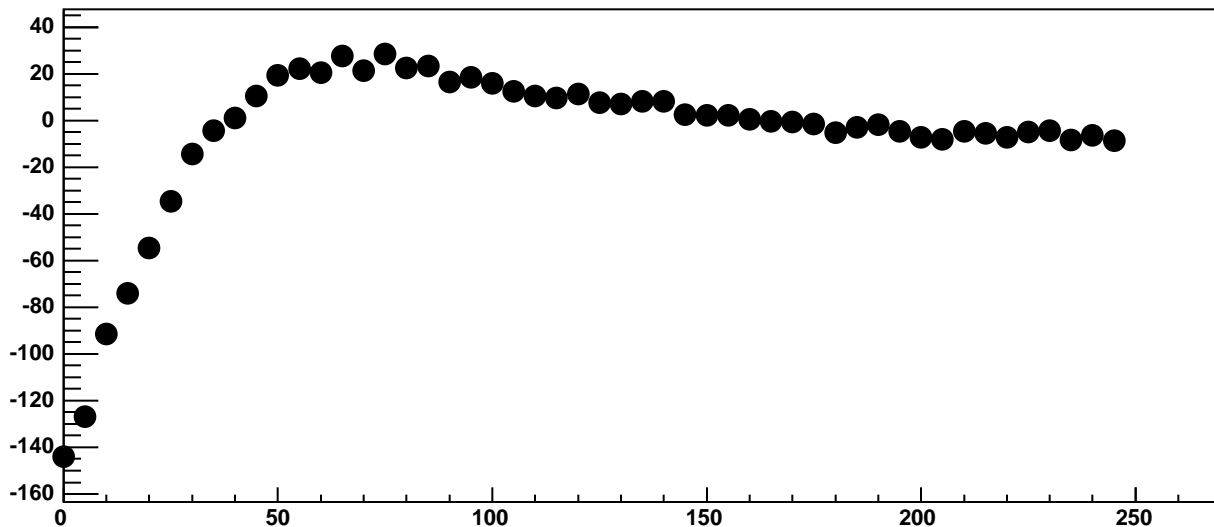


χ^2 / ndf	5177 / 41
p0	-198.5 ± 0.2433
p1	247.4 ± 1.935
p2	197.6 ± 84.85
p3	47.08 ± 1.414
p4	-0.1257 ± 0.001866

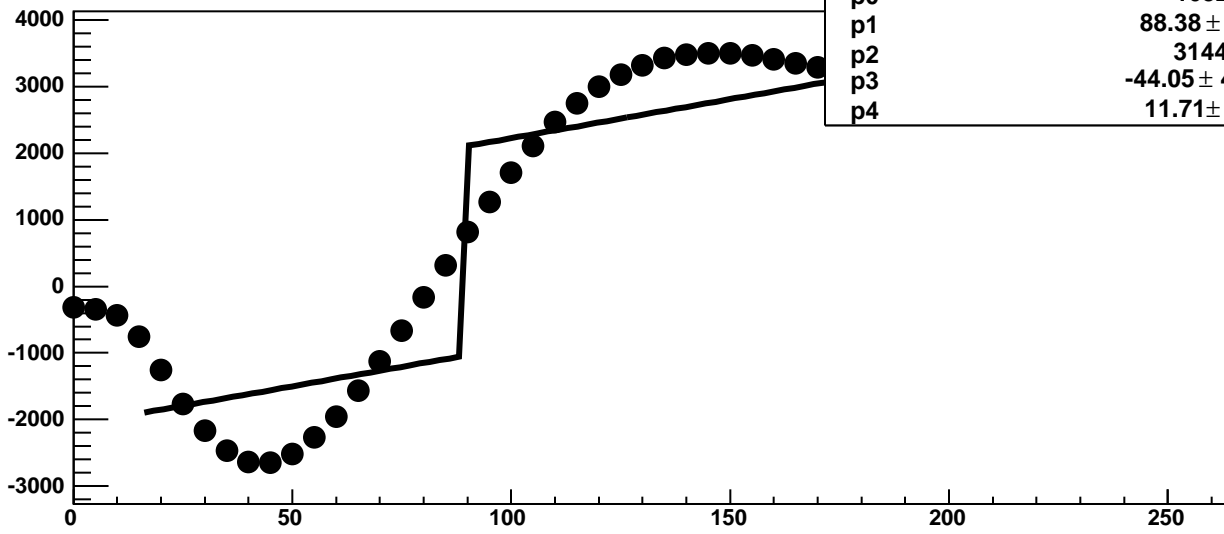
Chip 5, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold

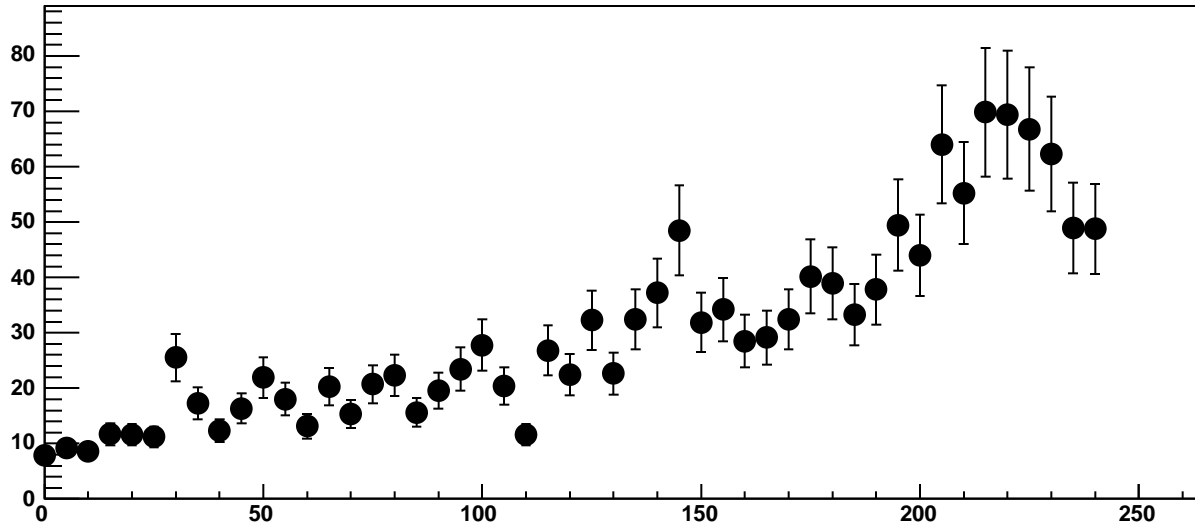


Chip 5, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

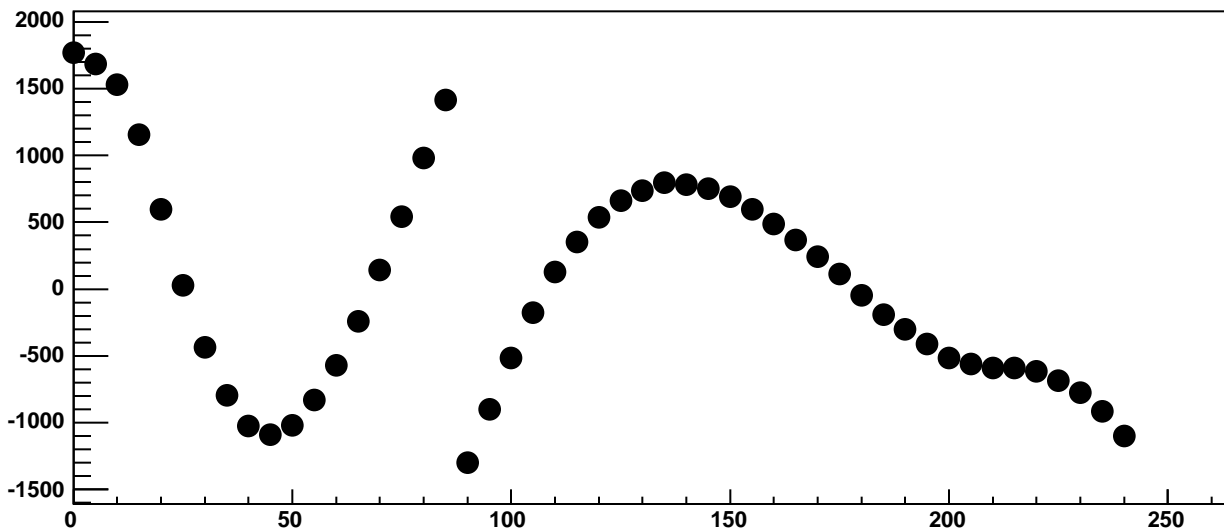


χ^2 / ndf	1.07e+06 / 41
p0	-1052 ± 0.912
p1	88.38 ± 0.07409
p2	3144 ± 2.807
p3	-44.05 ± 4.87e-05
p4	11.71 ± 0.02747

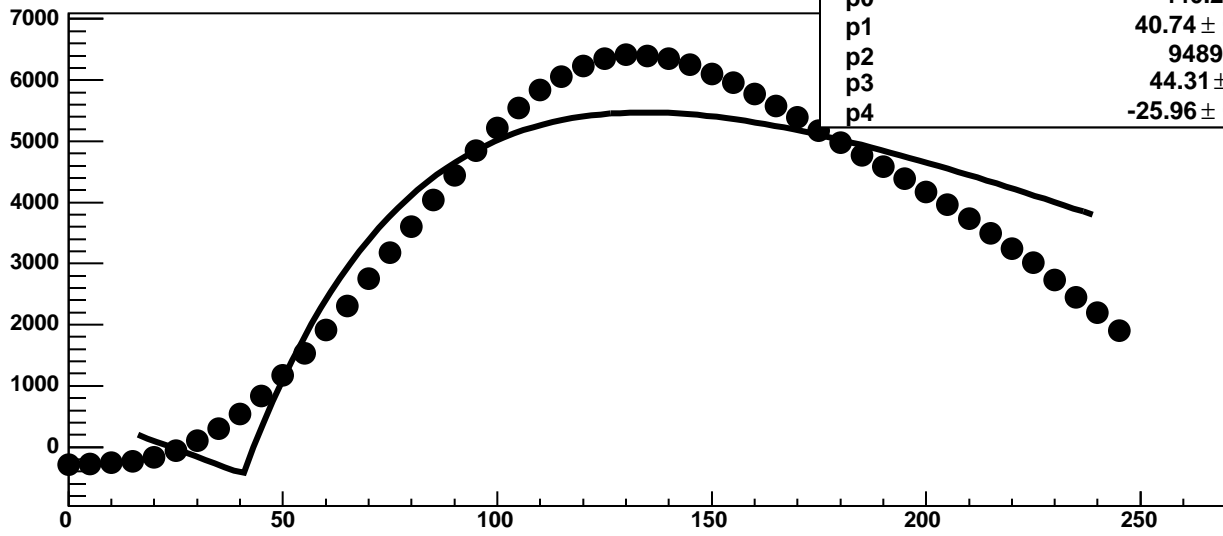
Chip 5, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold

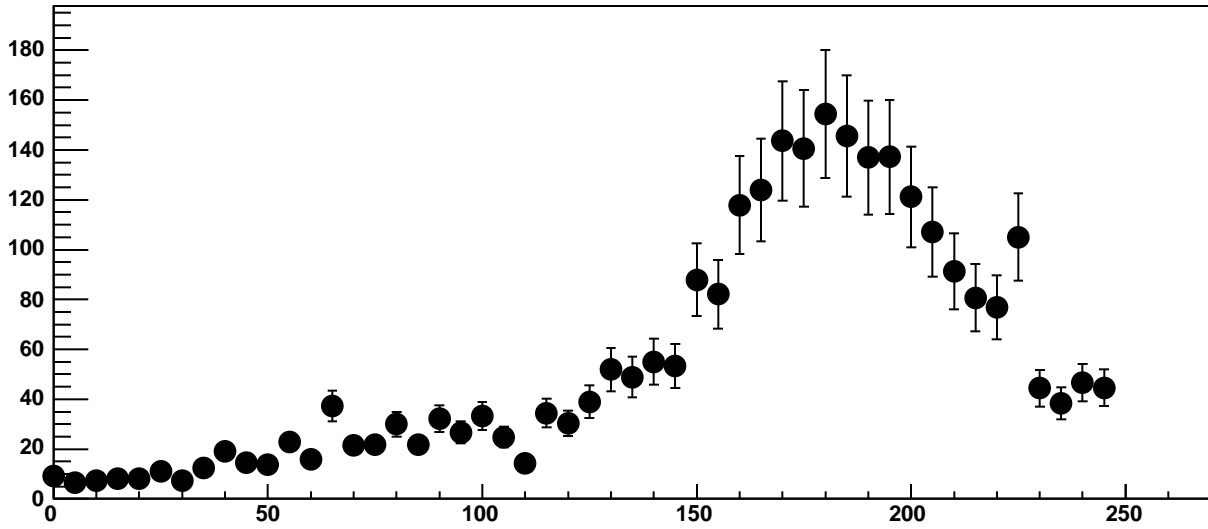


Chip 5, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

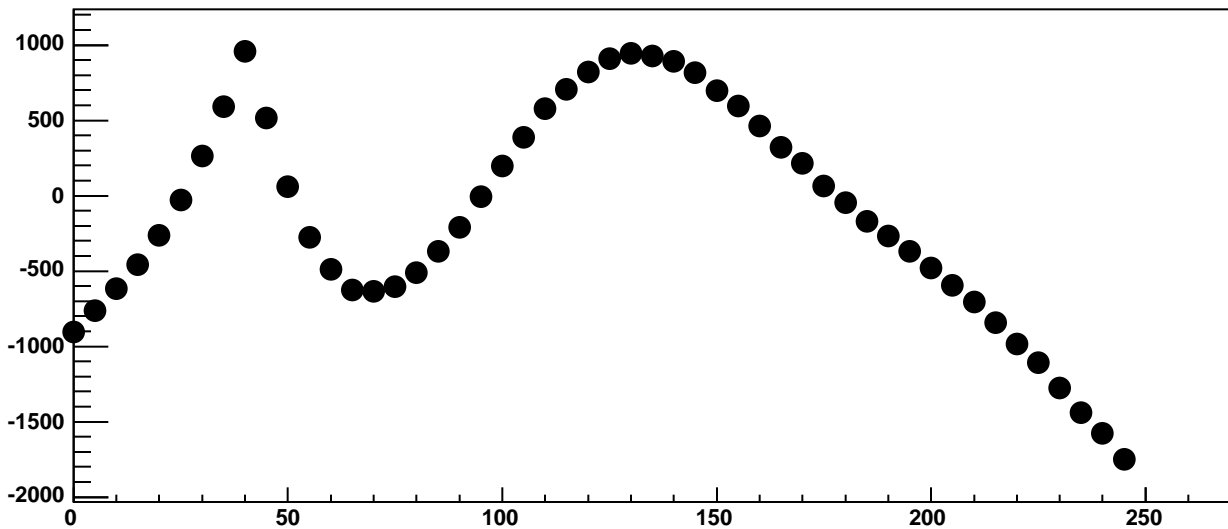


χ^2 / ndf	4.604e+05 / 41
p0	-440.2 ± 1.248
p1	40.74 ± 0.01576
p2	9489 ± 12.48
p3	44.31 ± 0.0675
p4	-25.96 ± 0.06577

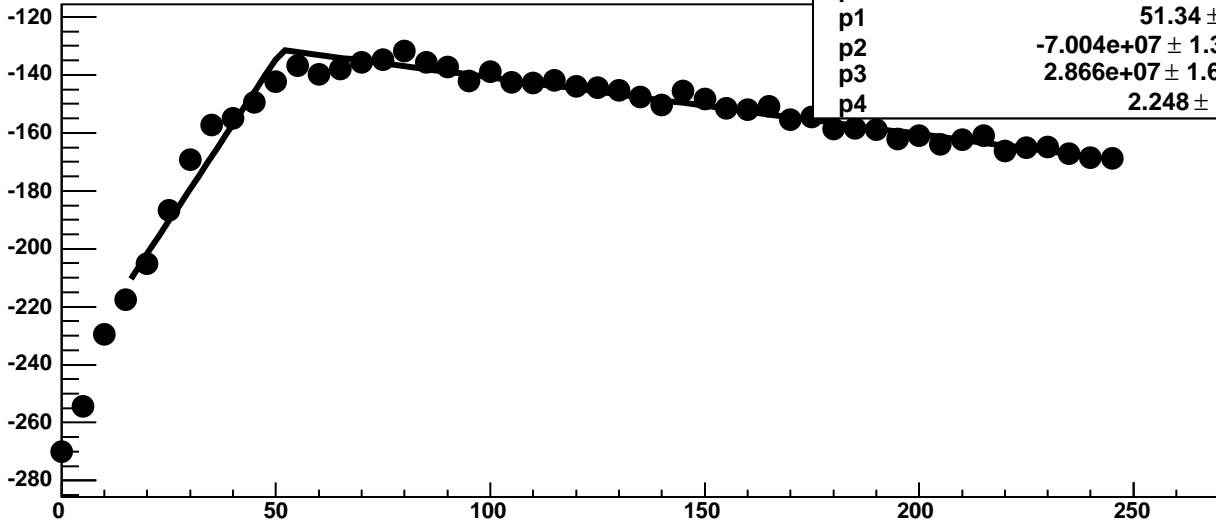
Chip 5, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



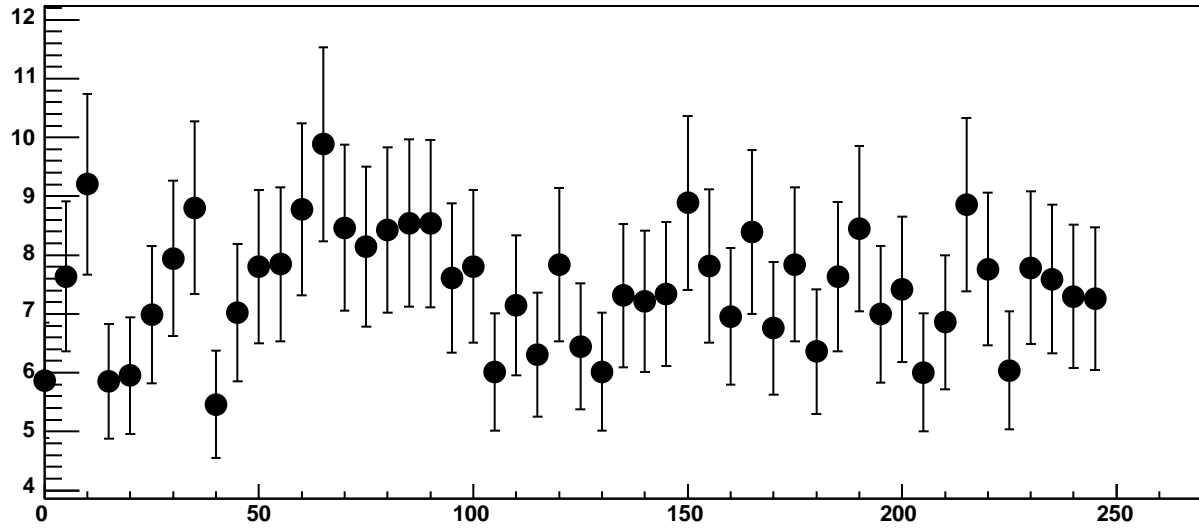
Chip 5, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold



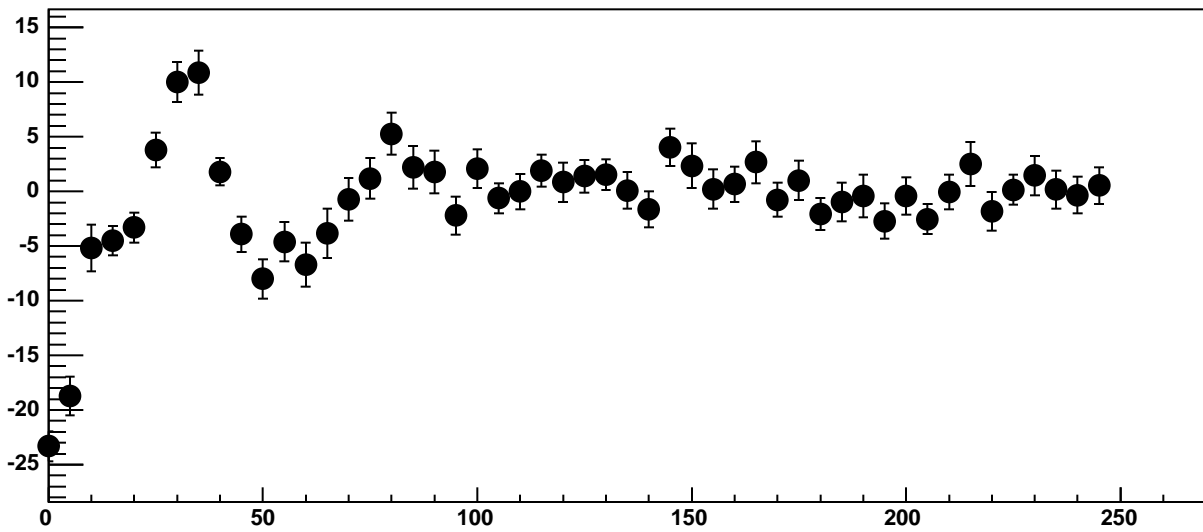
Chip 5, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold



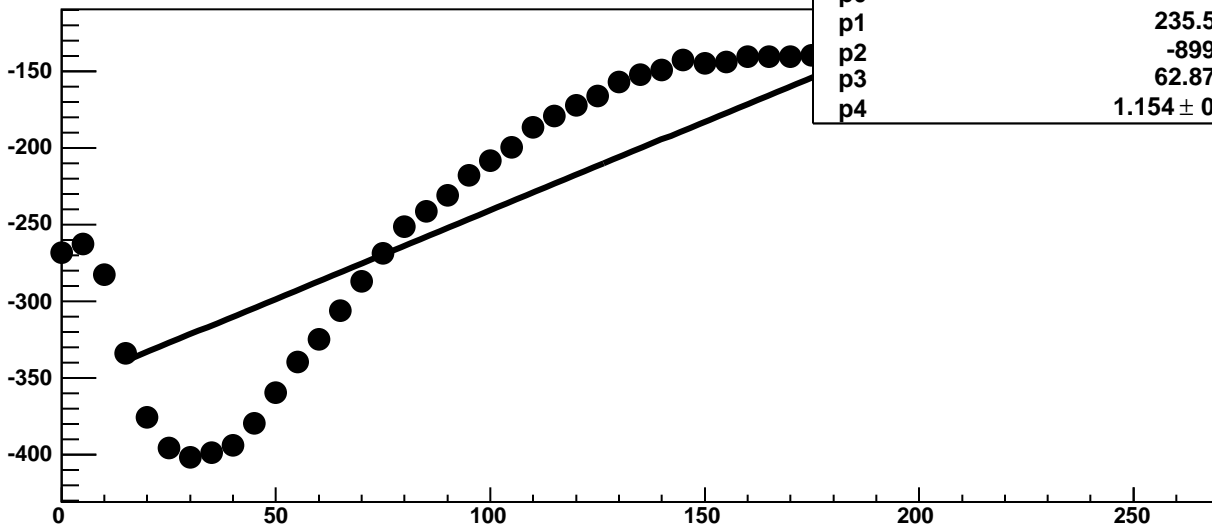
Chip 5, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold

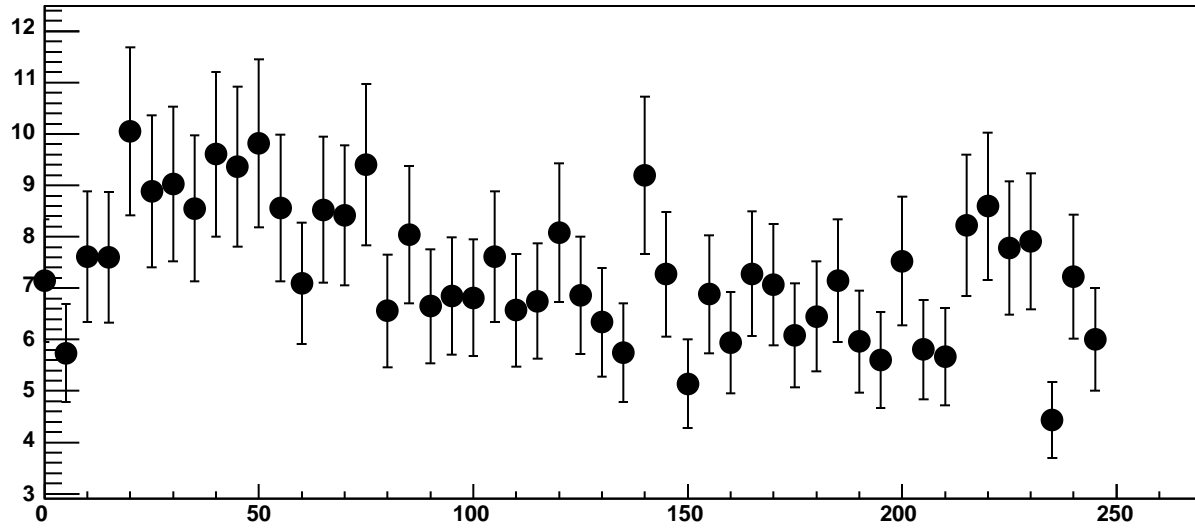


Chip 5, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

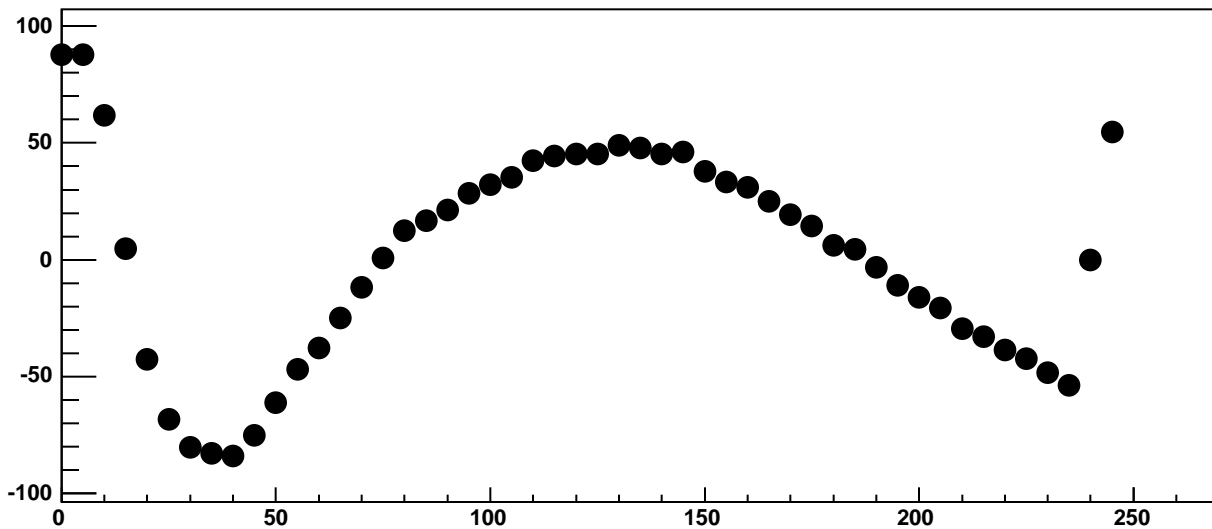


χ^2 / ndf	2.59e+04 / 41
p0	-84.22 ± 1.891
p1	235.5 ± 1.596
p2	-899 ± 294.5
p3	62.87 ± 17.37
p4	1.154 ± 0.003886

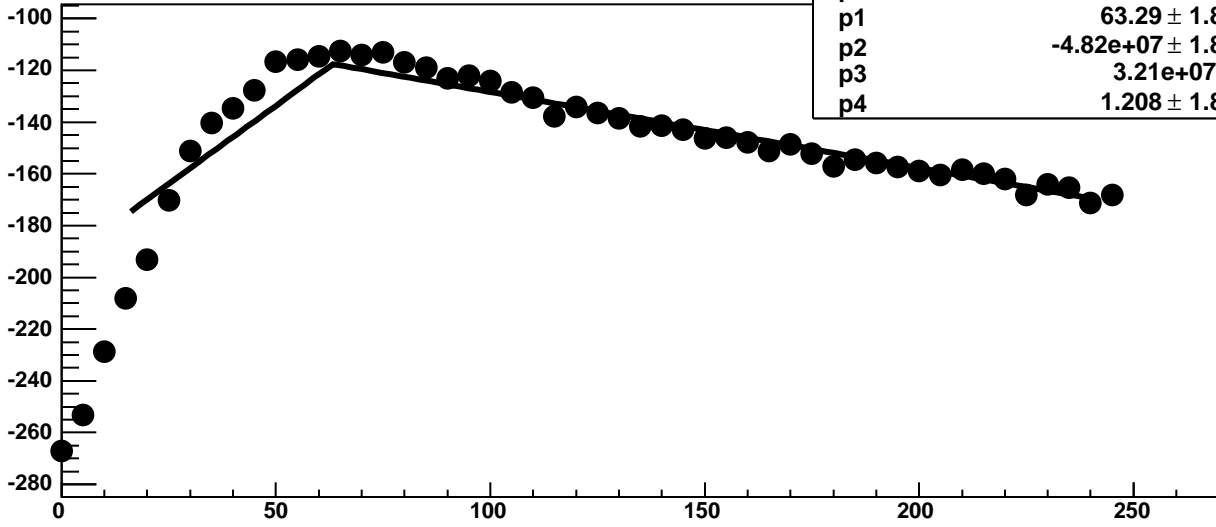
Chip 5, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



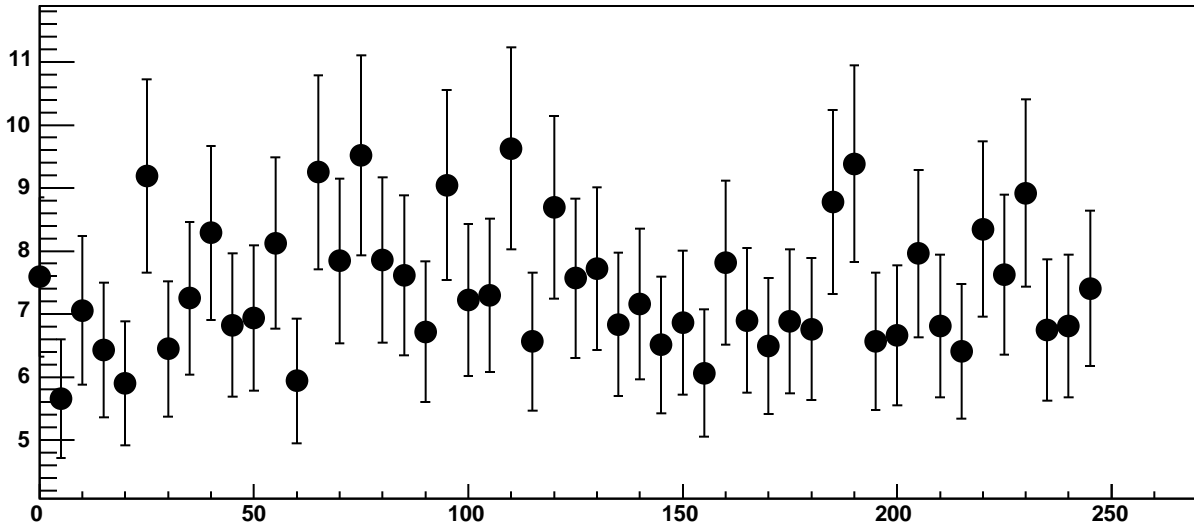
Chip 5, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold



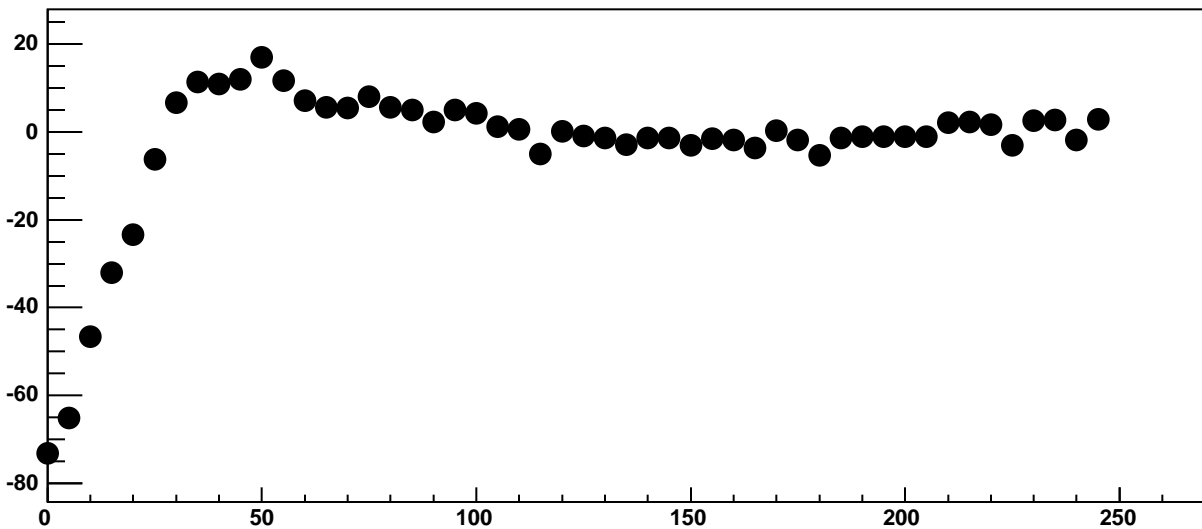
Chip 5, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold



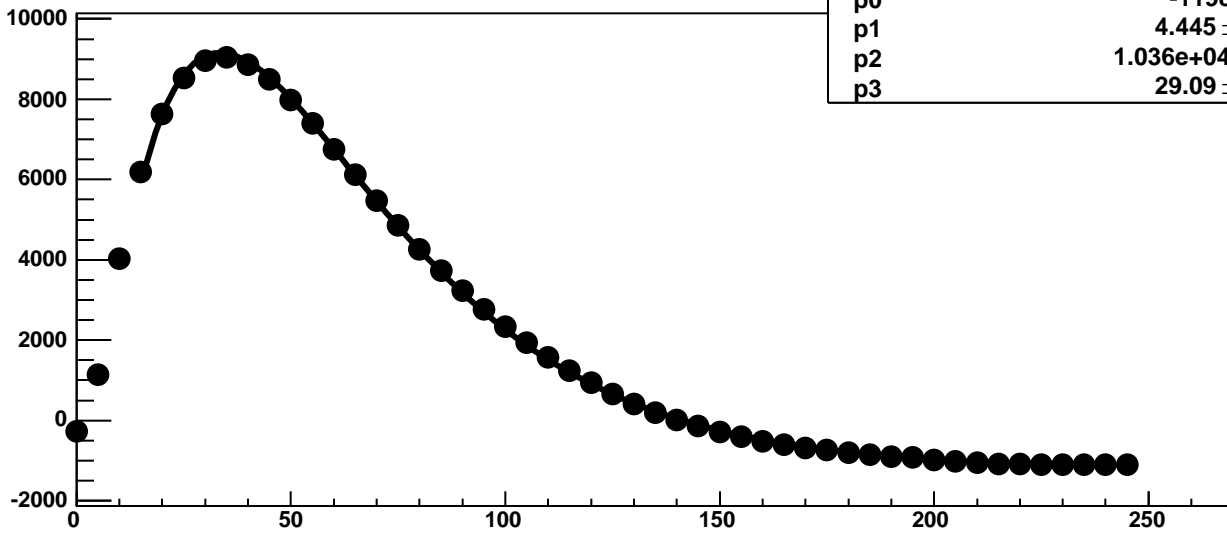
Chip 5, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

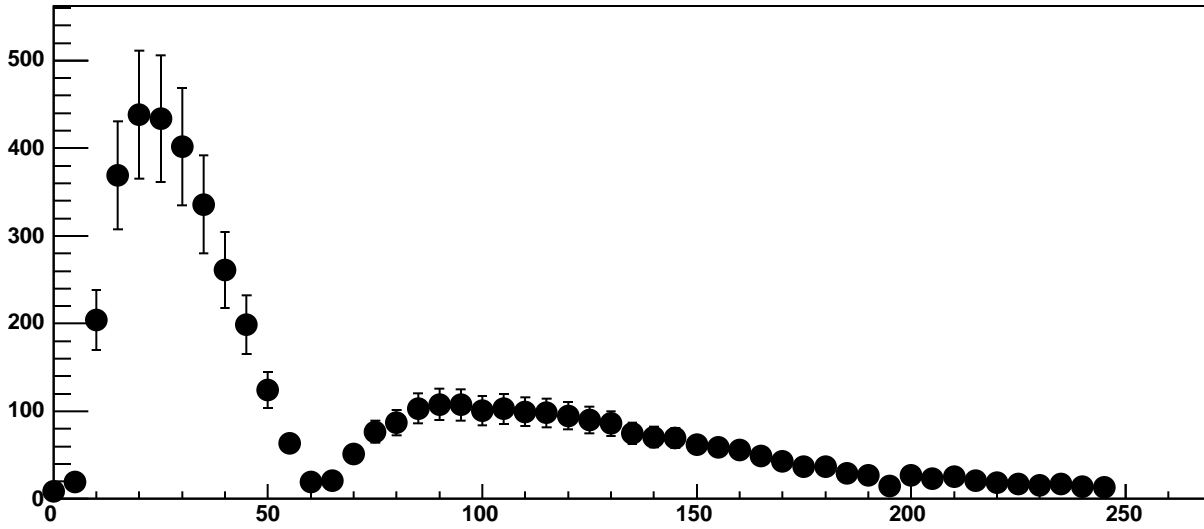


Chip 5, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold

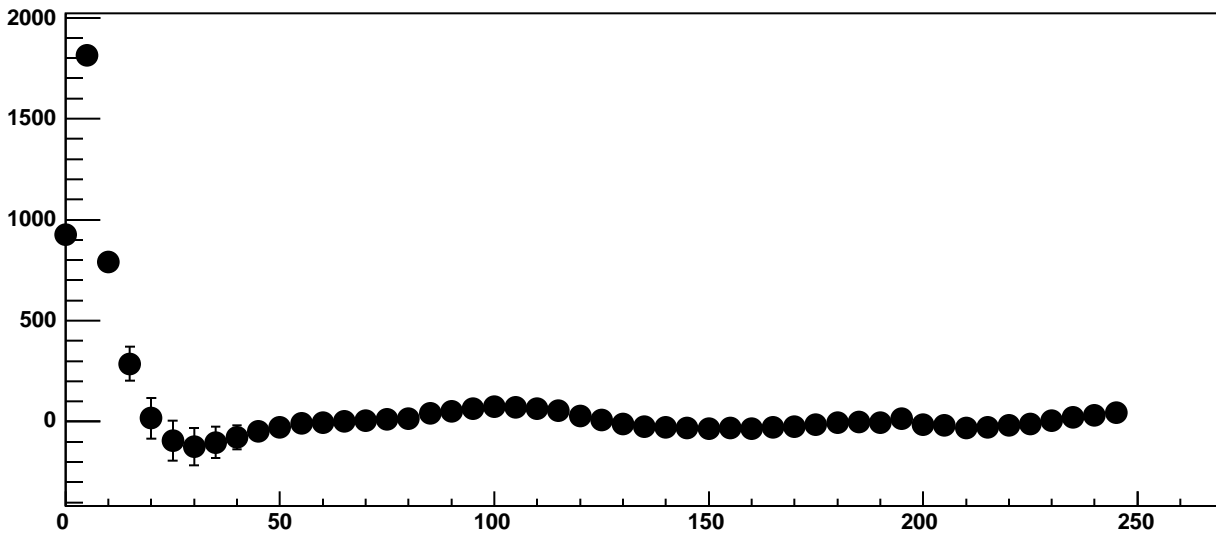


χ^2 / ndf	373.6 / 42
p0	-1198 ± 1.836
p1	4.445 ± 0.1356
p2	1.036e+04 ± 17.27
p3	29.09 ± 0.0466

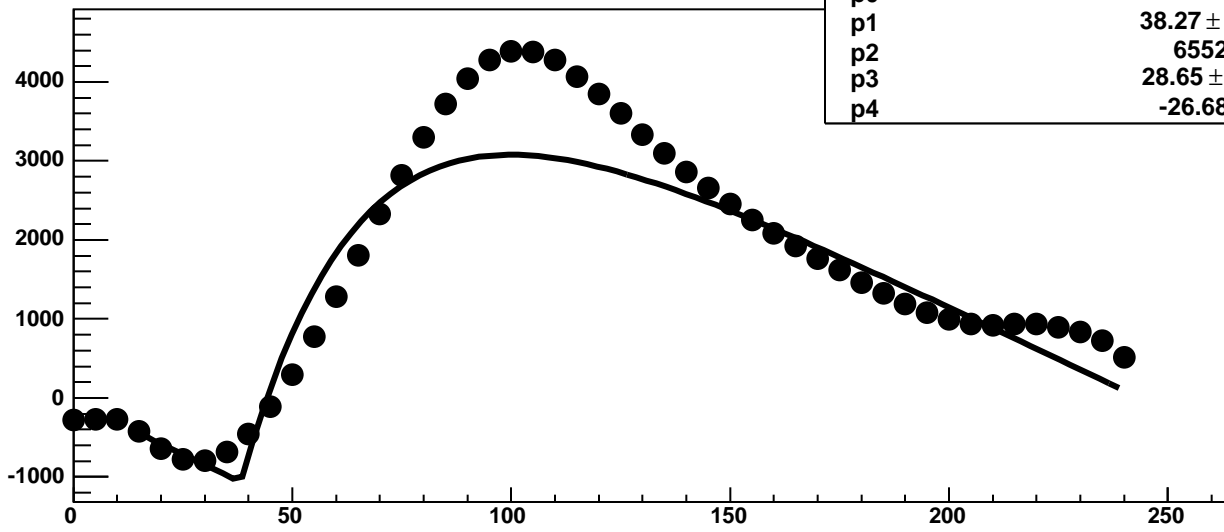
Chip 5, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold

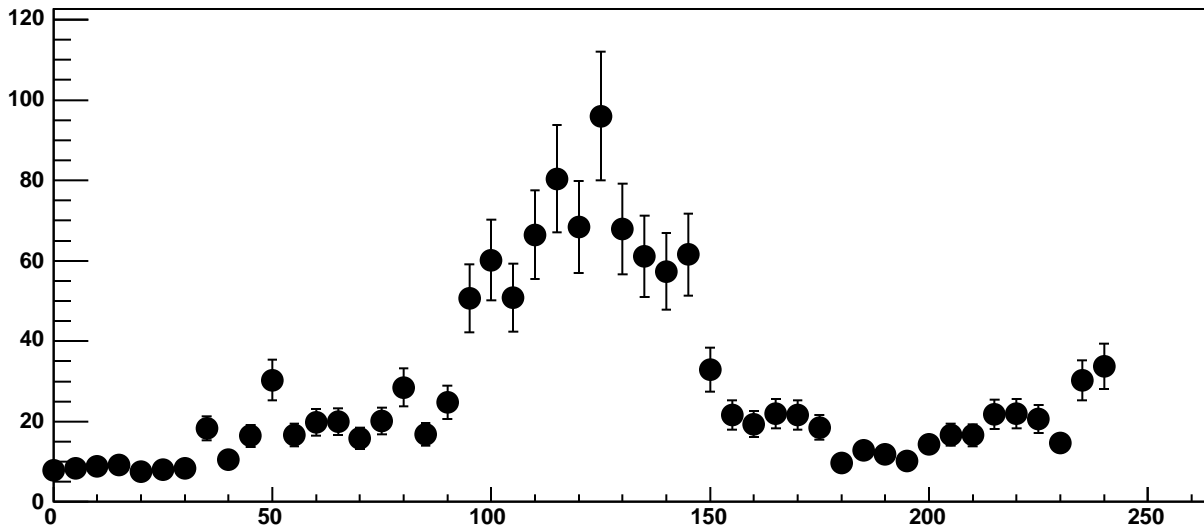


Chip 5, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

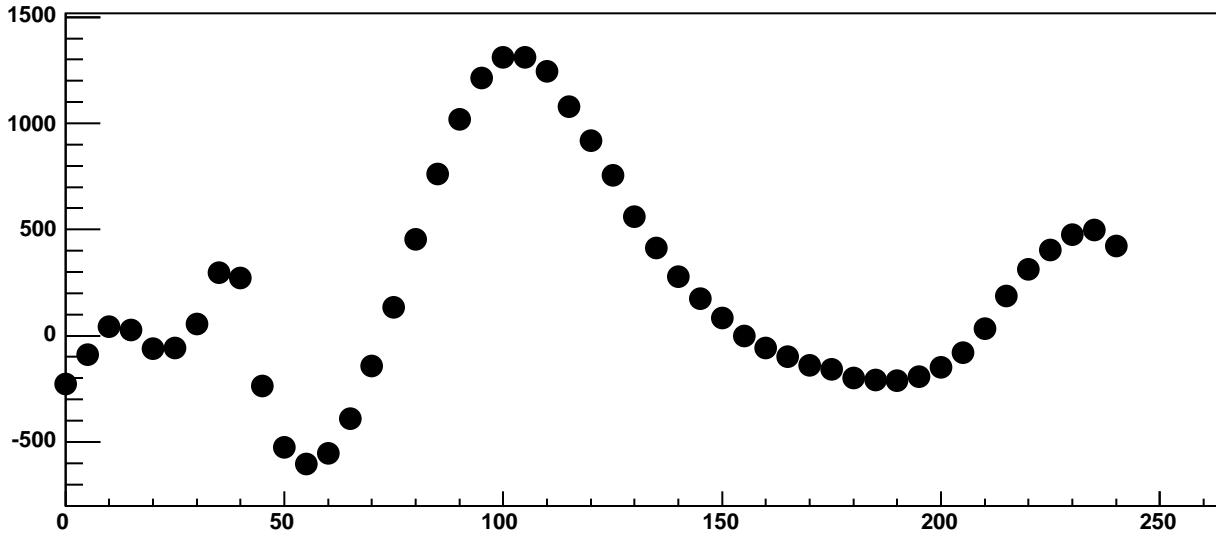


χ^2 / ndf	2.773e+05 / 41
p0	-1068 ± 0.9765
p1	38.27 ± 0.01185
p2	6552 ± 6.588
p3	28.65 ± 0.04141
p4	-26.68 ± 0.037

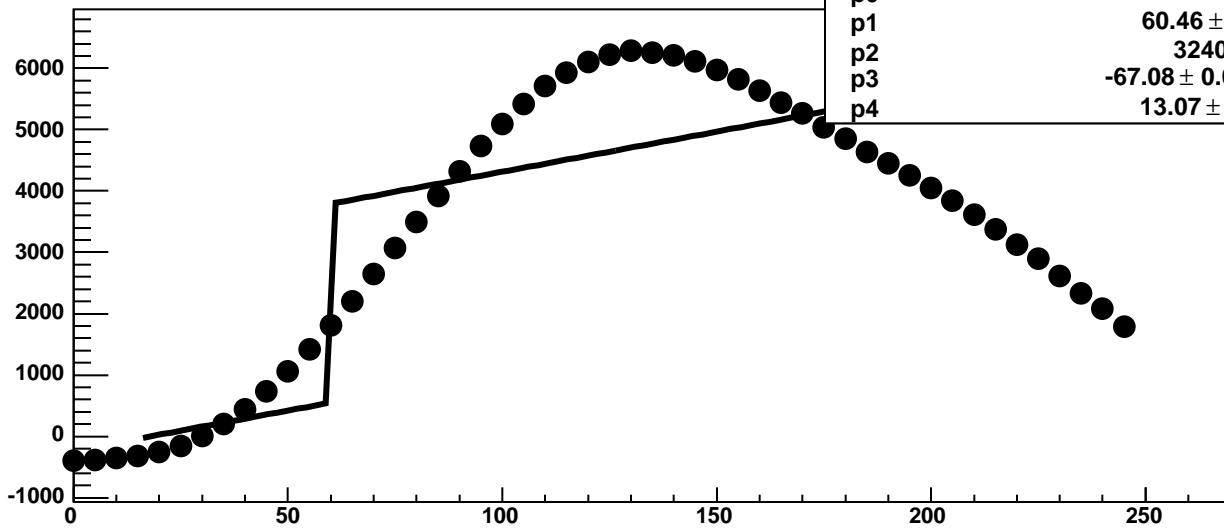
Chip 5, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

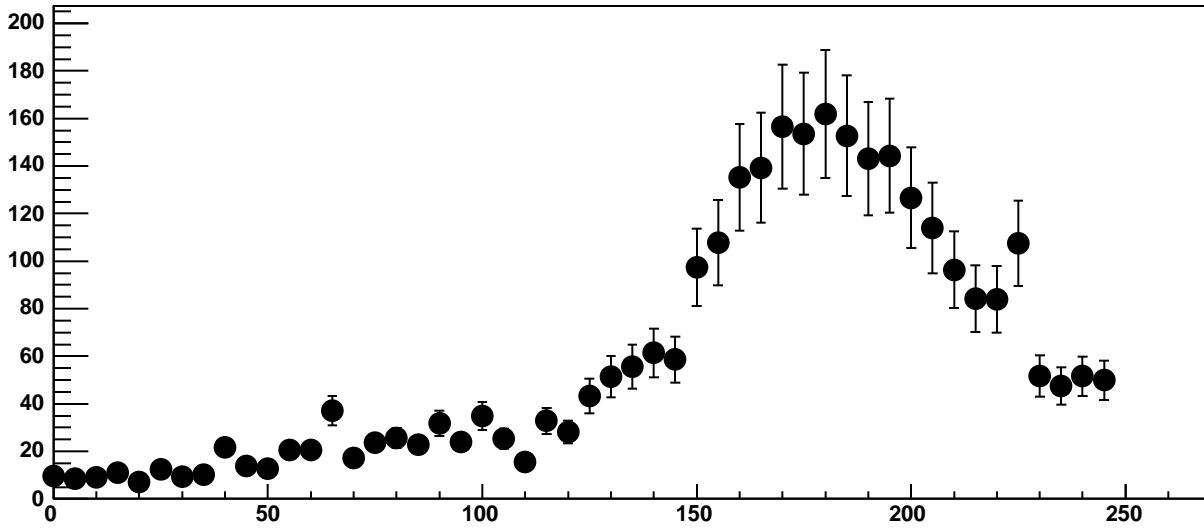


Chip 5, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

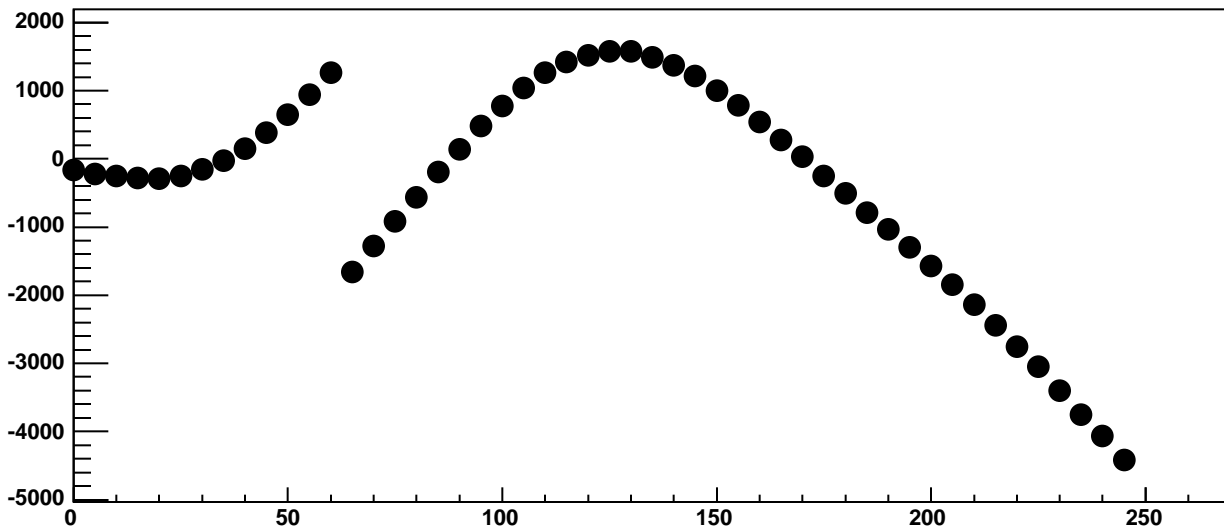


χ^2 / ndf	1.156e+06 / 41
p0	556.7 ± 0.7622
p1	60.46 ± 0.05821
p2	3240 ± 1.539
p3	-67.08 ± 0.0003545
p4	13.07 ± 0.01737

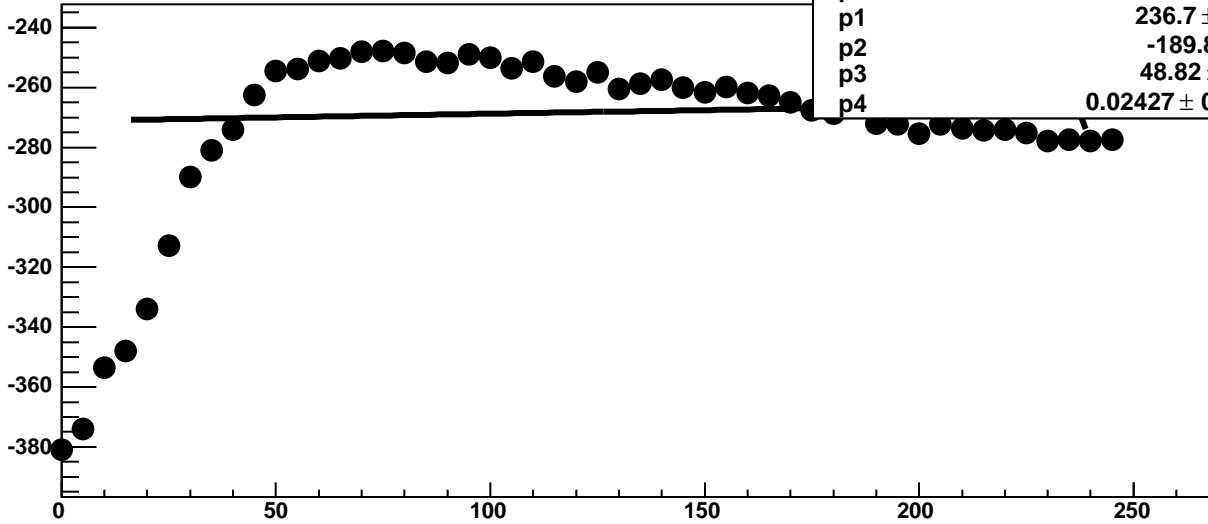
Chip 5, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



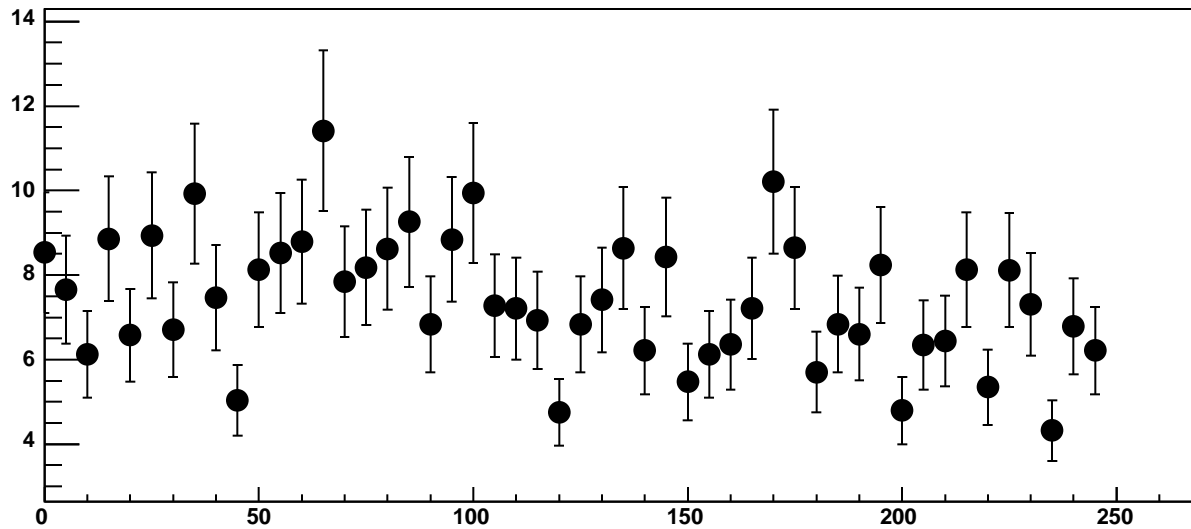
Chip 5, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold



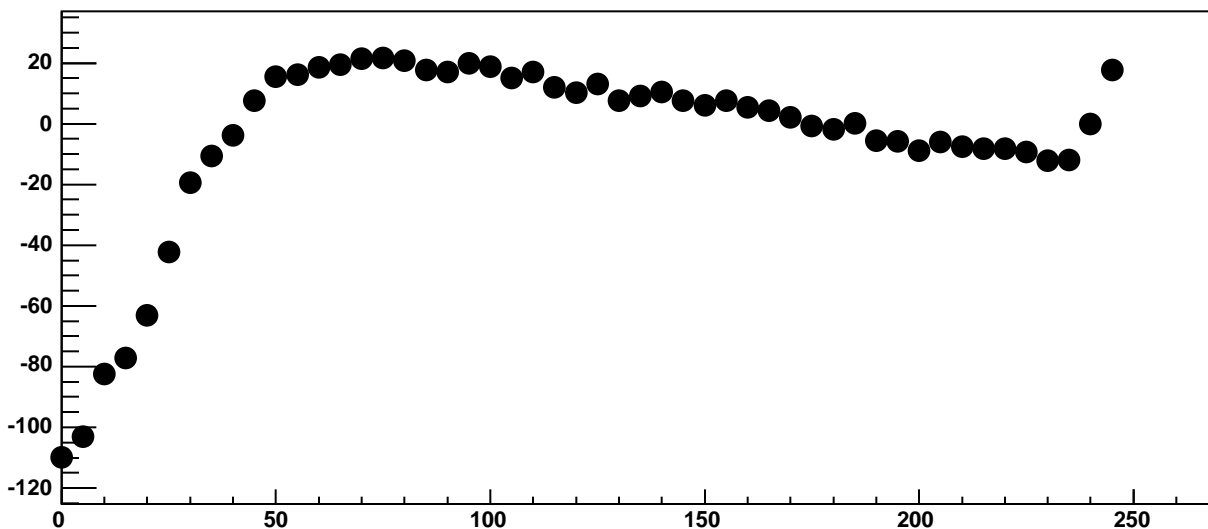
Chip 5, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold



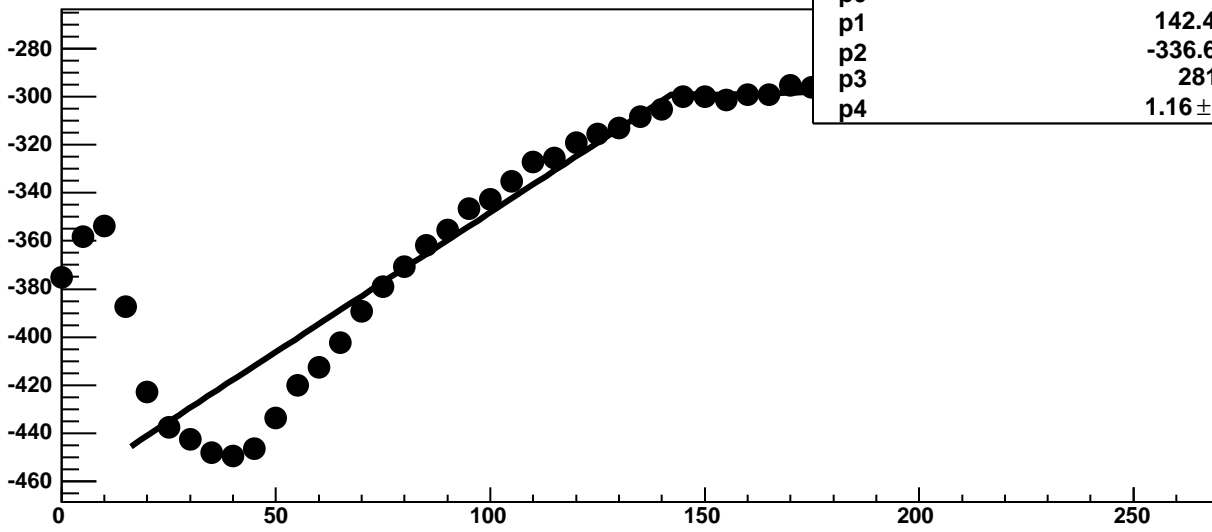
Chip 5, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

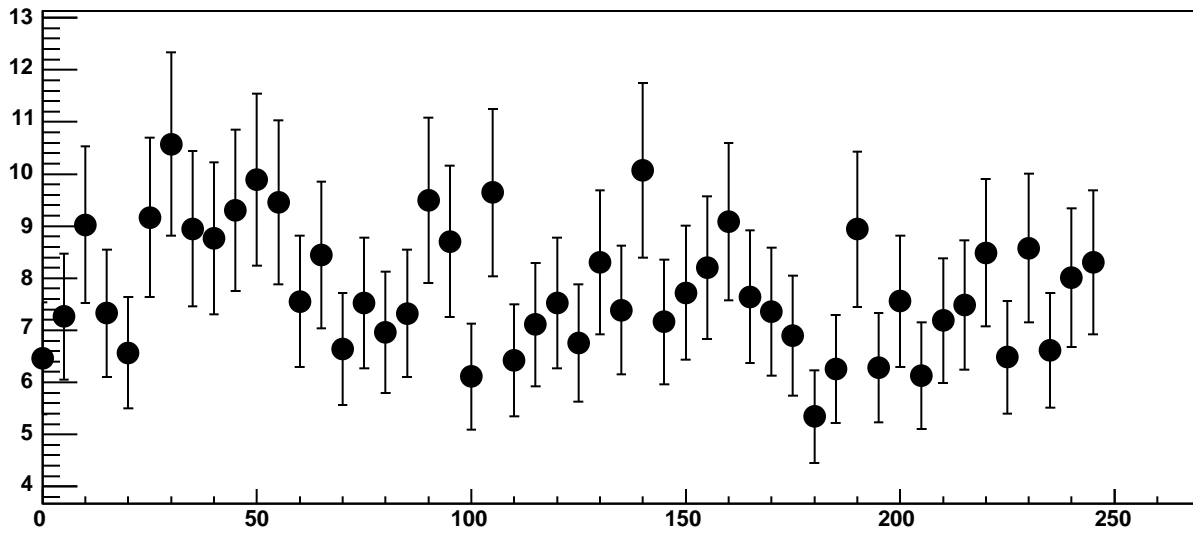


Chip 5, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold

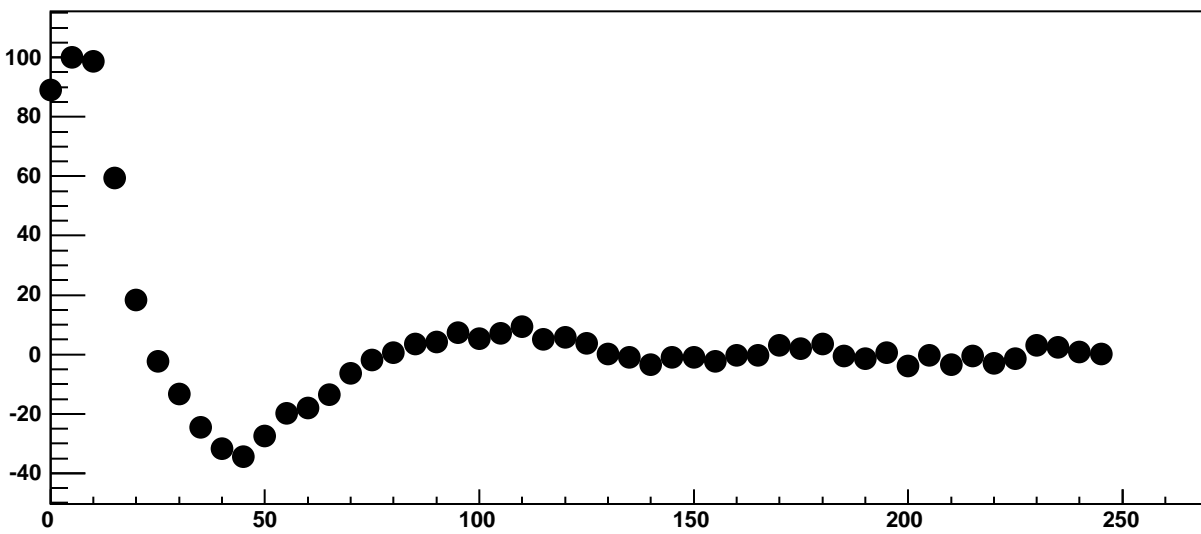


χ^2 / ndf	2639 / 41
p0	-298.9 ± 0.967
p1	142.4 ± 1.003
p2	-336.6 ± 49.97
p3	281 ± 49.47
p4	1.16 ± 0.00951

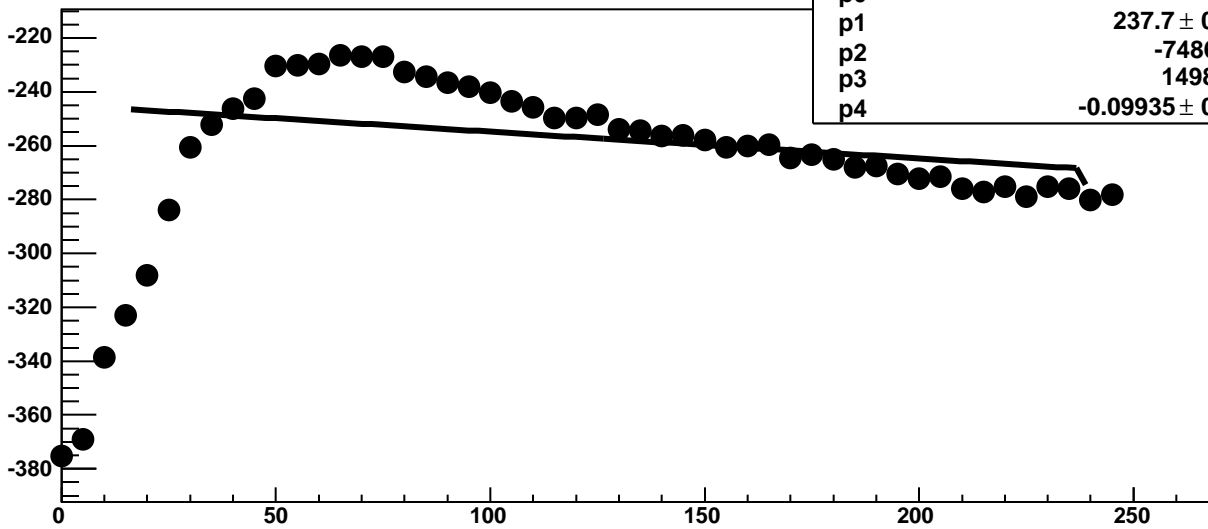
Chip 5, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold

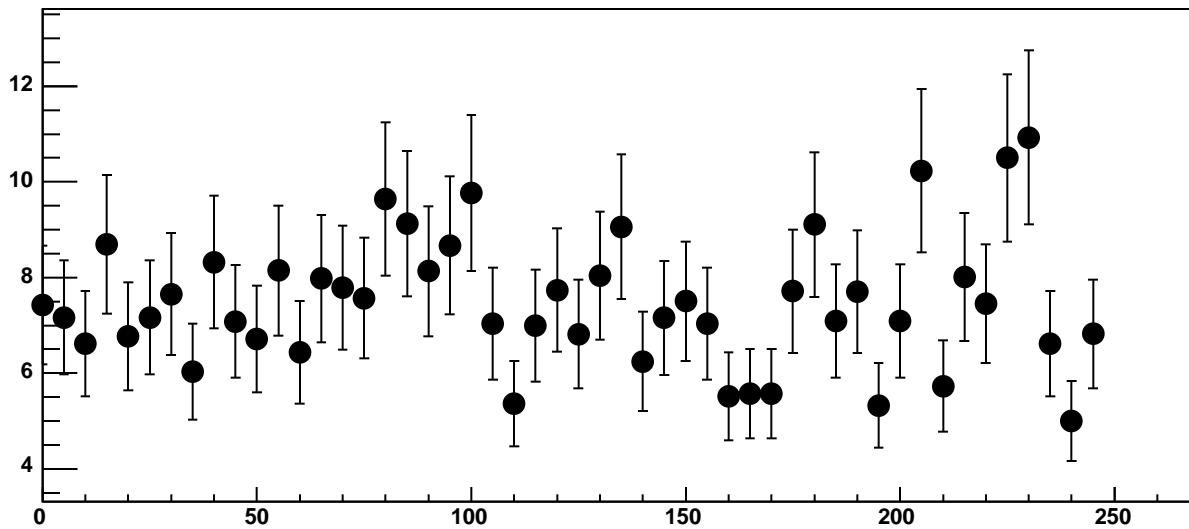


Chip 5, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

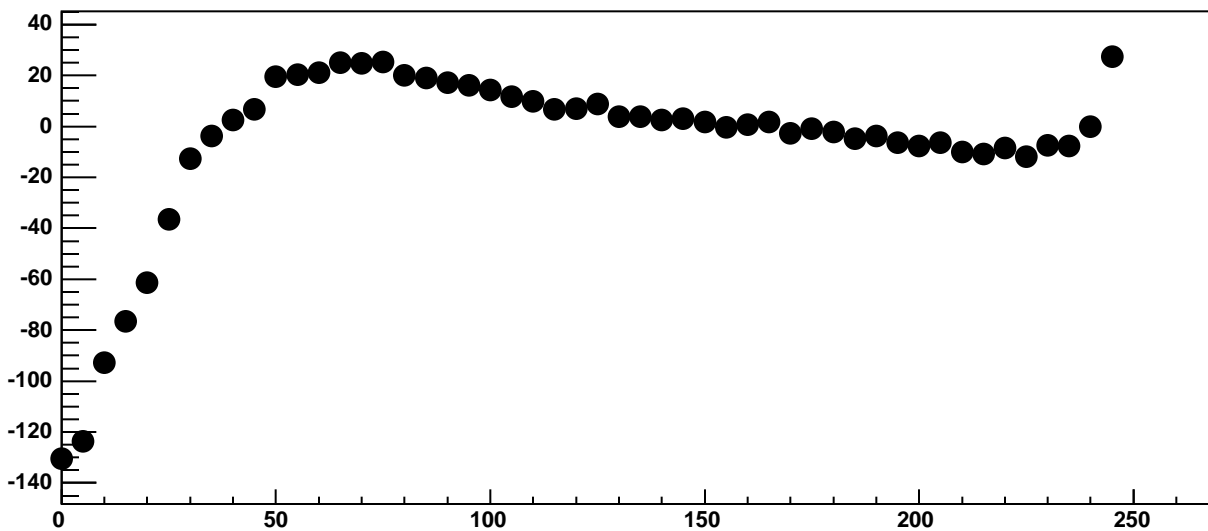


χ^2 / ndf	5494 / 41
p0	-268.5 ± 0.4969
p1	237.7 ± 0.004233
p2	-7486 ± 811.2
p3	1498 ± 2.733
p4	-0.09935 ± 0.003887

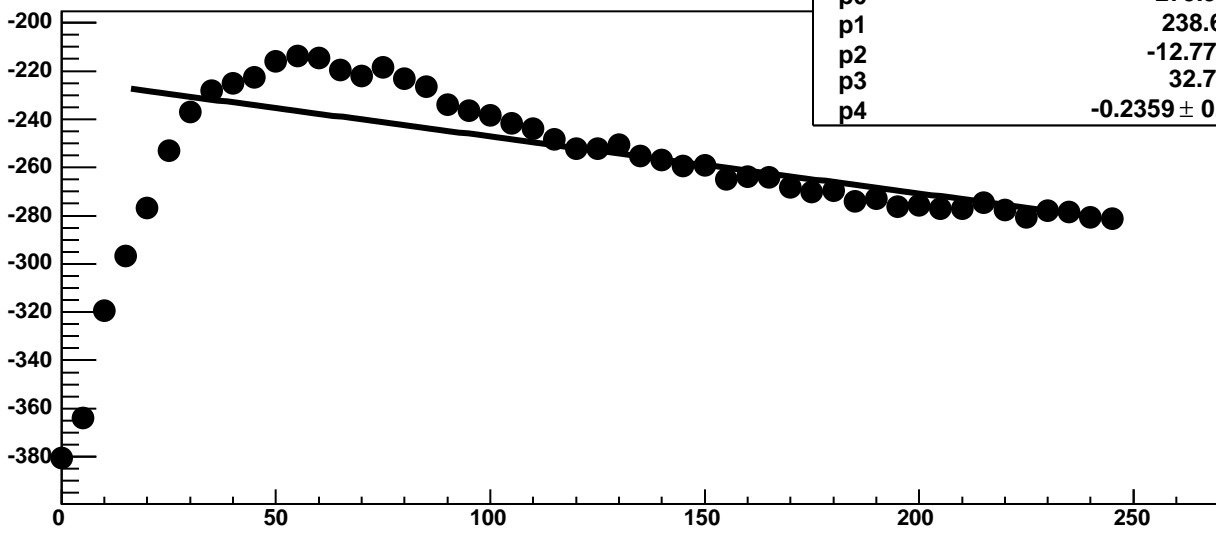
Chip 5, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

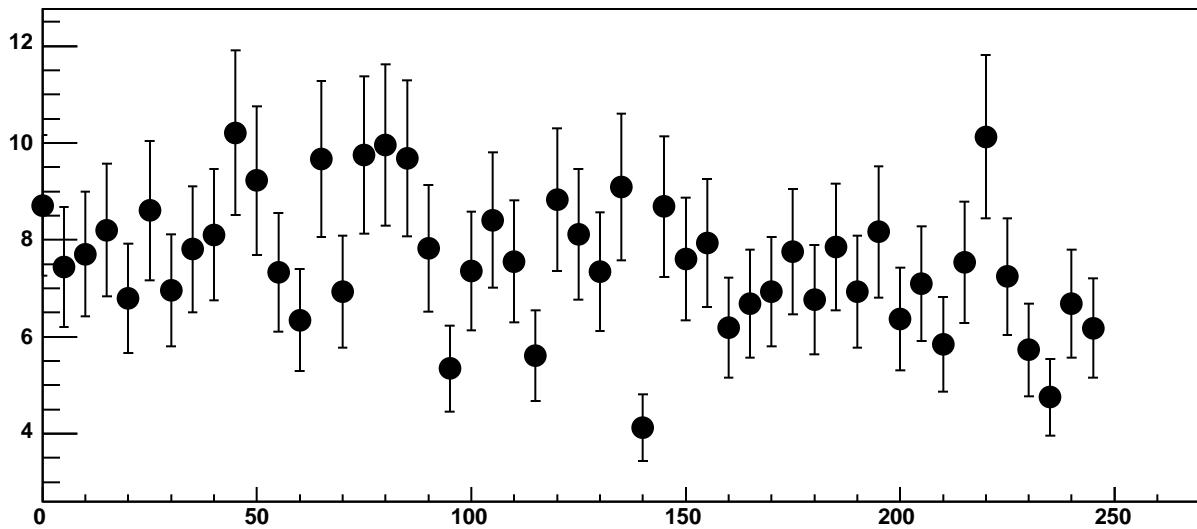


Chip 5, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

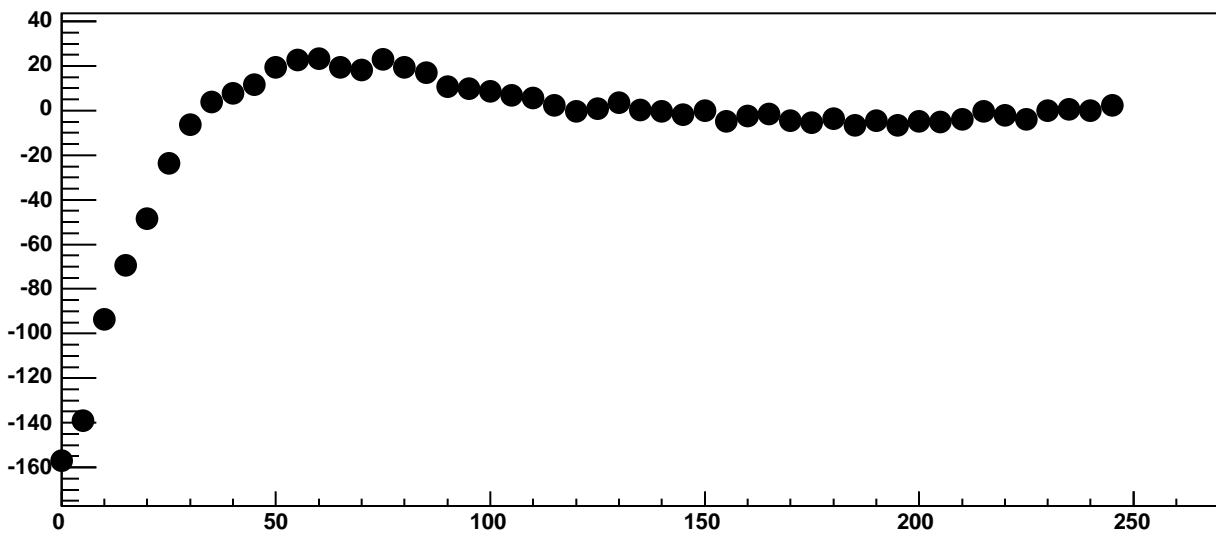


χ^2 / ndf	3765 / 41
p0	-279.9 ± 2.387
p1	238.6 ± 9.83
p2	-12.77 ± 79.33
p3	32.7 ± 165.8
p4	-0.2359 ± 0.003767

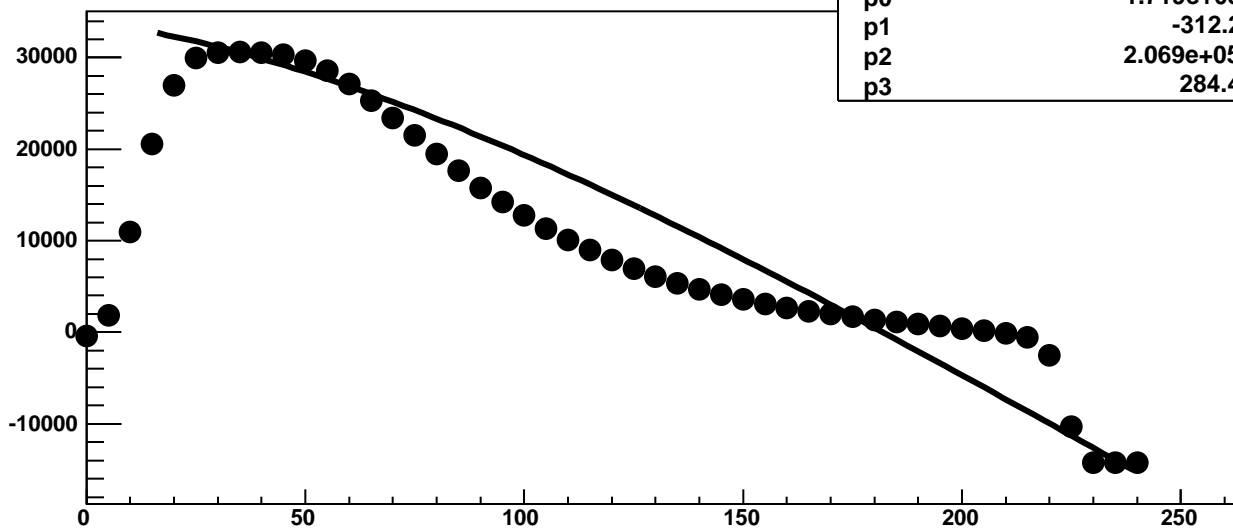
Chip 5, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



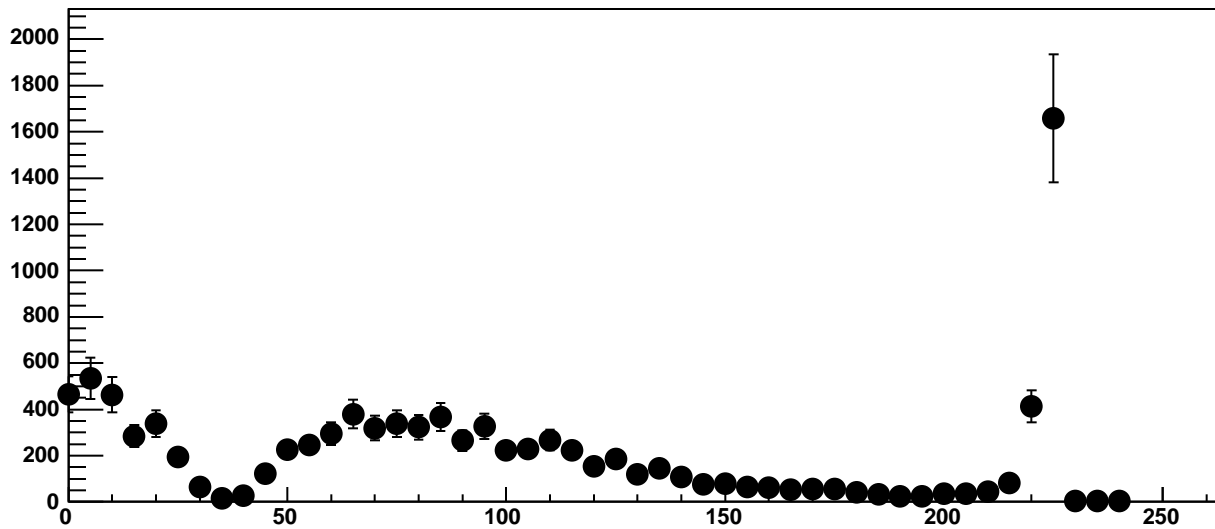
Chip 5, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold



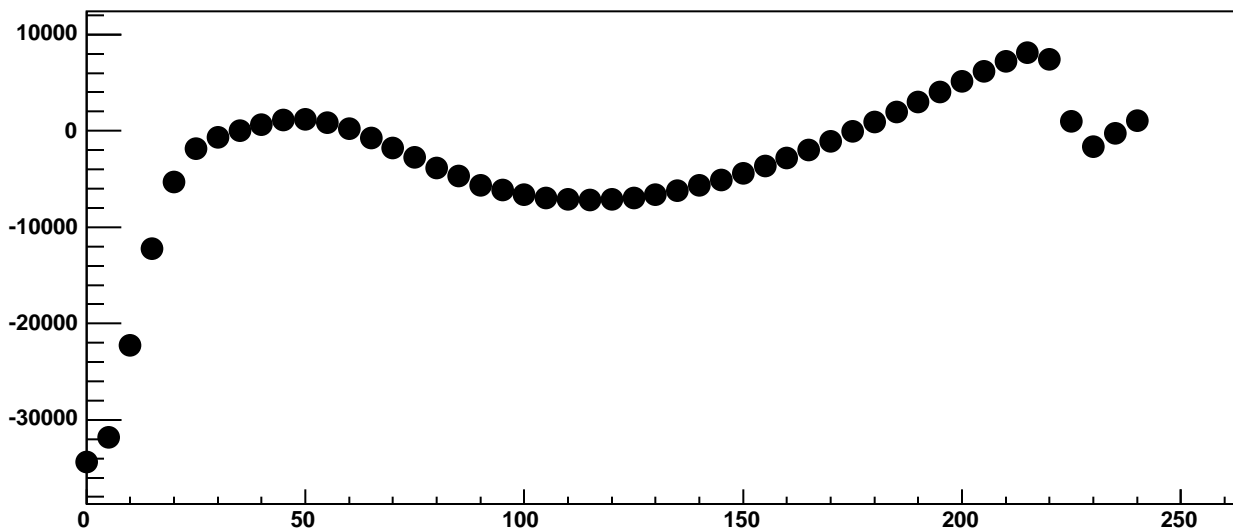
Chip 5, Channel 15, Enable 5!, DAC=4000, ADC Mean vs Hold



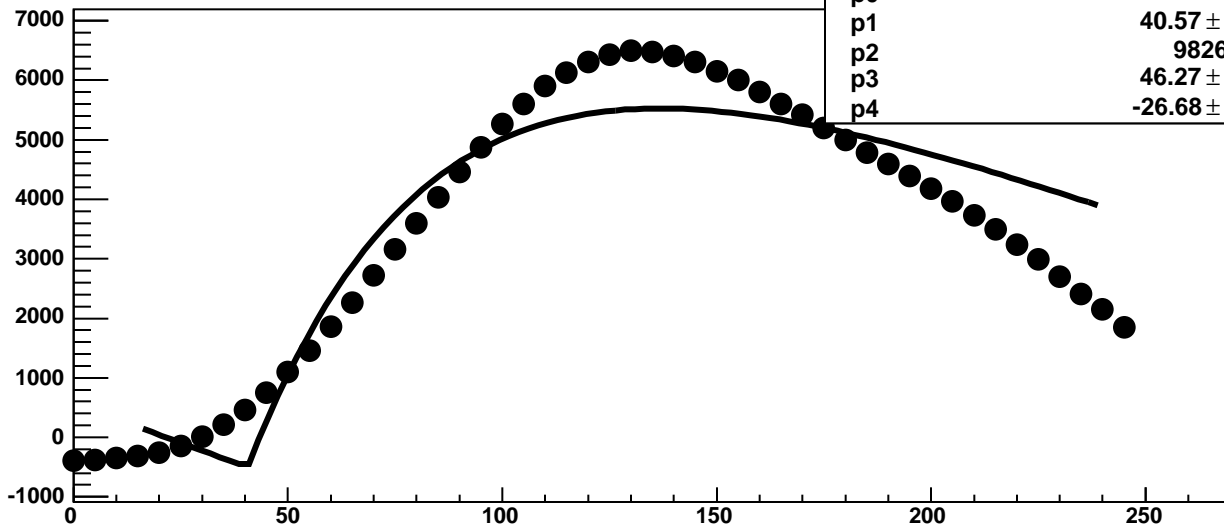
Chip 5, Channel 15, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 15, Enable 5!, DAC=4000, ADC Residuals vs Hold

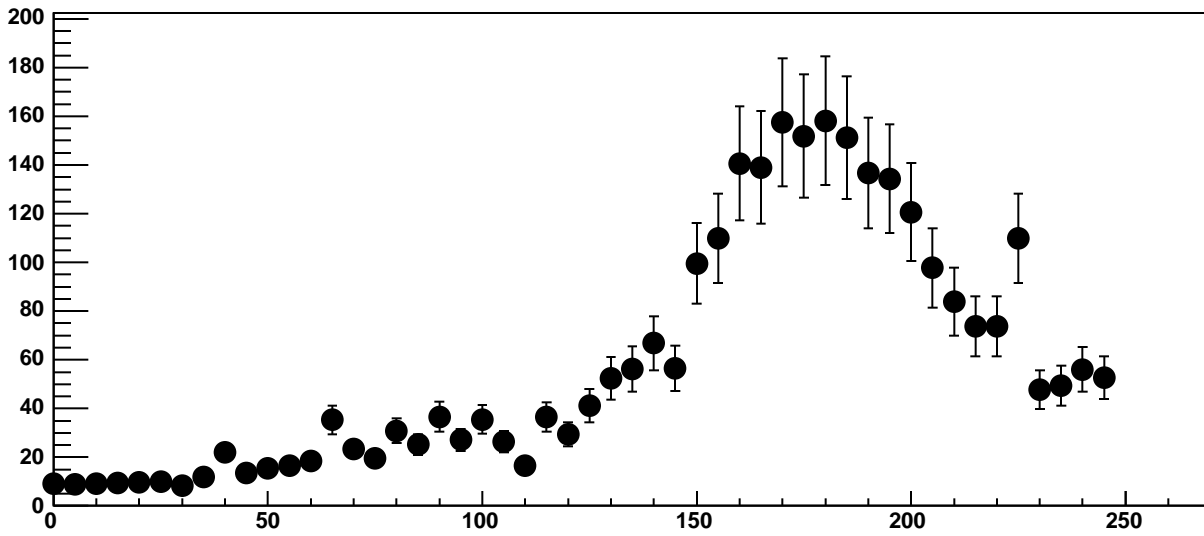


Chip 5, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

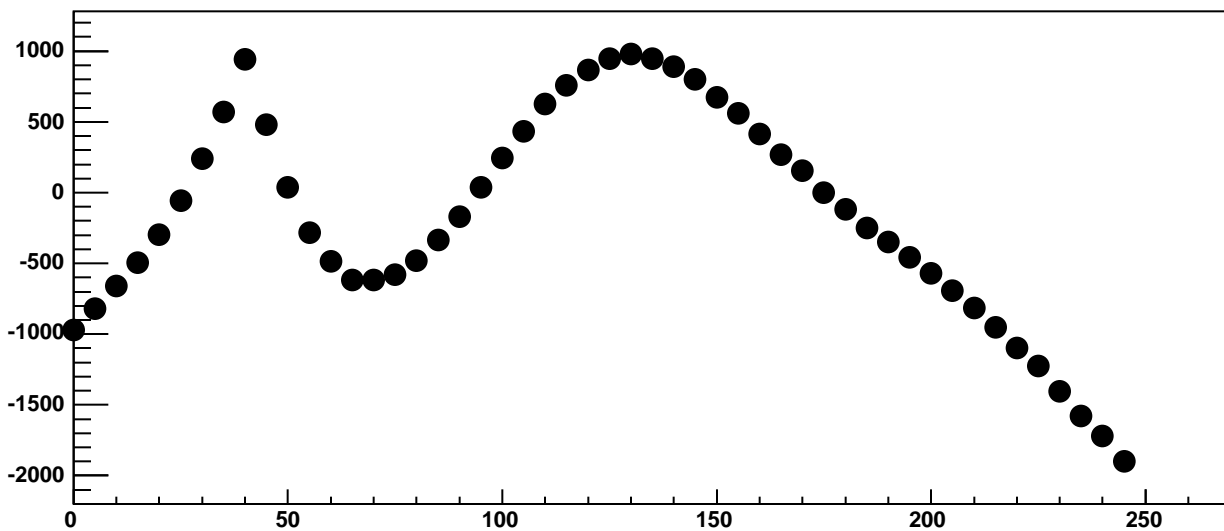


χ^2 / ndf	4.095e+05 / 41
p0	-504.2 ± 1.327
p1	40.57 ± 0.01498
p2	9826 ± 12.97
p3	46.27 ± 0.06946
p4	-26.68 ± 0.06829

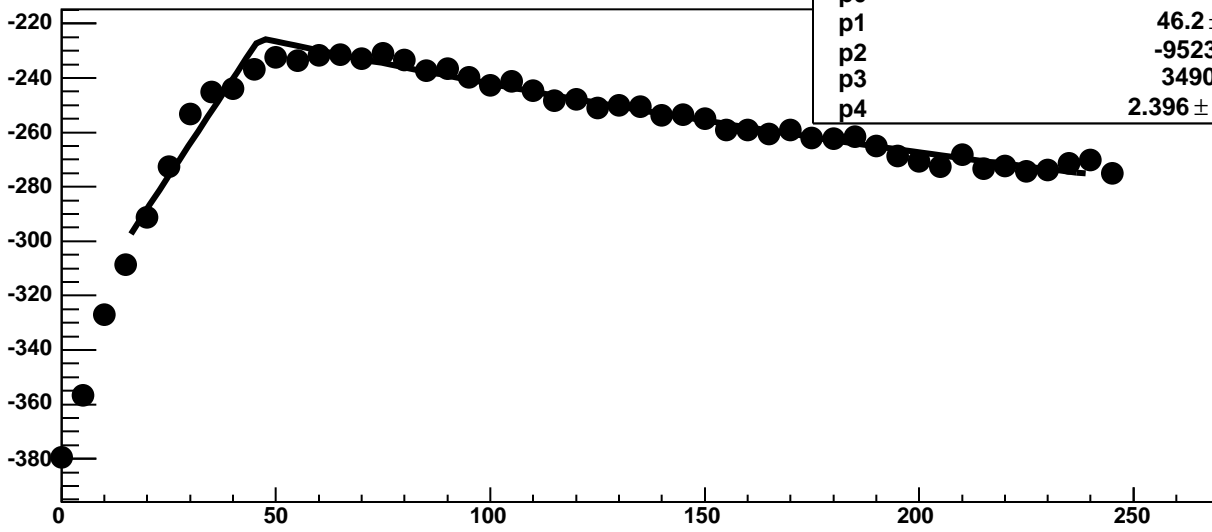
Chip 5, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

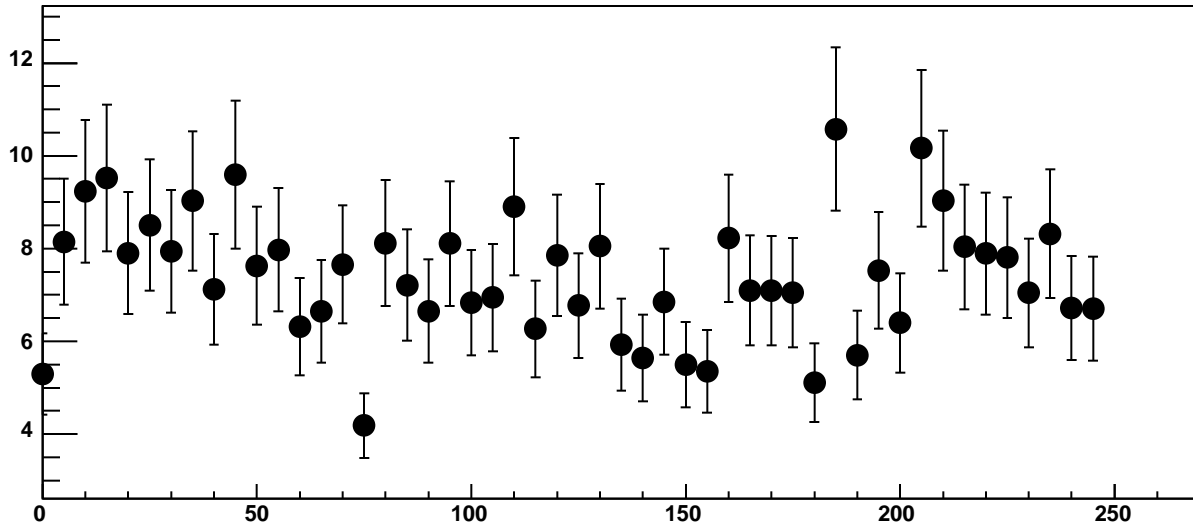


Chip 5, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold

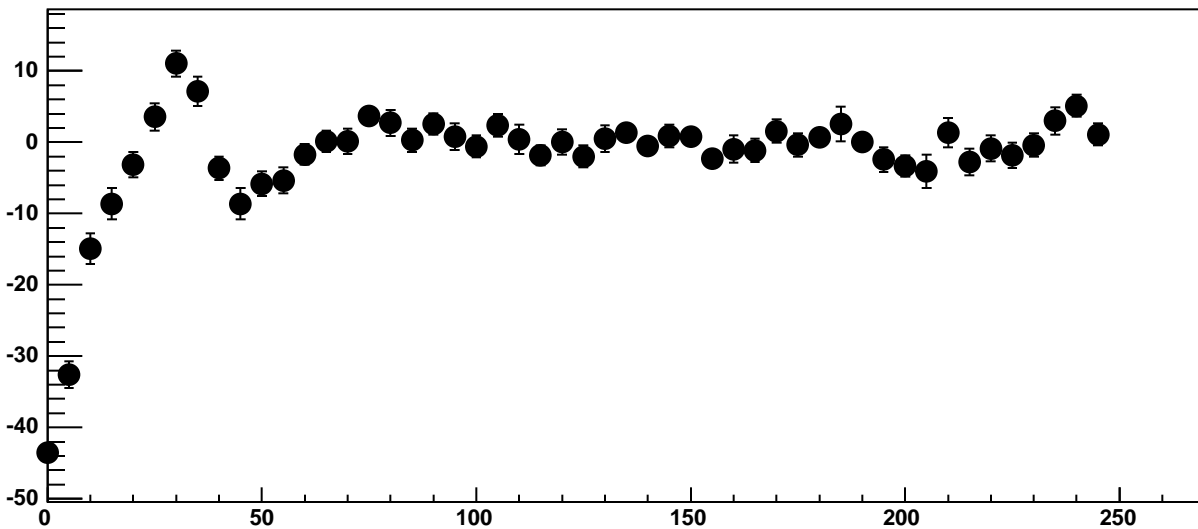


χ^2 / ndf	173.7 / 41
p0	-225.3 ± 0.7004
p1	46.2 ± 0.5843
p2	-9523 ± 2252
p3	3490 ± 831.3
p4	2.396 ± 0.07538

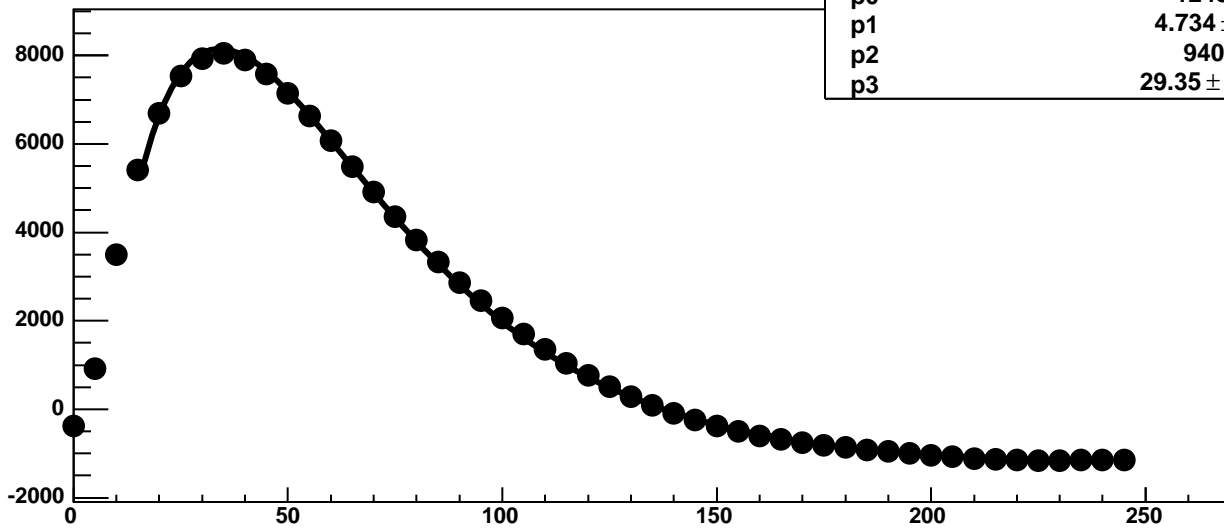
Chip 5, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



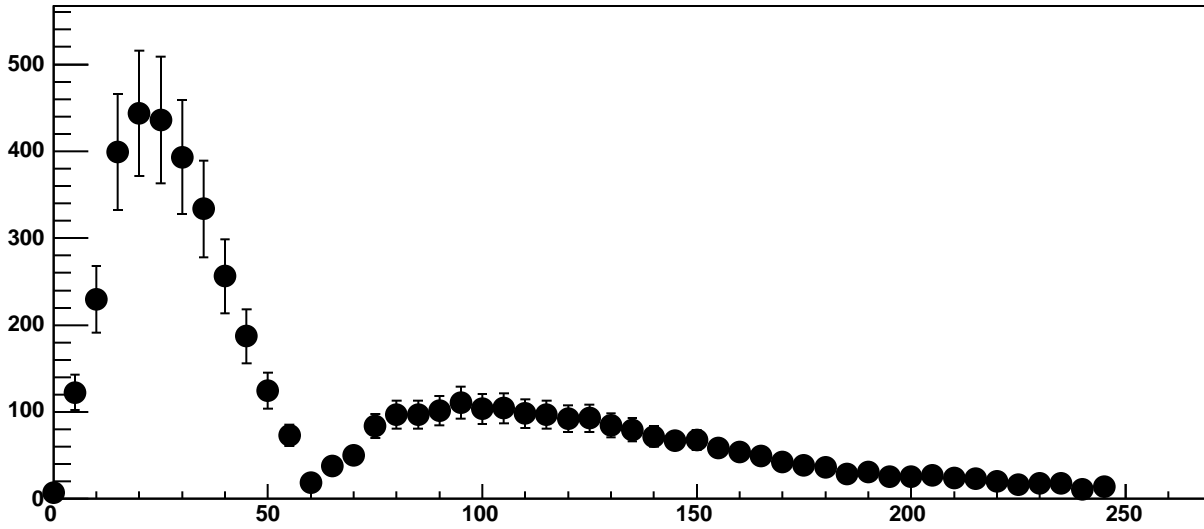
Chip 5, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold



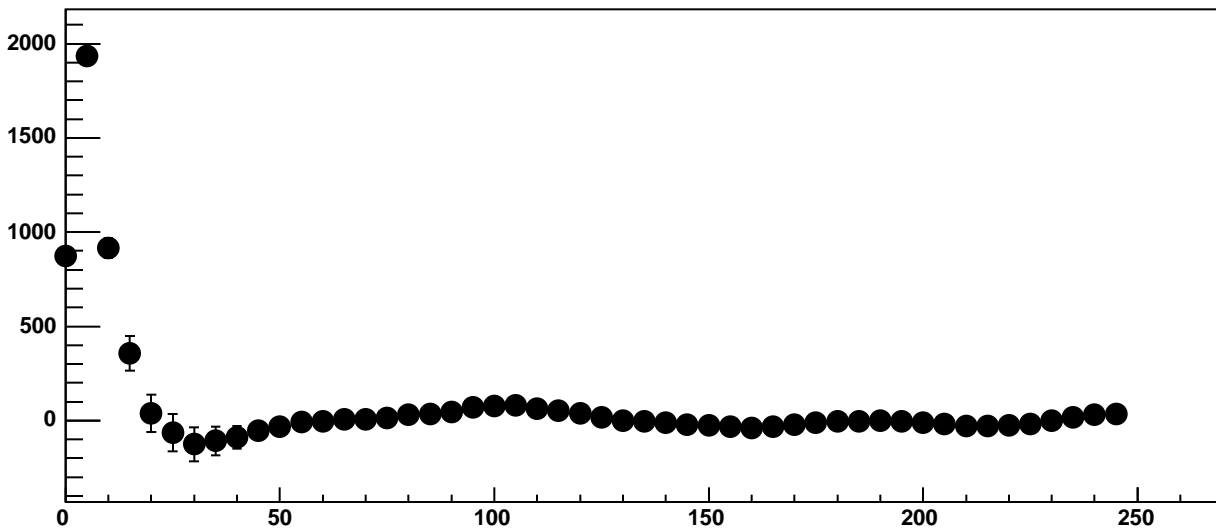
Chip 5, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold



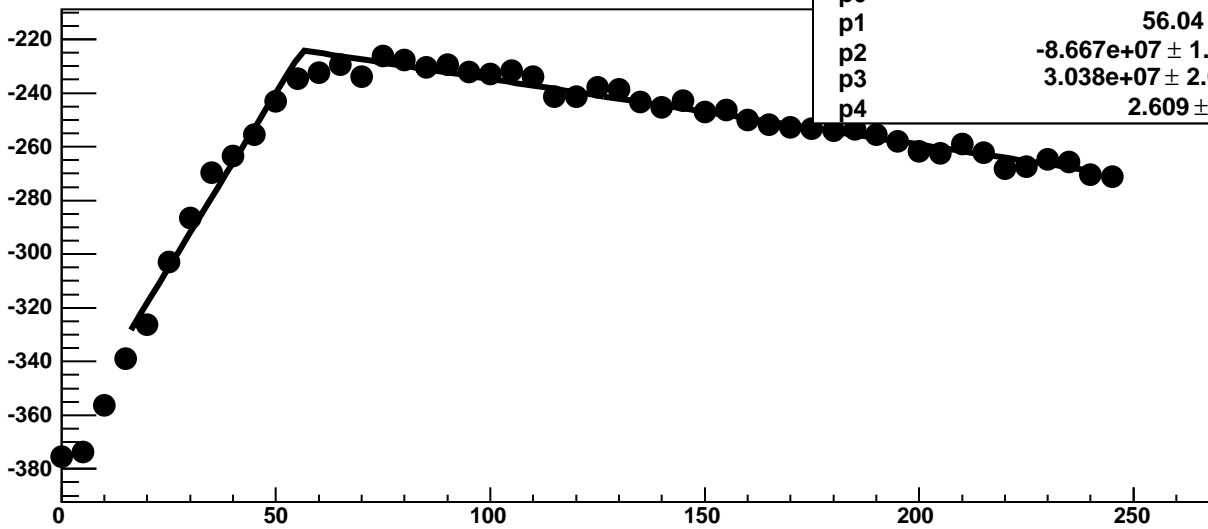
Chip 5, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold

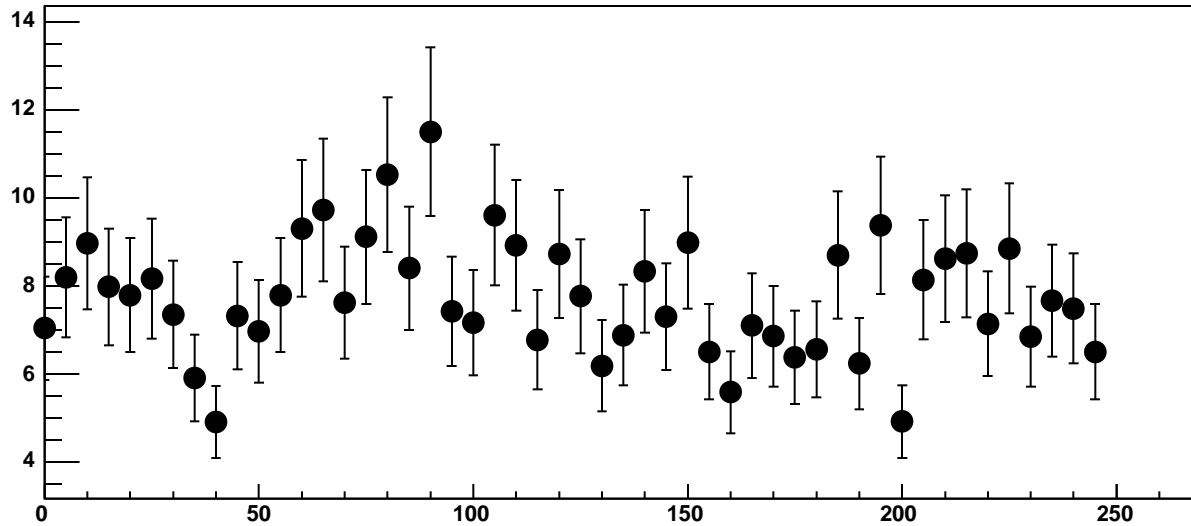


Chip 5, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

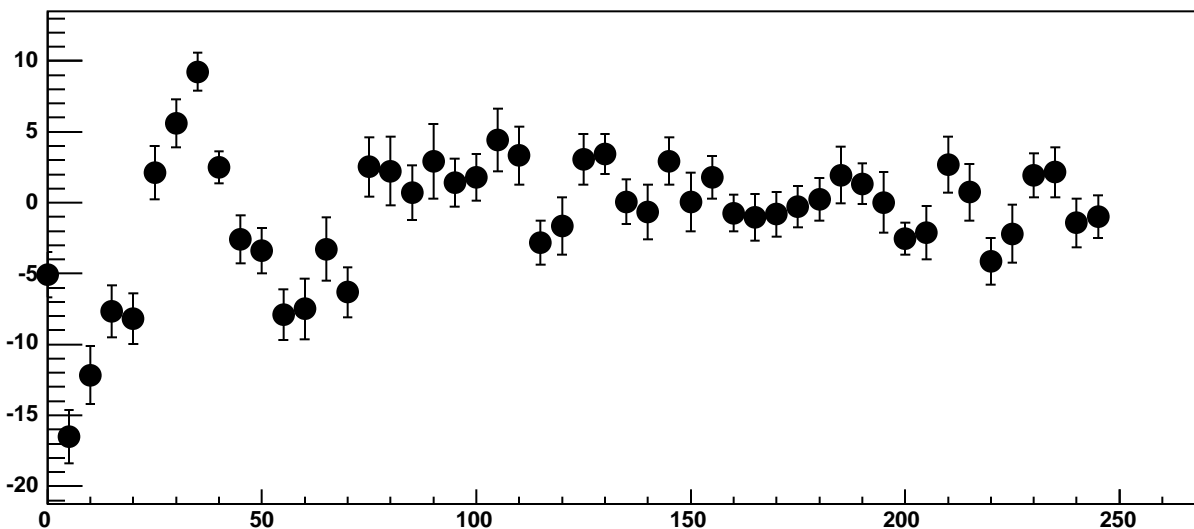


χ^2 / ndf	208 / 41
p0	-224.1 ± 0.5913
p1	56.04 ± 0.4227
p2	-8.667e+07 ± 1.485e+06
p3	3.038e+07 ± 2.015e+05
p4	2.609 ± 0.04492

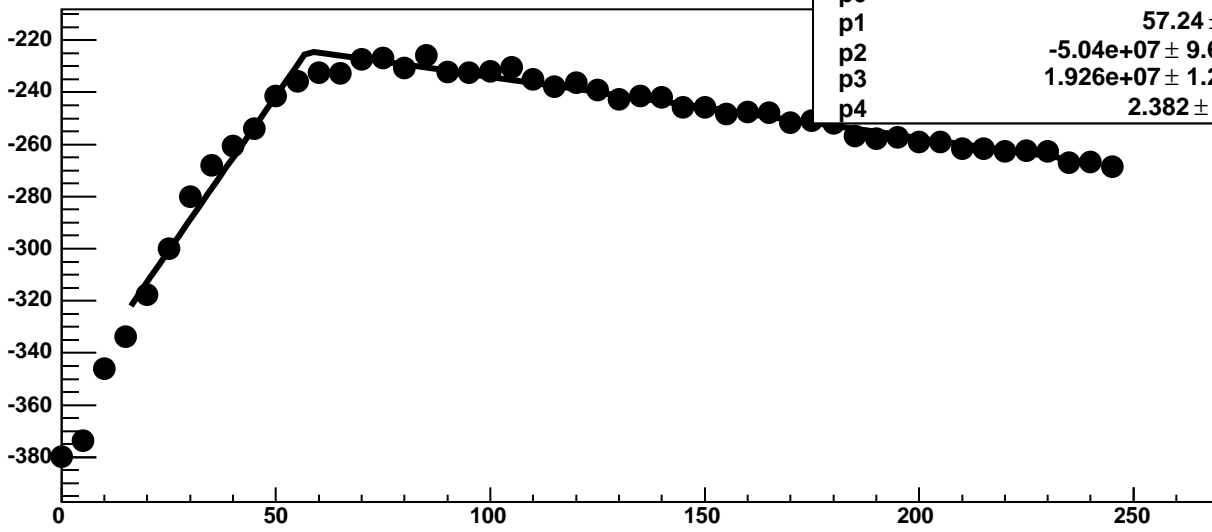
Chip 5, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

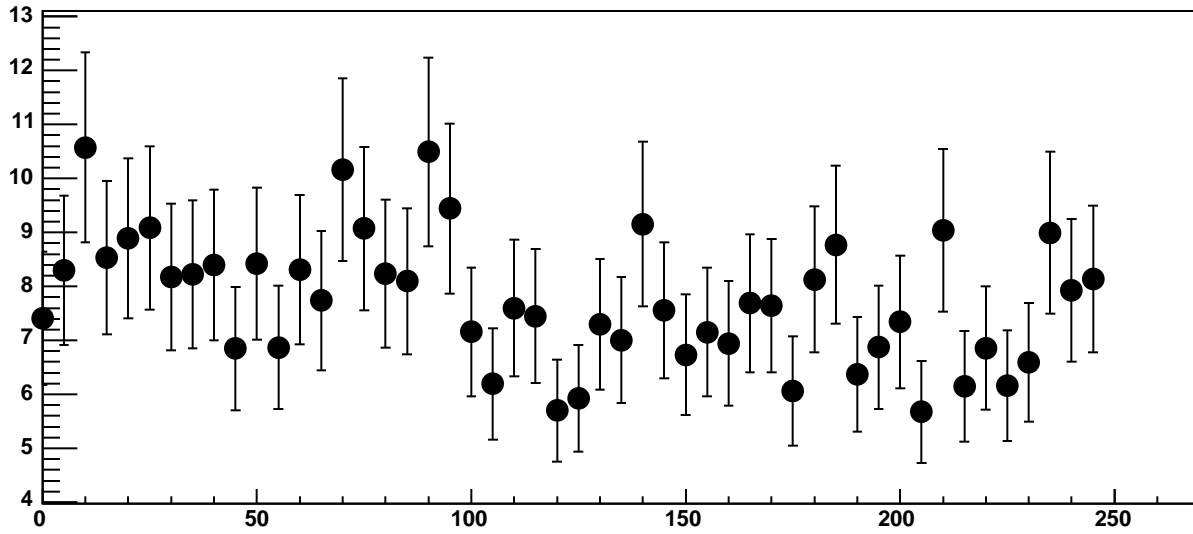


Chip 5, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

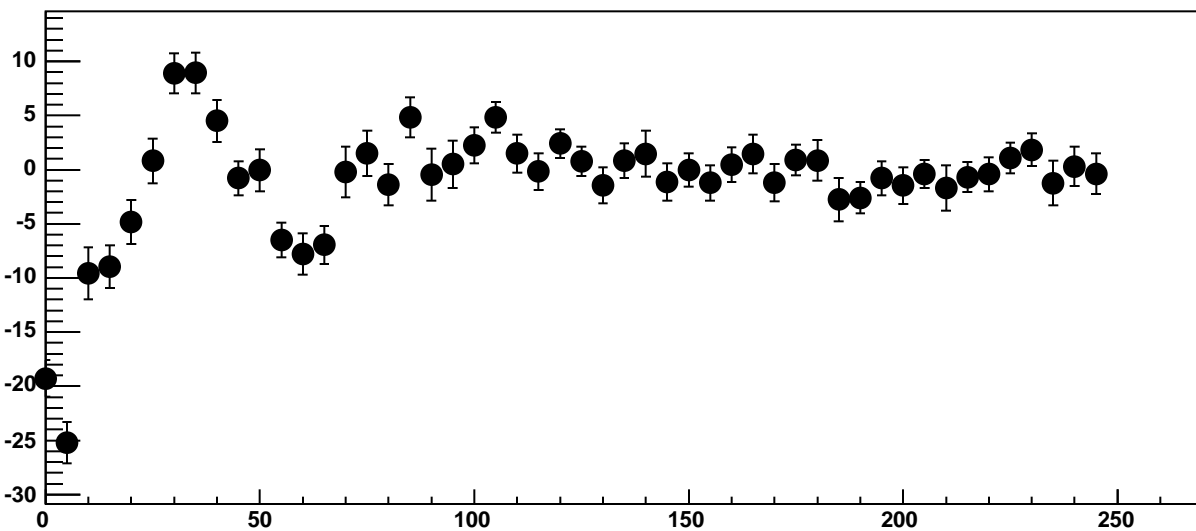


χ^2 / ndf	166.2 / 41
p0	-224.1 ± 0.551
p1	57.24 ± 0.4913
p2	$-5.04\text{e}+07 \pm 9.643\text{e}+05$
p3	$1.926\text{e}+07 \pm 1.223\text{e}+05$
p4	2.382 ± 0.04704

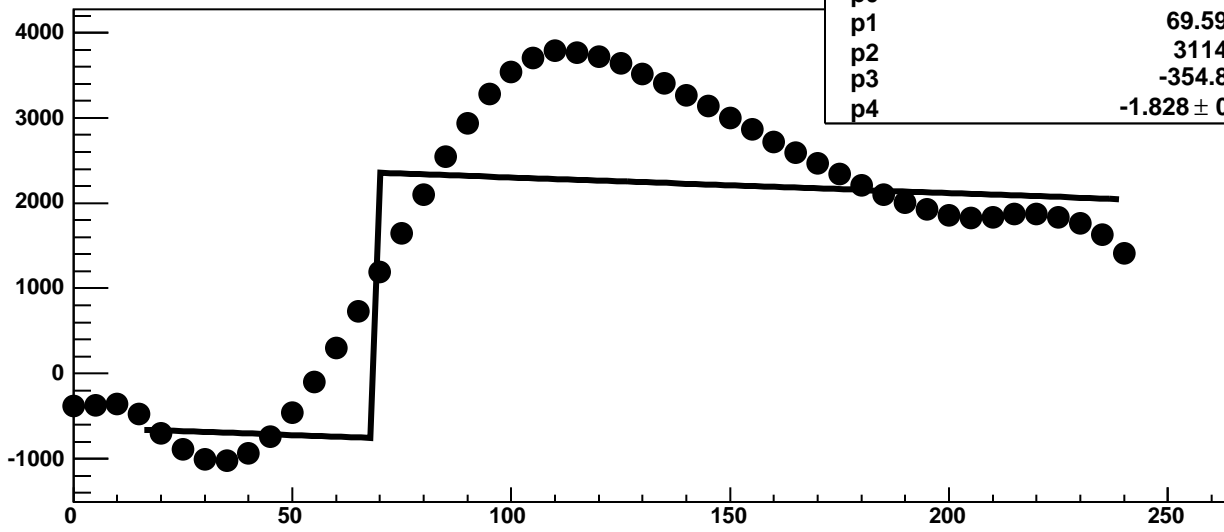
Chip 5, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

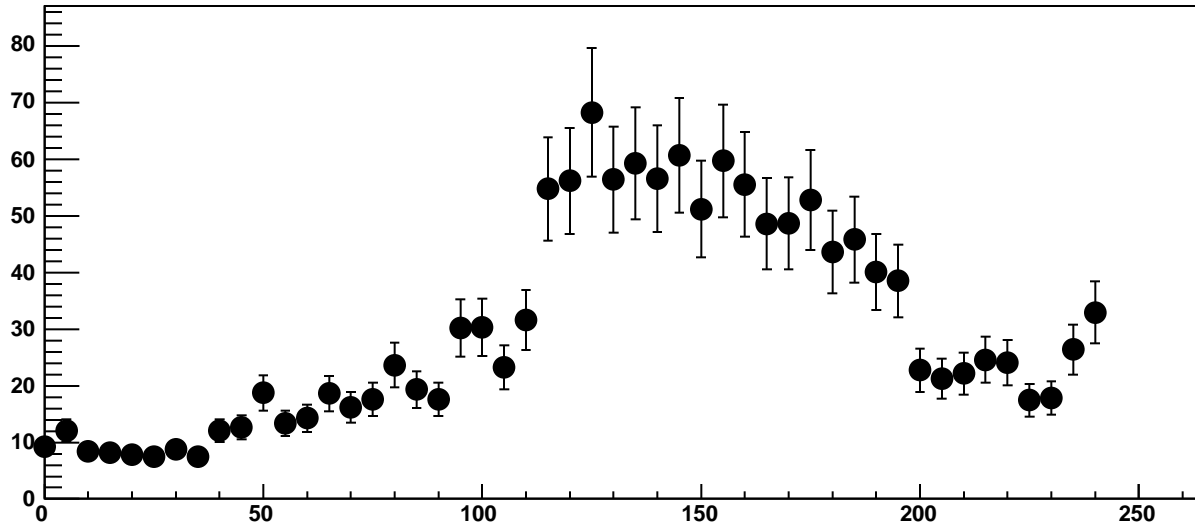


Chip 5, Channel 16, Enable 5, DAC=4000, ADC Mean vs Hold

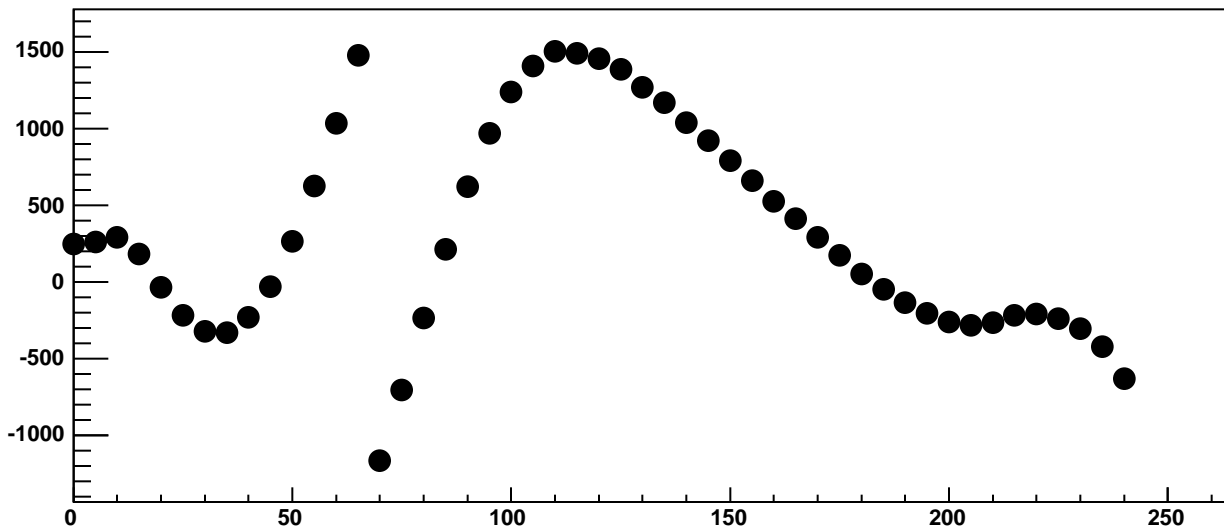


χ^2 / ndf	7.888e+05 / 41
p0	-757.7 ± 1.543
p1	69.59 ± 2.798
p2	3114 ± 1.214
p3	-354.8 ± 3.146
p4	-1.828 ± 0.007952

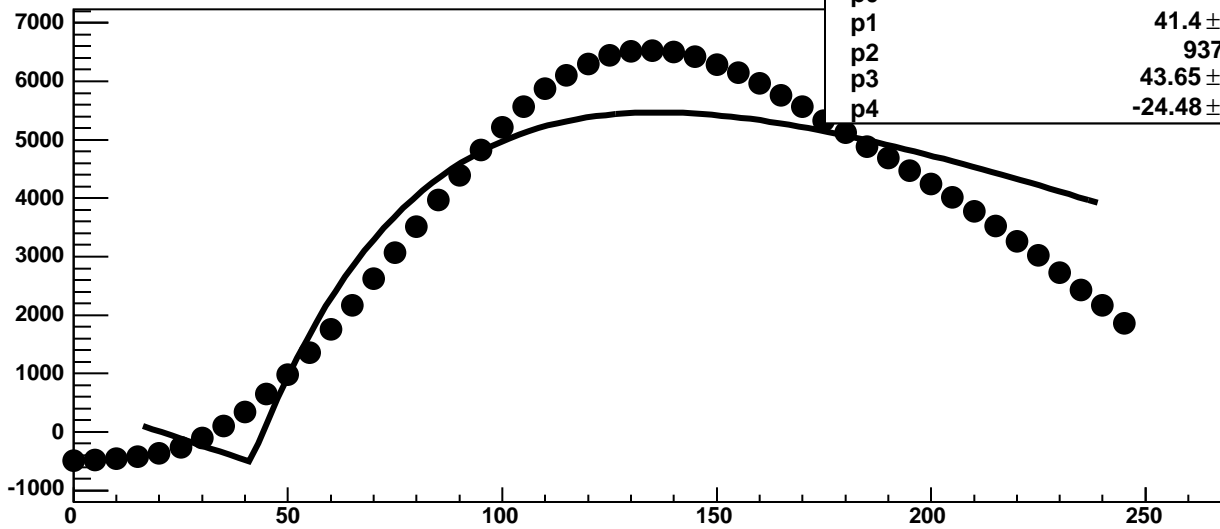
Chip 5, Channel 16, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 16, Enable 5, DAC=4000, ADC Residuals vs Hold

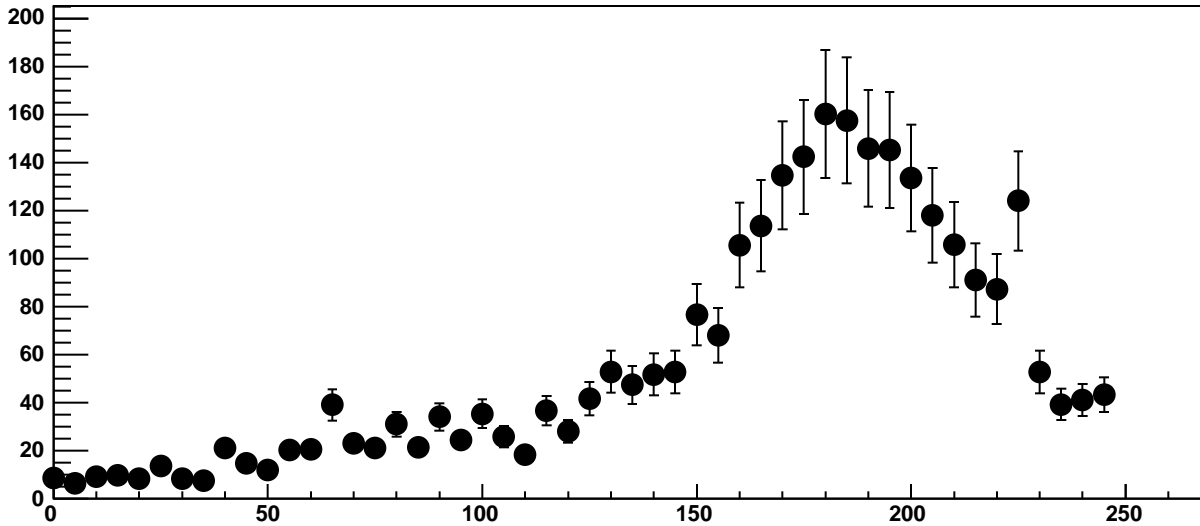


Chip 5, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

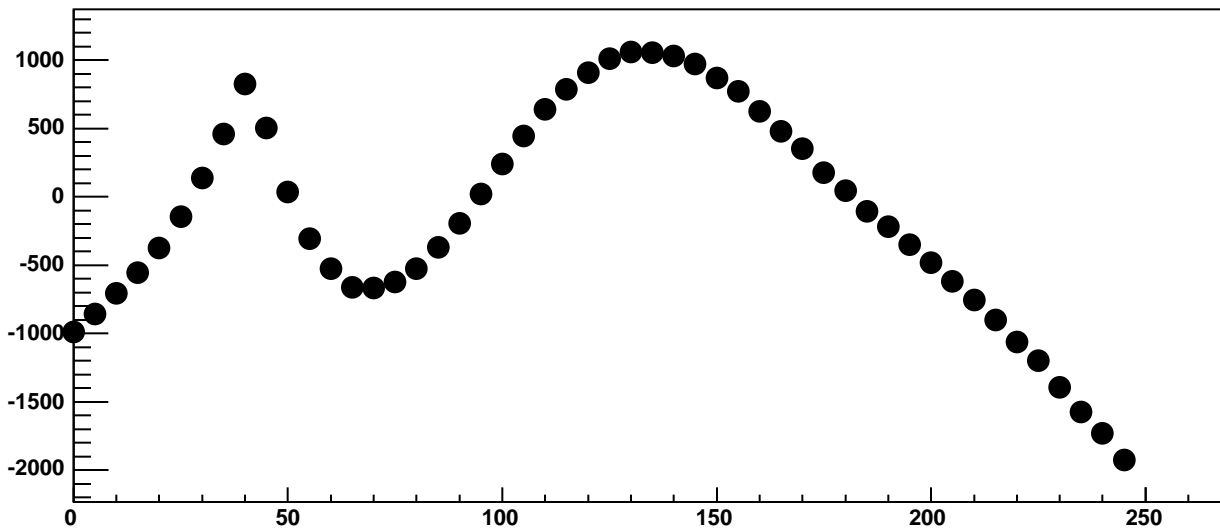


χ^2 / ndf	4.848e+05 / 41
p0	-513.8 ± 1.209
p1	41.4 ± 0.01485
p2	9371 ± 12.3
p3	43.65 ± 0.06659
p4	-24.48 ± 0.06589

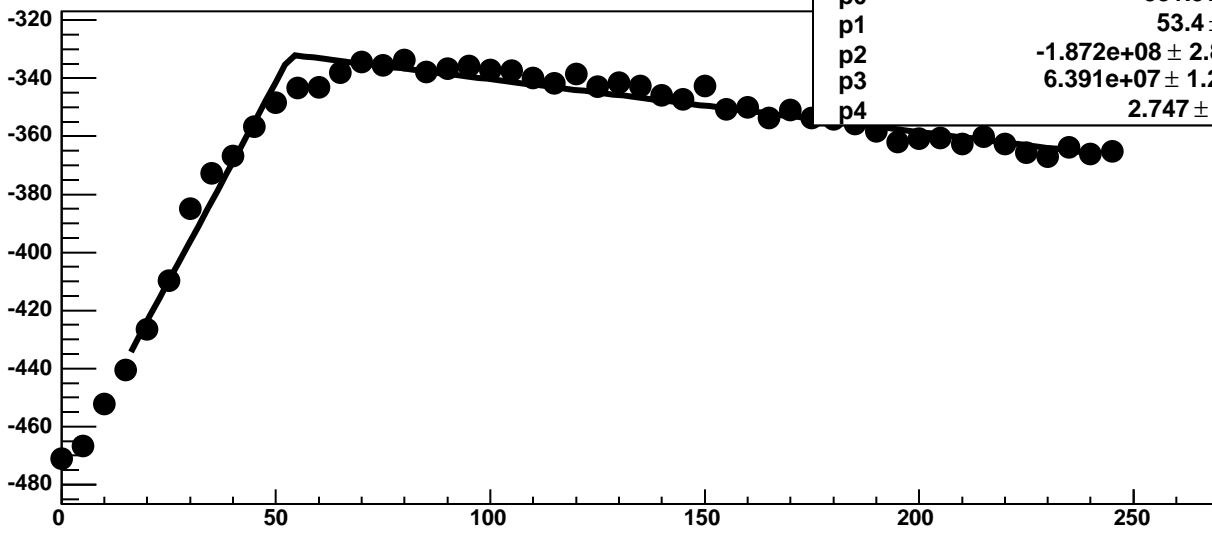
Chip 5, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

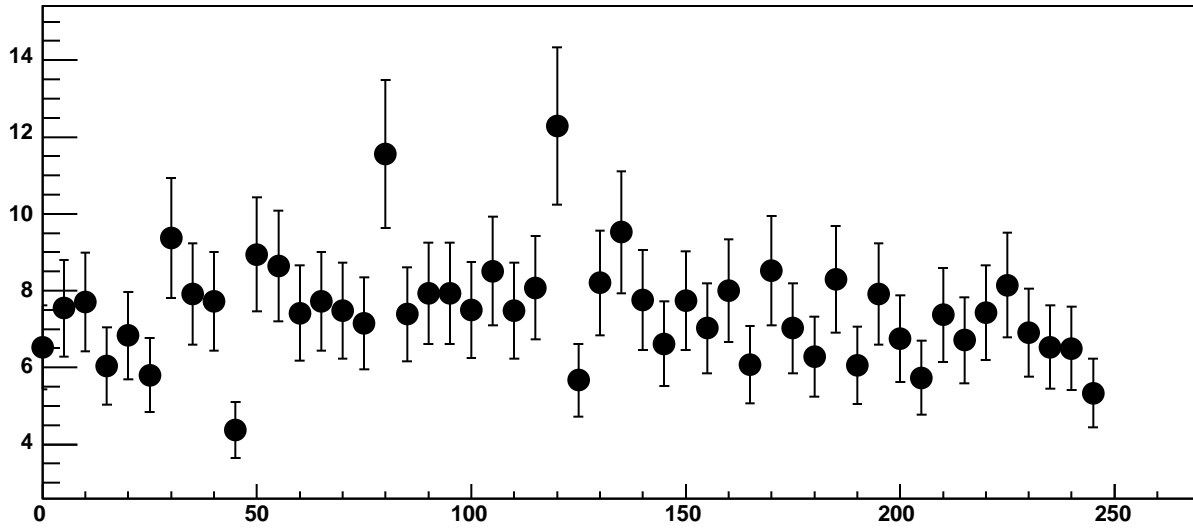


Chip 5, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

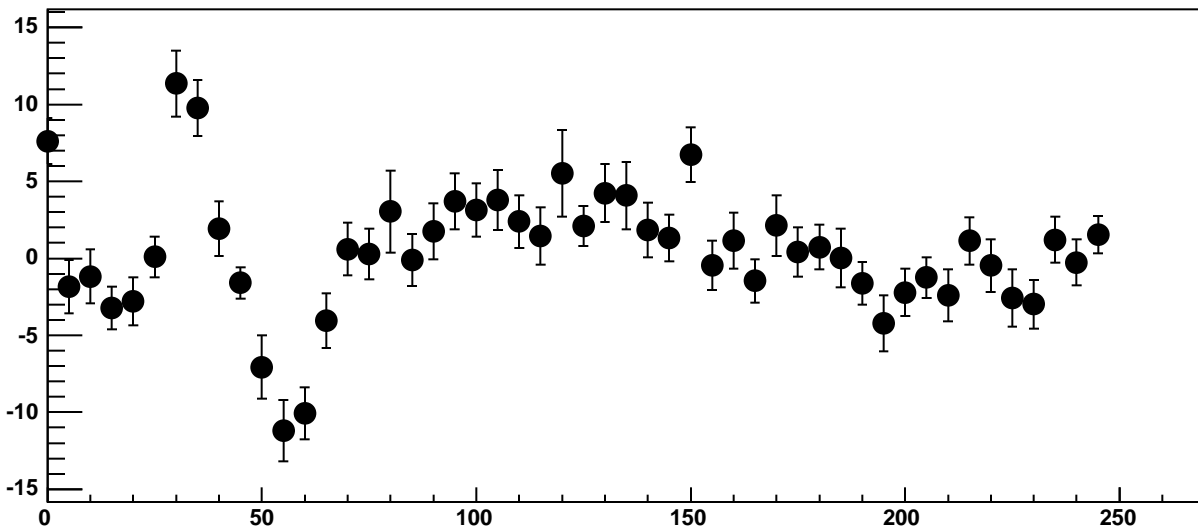


χ^2 / ndf	222.2 / 41
p0	-331.9 ± 0.5412
p1	53.4 ± 0.4092
p2	$-1.872\text{e}+08 \pm 2.874\text{e}+06$
p3	$6.391\text{e}+07 \pm 1.244\text{e}+05$
p4	2.747 ± 0.04434

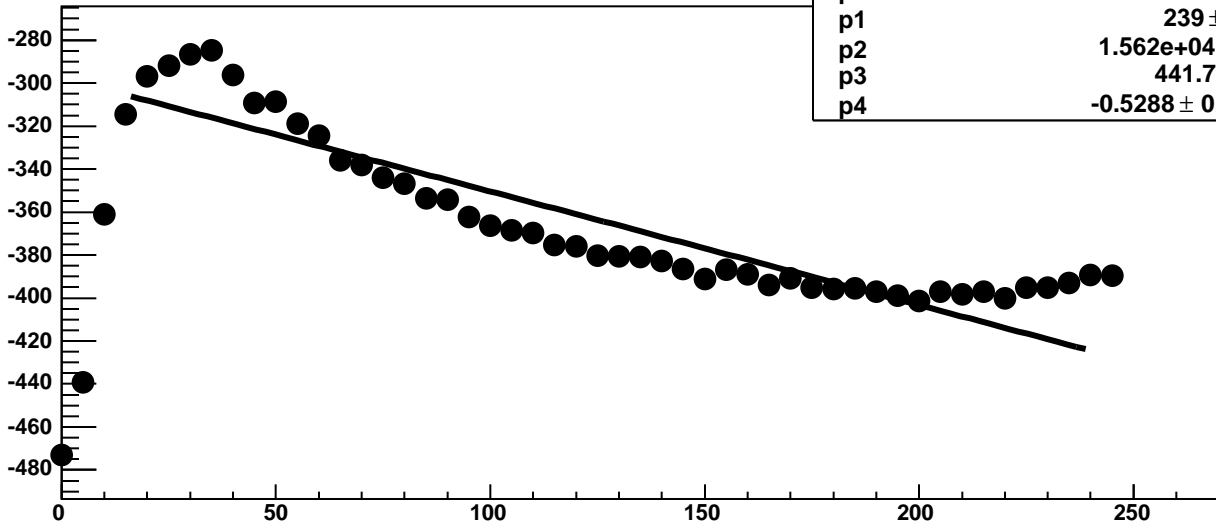
Chip 5, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



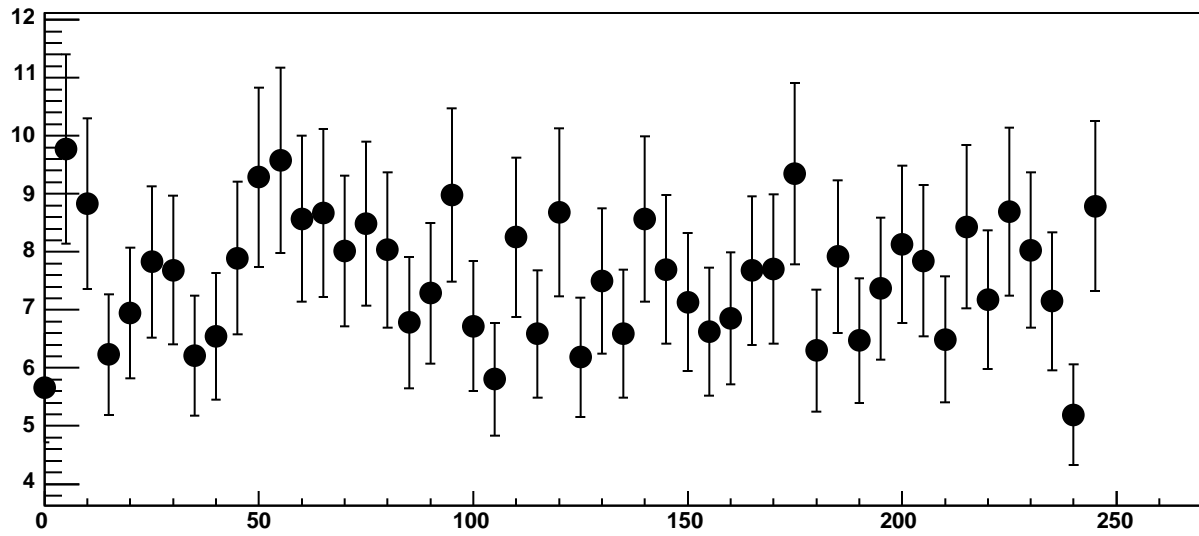
Chip 5, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold



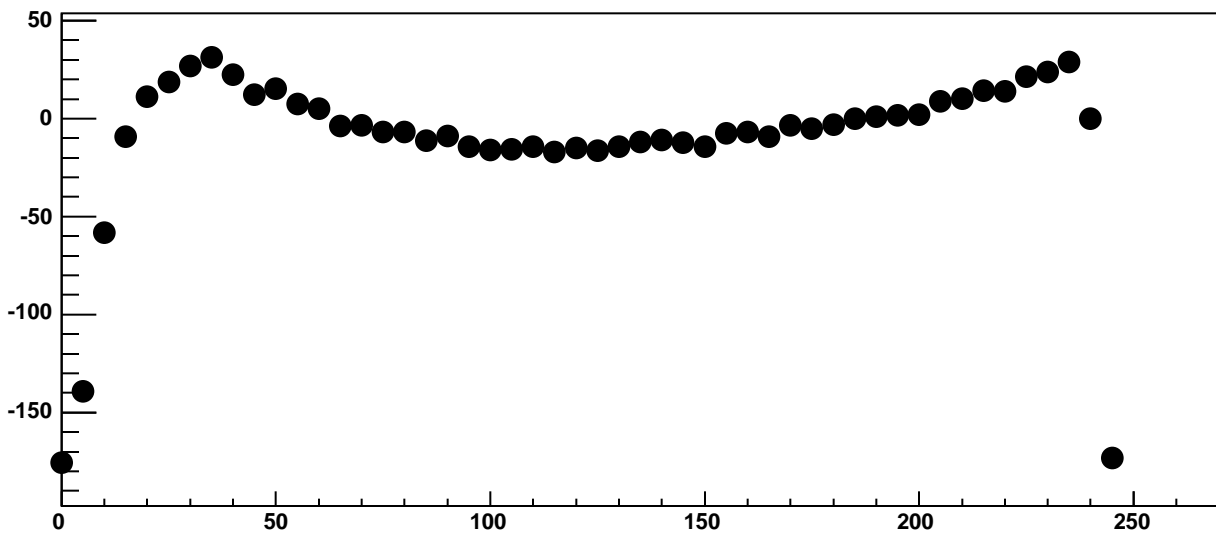
Chip 5, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold



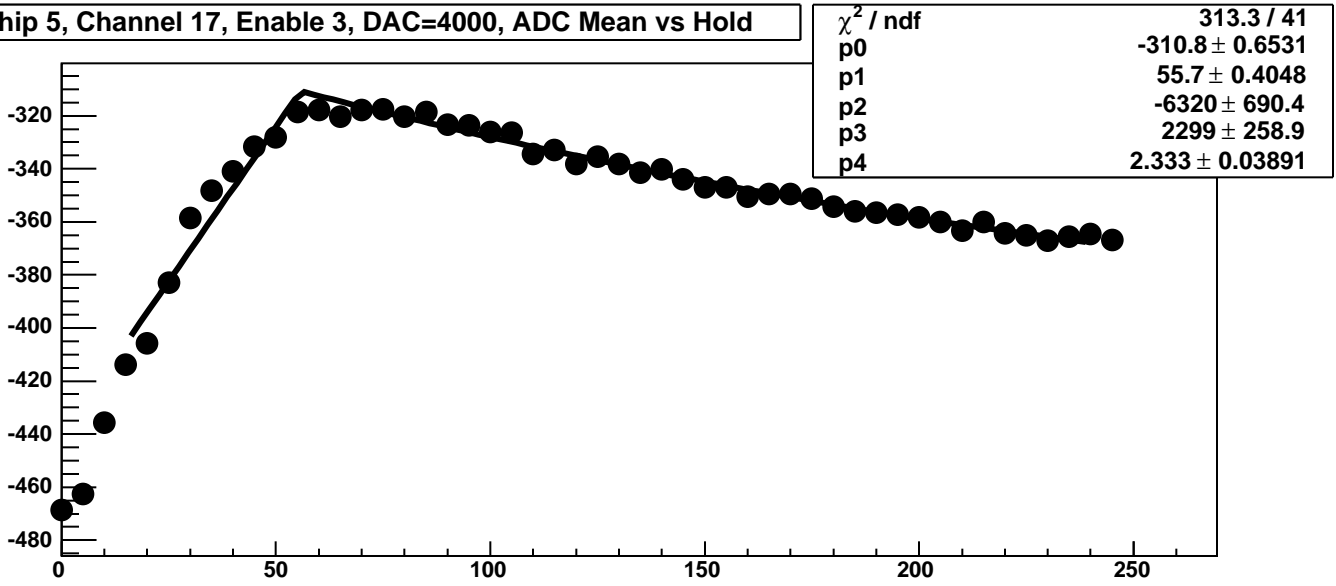
Chip 5, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold



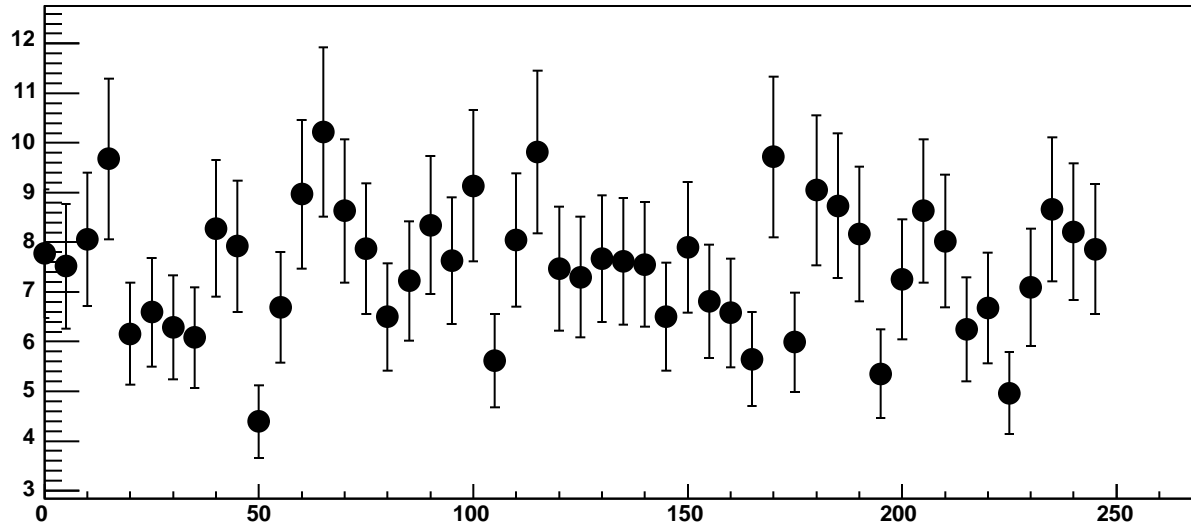
Chip 5, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold



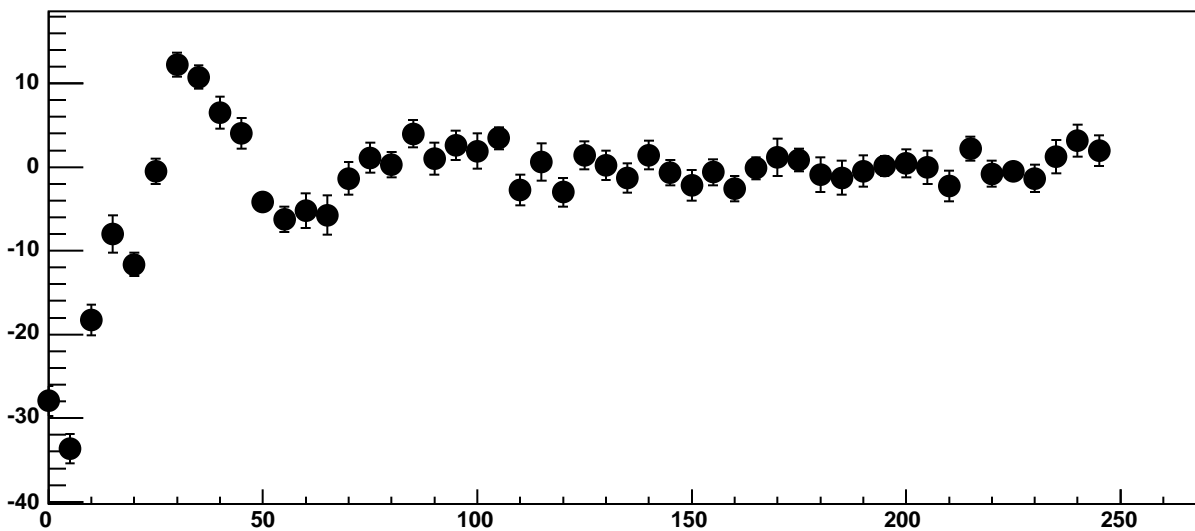
Chip 5, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



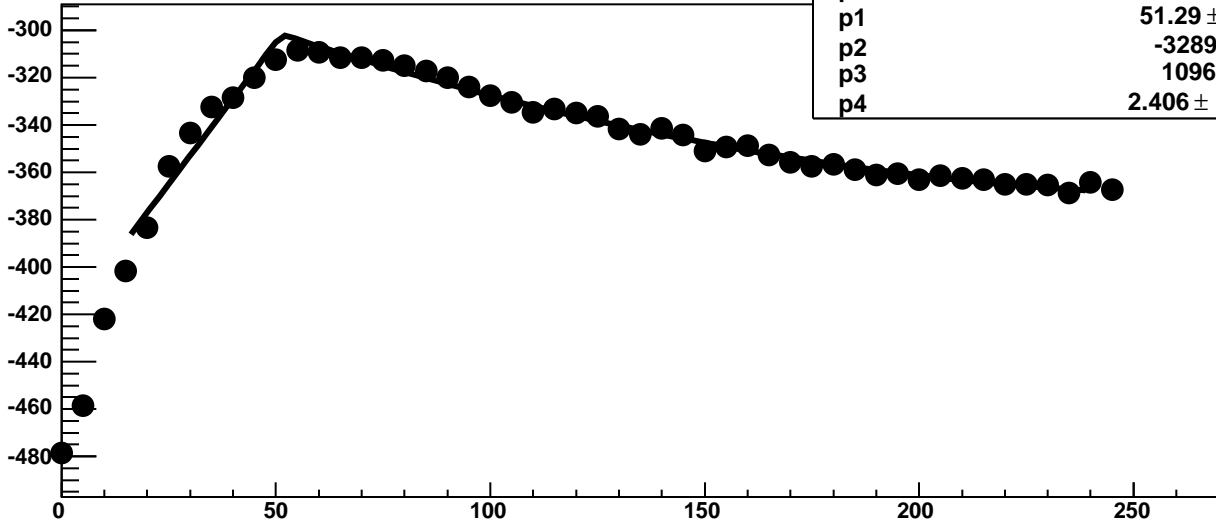
Chip 5, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold

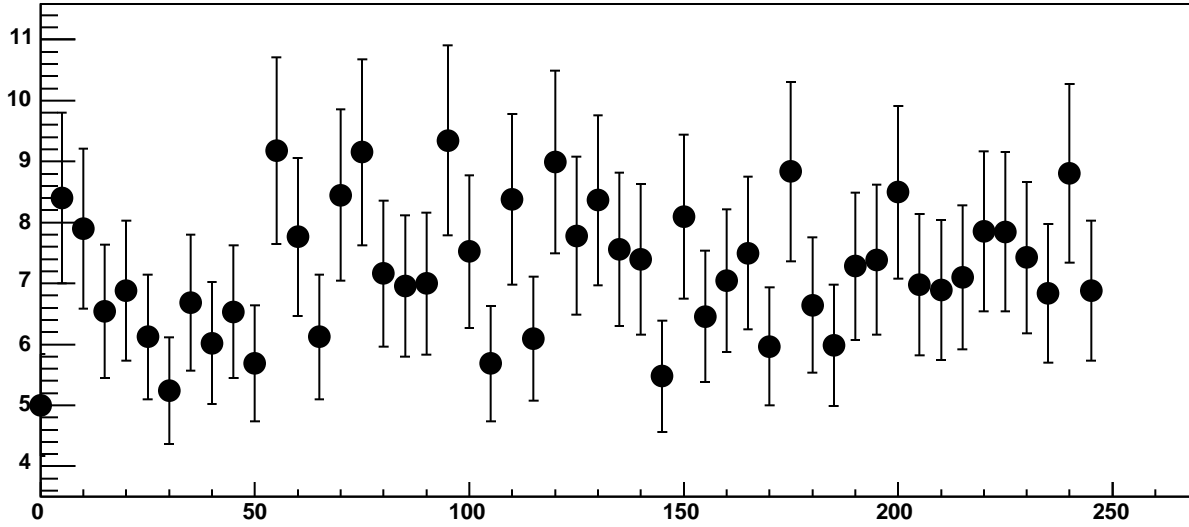


Chip 5, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold

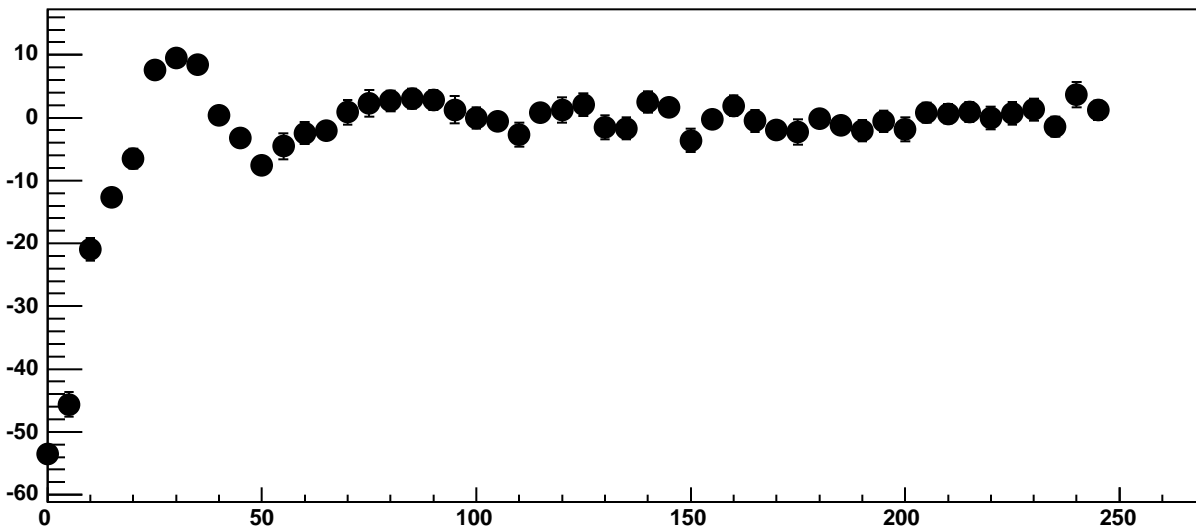


χ^2 / ndf	295.8 / 41
p0	-301.7 ± 0.6958
p1	51.29 ± 0.4075
p2	-3289 ± 230.3
p3	1096 ± 78.74
p4	2.406 ± 0.04387

Chip 5, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold

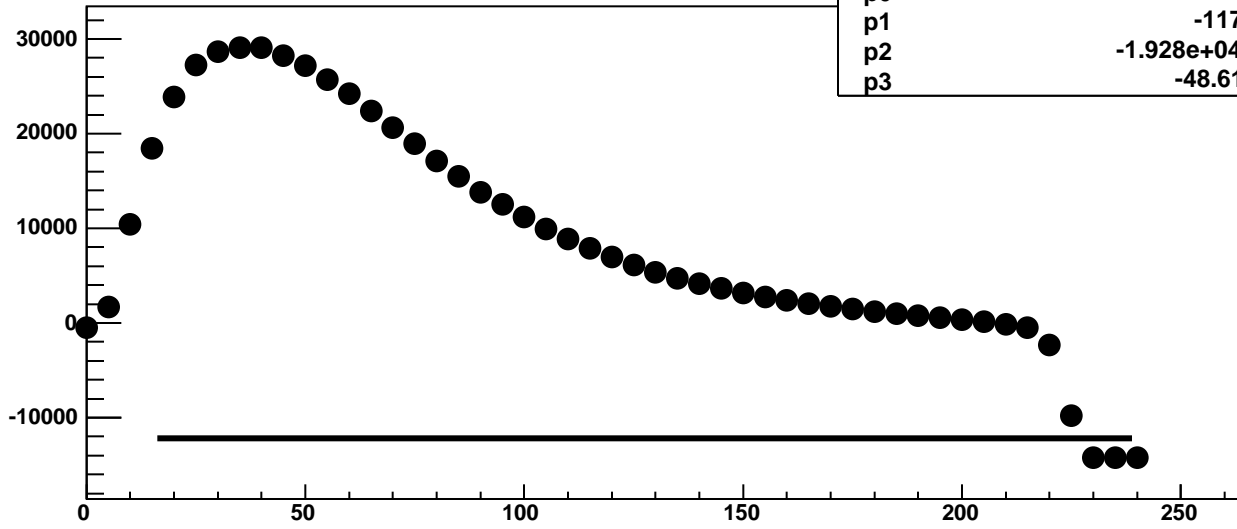


Chip 5, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

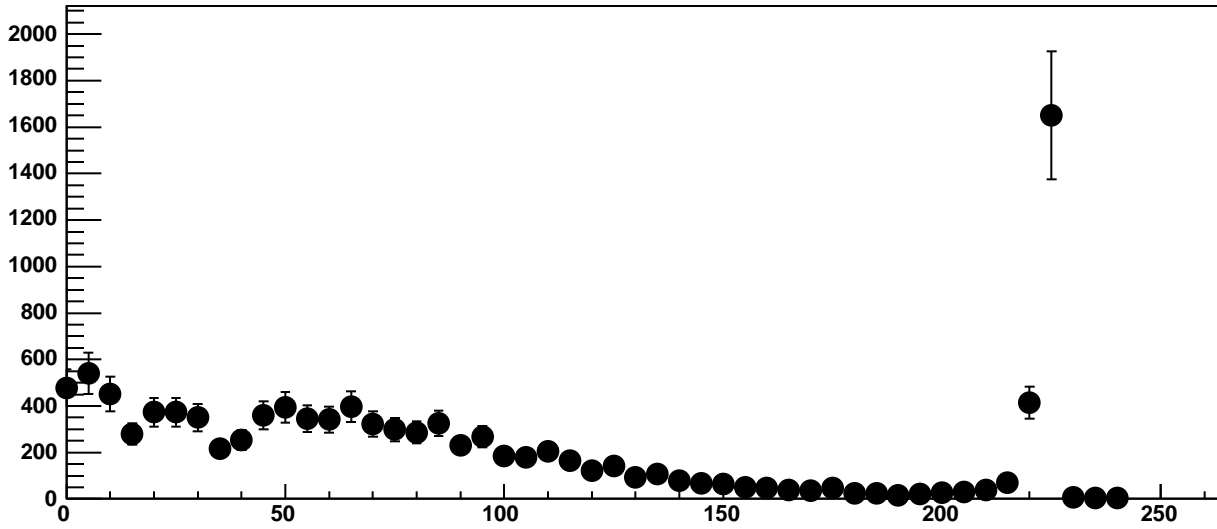


Chip 5, Channel 17, Enable 5!, DAC=4000, ADC Mean vs Hold

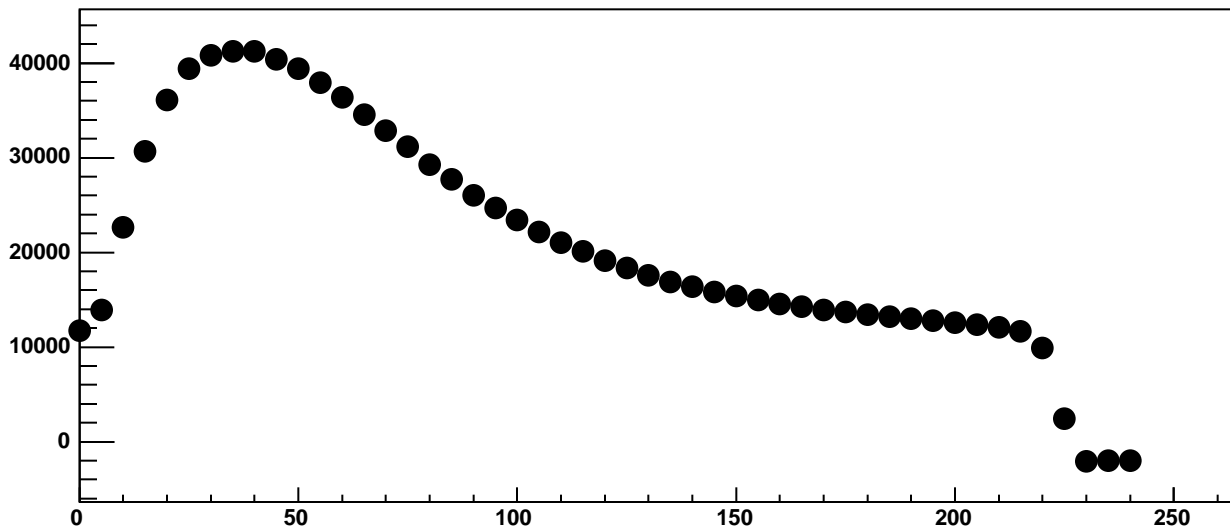
χ^2 / ndf	7.013e+07 / 42
p0	-1.22e+04 ± 0.6343
p1	-1175 ± 2.7
p2	-1.928e+04 ± 1.414
p3	-48.61 ± 1.414



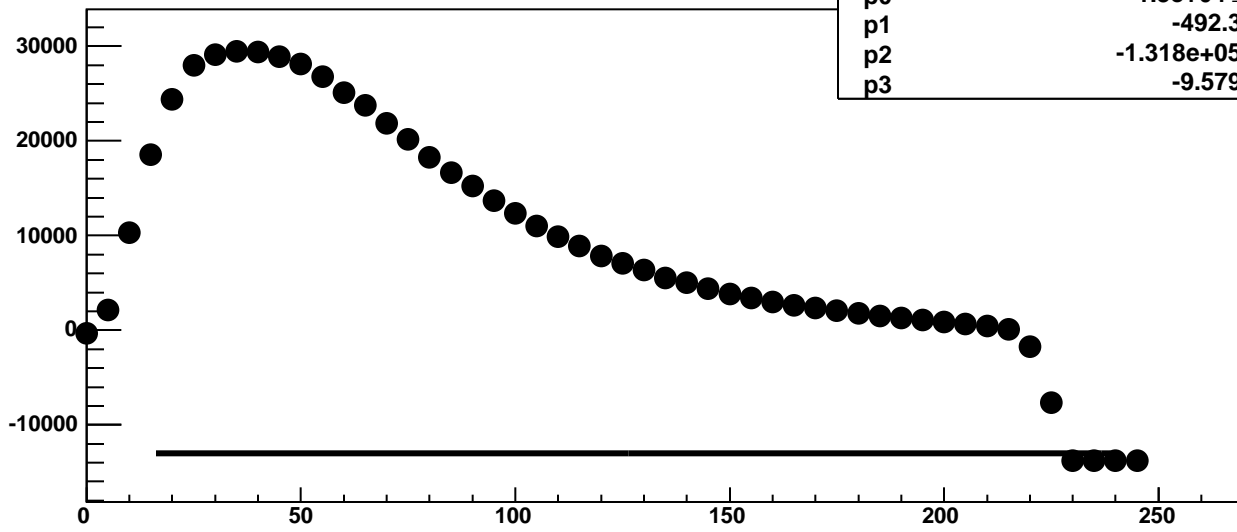
Chip 5, Channel 17, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 17, Enable 5!, DAC=4000, ADC Residuals vs Hold

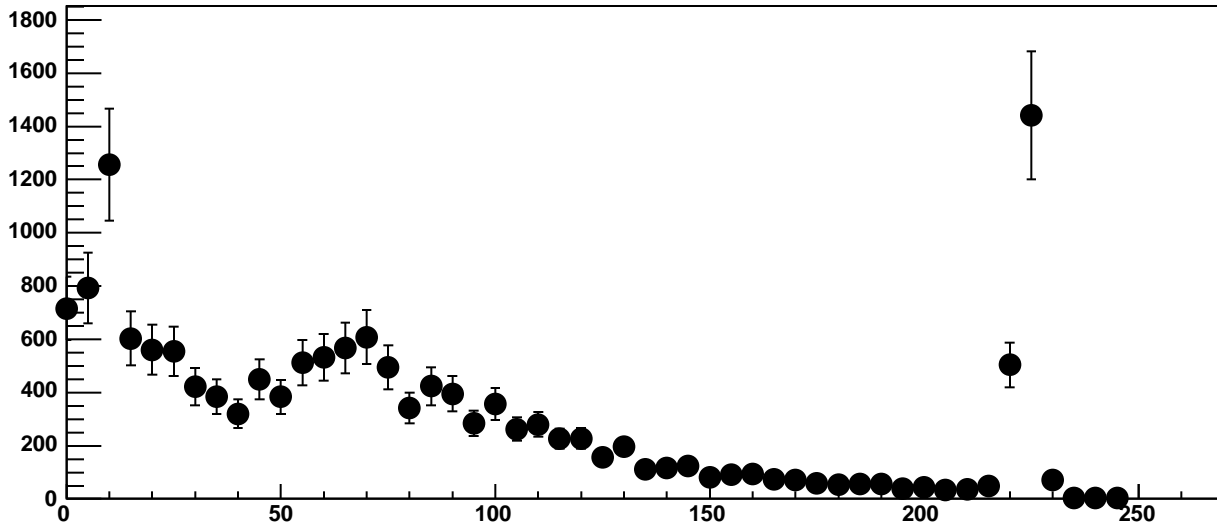


Chip 6, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold

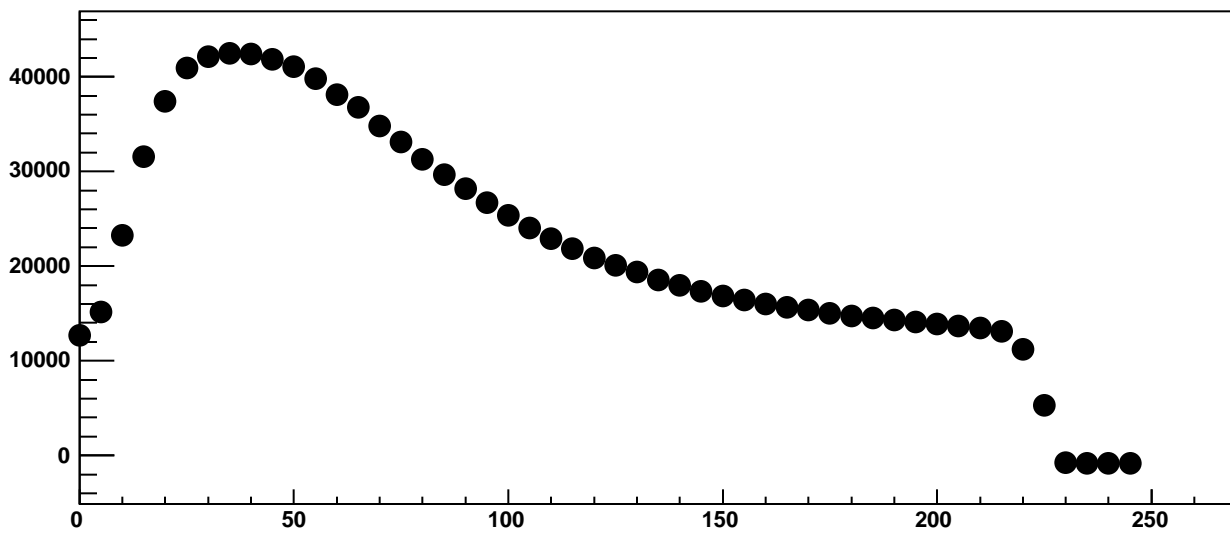


χ^2 / ndf	2.681e+07 / 42
p0	-1.3e+04 \pm 0.6784
p1	-492.3 \pm 1.909
p2	-1.318e+05 \pm 1.414
p3	-9.579 \pm 1.414

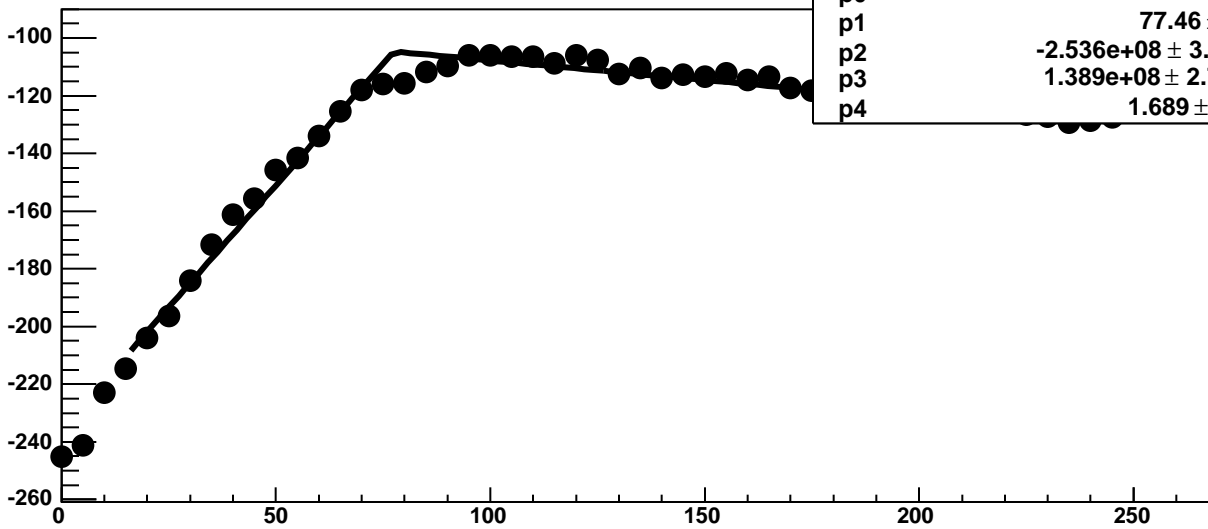
Chip 6, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold

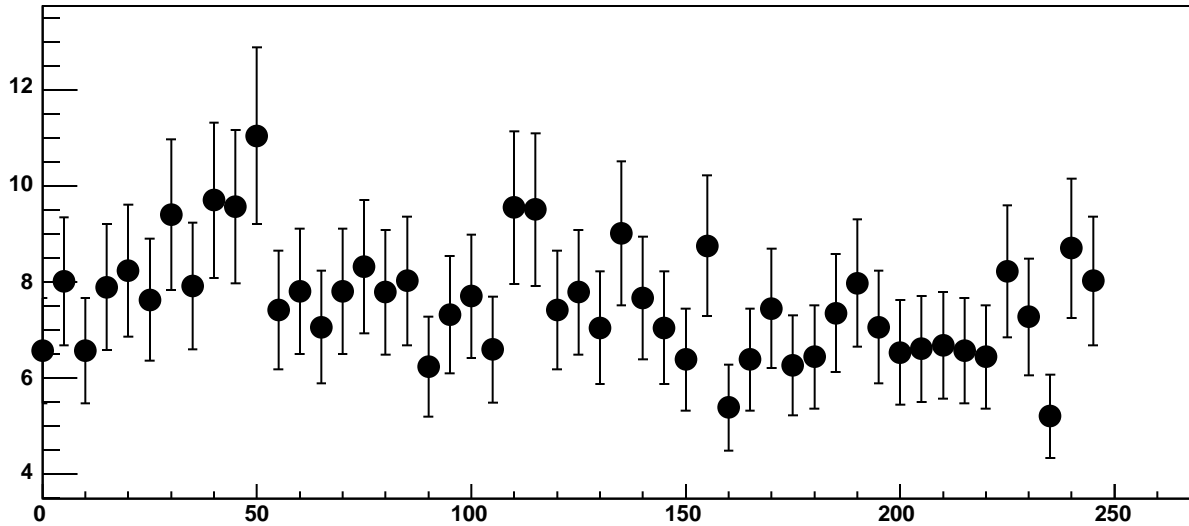


Chip 6, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

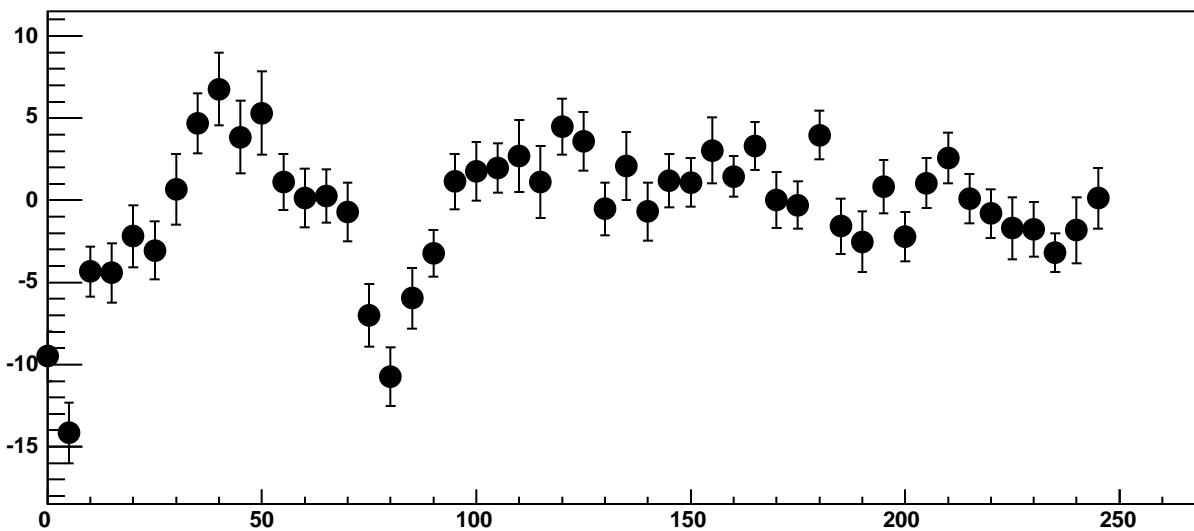


χ^2 / ndf	152.3 / 41
p0	-104.7 ± 0.5156
p1	77.46 ± 0.6414
p2	$-2.536\text{e}+08 \pm 3.599\text{e}+06$
p3	$1.389\text{e}+08 \pm 2.764\text{e}+05$
p4	1.689 ± 0.02573

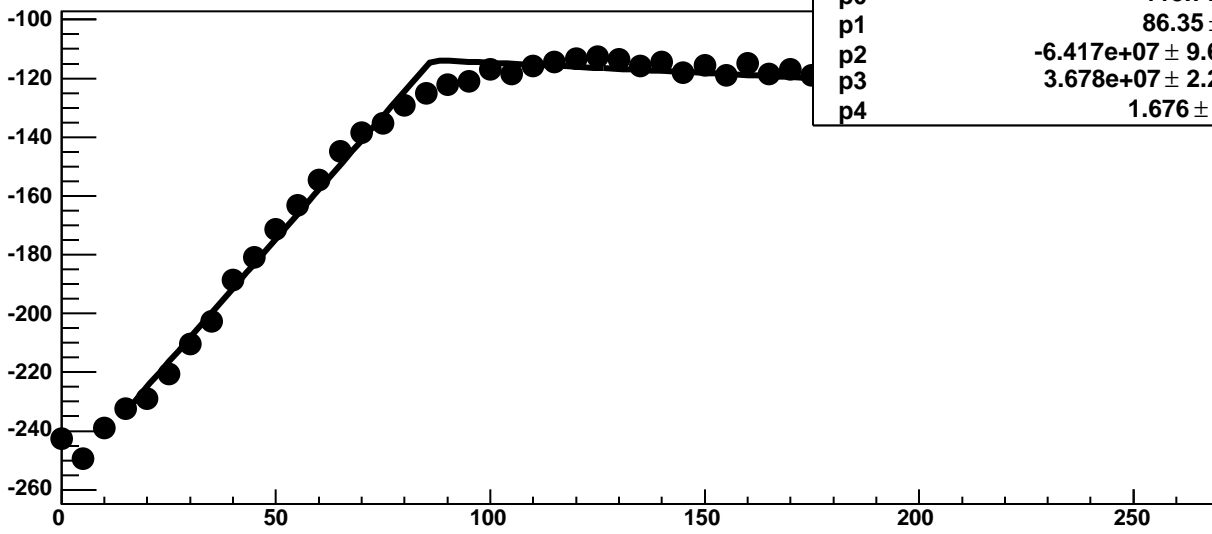
Chip 6, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold

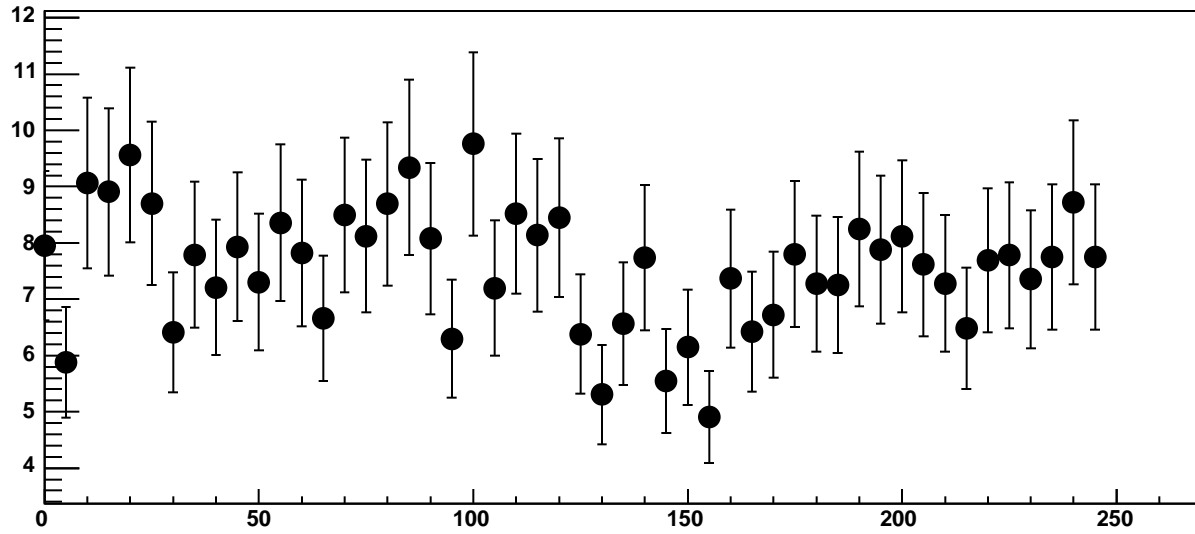


Chip 6, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

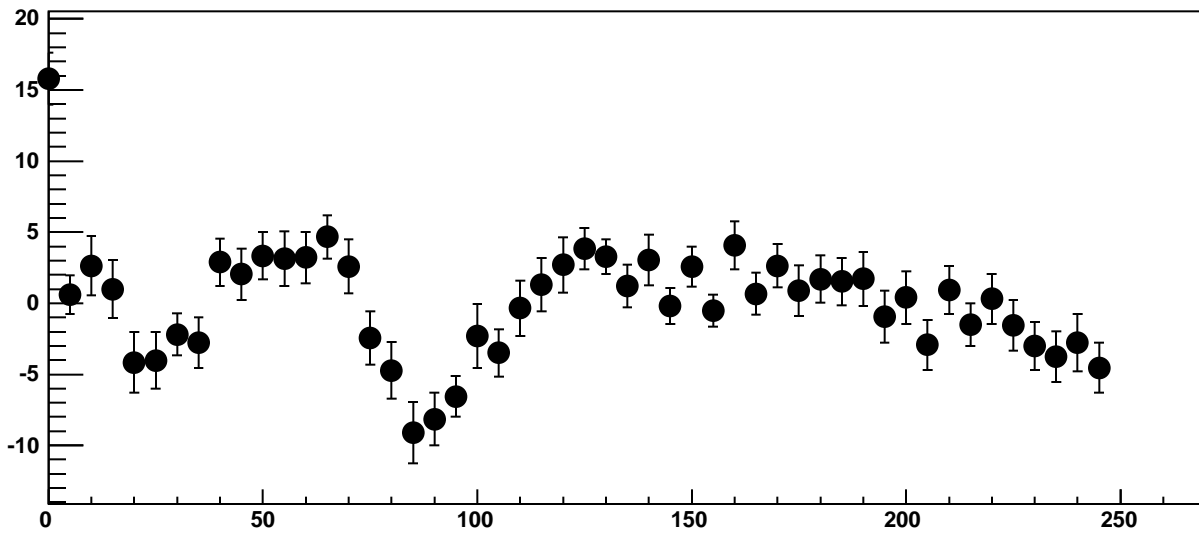


χ^2 / ndf	159.4 / 41
p0	-113.7 ± 0.5818
p1	86.35 ± 0.6548
p2	$-6.417\text{e}+07 \pm 9.654\text{e}+05$
p3	$3.678\text{e}+07 \pm 2.265\text{e}+05$
p4	1.676 ± 0.02315

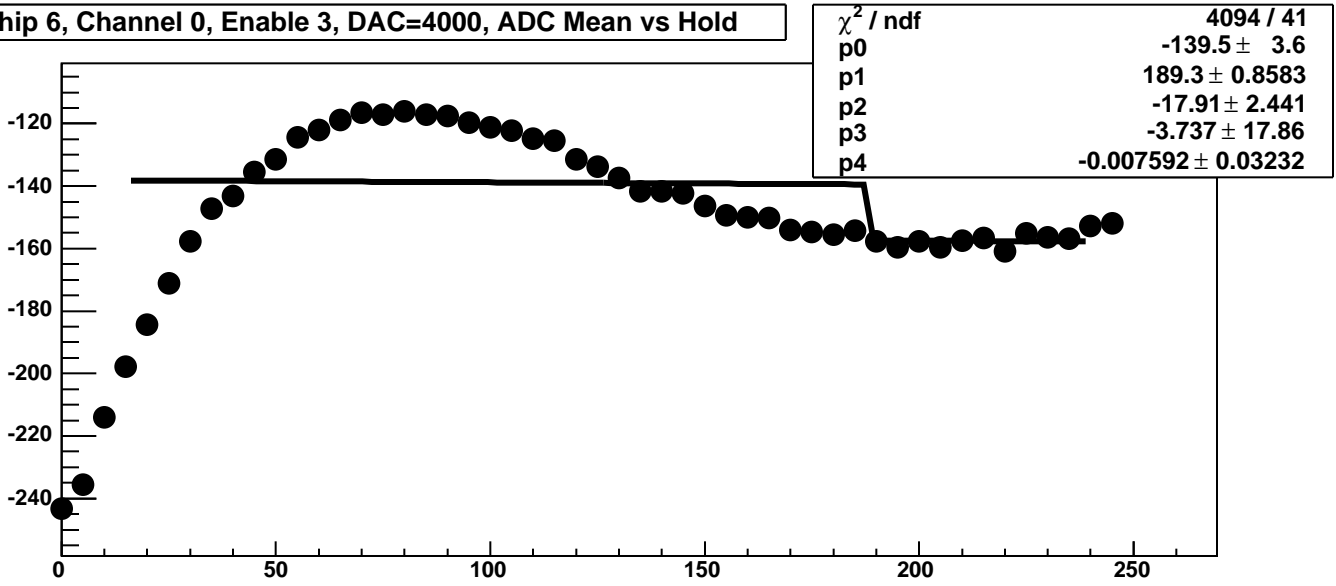
Chip 6, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



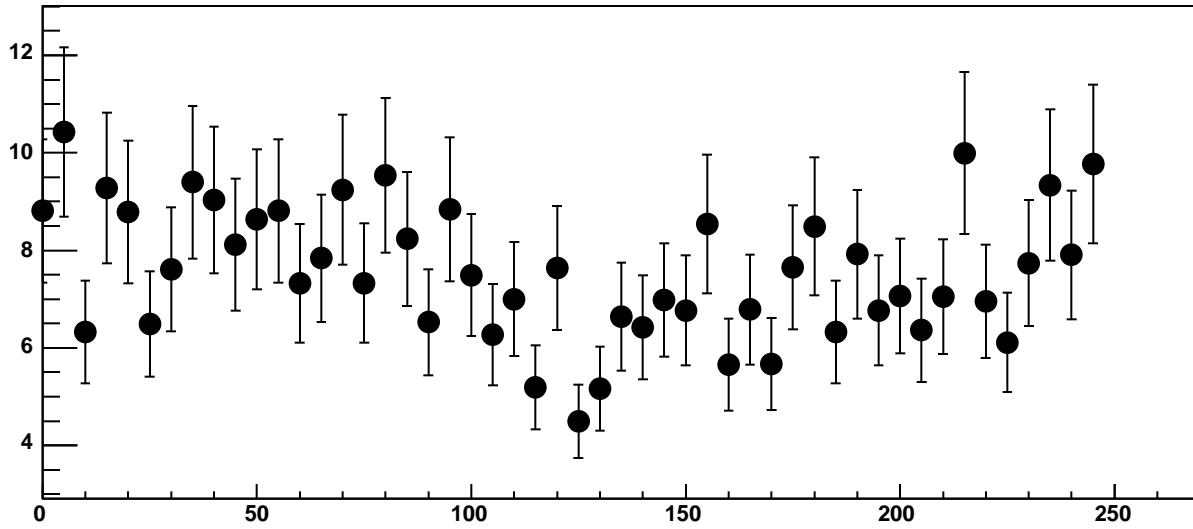
Chip 6, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold



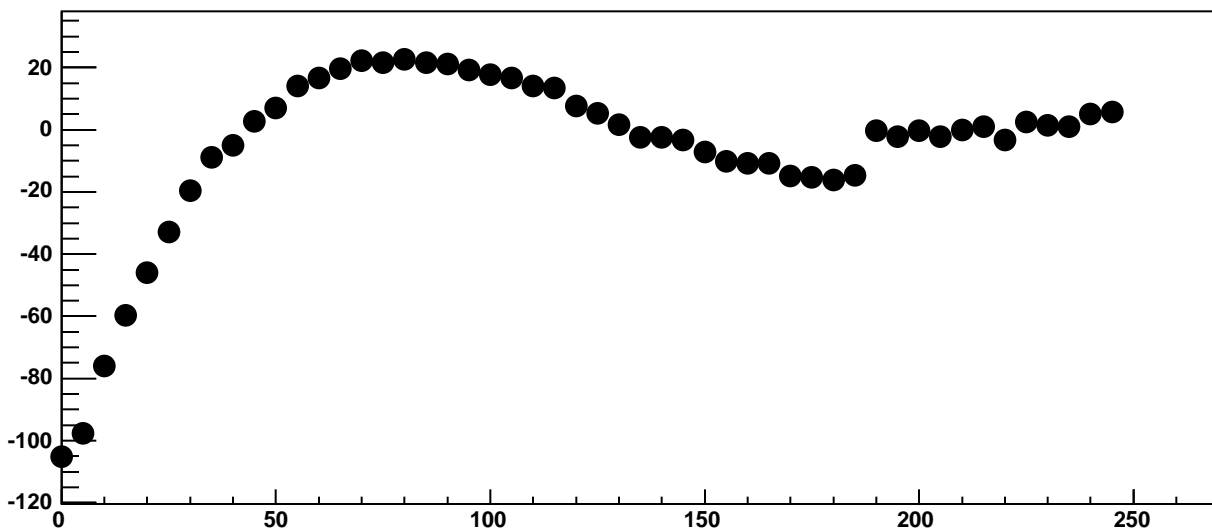
Chip 6, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold



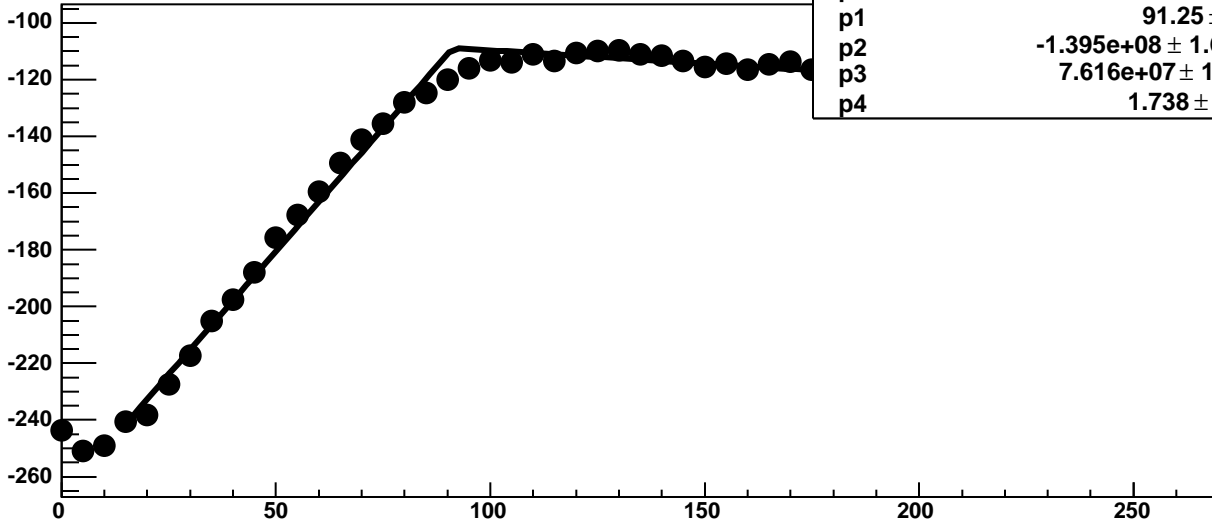
Chip 6, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold

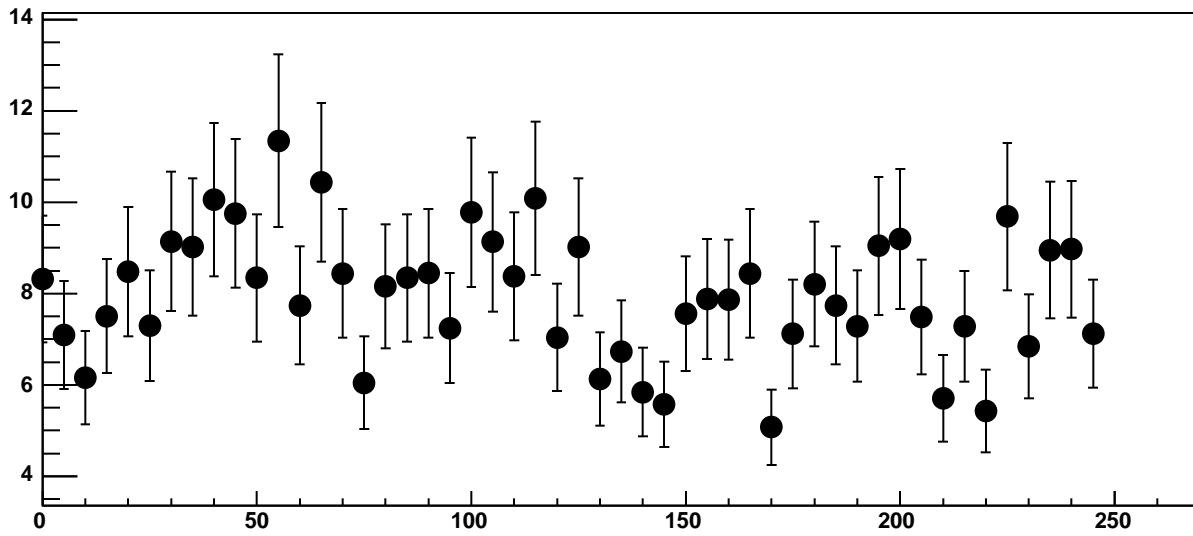


Chip 6, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

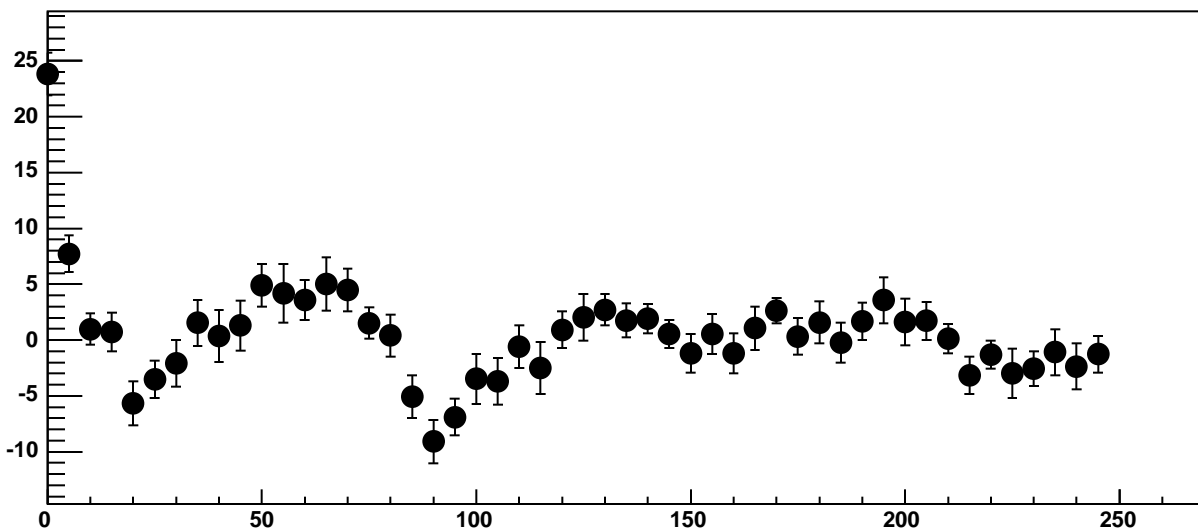


χ^2 / ndf	124.3 / 41
p0	-108.8 ± 0.6232
p1	91.25 ± 0.6087
p2	$-1.395\text{e}+08 \pm 1.639\text{e}+06$
p3	$7.616\text{e}+07 \pm 1.83\text{e}+05$
p4	1.738 ± 0.01997

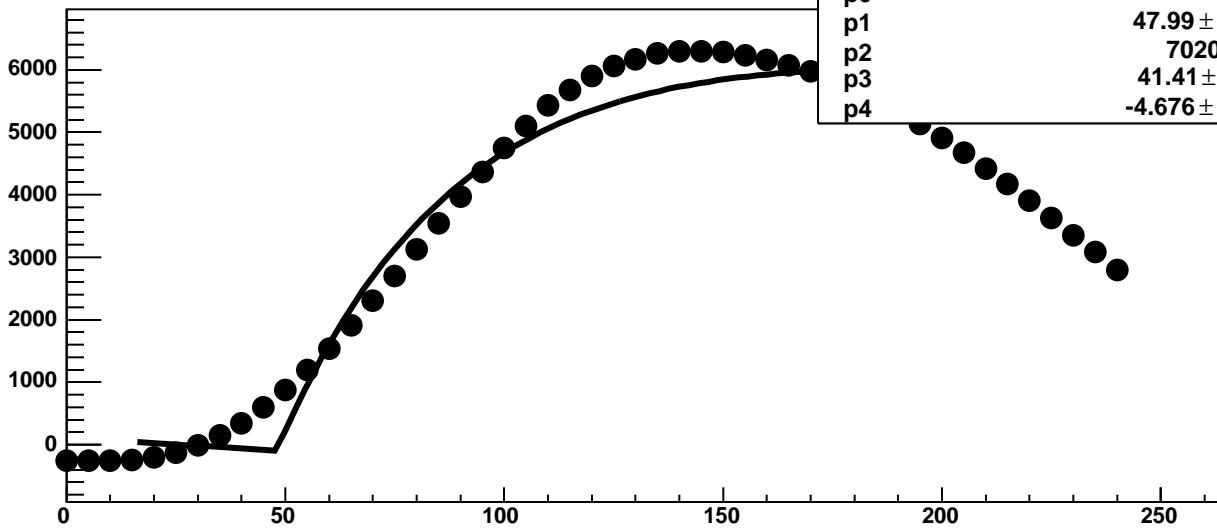
Chip 6, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

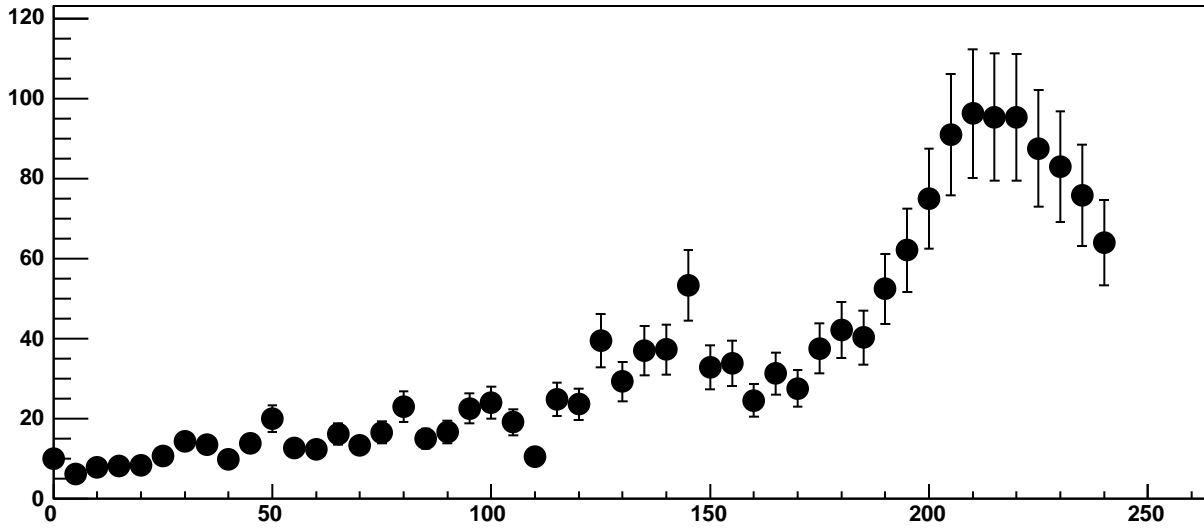


Chip 6, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold

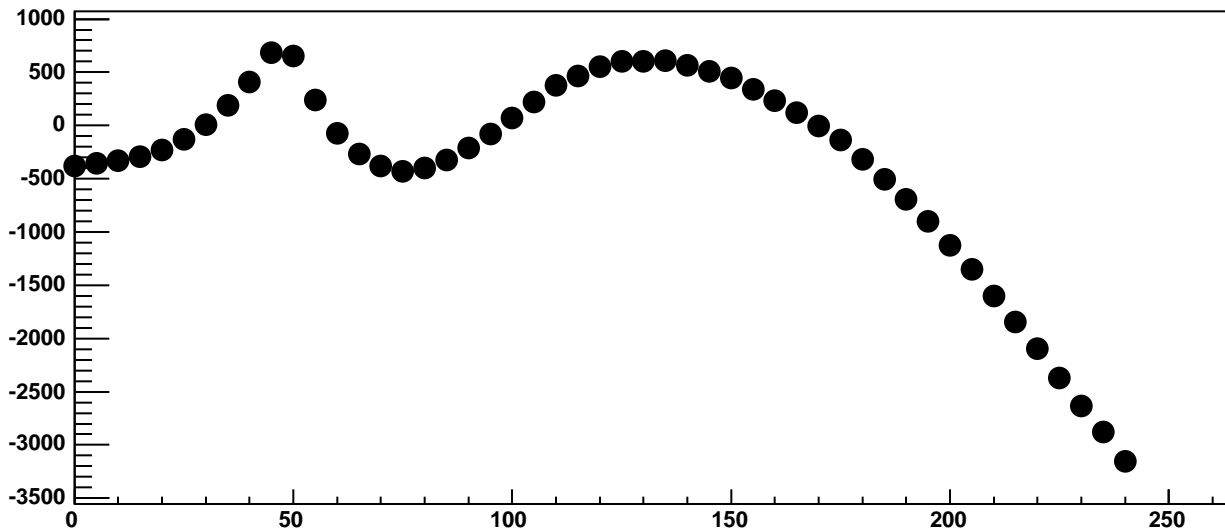


χ^2 / ndf	4.254e+05 / 41
p0	-101.3 ± 1.519
p1	47.99 ± 0.01838
p2	7020 ± 10.22
p3	41.41 ± 0.06451
p4	-4.676 ± 0.05994

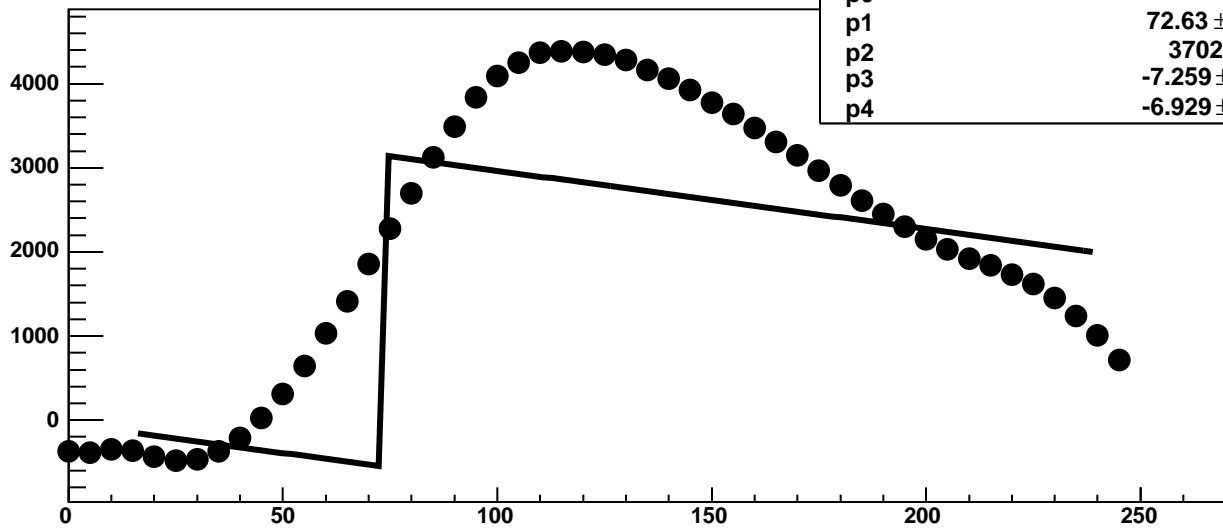
Chip 6, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



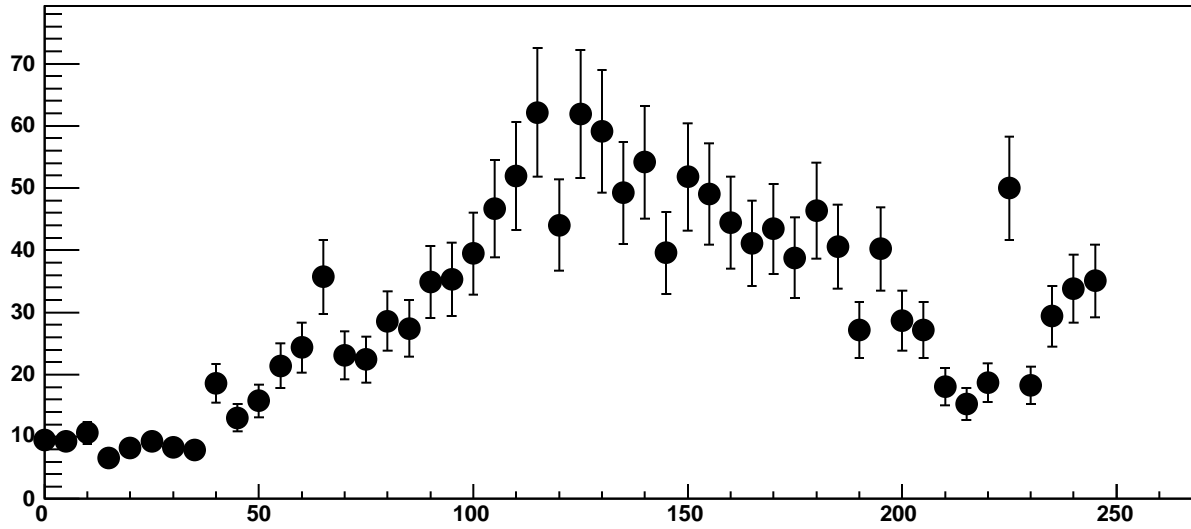
Chip 6, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold



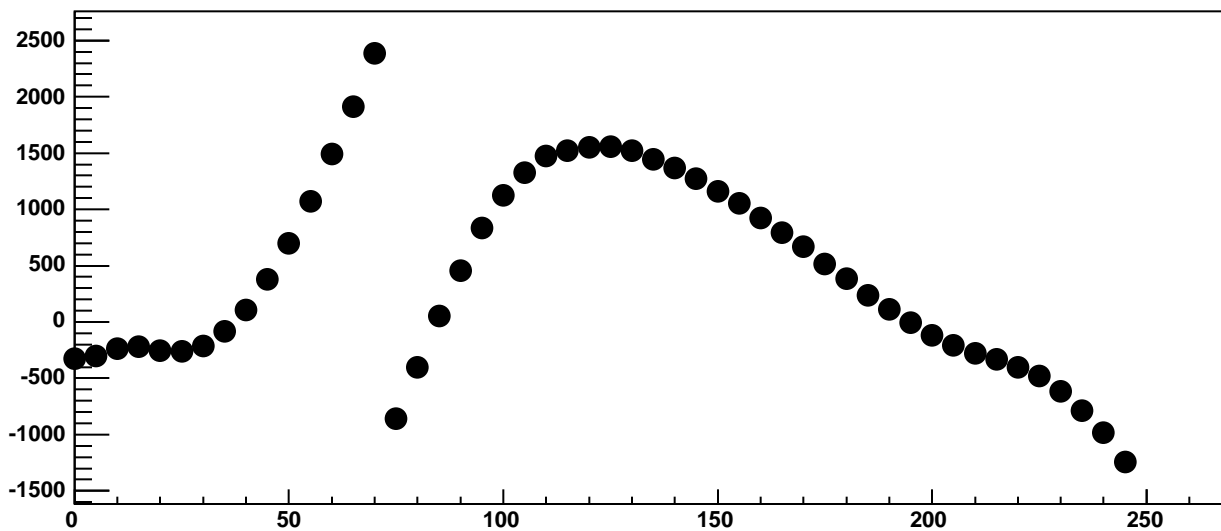
Chip 6, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold



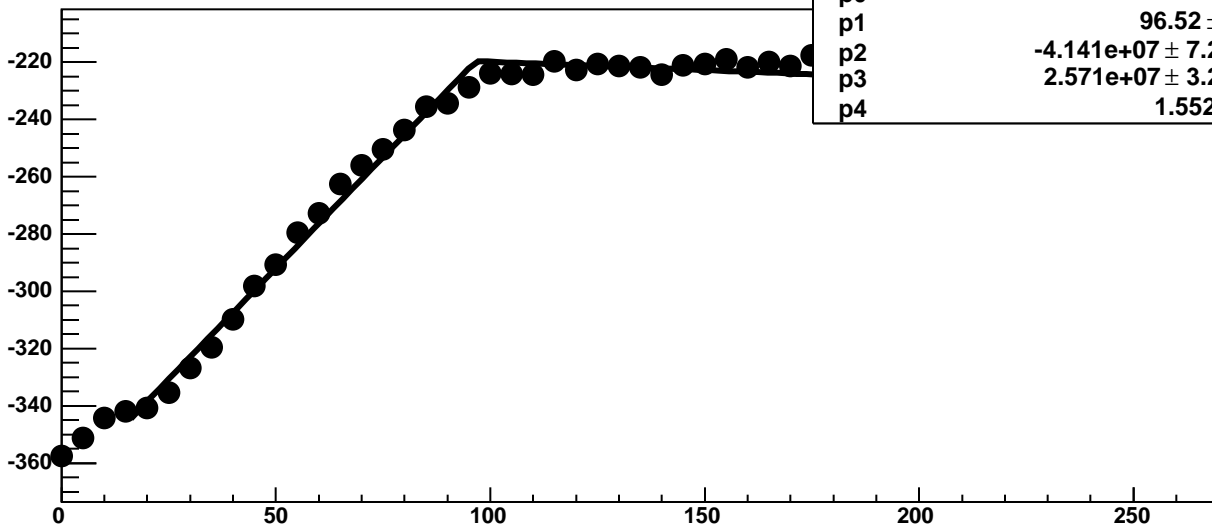
Chip 6, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold

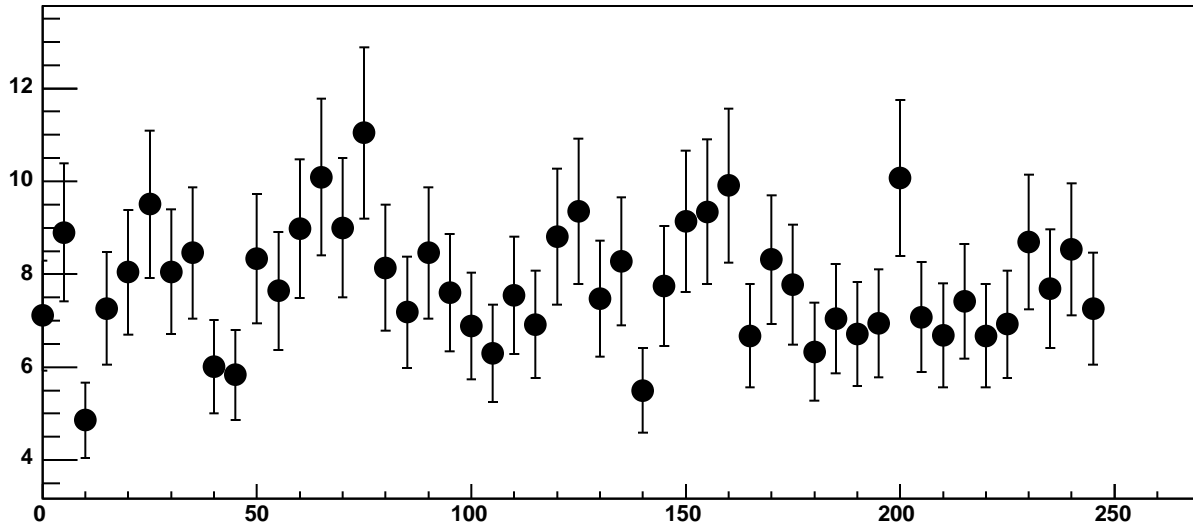


Chip 6, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

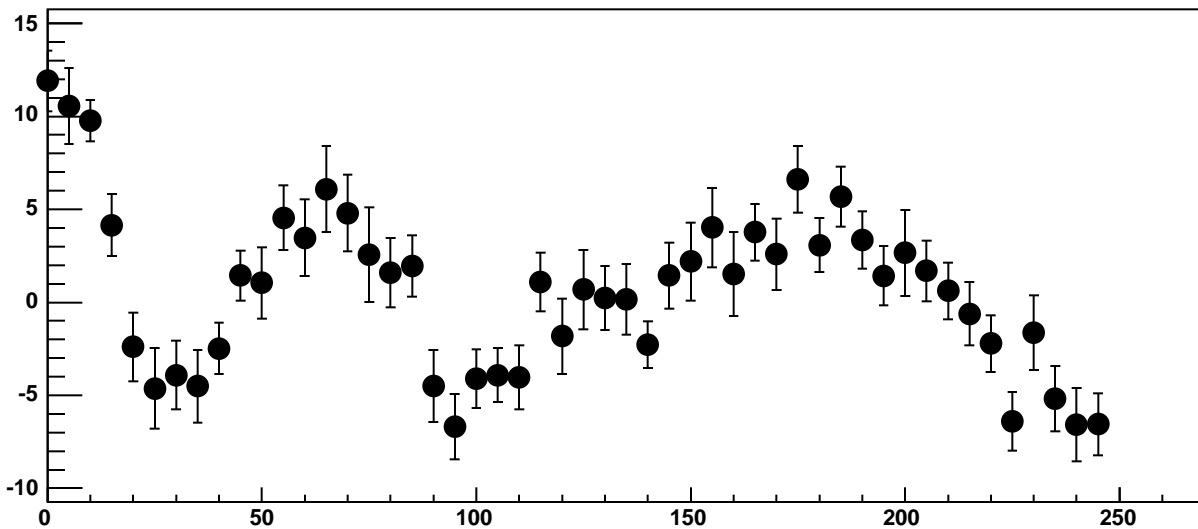


χ^2 / ndf	187.6 / 41
p0	-219.6 ± 0.6114
p1	96.52 ± 0.6785
p2	$-4.141\text{e}+07 \pm 7.204\text{e}+05$
p3	$2.571\text{e}+07 \pm 3.226\text{e}+05$
p4	1.552 ± 0.018

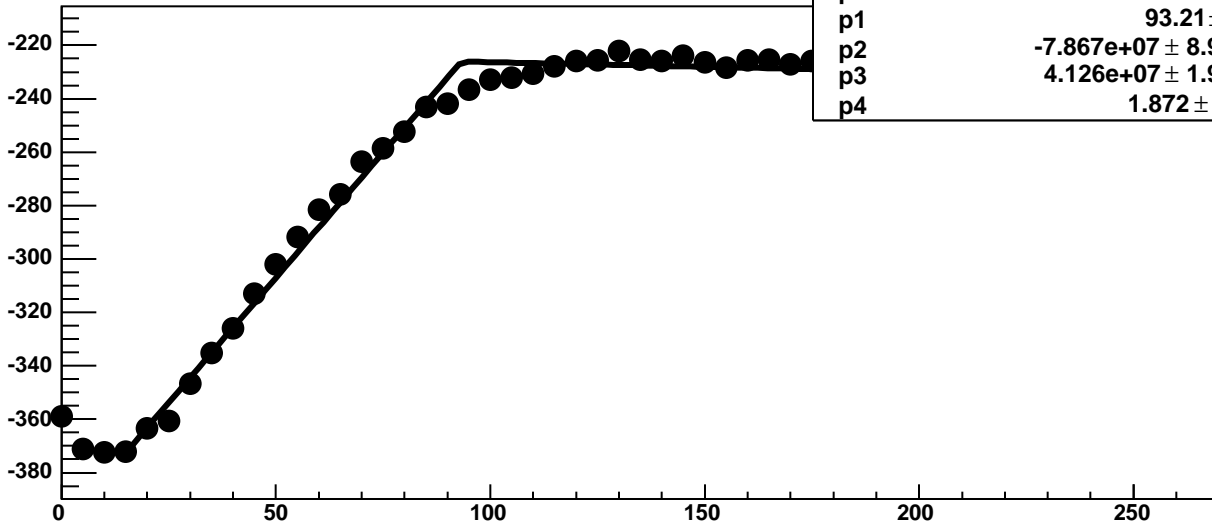
Chip 6, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

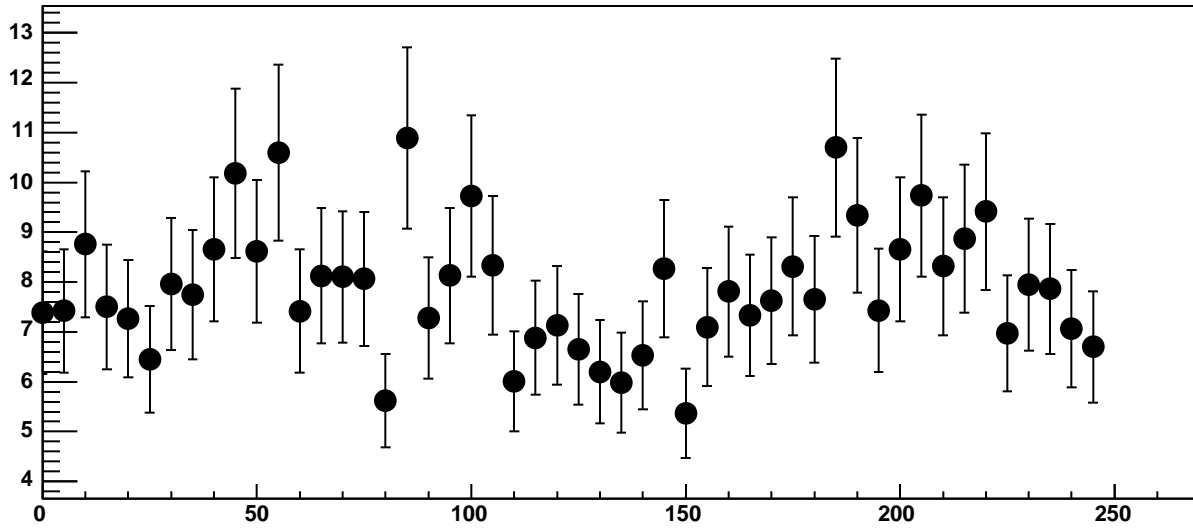


Chip 6, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

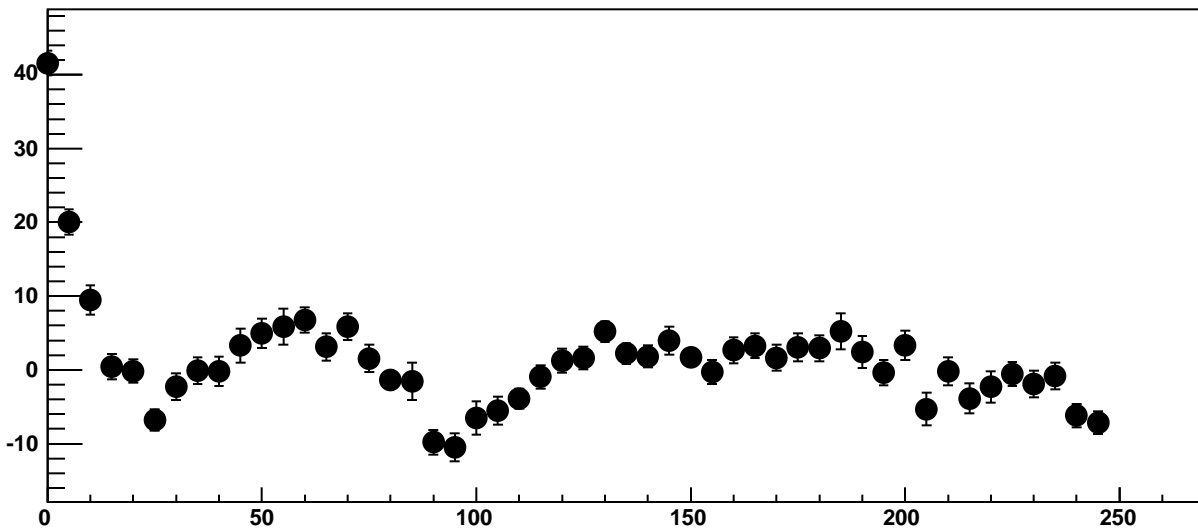


χ^2 / ndf	232.1 / 41
p0	-226.1 ± 0.5916
p1	93.21 ± 0.5583
p2	$-7.867\text{e}+07 \pm 8.902\text{e}+05$
p3	$4.126\text{e}+07 \pm 1.994\text{e}+05$
p4	1.872 ± 0.01839

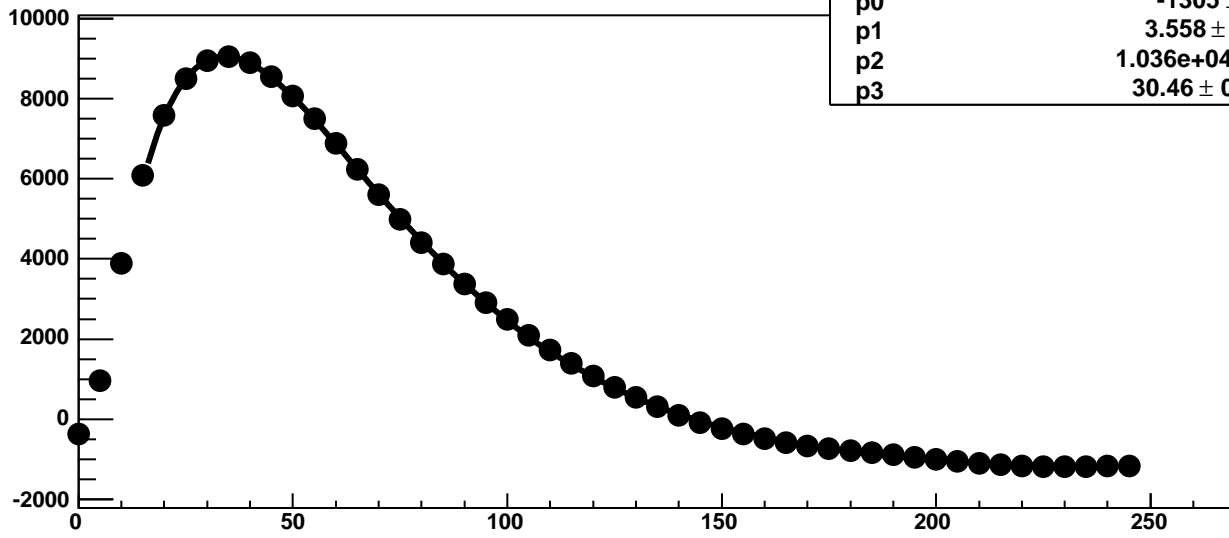
Chip 6, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

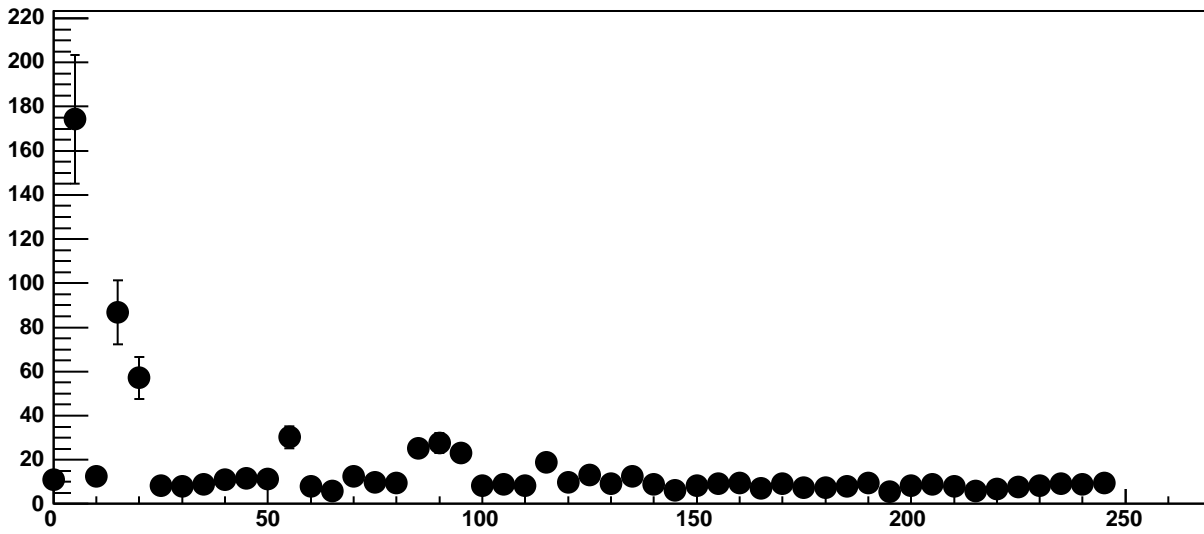


Chip 6, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold

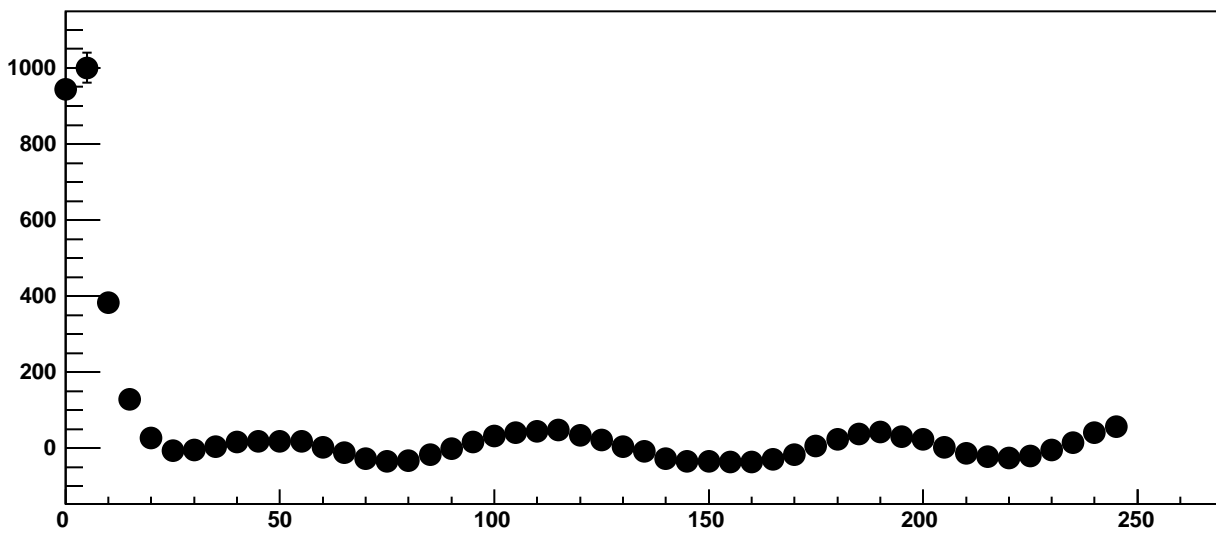


χ^2 / ndf	7410 / 42
p0	-1305 ± 0.6058
p1	3.558 ± 0.01387
p2	1.036e+04 ± 0.932
p3	30.46 ± 0.007332

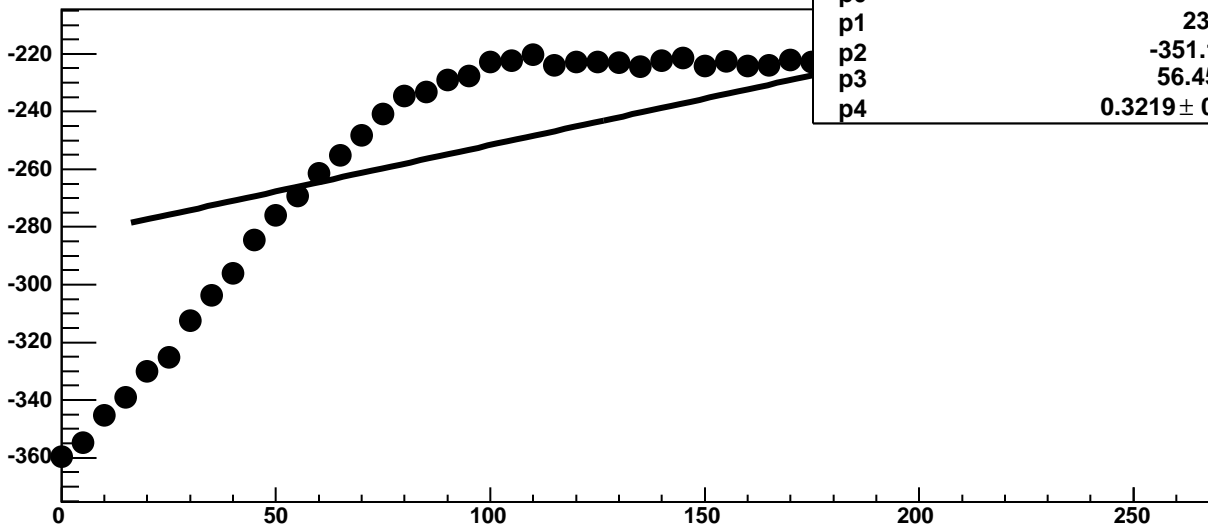
Chip 6, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold

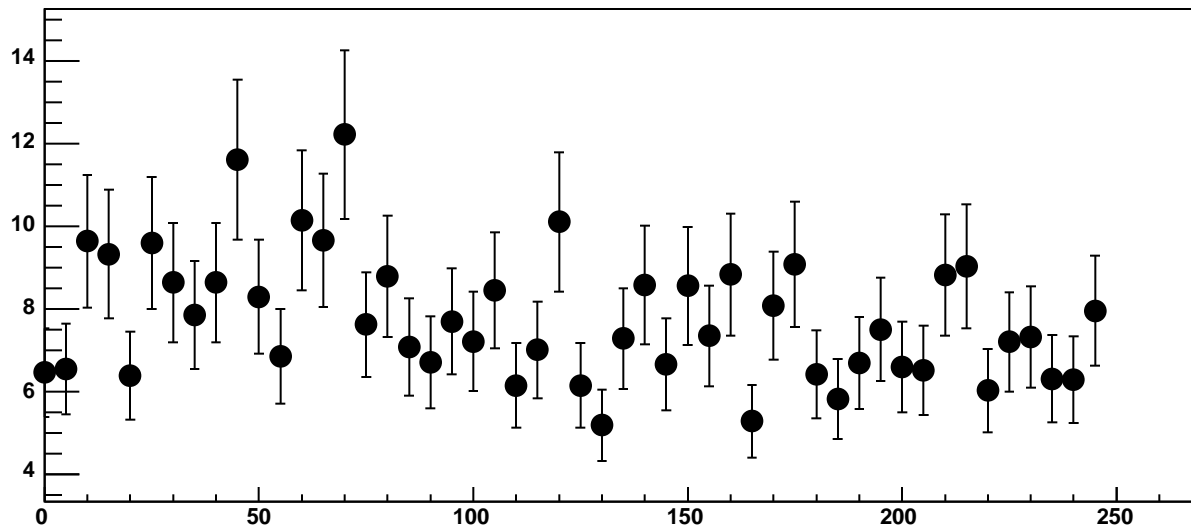


Chip 6, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

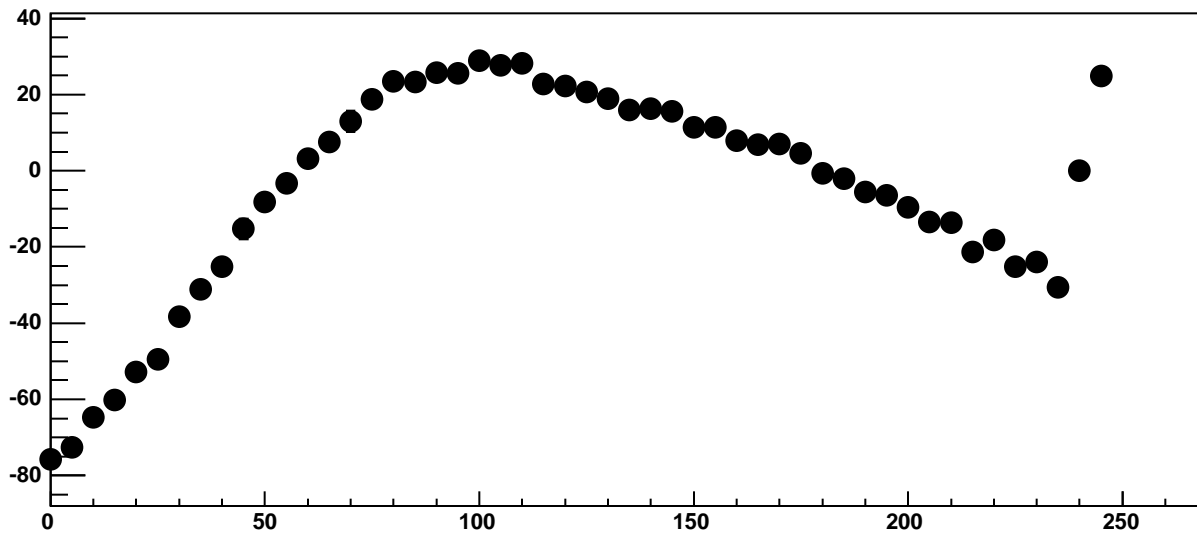


χ^2 / ndf	7908 / 41
p0	-208.1 ± 1.112
p1	235 ± 3.14
p2	-351.1 ± 205.3
p3	56.45 ± 28.41
p4	0.3219 ± 0.004015

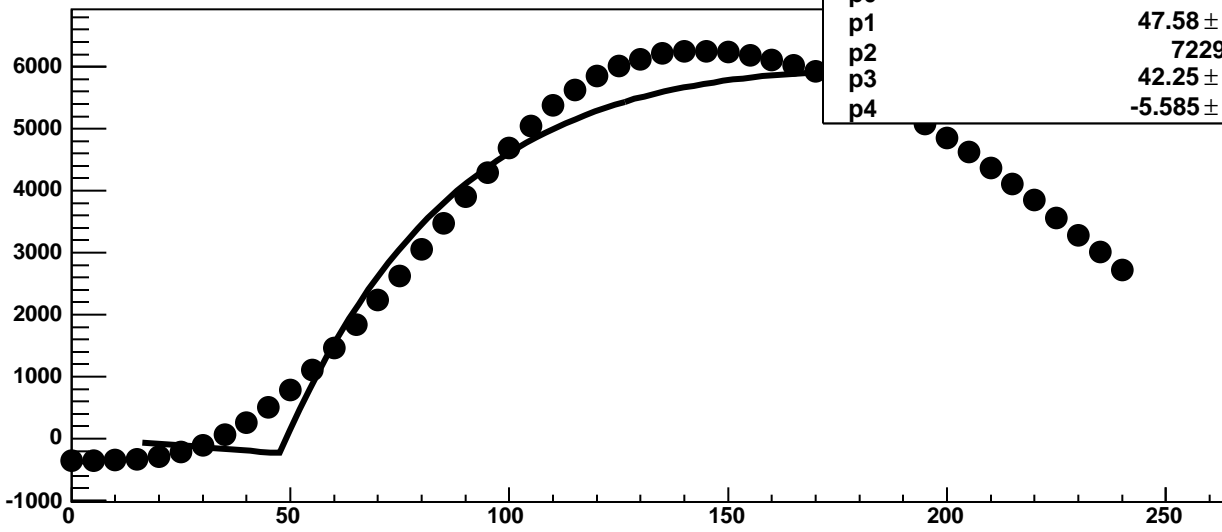
Chip 6, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

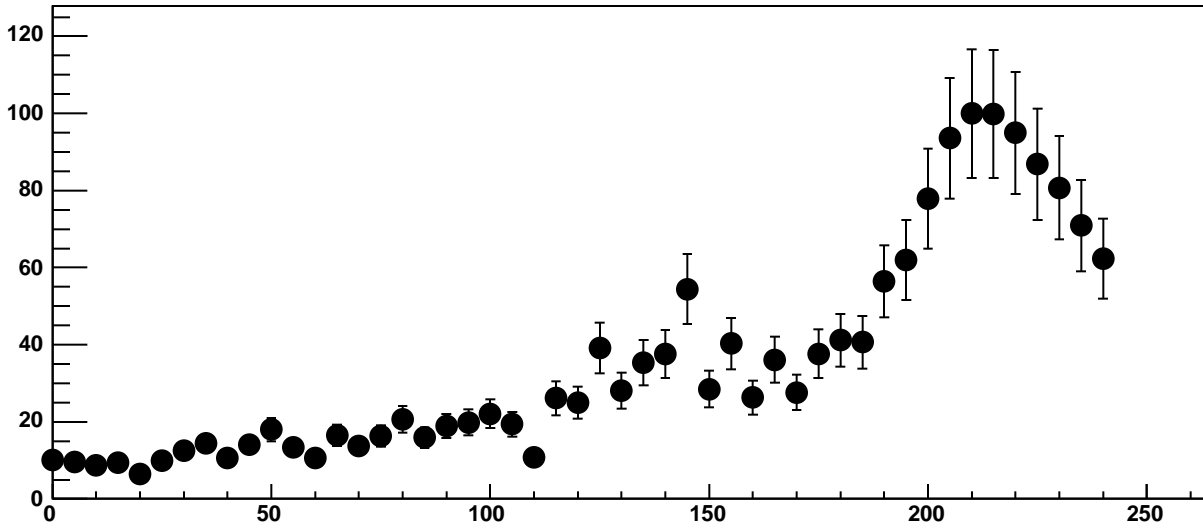


Chip 6, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

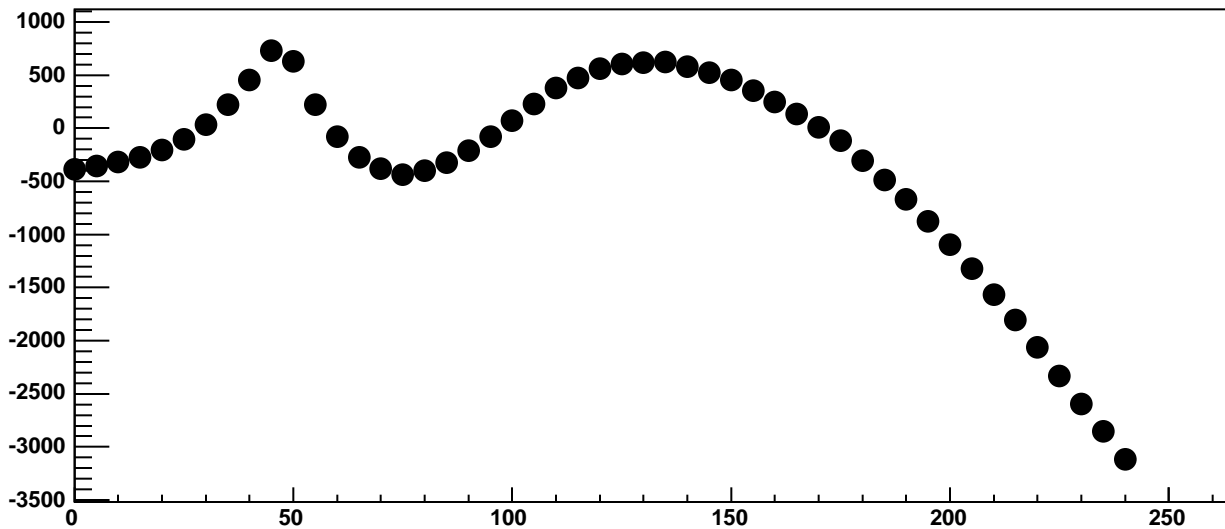


χ^2 / ndf	4.354e+05 / 41
p0	-235 ± 1.513
p1	47.58 ± 0.01779
p2	7229 ± 10.52
p3	42.25 ± 0.06526
p4	-5.585 ± 0.06099

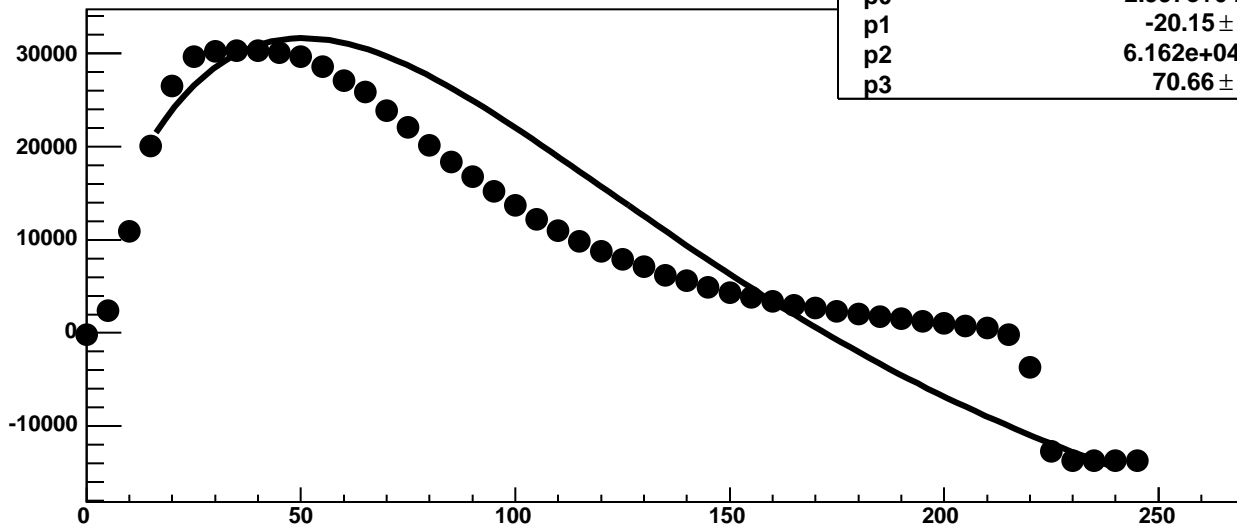
Chip 6, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold

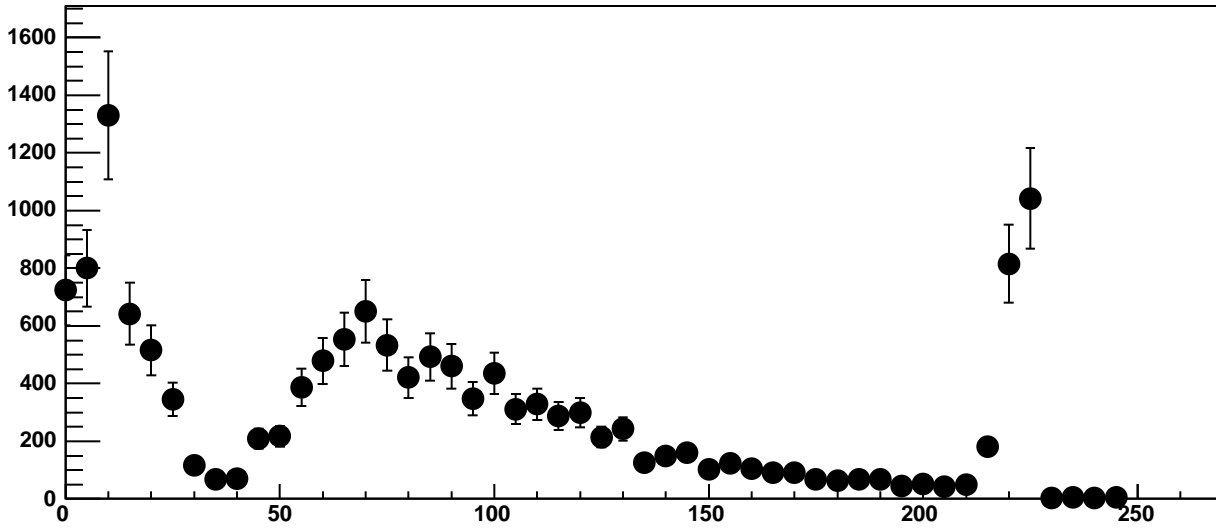


Chip 6, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold

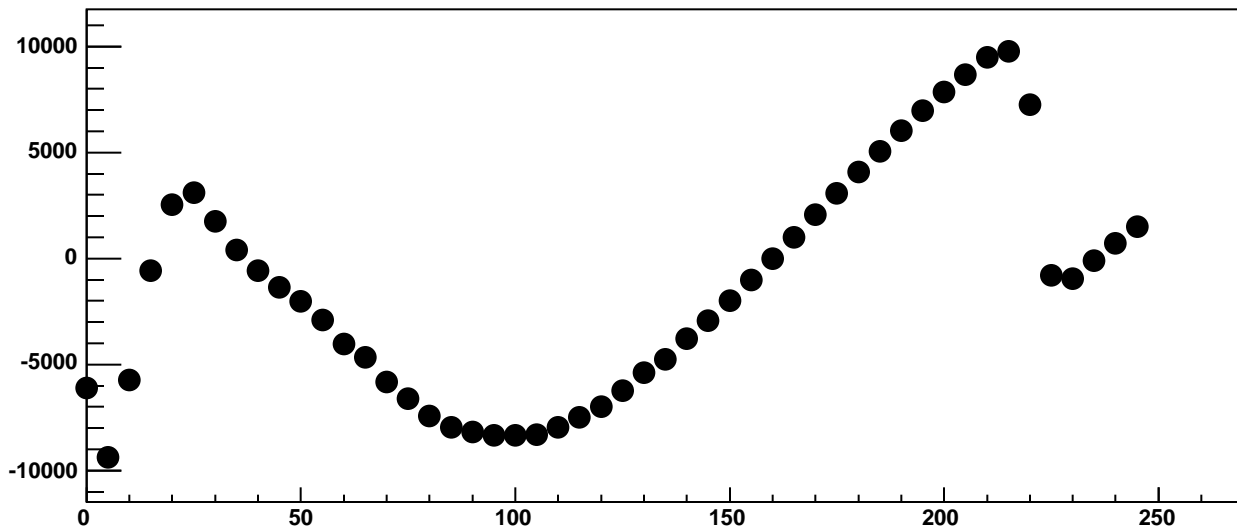


χ^2 / ndf	4.769e+06 / 42
p0	-2.997e+04 ± 21.06
p1	-20.15 ± 0.08945
p2	6.162e+04 ± 16.02
p3	70.66 ± 0.05267

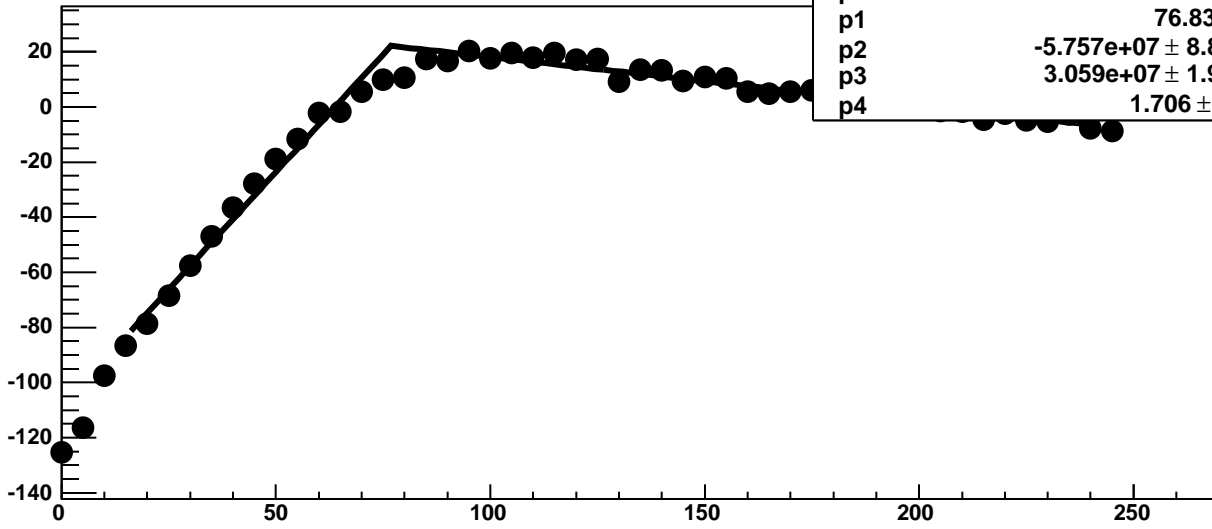
Chip 6, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold

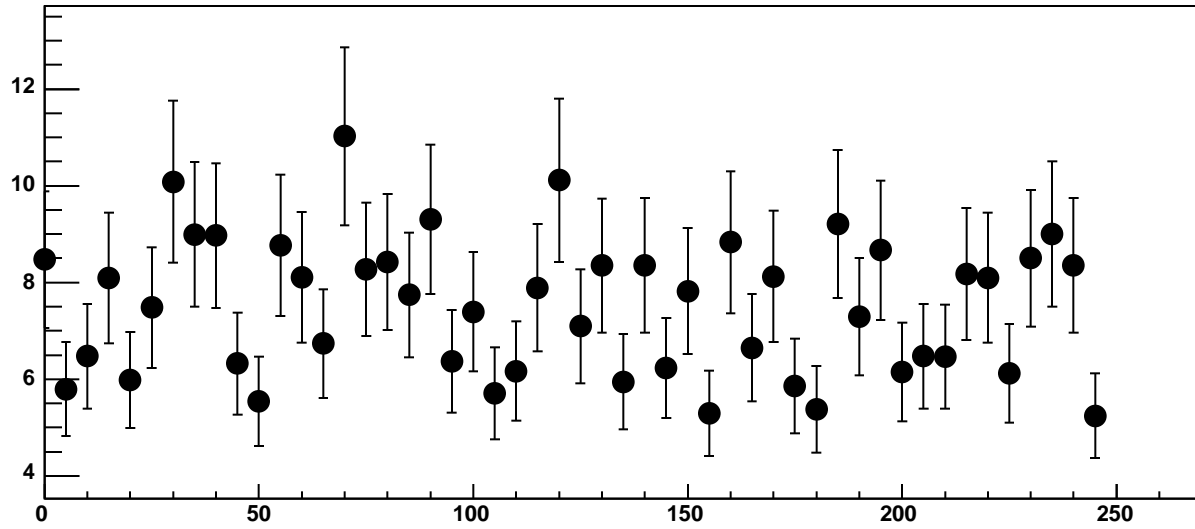


Chip 6, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

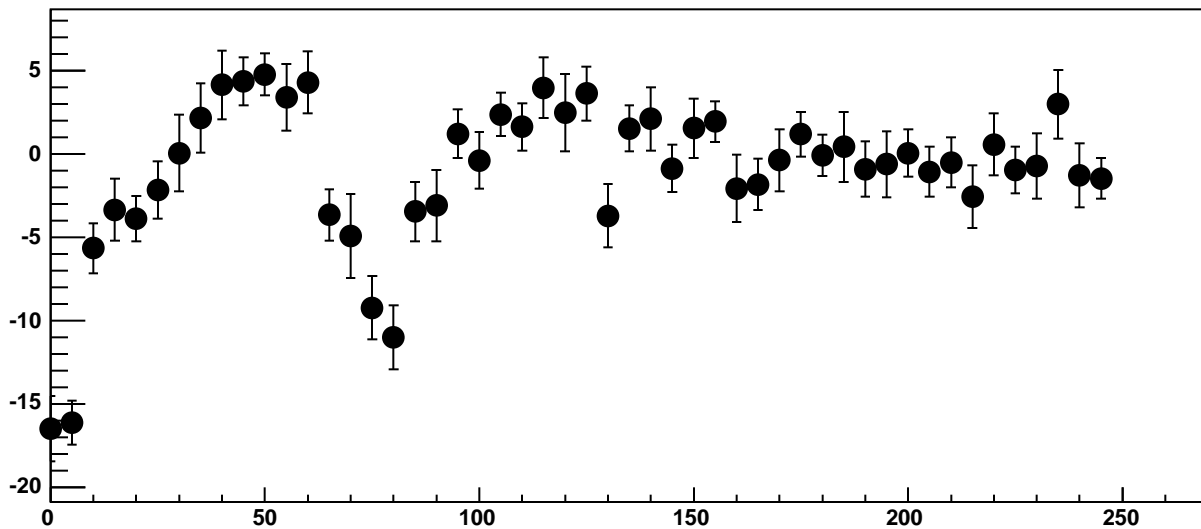


χ^2 / ndf	156.5 / 41
p0	22.2 ± 0.5454
p1	76.83 ± 0.619
p2	$-5.757\text{e}+07 \pm 8.897\text{e}+05$
p3	$3.059\text{e}+07 \pm 1.999\text{e}+05$
p4	1.706 ± 0.02661

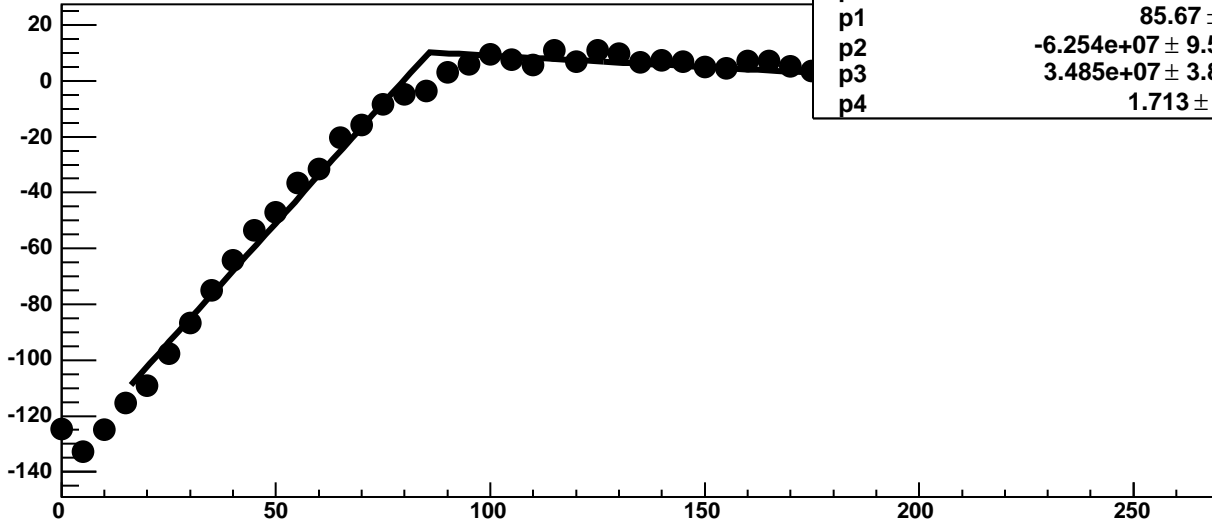
Chip 6, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold

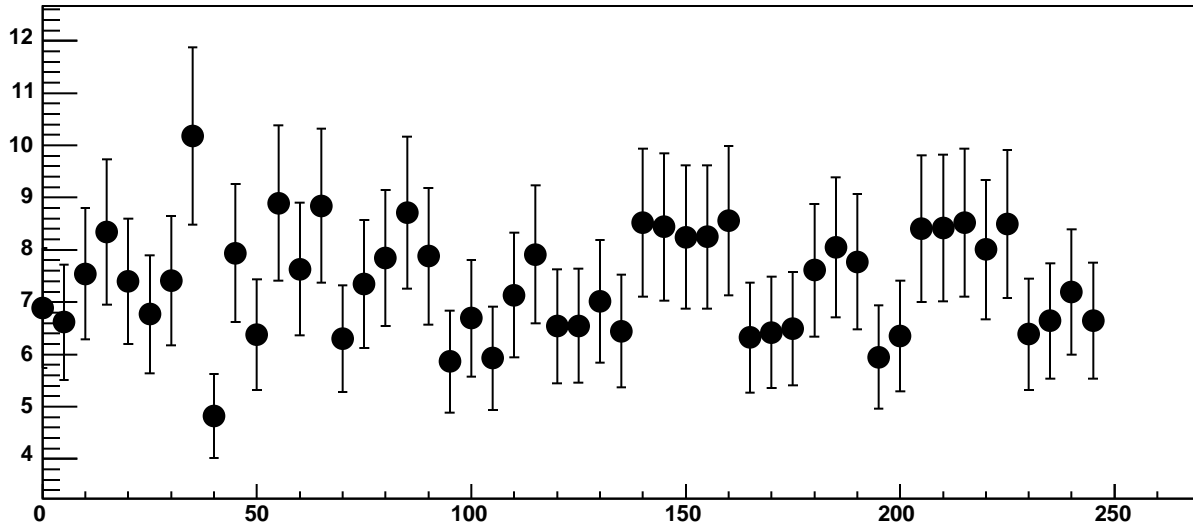


Chip 6, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

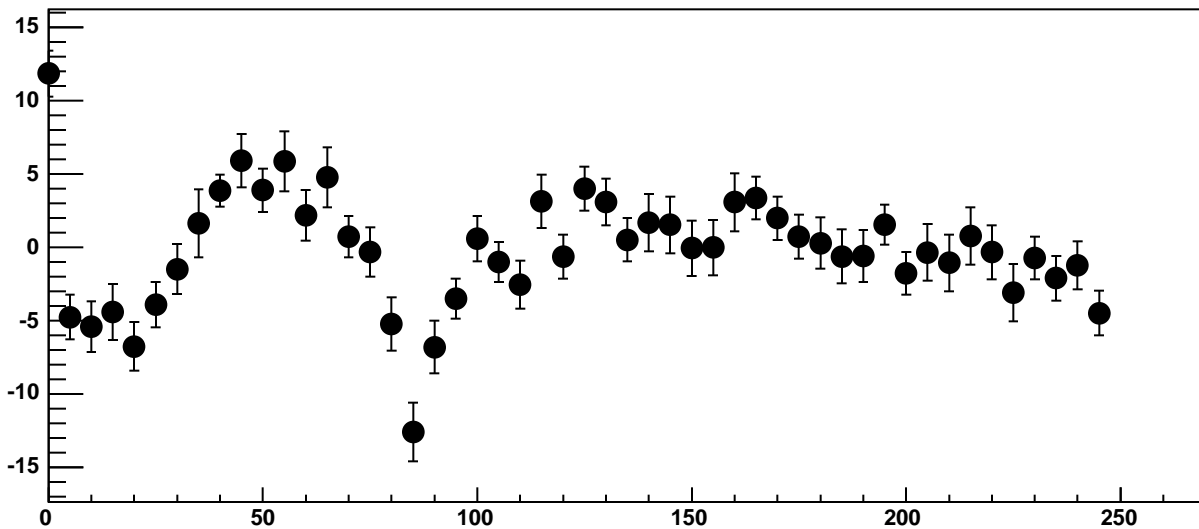


χ^2 / ndf	181.5 / 41
p0	10.22 ± 0.5503
p1	85.67 ± 0.5824
p2	$-6.254\text{e}+07 \pm 9.568\text{e}+05$
p3	$3.485\text{e}+07 \pm 3.852\text{e}+05$
p4	1.713 ± 0.02087

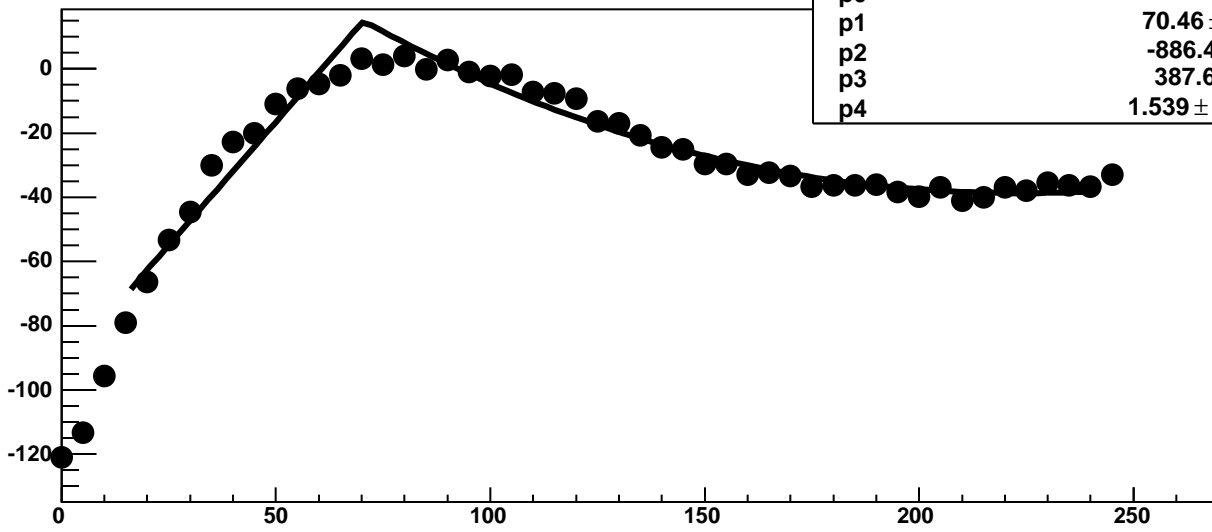
Chip 6, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

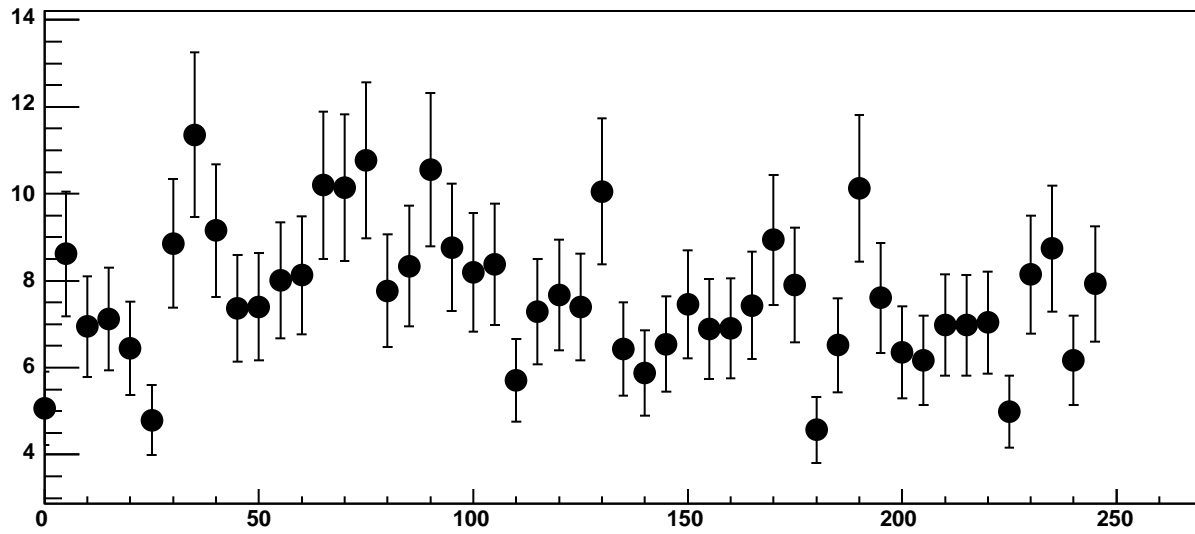


Chip 6, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold

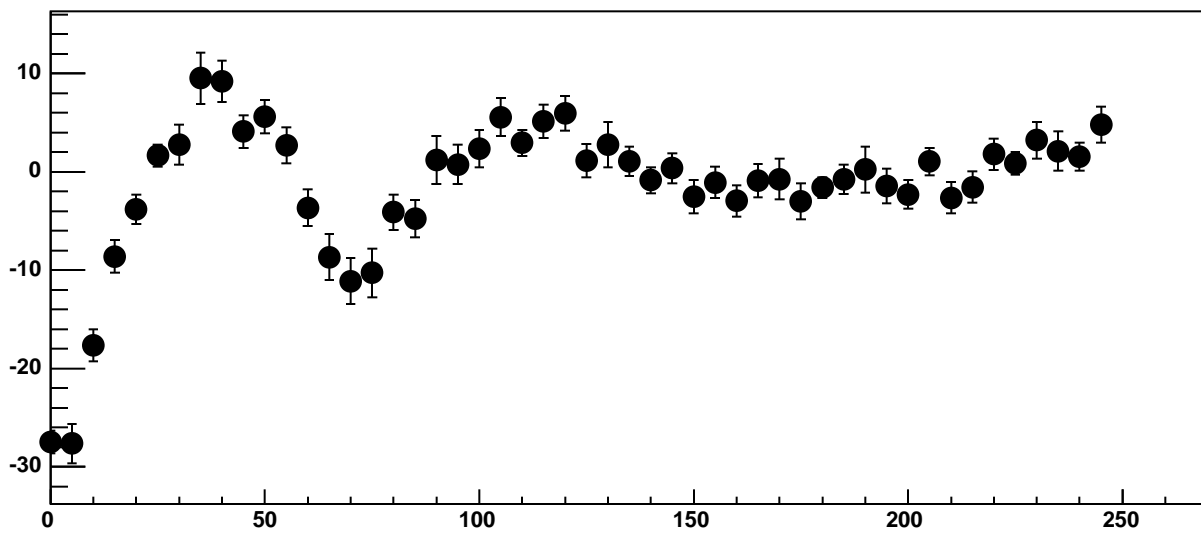


χ^2 / ndf	224.6 / 41
p0	14.99 ± 0.8017
p1	70.46 ± 0.6753
p2	-886.4 ± 48.32
p3	387.6 ± 22.94
p4	1.539 ± 0.02997

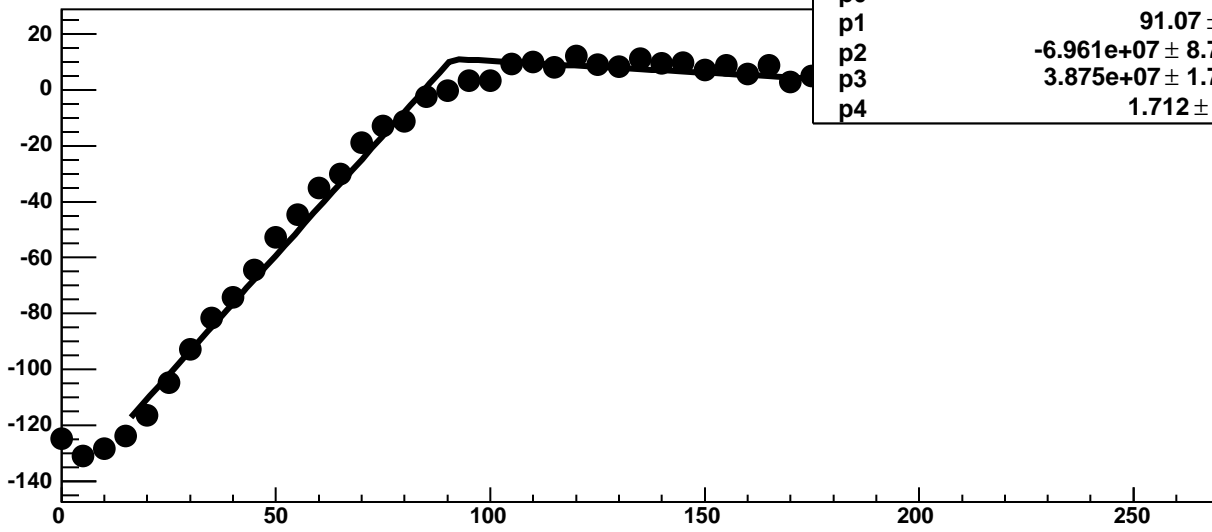
Chip 6, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold

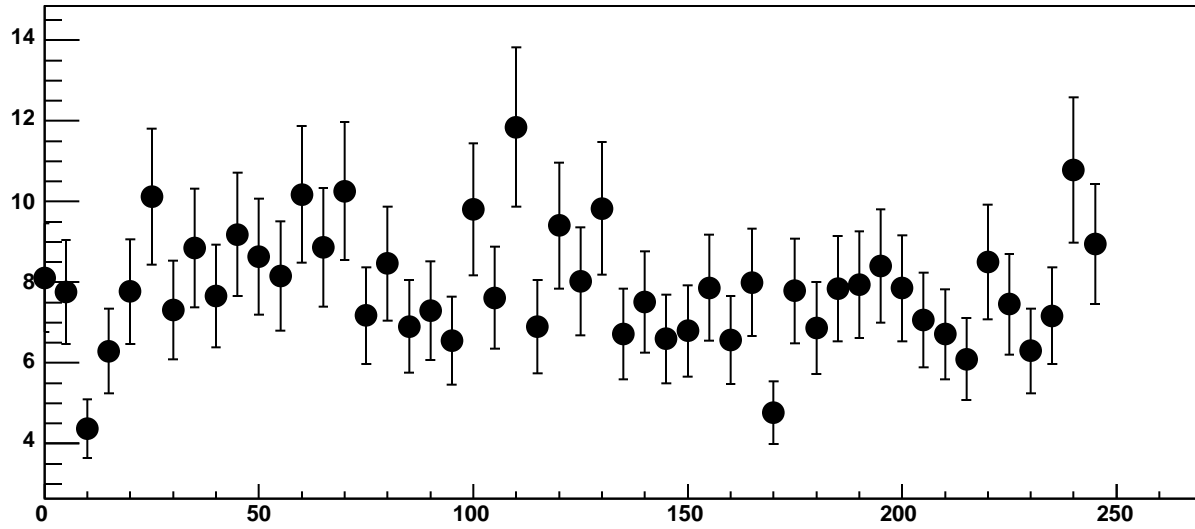


Chip 6, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold

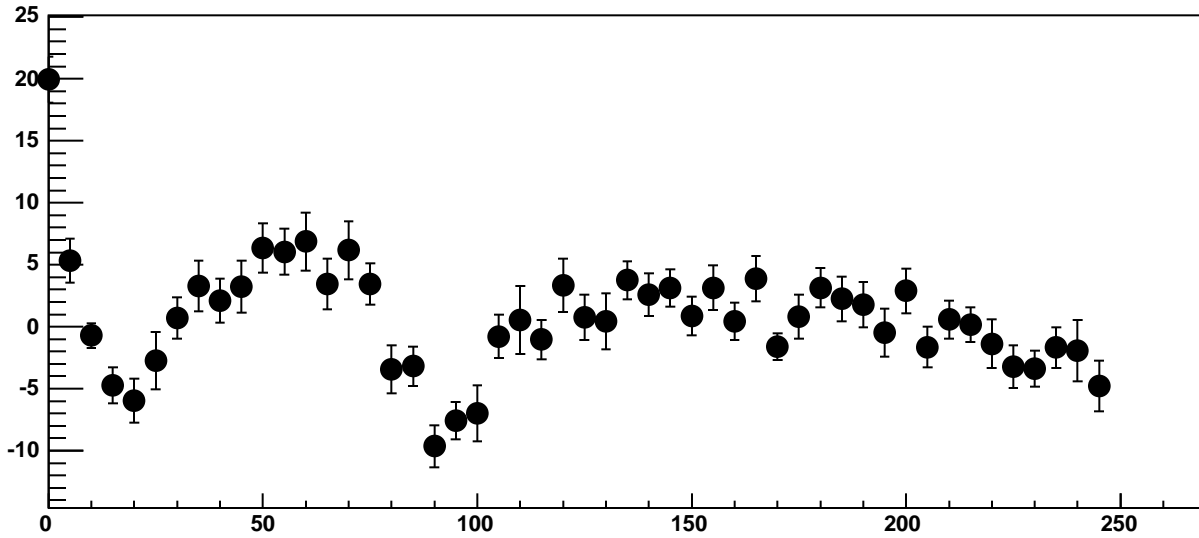


χ^2 / ndf	196.4 / 41
p0	11.11 ± 0.6678
p1	91.07 ± 0.6296
p2	$-6.961\text{e}+07 \pm 8.732\text{e}+05$
p3	$3.875\text{e}+07 \pm 1.716\text{e}+05$
p4	1.712 ± 0.01896

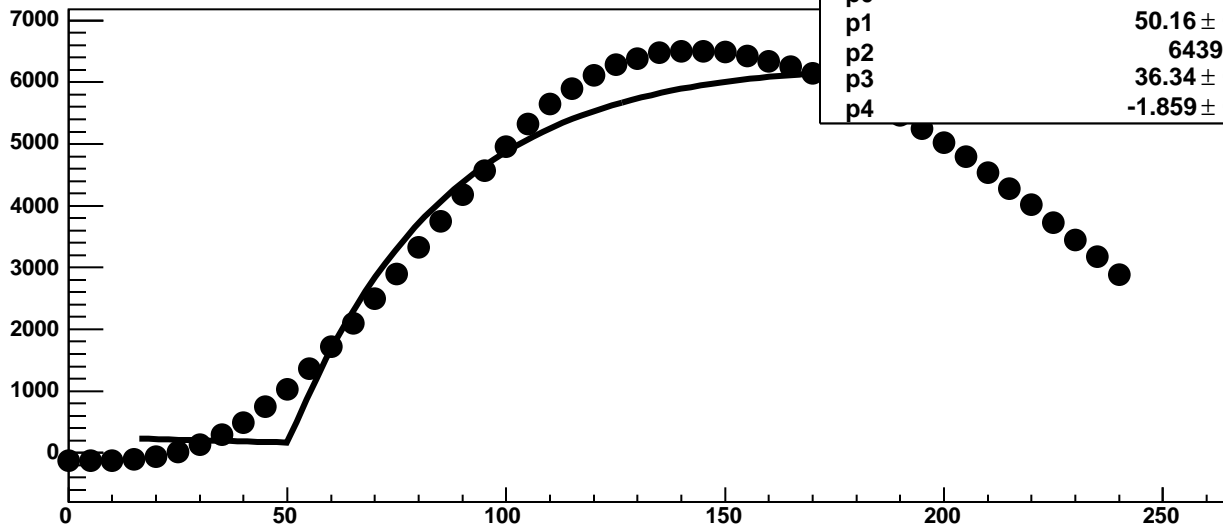
Chip 6, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

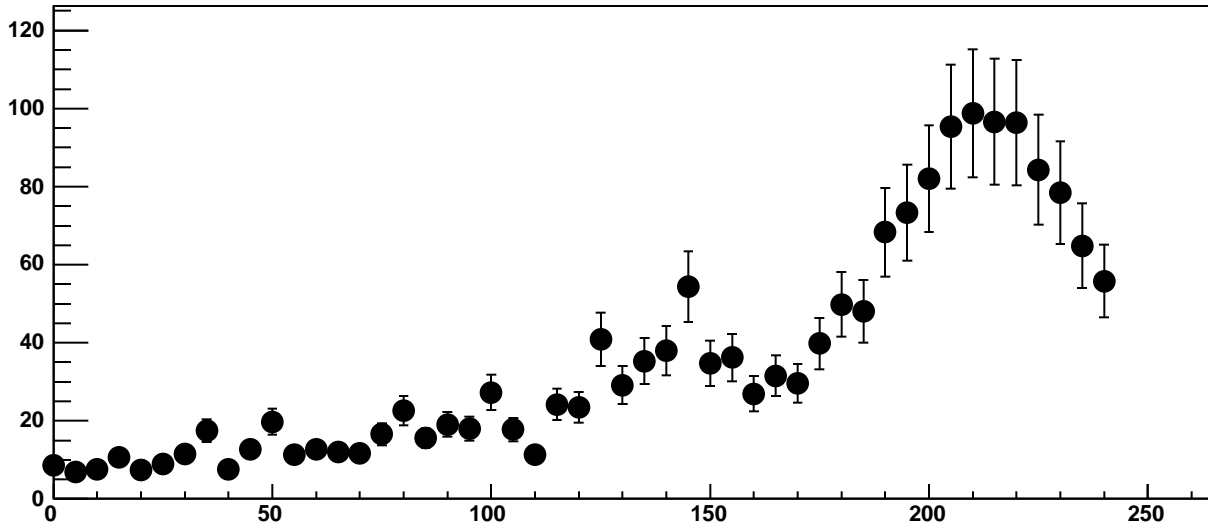


Chip 6, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

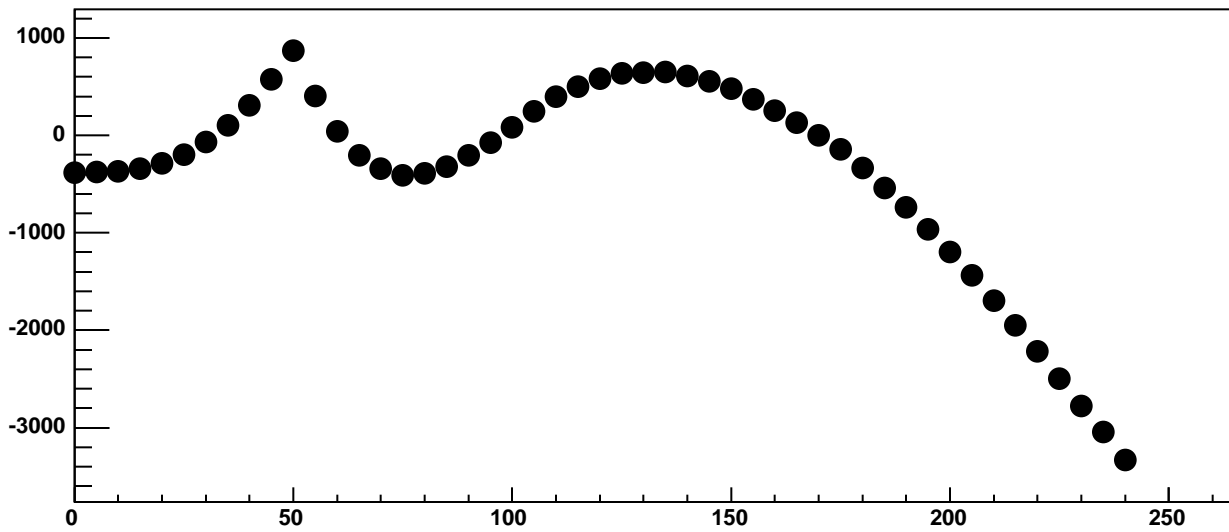


χ^2 / ndf	5.104e+05 / 41
p0	166 ± 1.368
p1	50.16 ± 0.01713
p2	6439 ± 8.466
p3	36.34 ± 0.05547
p4	-1.859 ± 0.05332

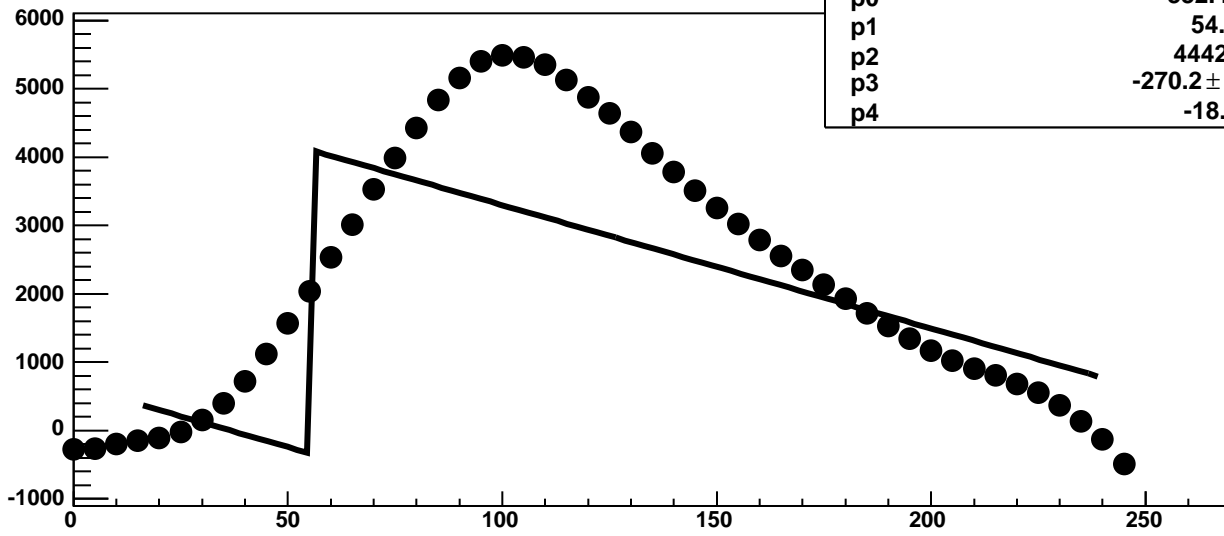
Chip 6, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

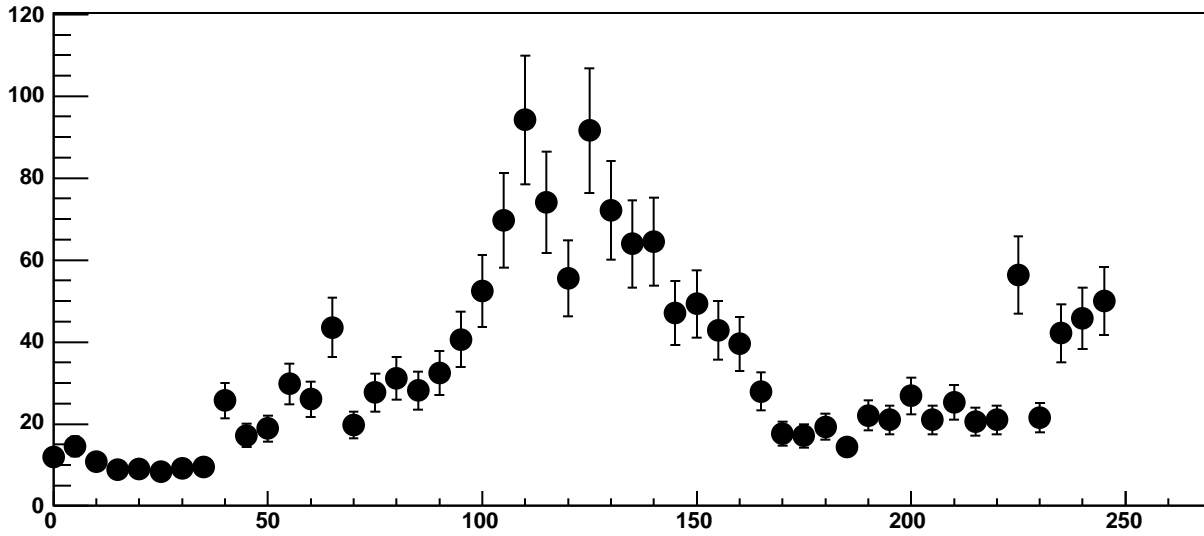


Chip 6, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold

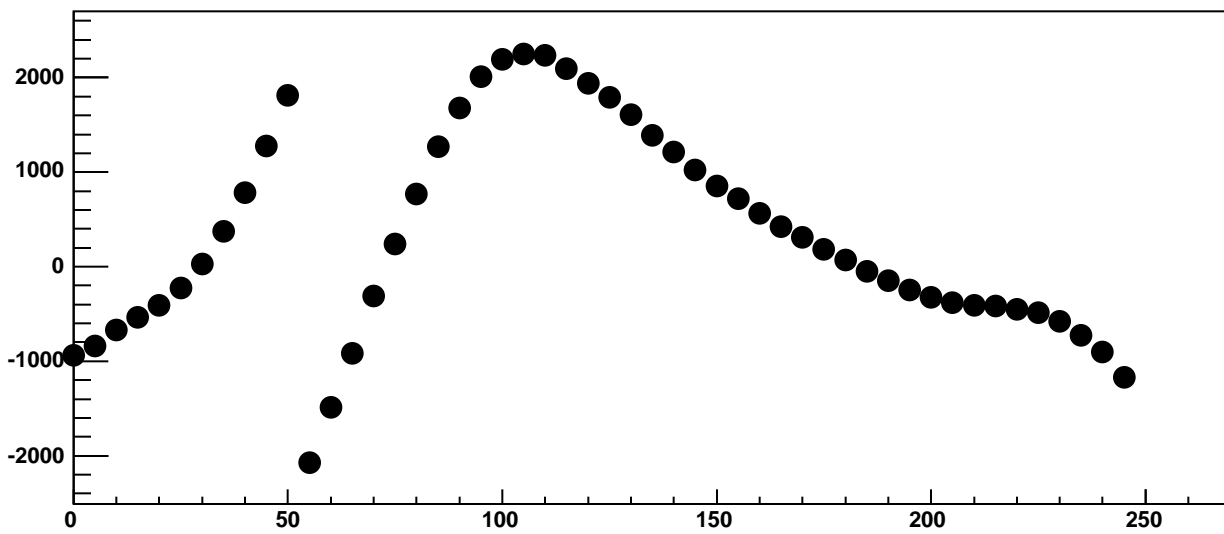


χ^2 / ndf	9.971e+05 / 41
p0	-332.4 ± 1.435
p1	54.96 ± 1
p2	4442 ± 3.203
p3	-270.2 ± 0.01329
p4	-18.05 ± 1

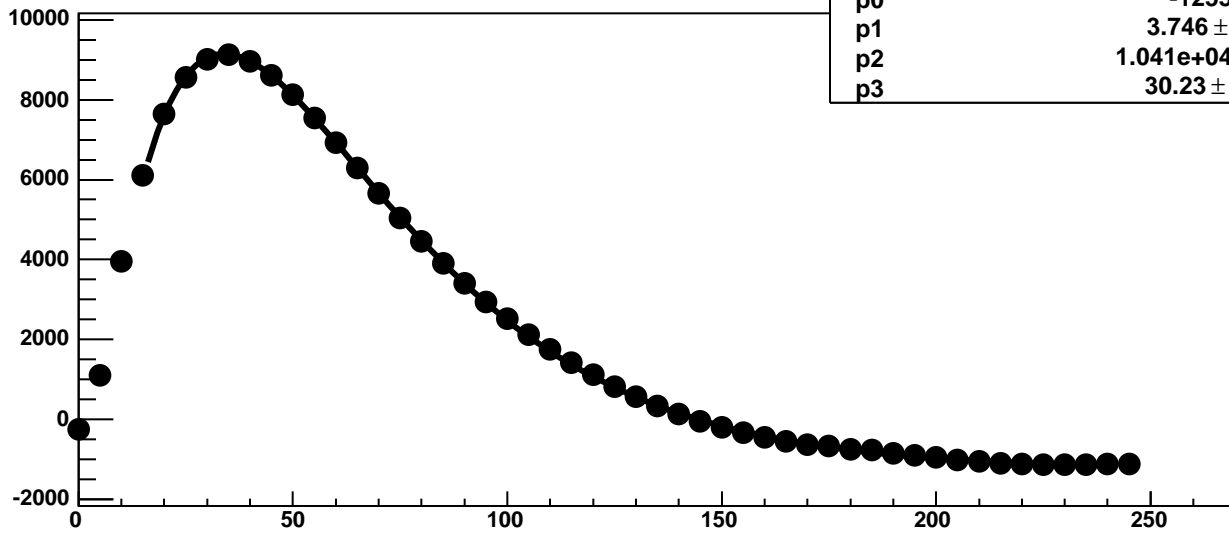
Chip 6, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

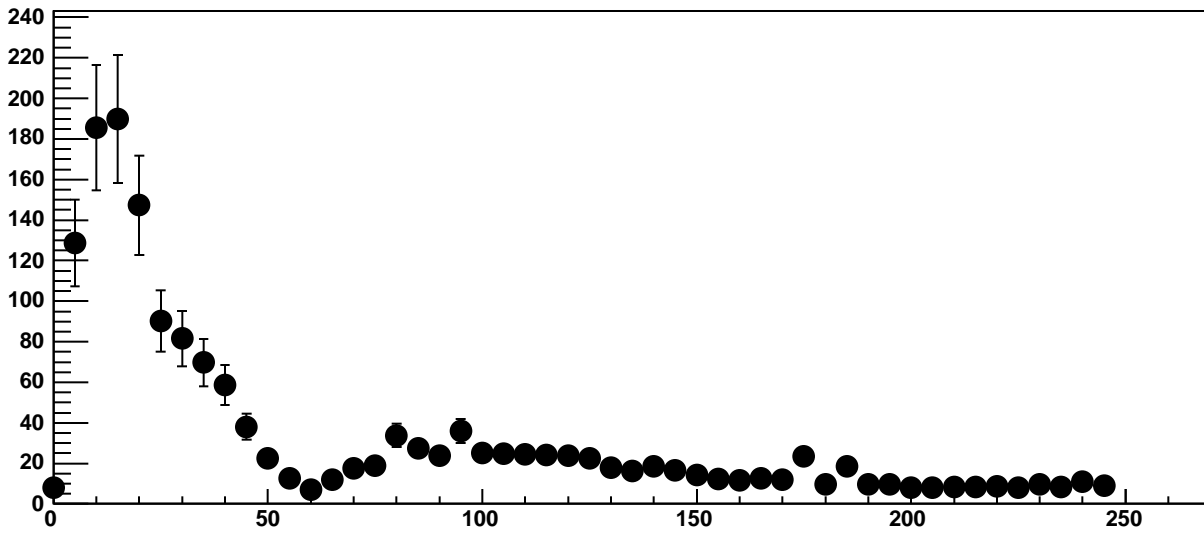


Chip 6, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold

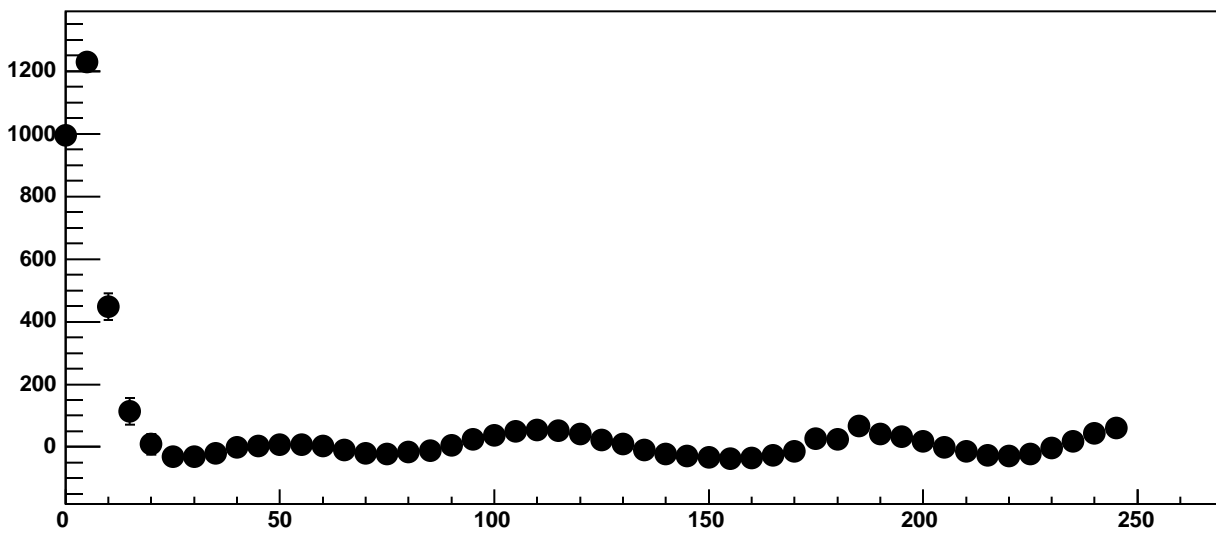


χ^2 / ndf	3163 / 42
p0	-1255 ± 0.838
p1	3.746 ± 0.04871
p2	$1.041\text{e}+04 \pm 5.149$
p3	30.23 ± 0.01516

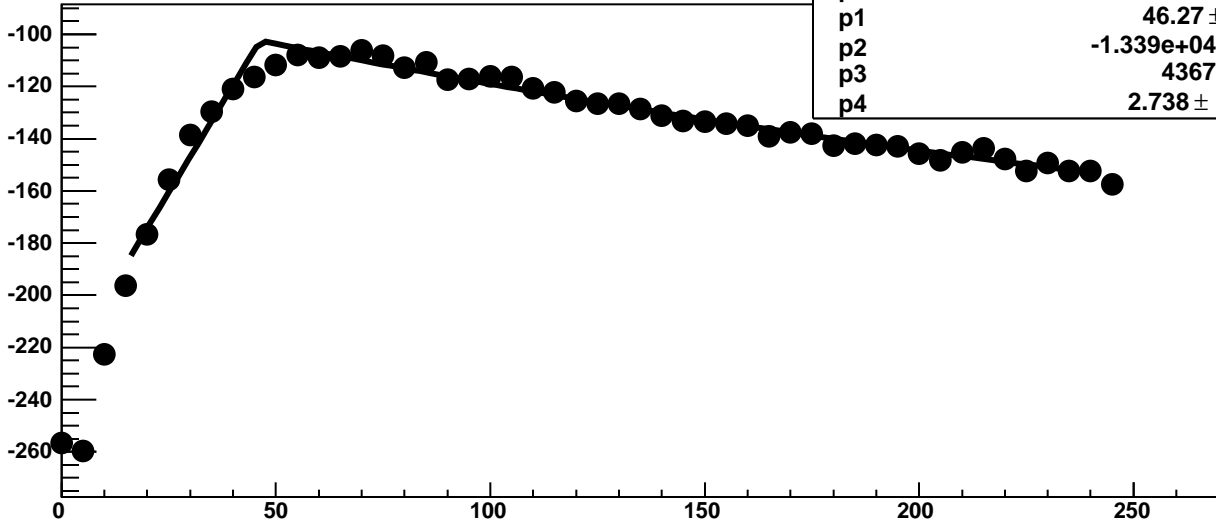
Chip 6, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold

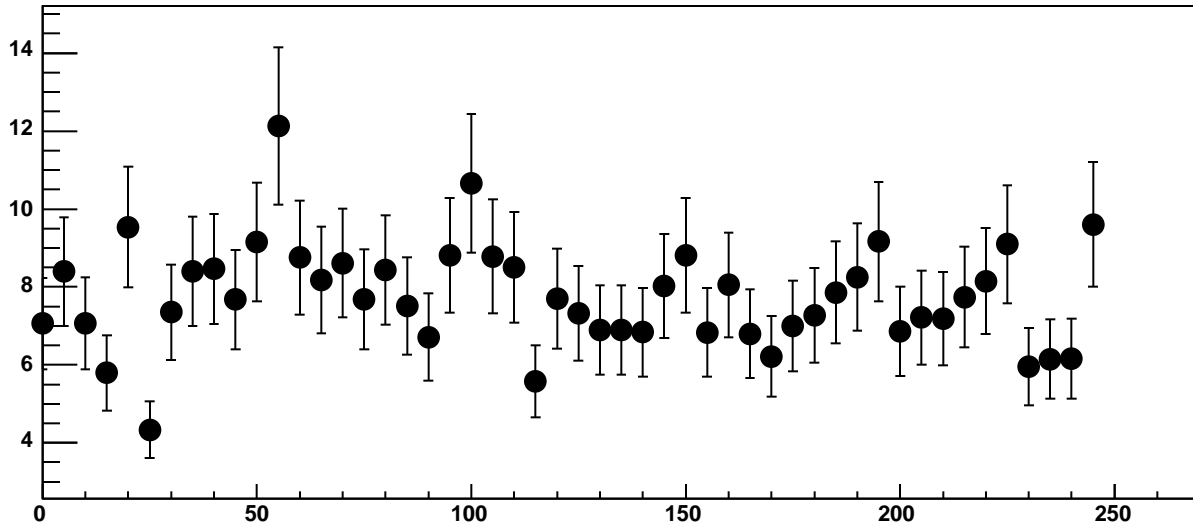


Chip 6, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

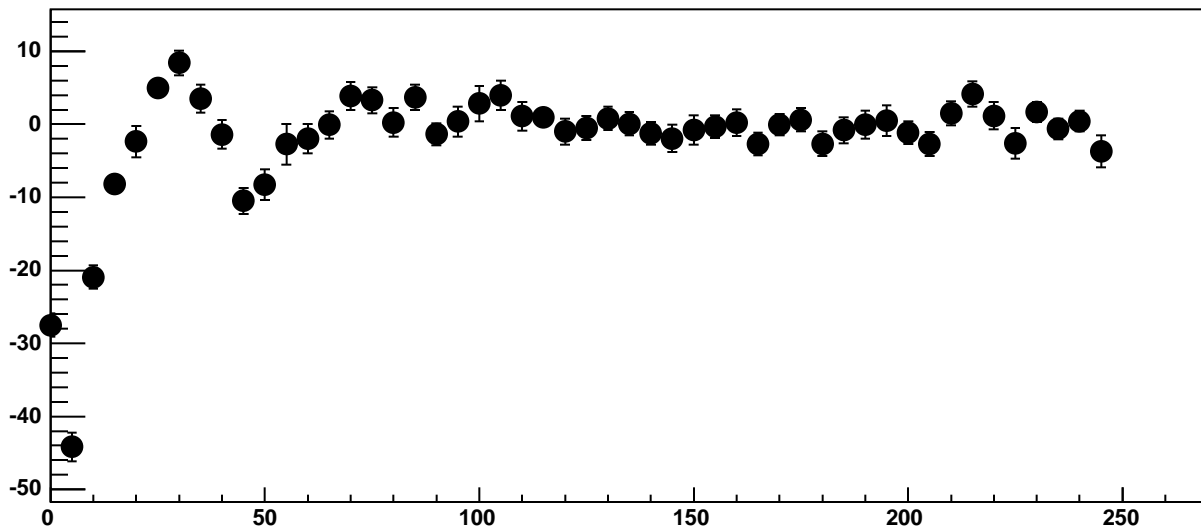


χ^2 / ndf	186.6 / 41
p0	-102.4 ± 0.8824
p1	46.27 ± 0.5272
p2	$-1.339\text{e}+04 \pm 3767$
p3	4367 ± 1248
p4	2.738 ± 0.06185

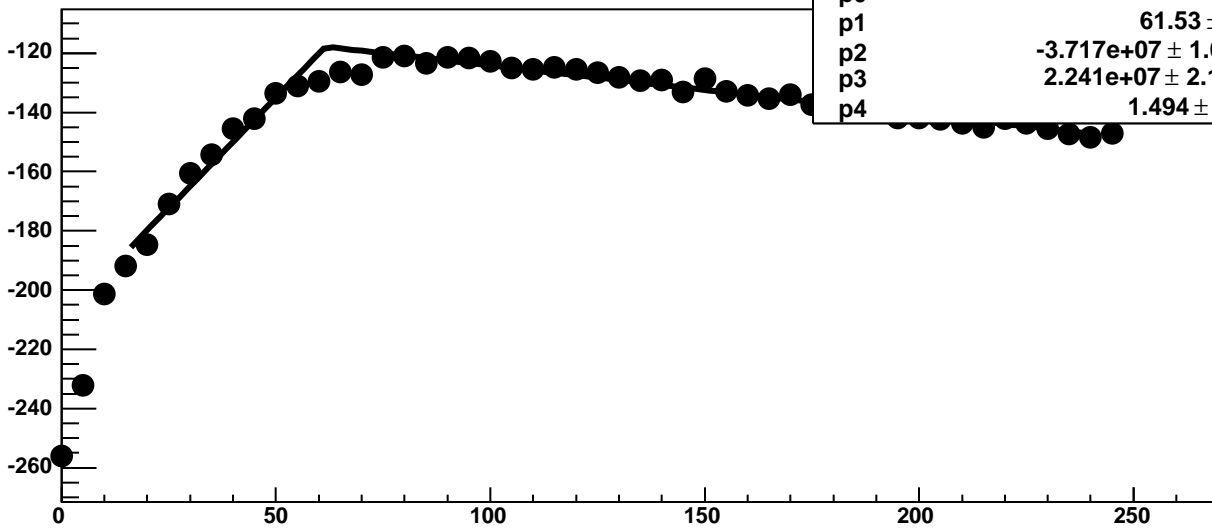
Chip 6, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

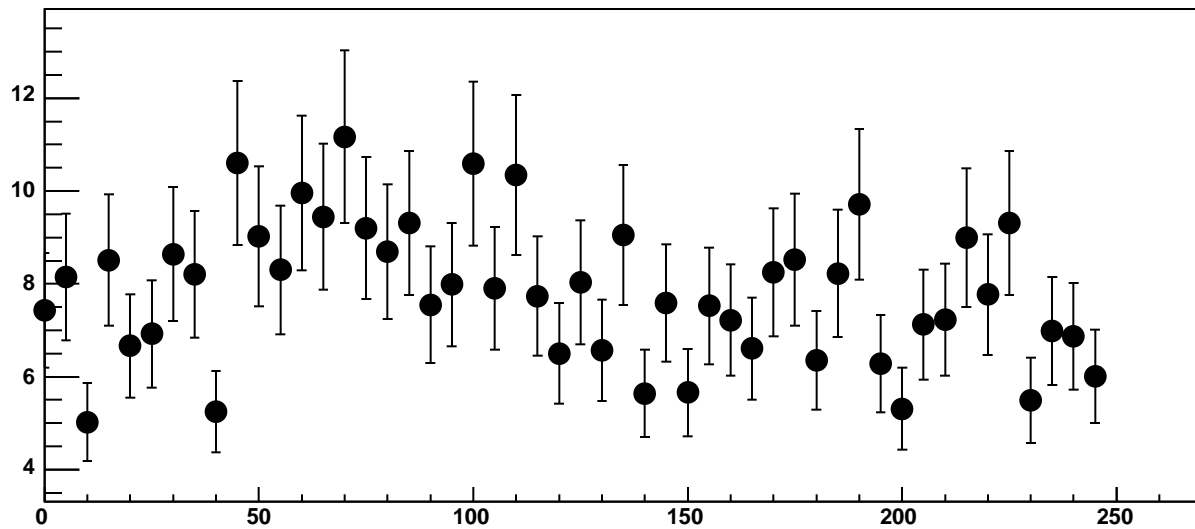


Chip 6, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

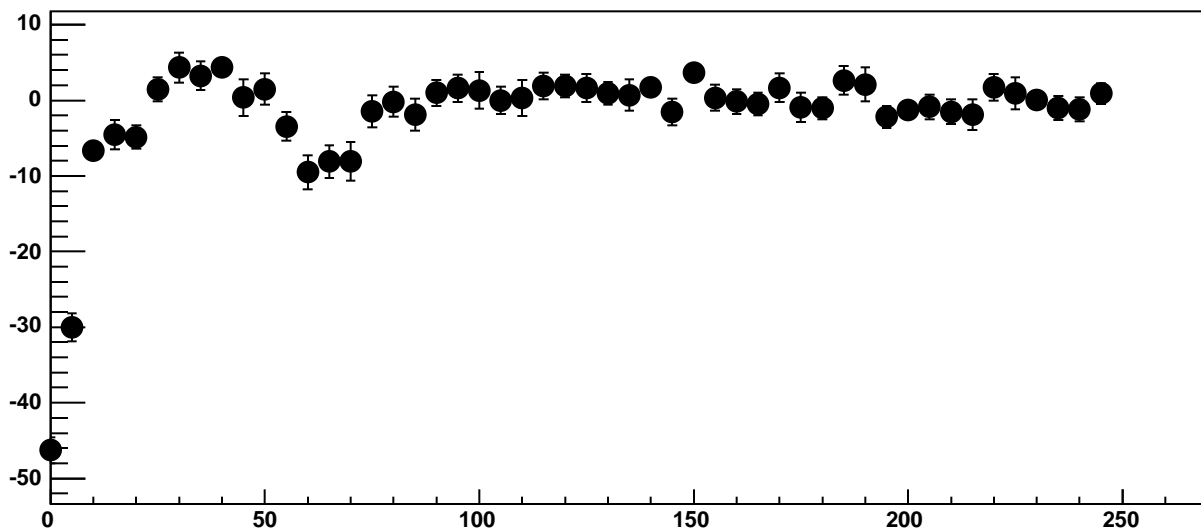


χ^2 / ndf	111.9 / 41
p0	-117.8 ± 0.5905
p1	61.53 ± 0.8345
p2	$-3.717\text{e}+07 \pm 1.013\text{e}+06$
p3	$2.241\text{e}+07 \pm 2.151\text{e}+05$
p4	1.494 ± 0.04215

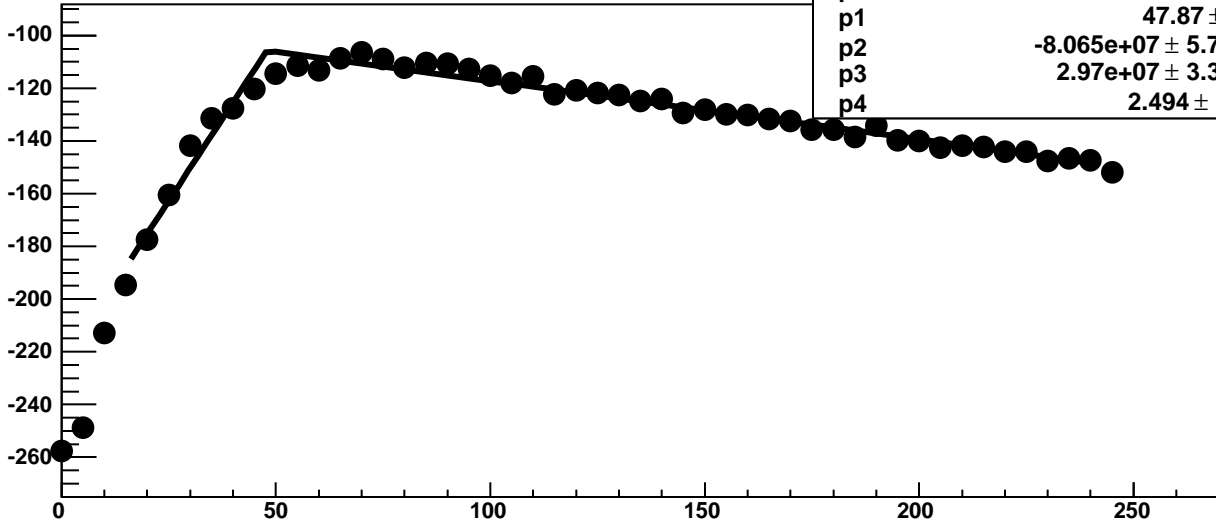
Chip 6, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



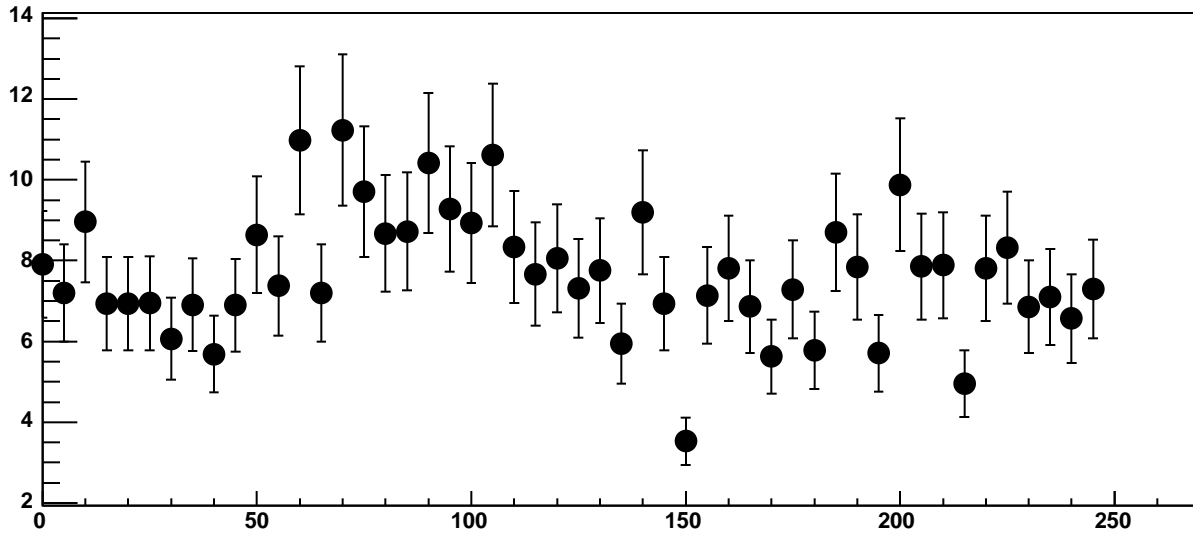
Chip 6, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold



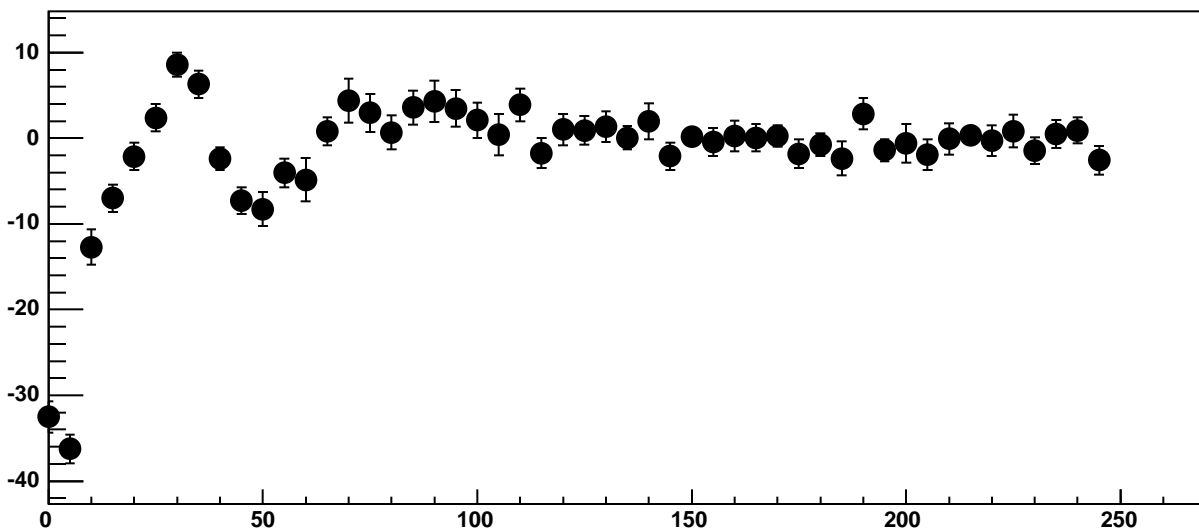
Chip 6, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold



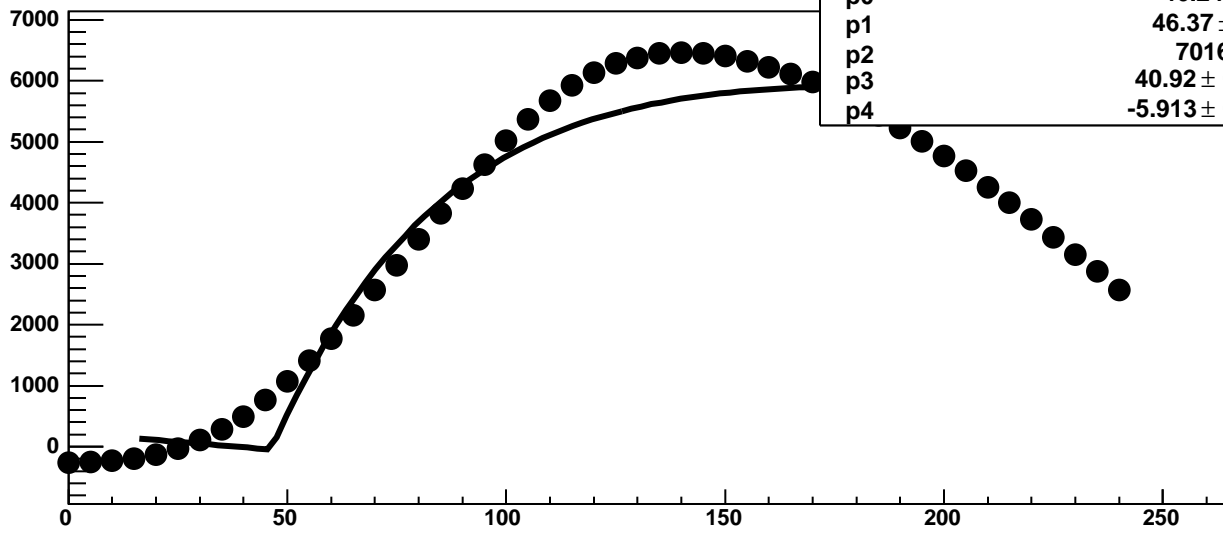
Chip 6, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold

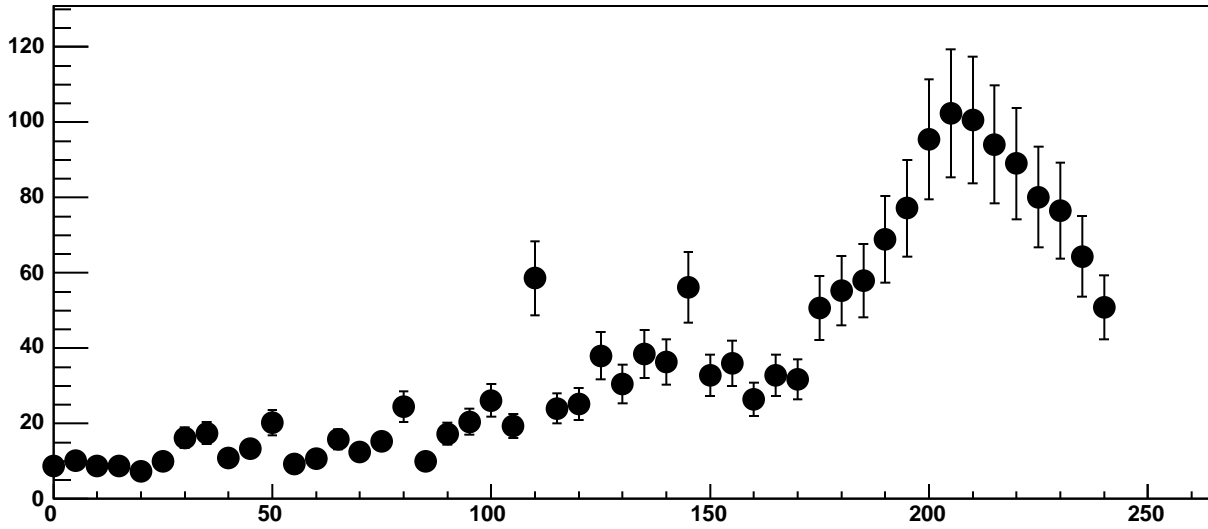


Chip 6, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold

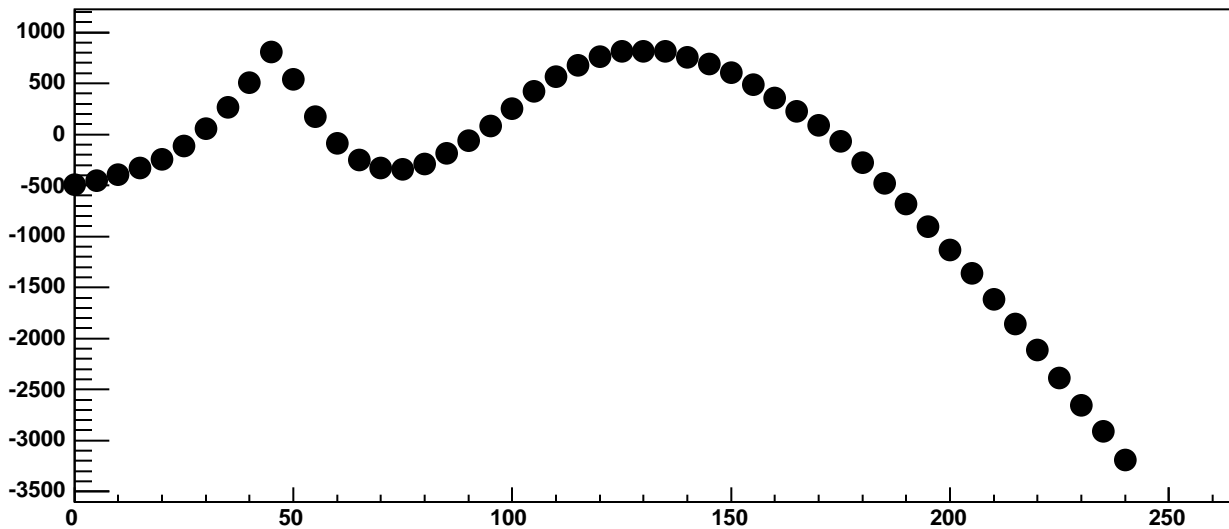


χ^2 / ndf	5.154e+05 / 41
p0	-46.24 ± 1.474
p1	46.37 ± 0.0181
p2	7016 ± 10.3
p3	40.92 ± 0.06473
p4	-5.913 ± 0.05996

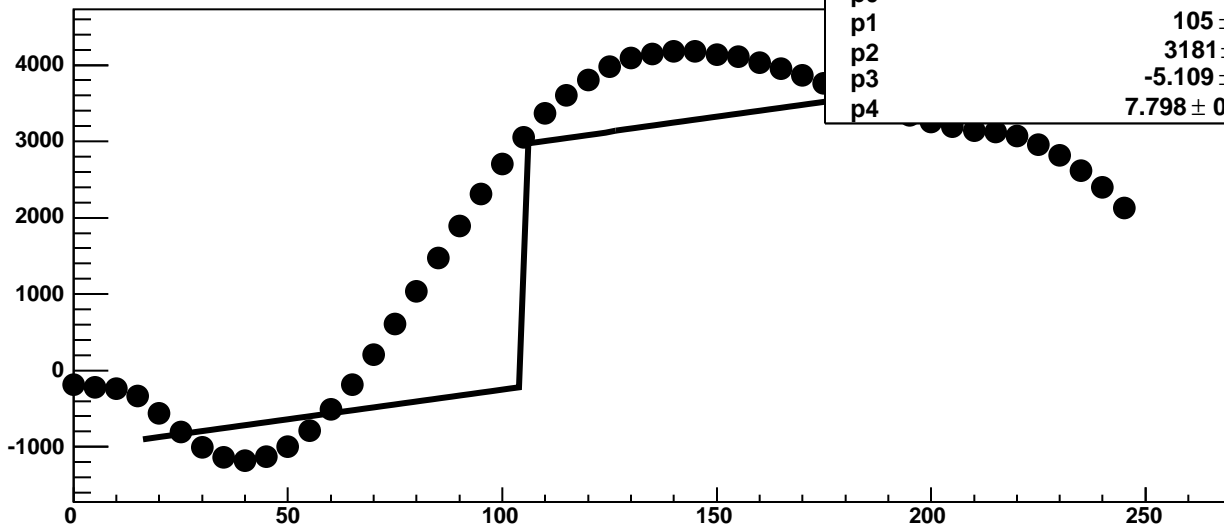
Chip 6, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



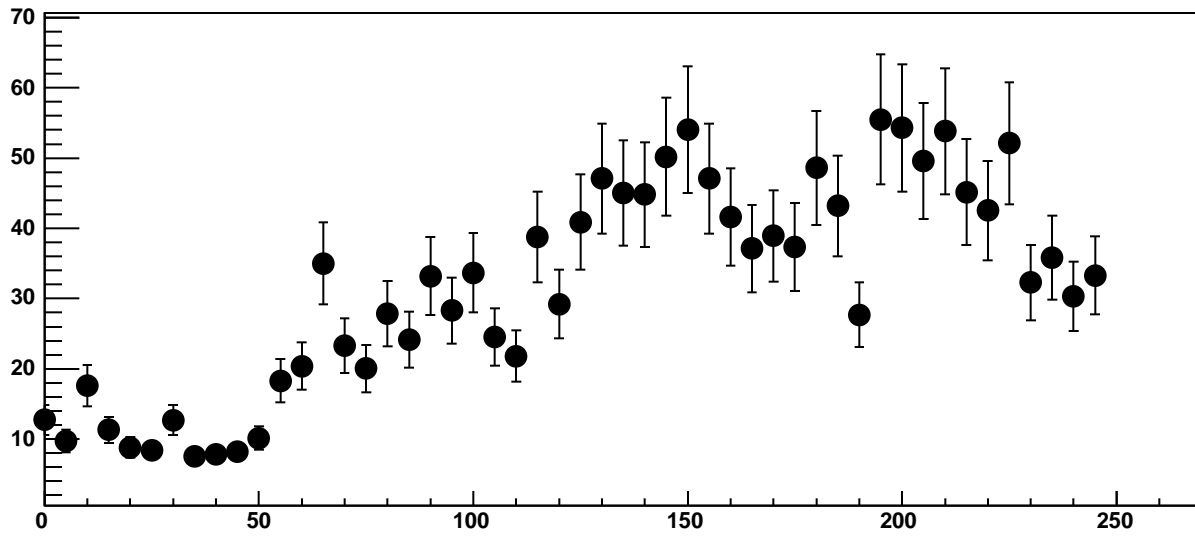
Chip 6, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold



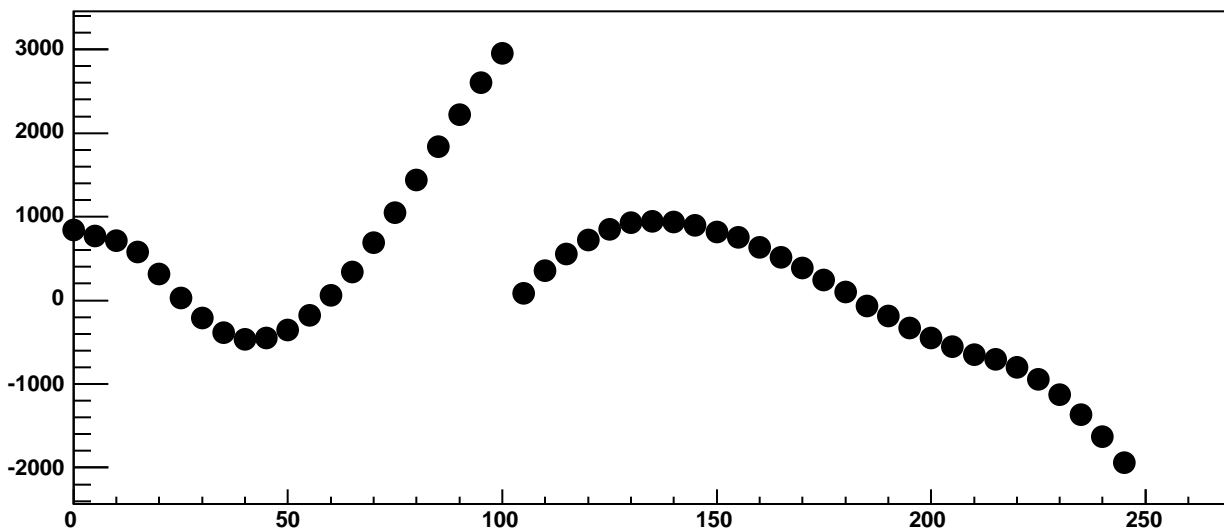
Chip 6, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold



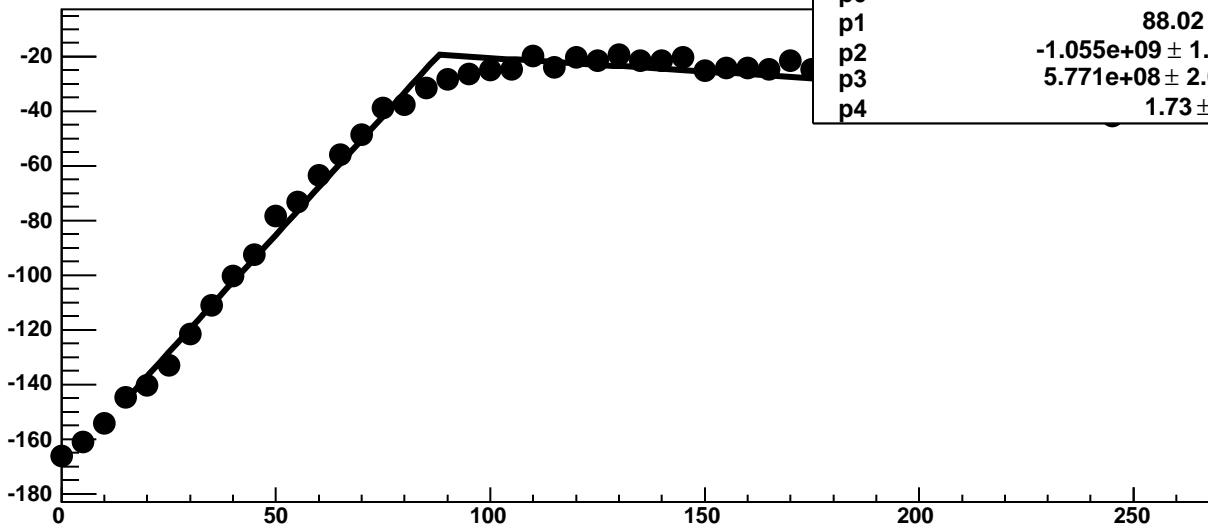
Chip 6, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

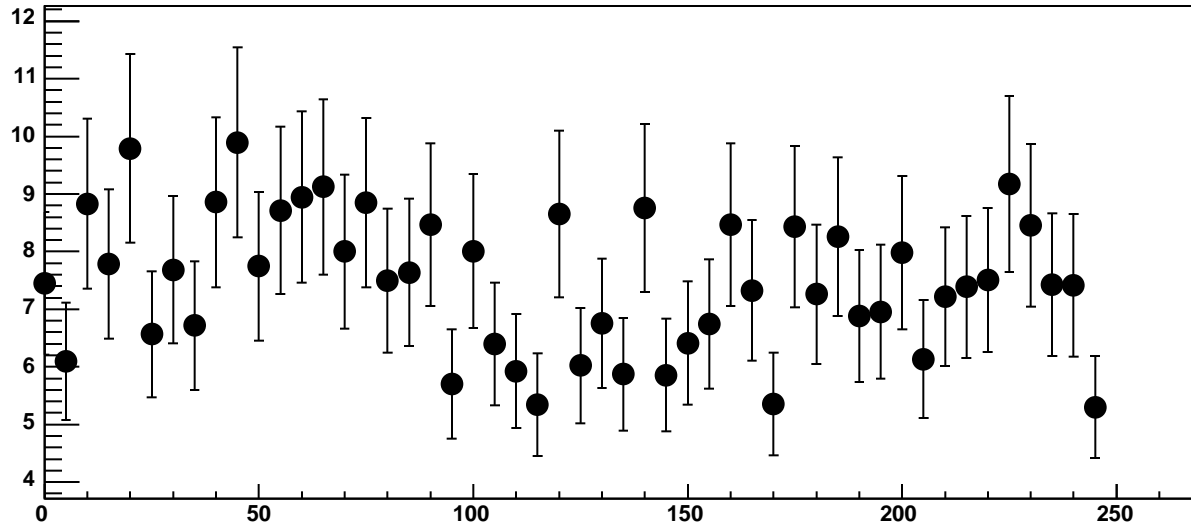


Chip 6, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold

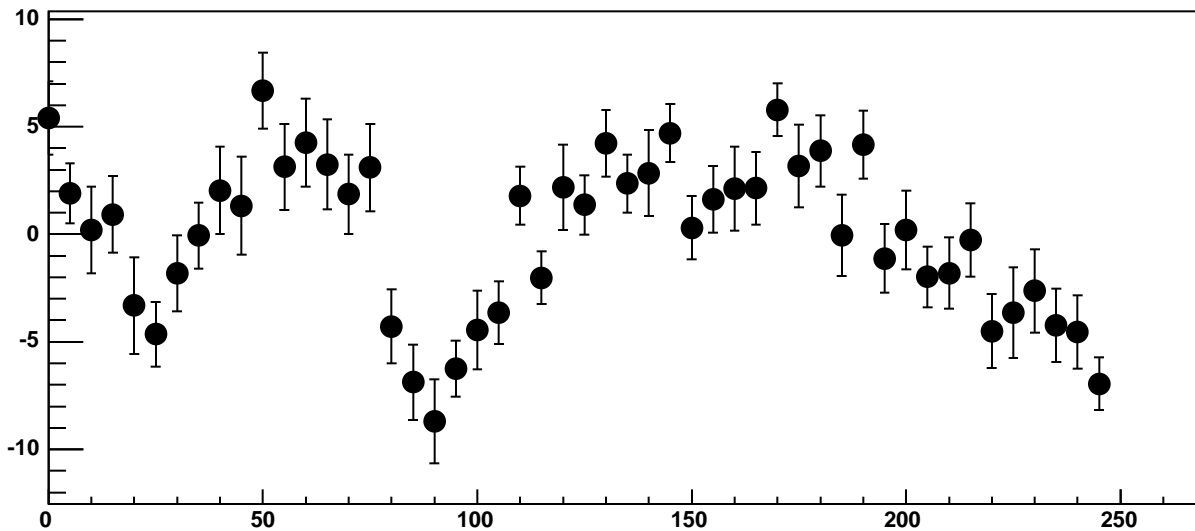


χ^2 / ndf	219.1 / 41
p0	-19.32 ± 0.5186
p1	88.02 ± 0.5569
p2	$-1.055\text{e}+09 \pm 1.314\text{e}+07$
p3	$5.771\text{e}+08 \pm 2.068\text{e}+05$
p4	1.73 ± 0.02171

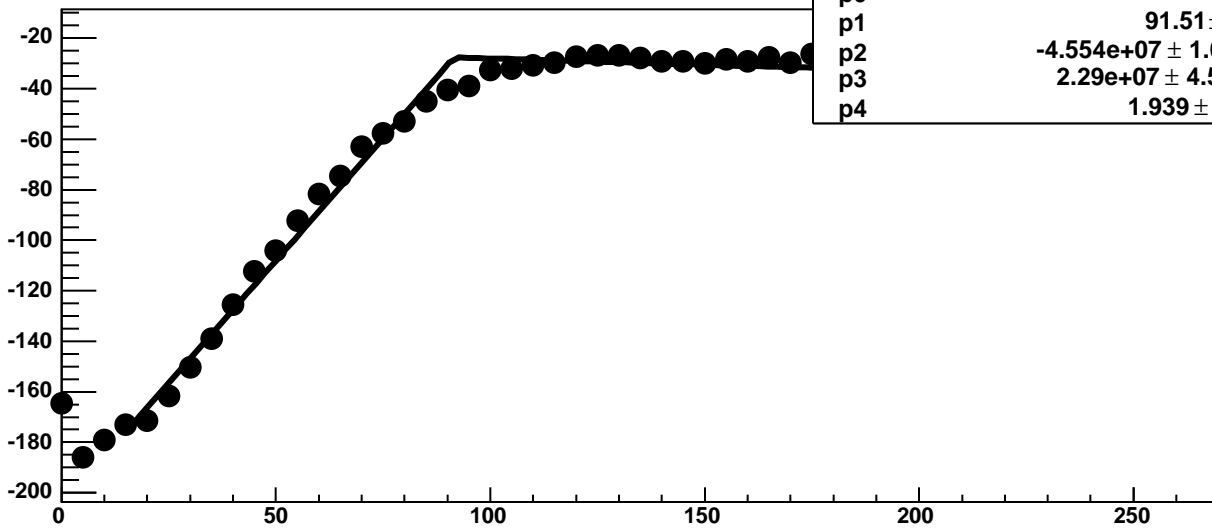
Chip 6, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold

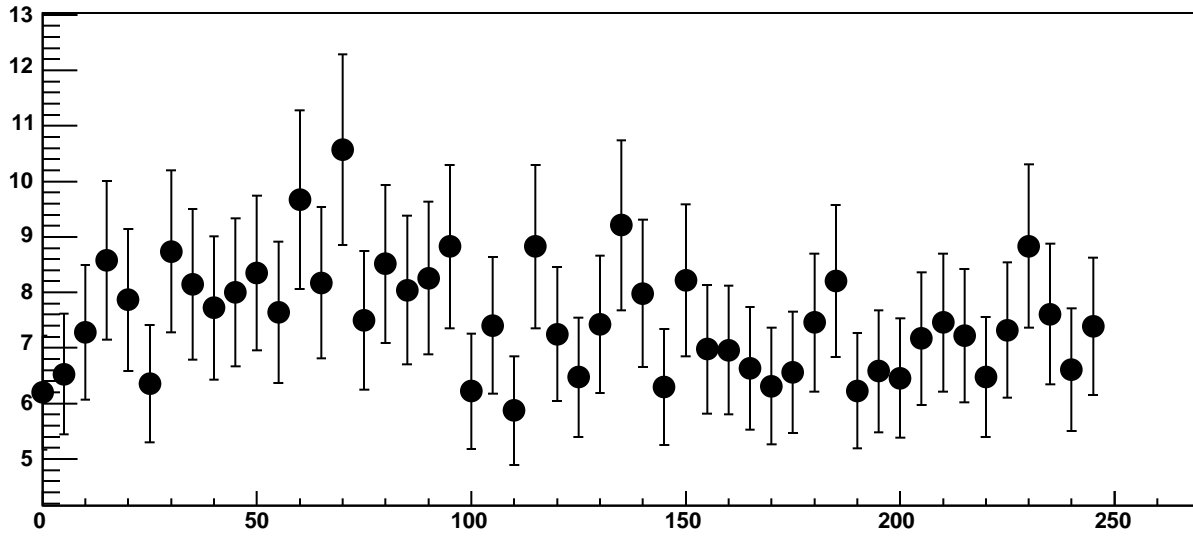


Chip 6, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

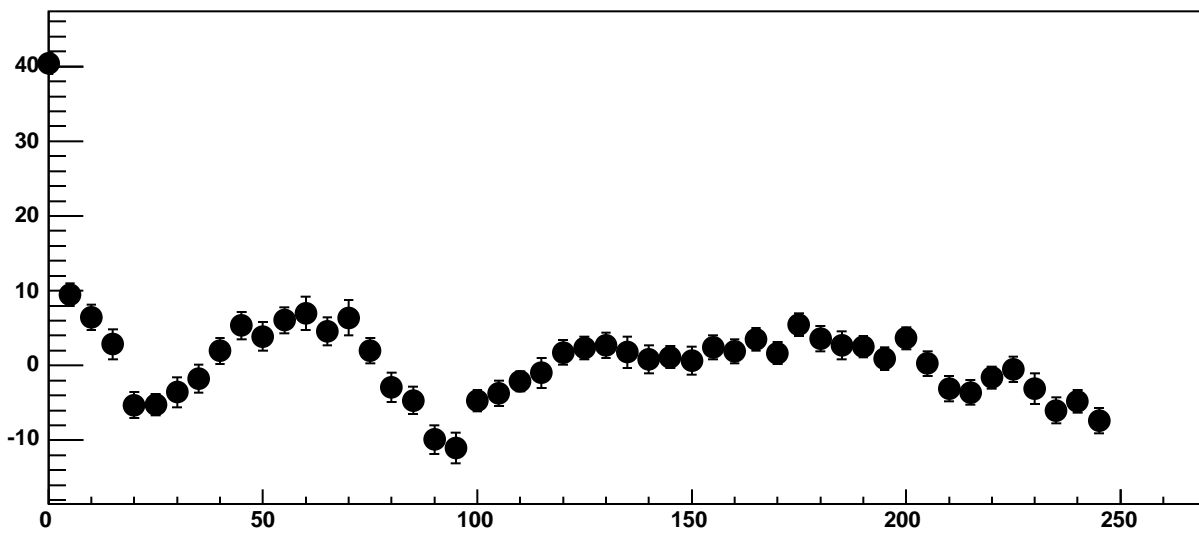


χ^2 / ndf	243.2 / 41
p0	-27.59 ± 0.5936
p1	91.51 ± 0.5602
p2	$-4.554e+07 \pm 1.006e+06$
p3	$2.29e+07 \pm 4.513e+05$
p4	1.939 ± 0.01995

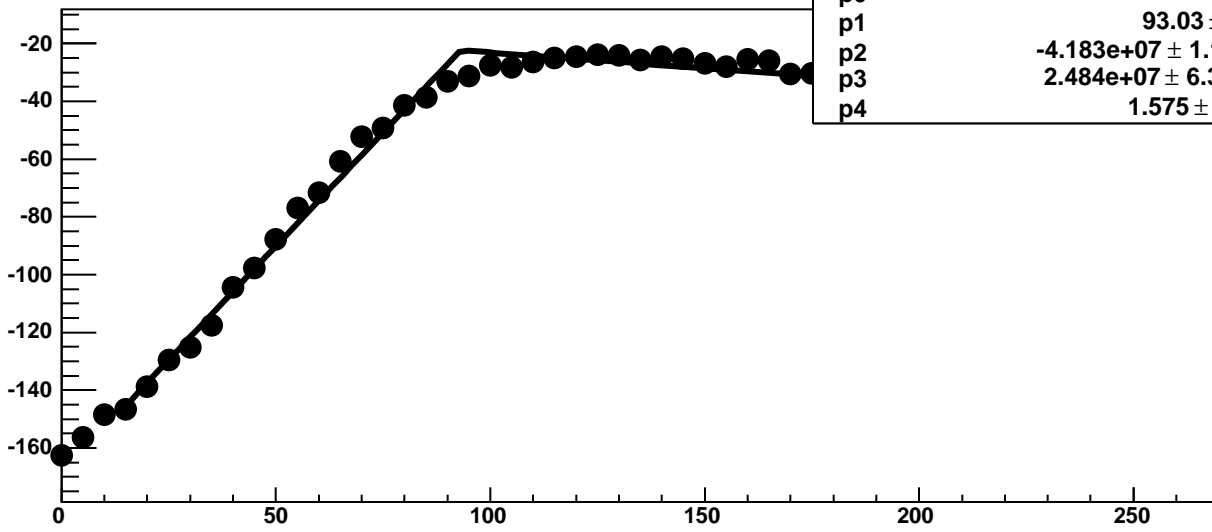
Chip 6, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

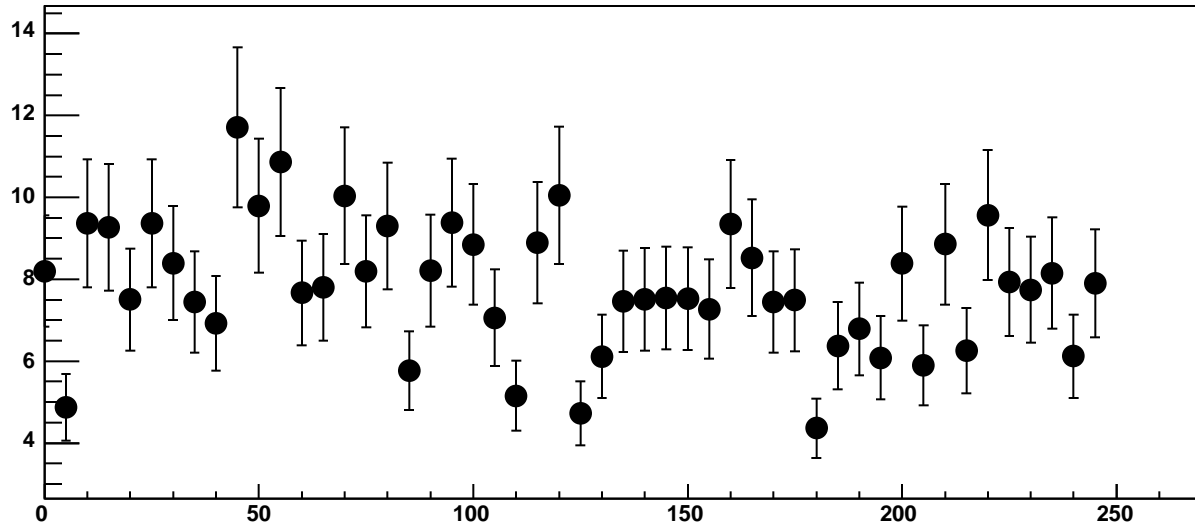


Chip 6, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

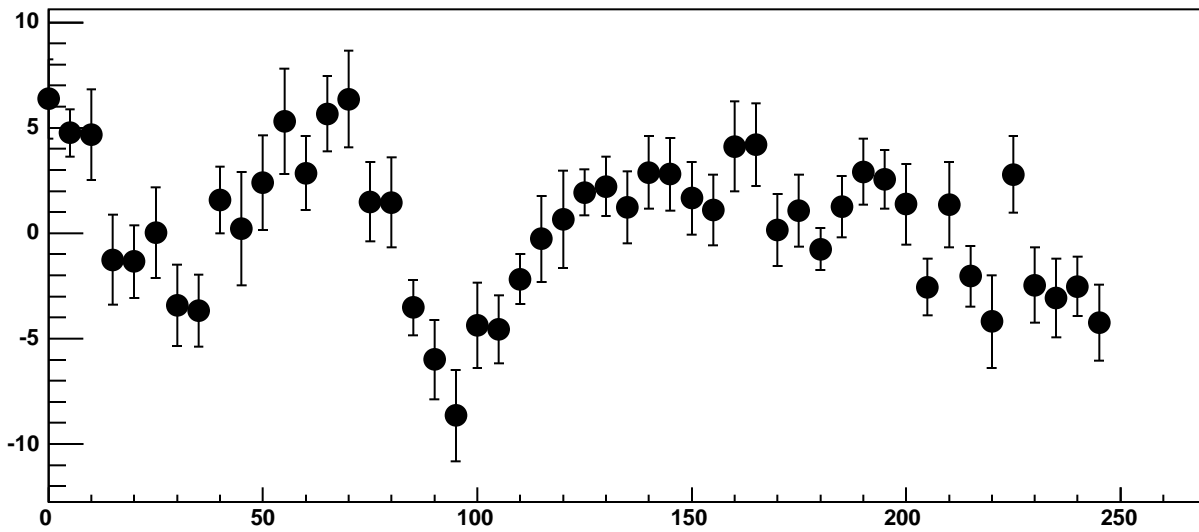


χ^2 / ndf	136.3 / 41
p0	-22.34 ± 0.56
p1	93.03 ± 0.6437
p2	$-4.183\text{e}+07 \pm 1.157\text{e}+06$
p3	$2.484\text{e}+07 \pm 6.386\text{e}+05$
p4	1.575 ± 0.01976

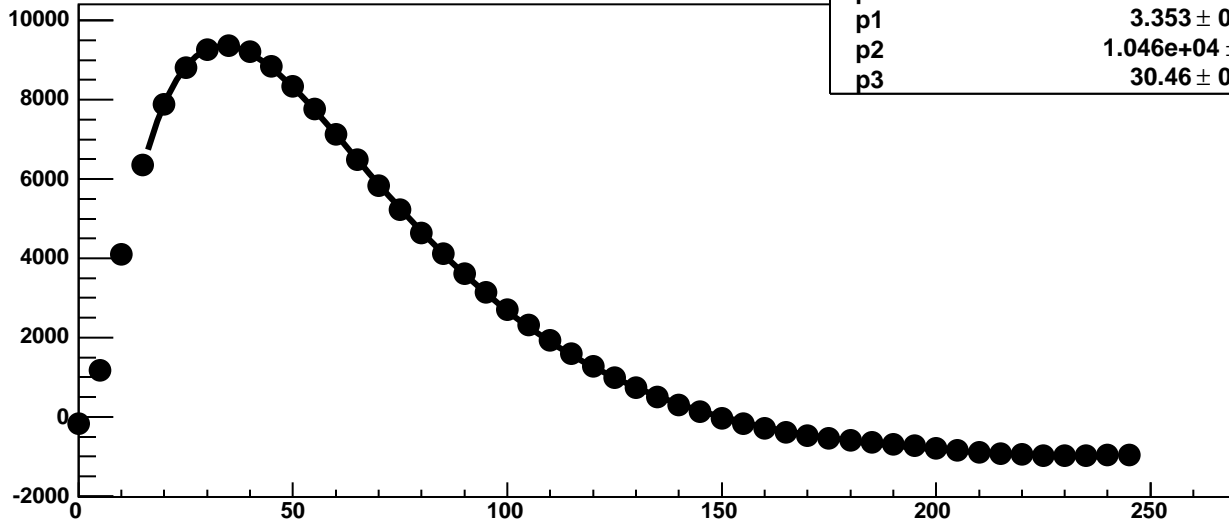
Chip 6, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

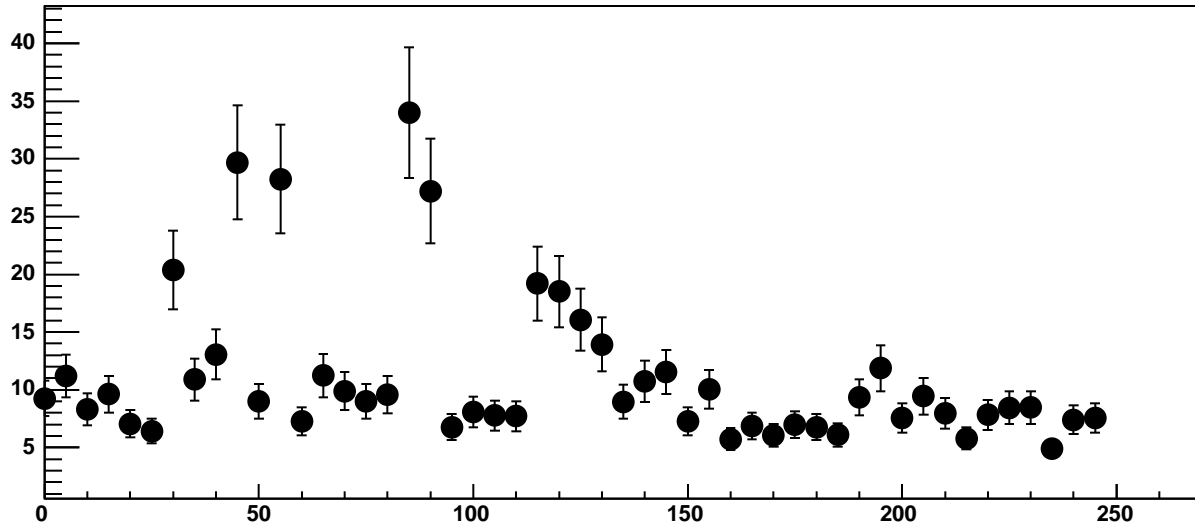


Chip 6, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold

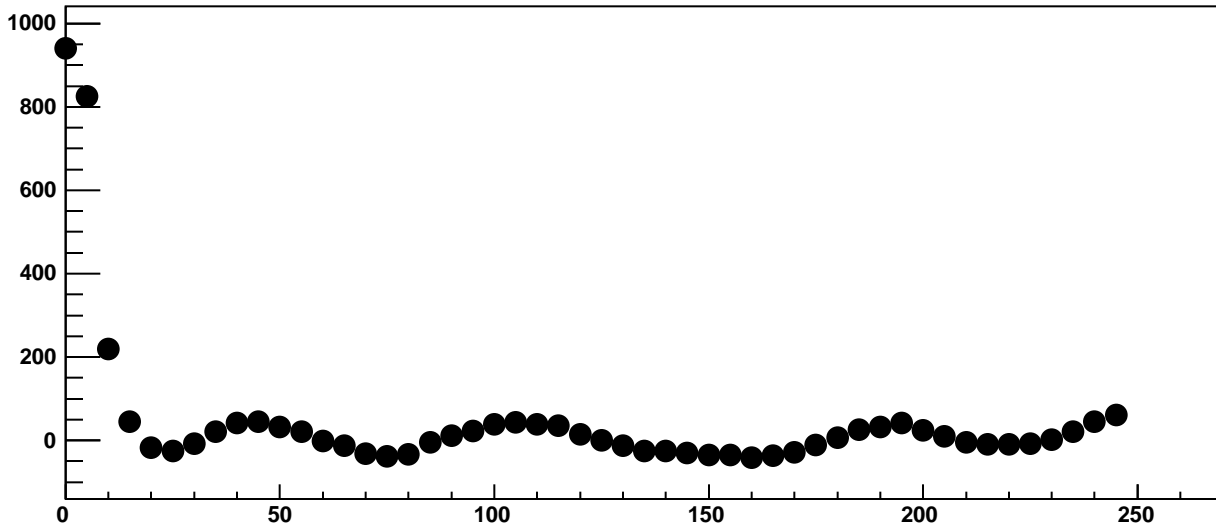


χ^2 / ndf	9198 / 42
p0	-1105 ± 0.5231
p1	3.353 ± 0.00623
p2	1.046e+04 ± 0.9477
p3	30.46 ± 0.005206

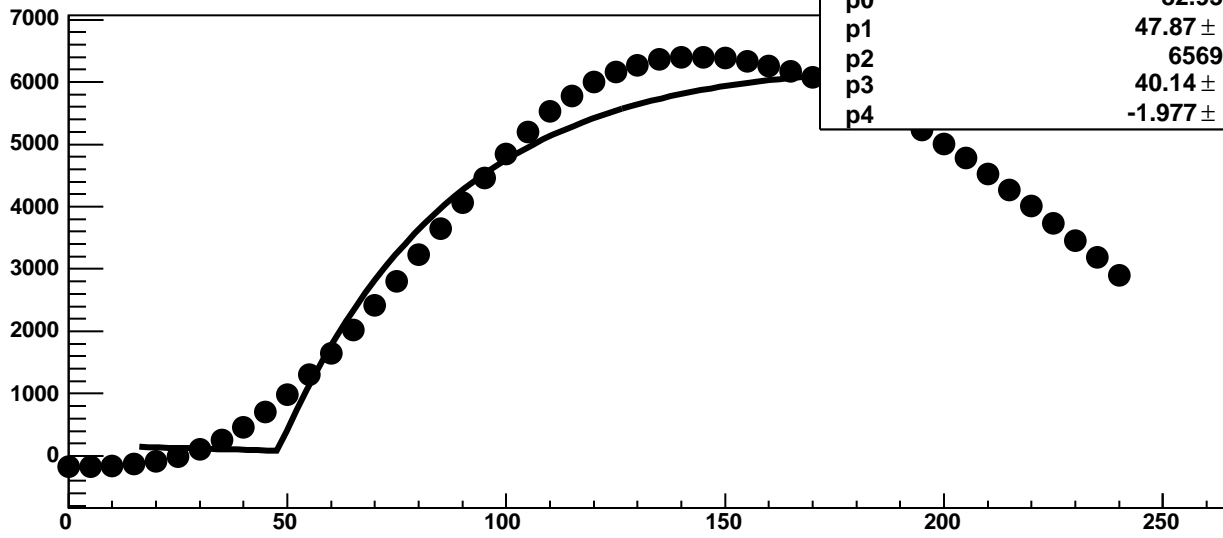
Chip 6, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold

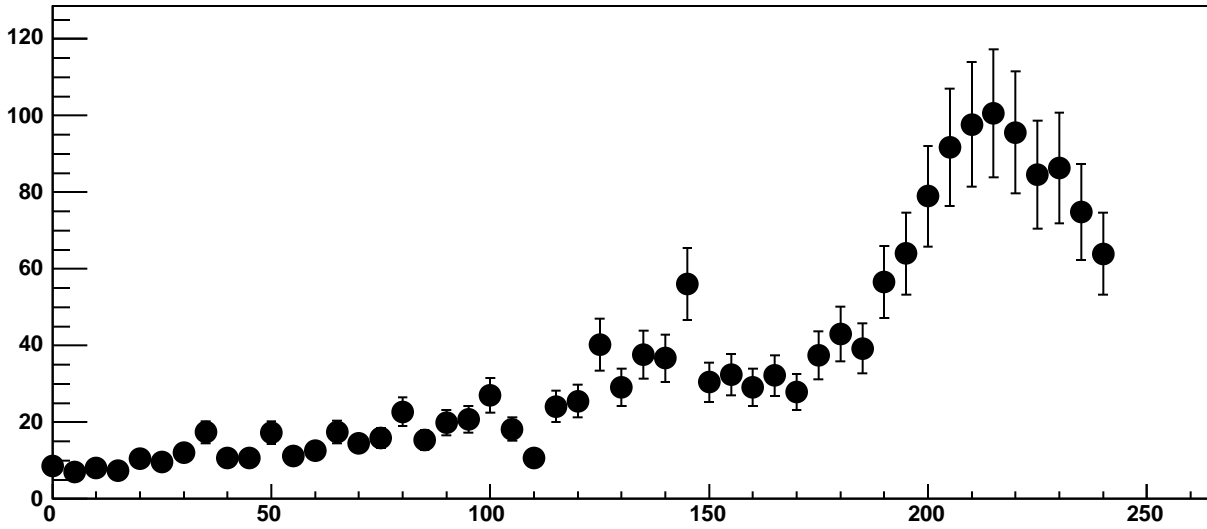


Chip 6, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold

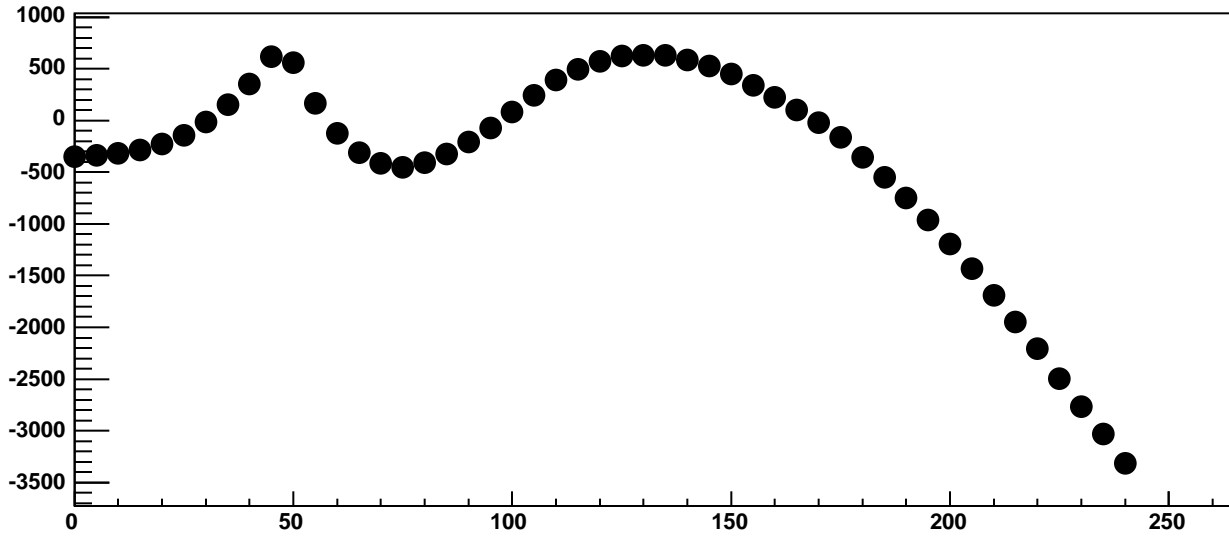


χ^2 / ndf	4.49e+05 / 41
p0	82.93 ± 1.442
p1	47.87 ± 0.01767
p2	6569 ± 9.339
p3	40.14 ± 0.06247
p4	-1.977 ± 0.05619

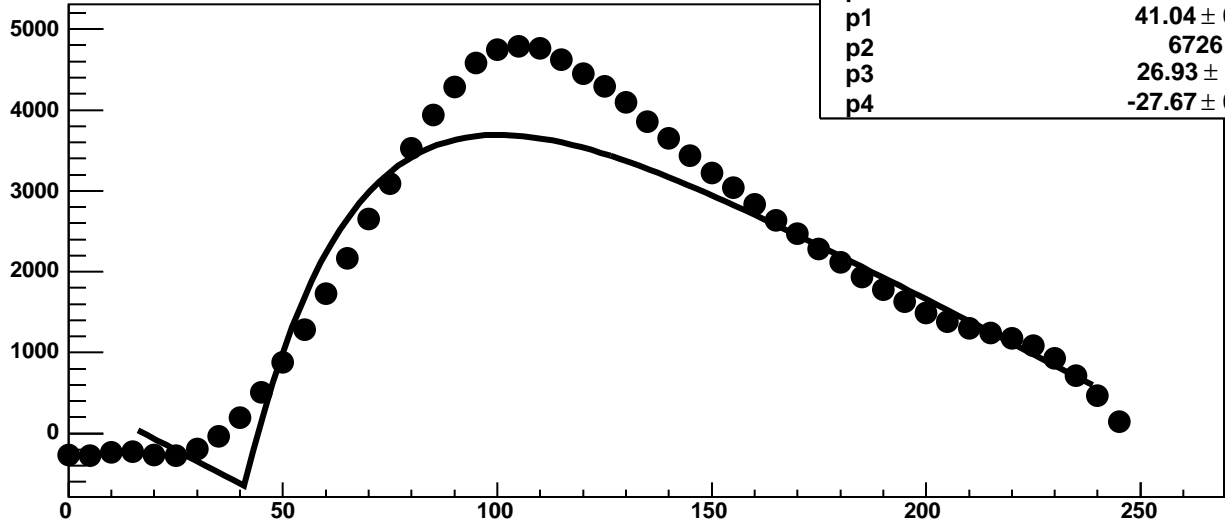
Chip 6, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



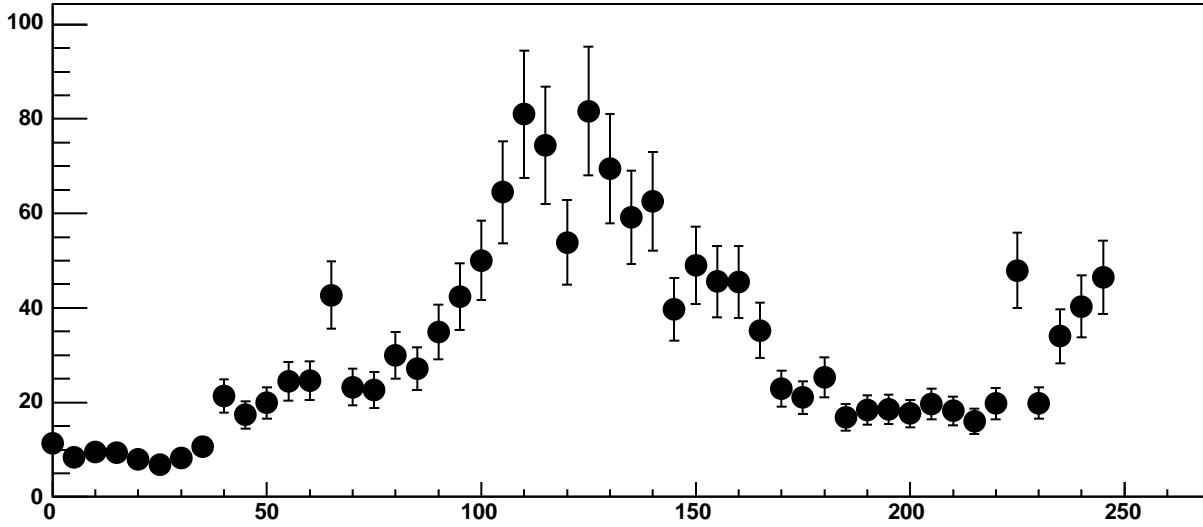
Chip 6, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold



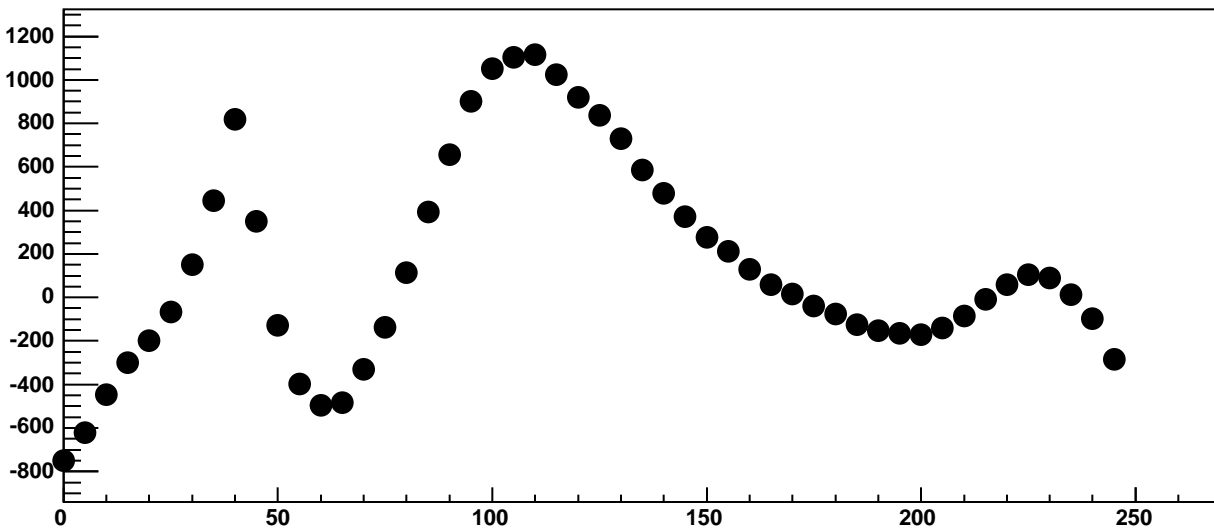
Chip 6, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold



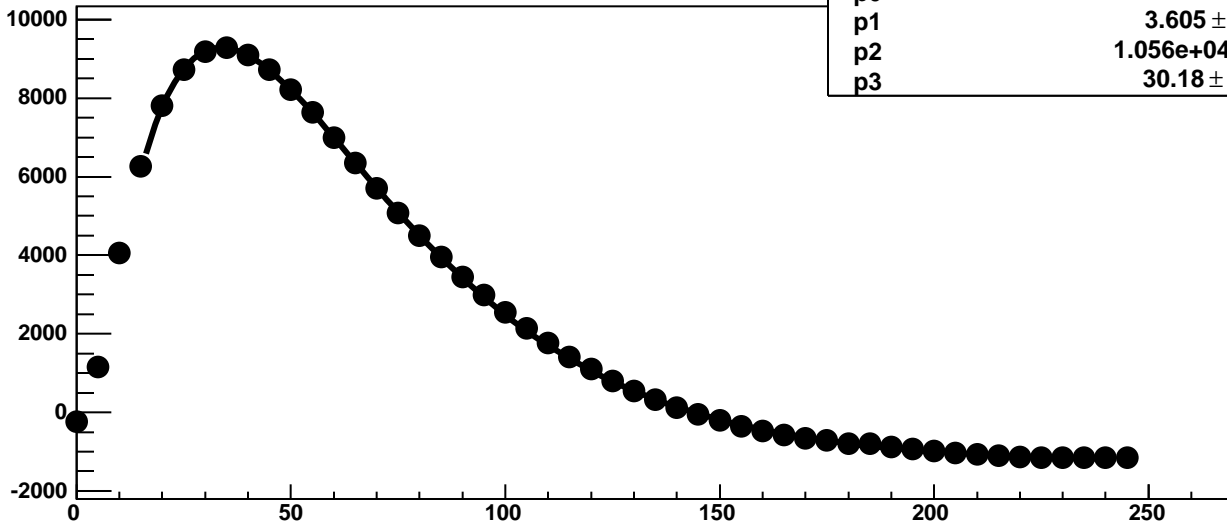
Chip 6, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

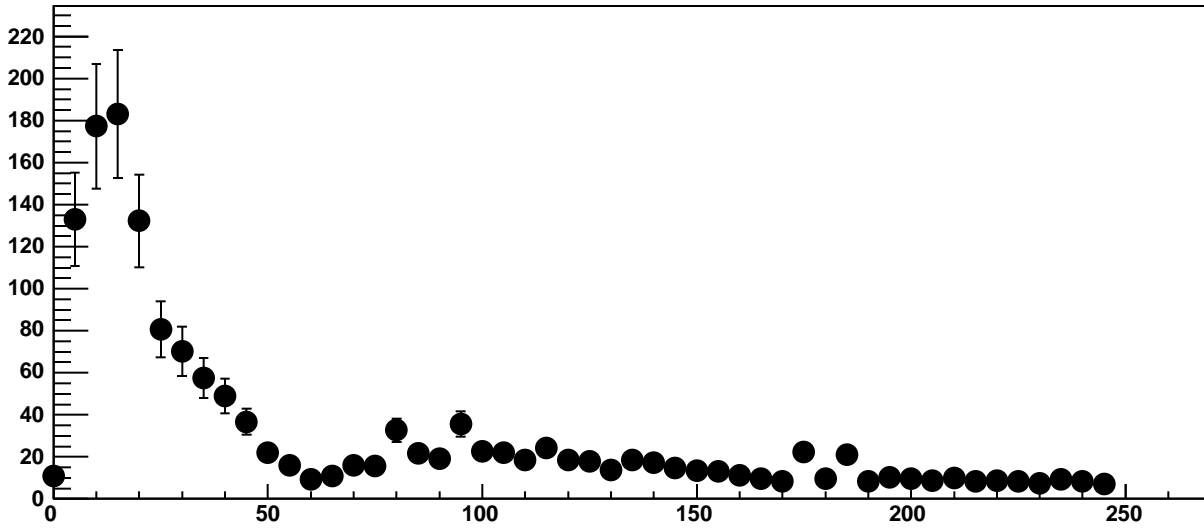


Chip 6, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold

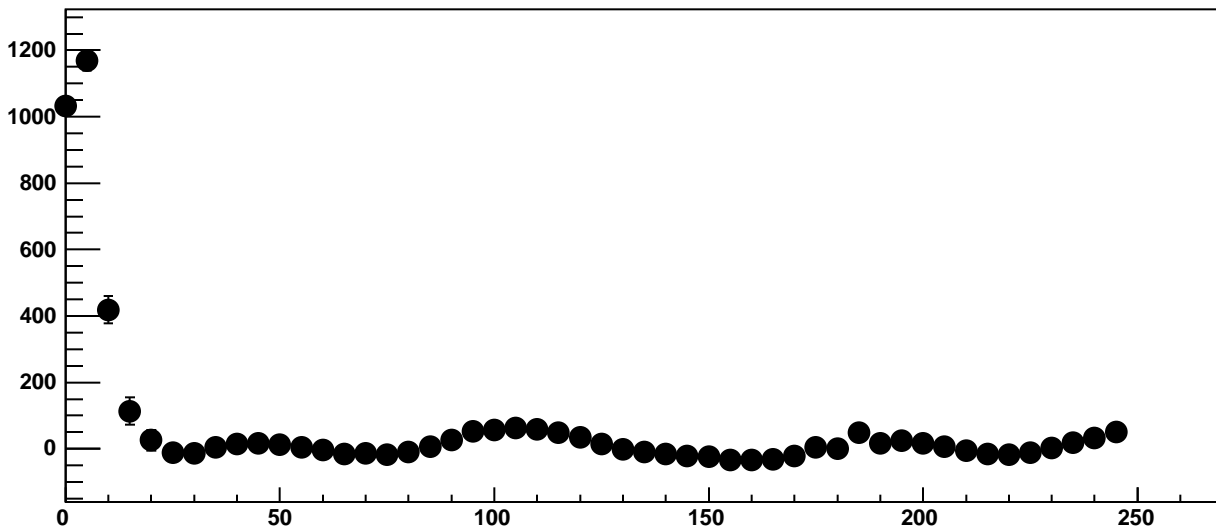


χ^2 / ndf	2445 / 42
p0	-1275 ± 0.8044
p1	3.605 ± 0.04431
p2	1.056e+04 ± 4.756
p3	30.18 ± 0.01387

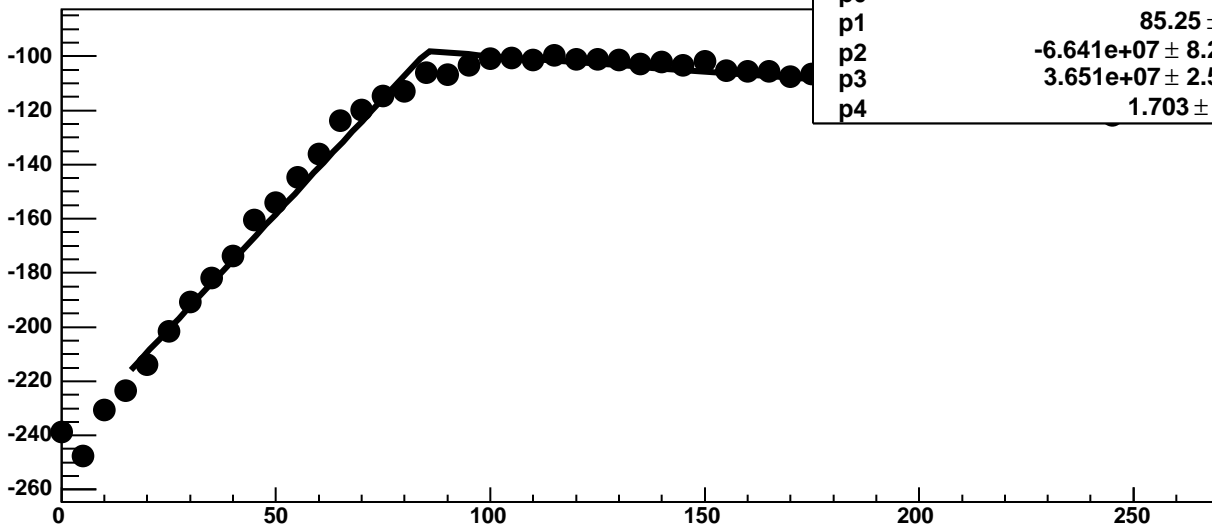
Chip 6, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold

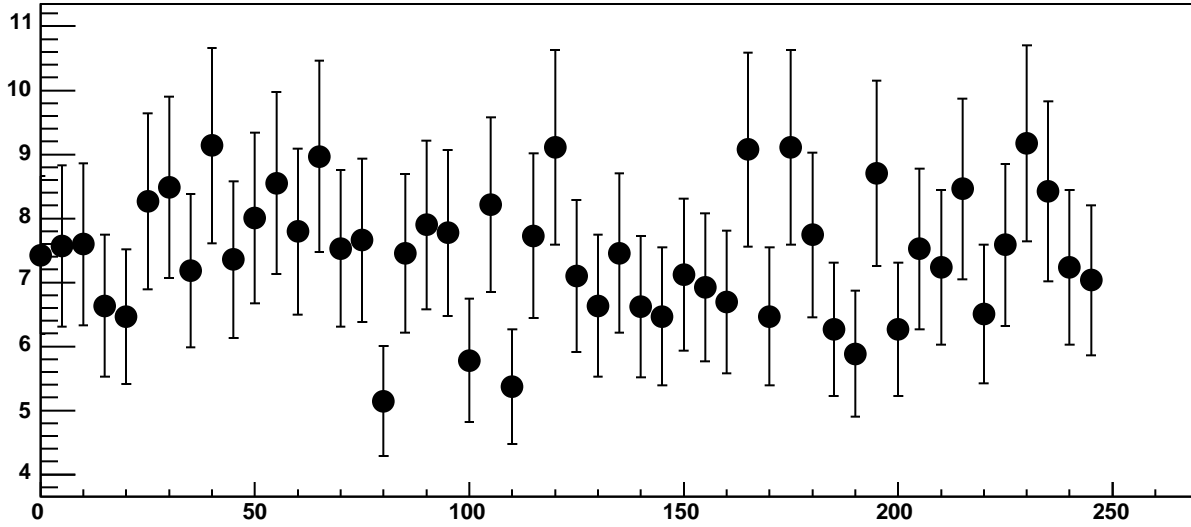


Chip 6, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

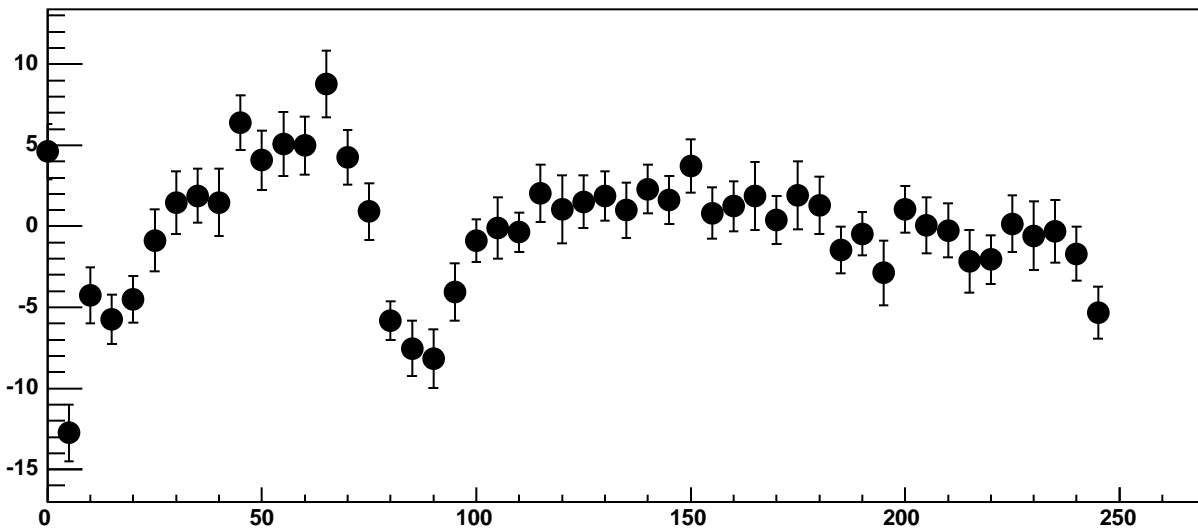


χ^2 / ndf	178.9 / 41
p0	-98.19 ± 0.5615
p1	85.25 ± 0.5385
p2	$-6.641\text{e}+07 \pm 8.283\text{e}+05$
p3	$3.651\text{e}+07 \pm 2.575\text{e}+05$
p4	1.703 ± 0.01872

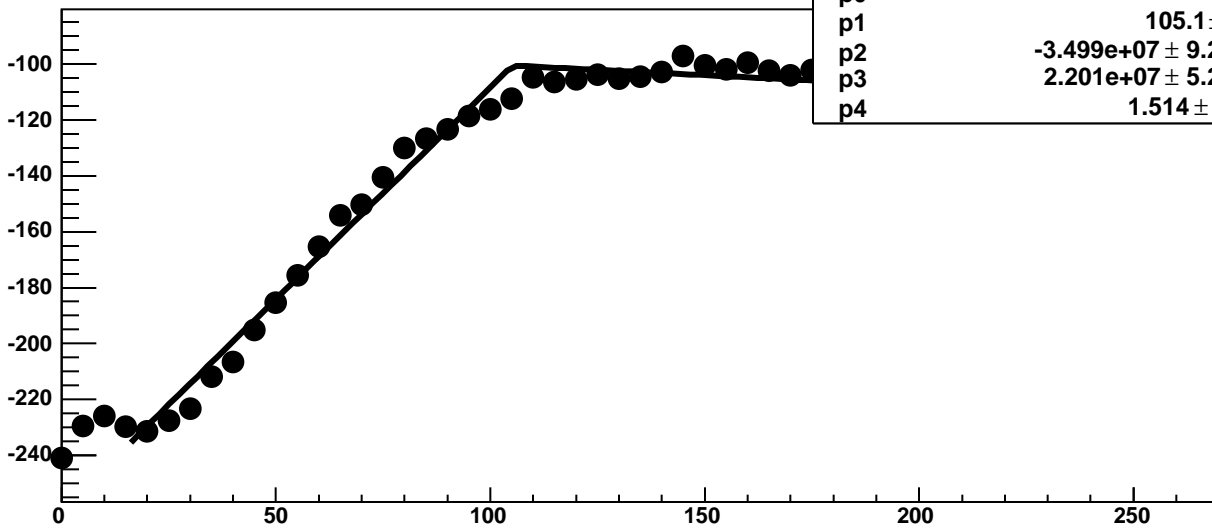
Chip 6, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

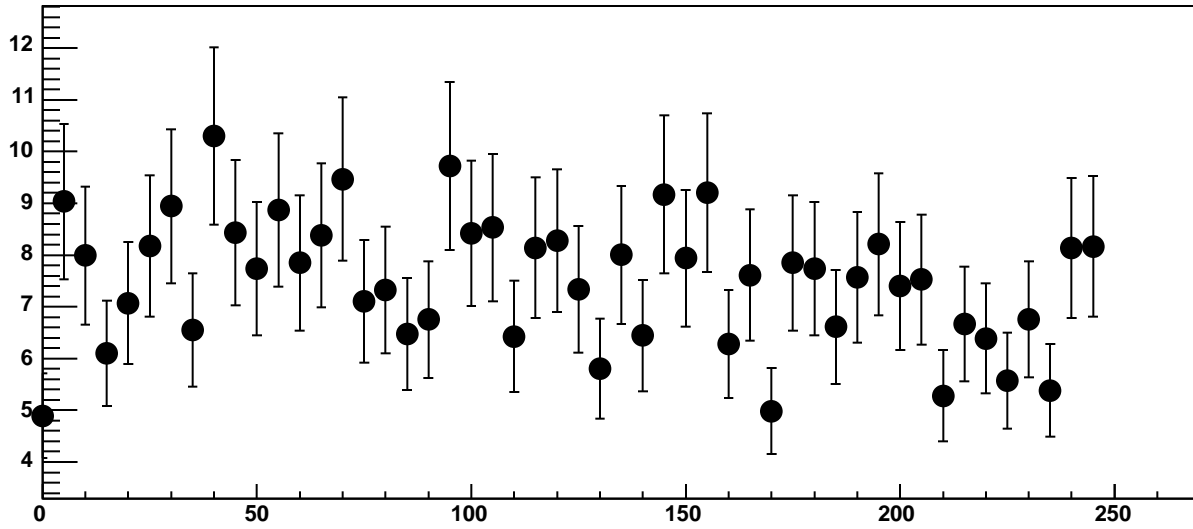


Chip 6, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

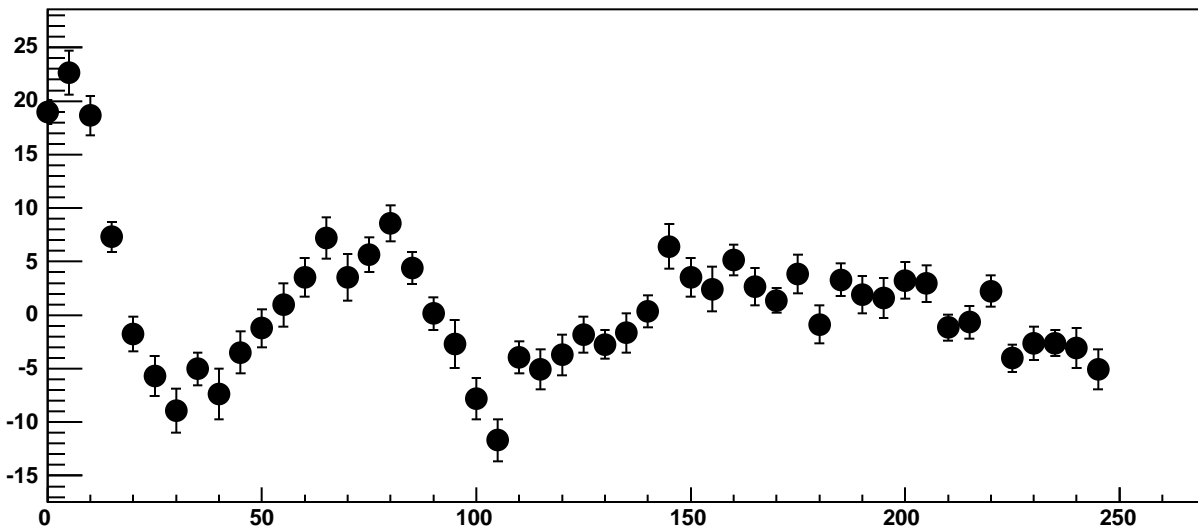


χ^2 / ndf	299 / 41
p0	-100.6 ± 0.6117
p1	105.1 ± 0.6393
p2	$-3.499\text{e}+07 \pm 9.268\text{e}+05$
p3	$2.201\text{e}+07 \pm 5.288\text{e}+05$
p4	1.514 ± 0.01447

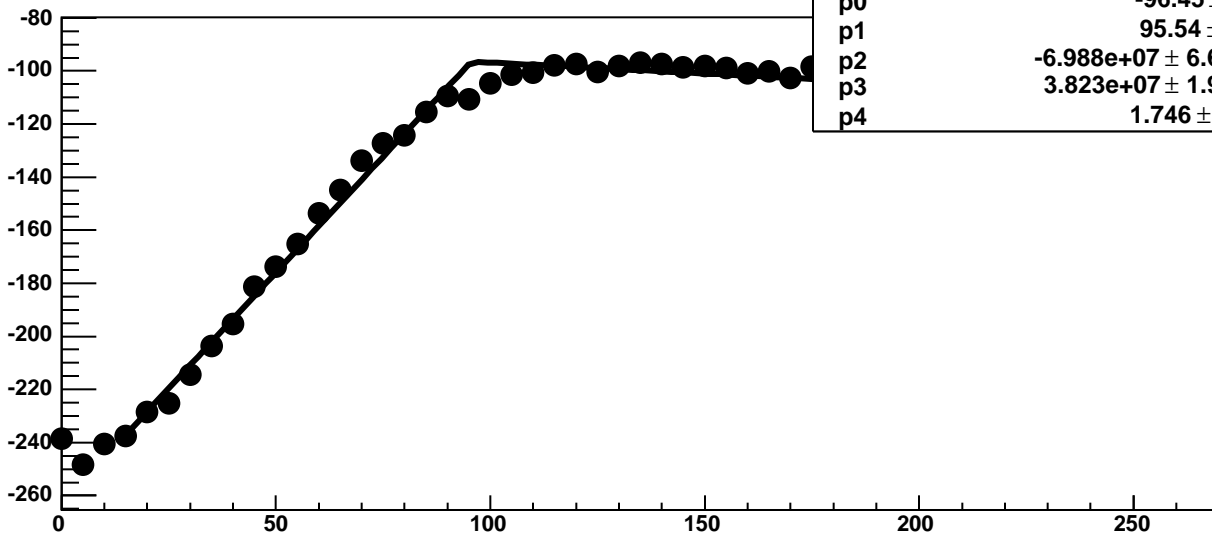
Chip 6, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

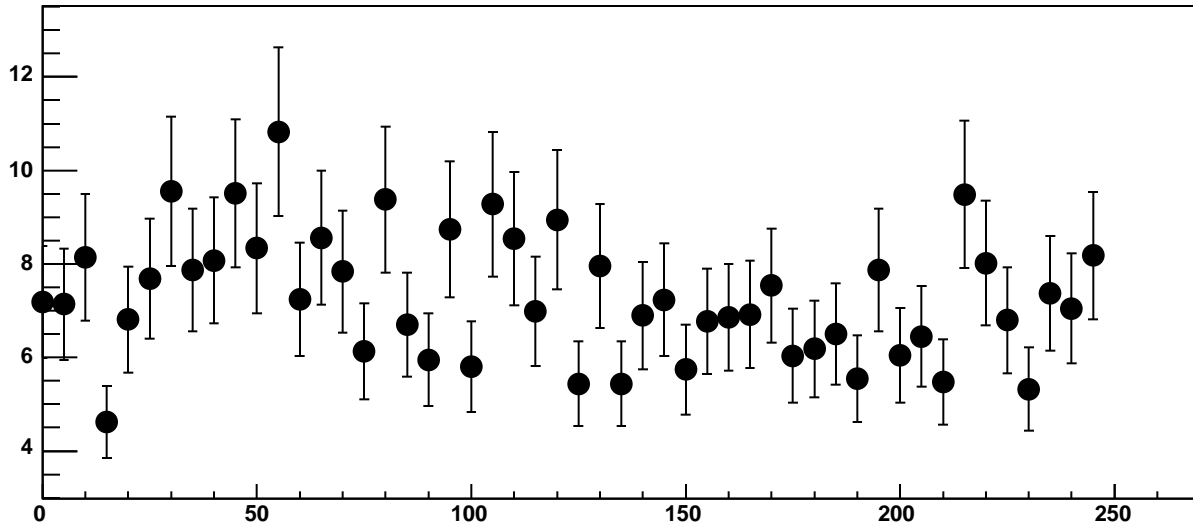


Chip 6, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold

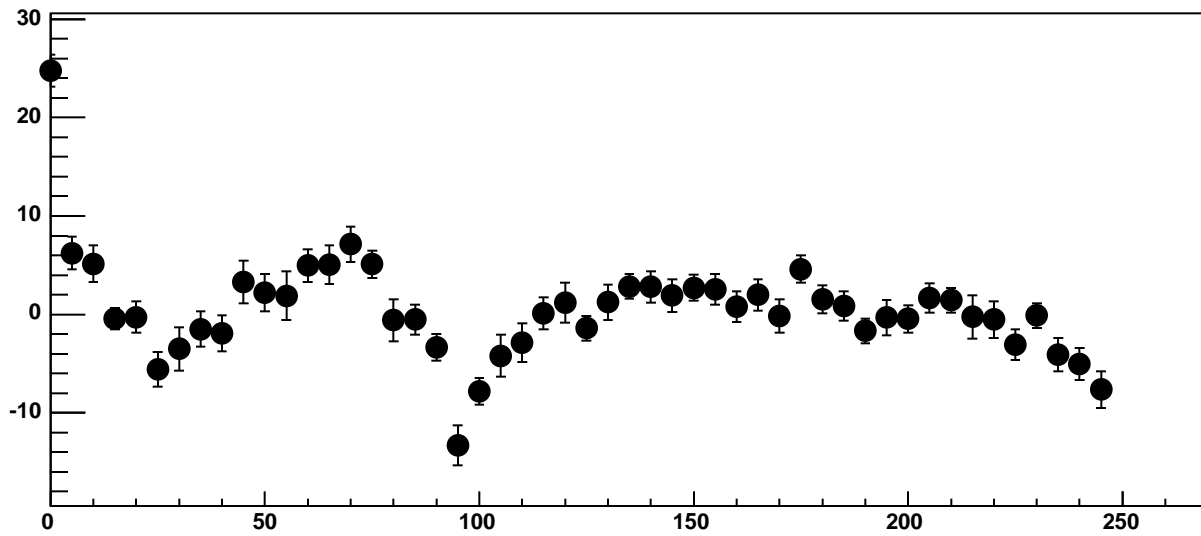


χ^2 / ndf	210.8 / 41
p0	-96.45 ± 0.6069
p1	95.54 ± 0.6334
p2	$-6.988\text{e}+07 \pm 6.608\text{e}+05$
p3	$3.823\text{e}+07 \pm 1.971\text{e}+05$
p4	1.746 ± 0.01621

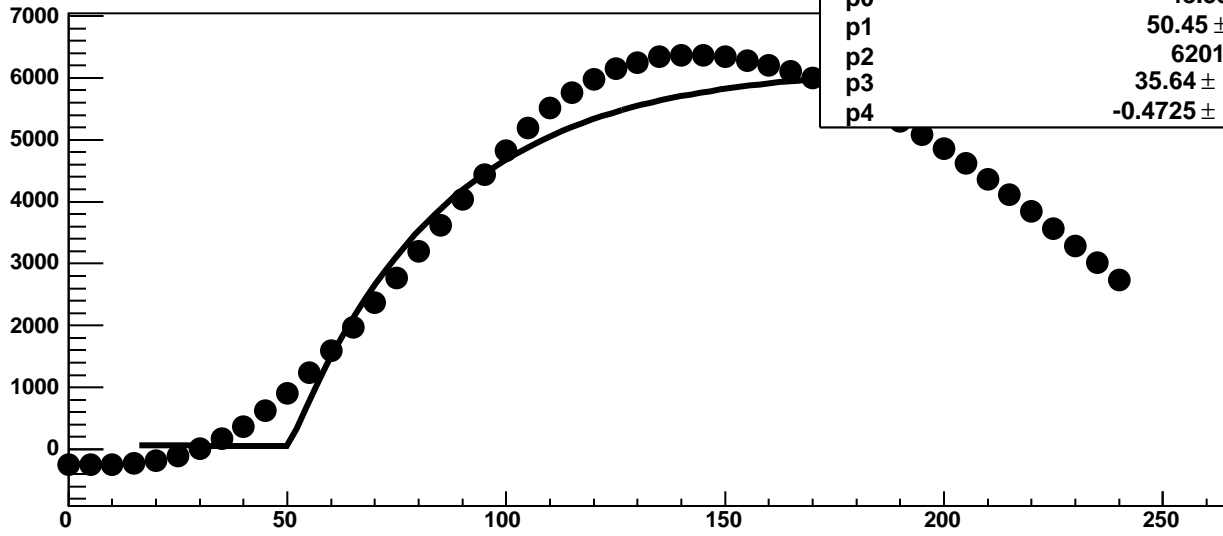
Chip 6, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold

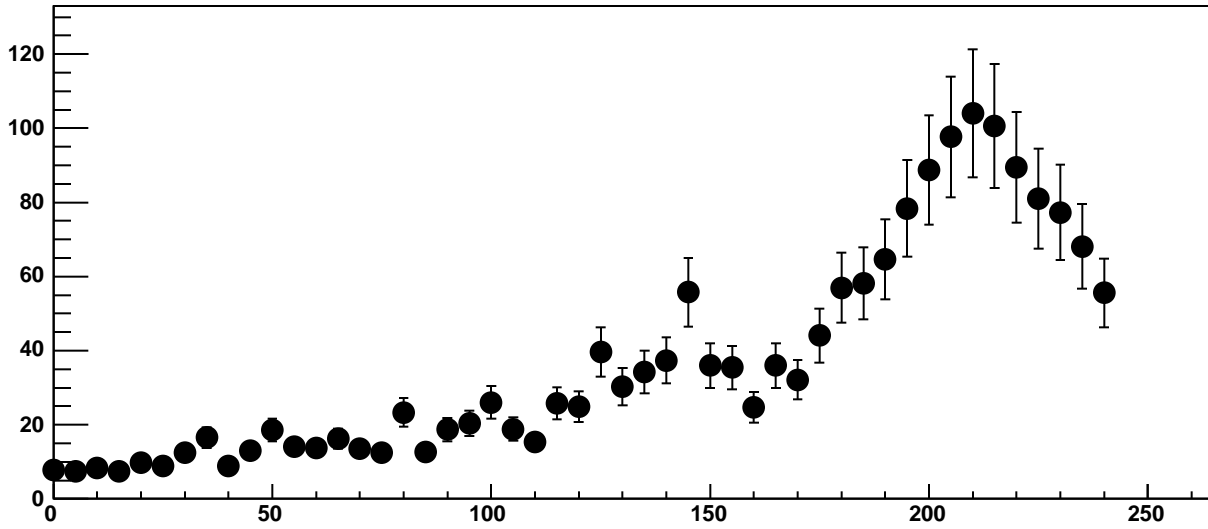


Chip 6, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

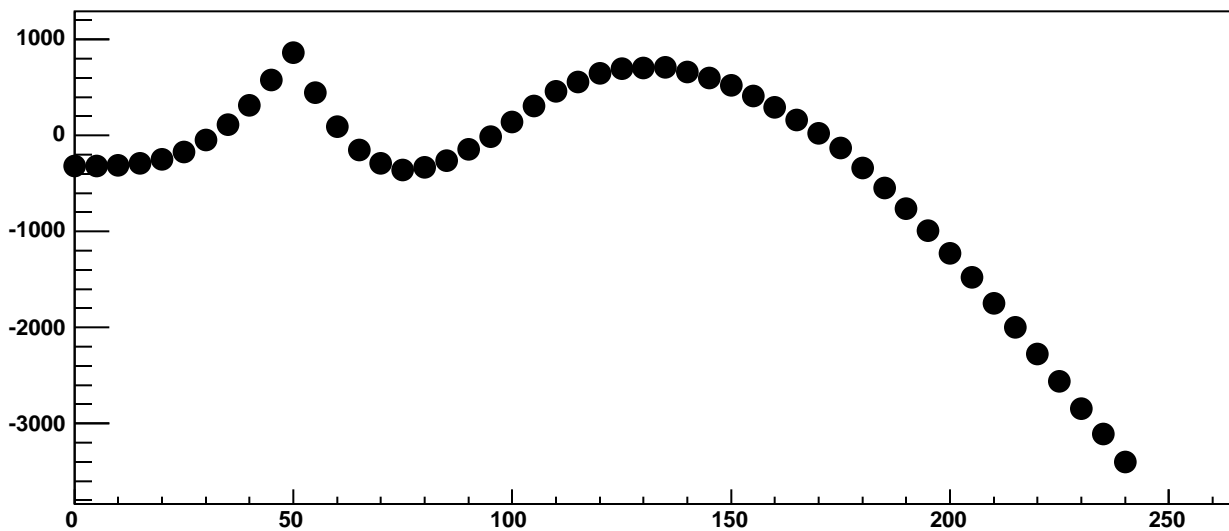


χ^2 / ndf	4.929e+05 / 41
p0	48.55 ± 1.46
p1	50.45 ± 0.0195
p2	6201 ± 8.487
p3	35.64 ± 0.05684
p4	-0.4725 ± 0.05307

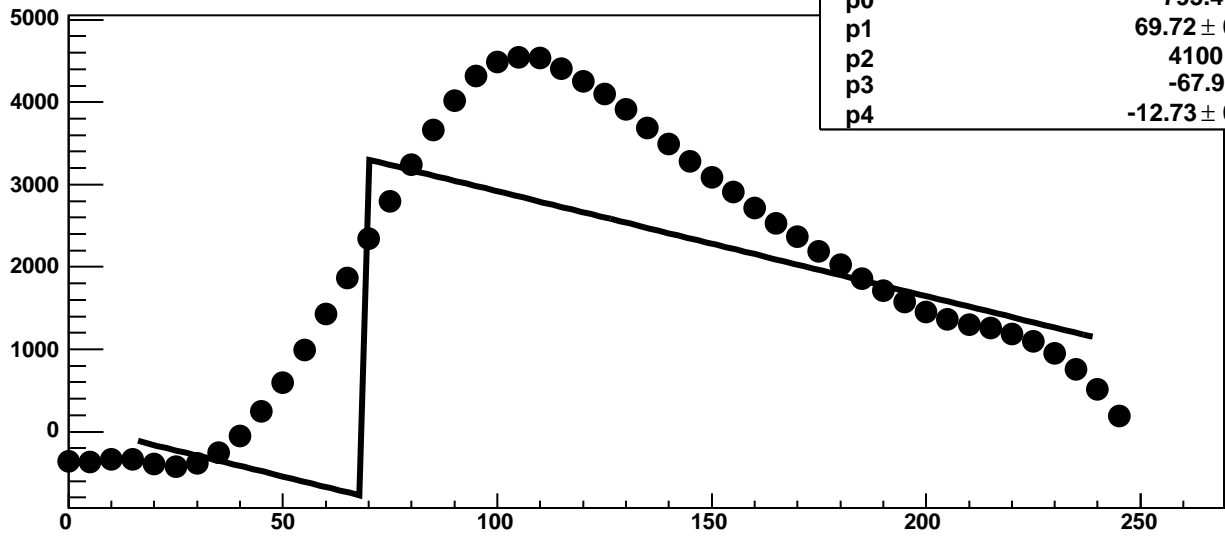
Chip 6, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

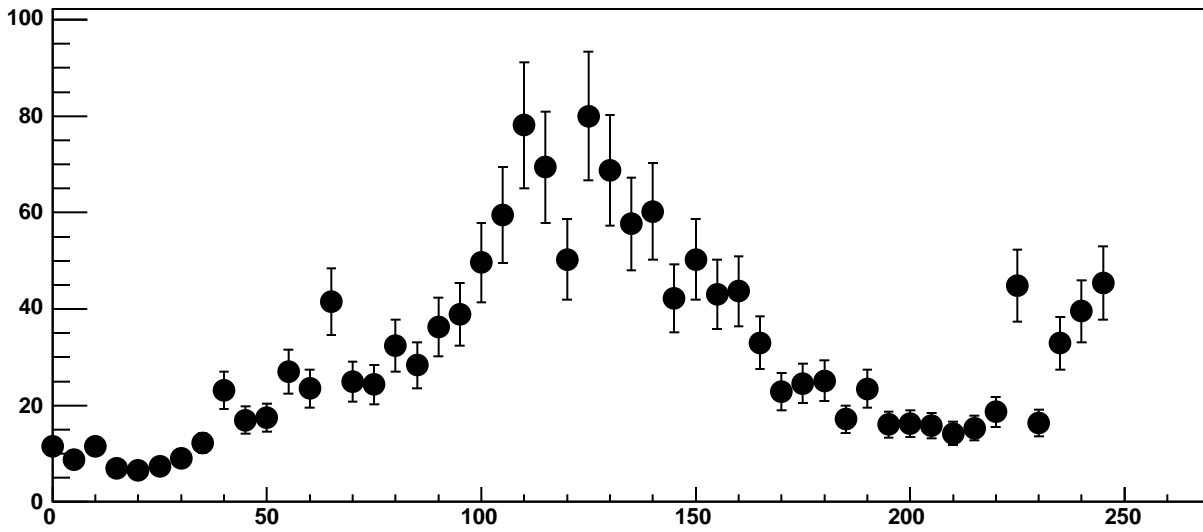


Chip 6, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

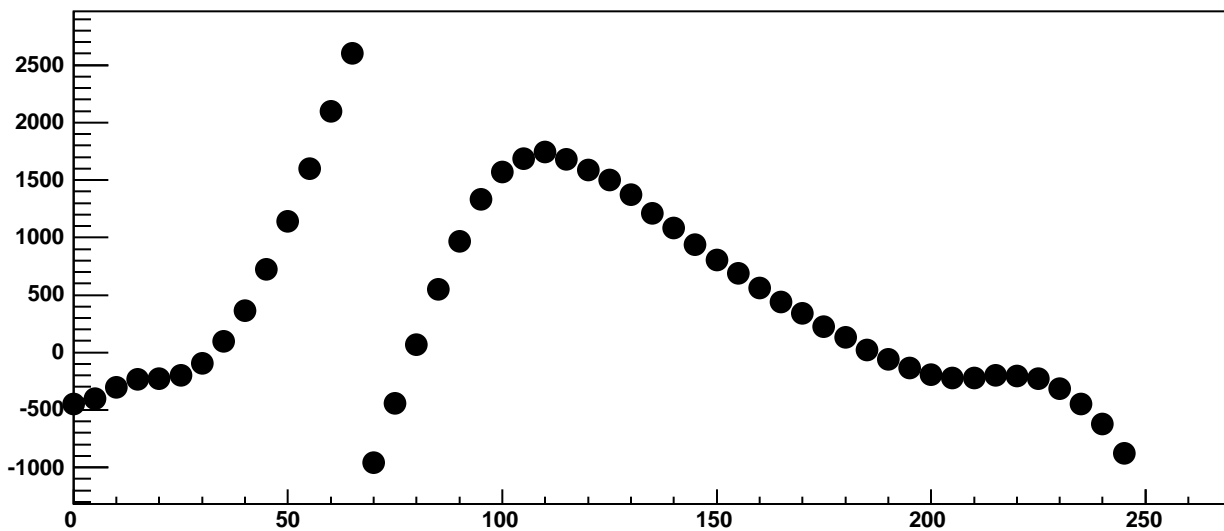


χ^2 / ndf	7.205e+05 / 41
p0	-795.4 ± 4.624
p1	69.72 ± 0.07087
p2	4100 ± 6.284
p3	-67.9 ± 2.351
p4	-12.73 ± 0.07409

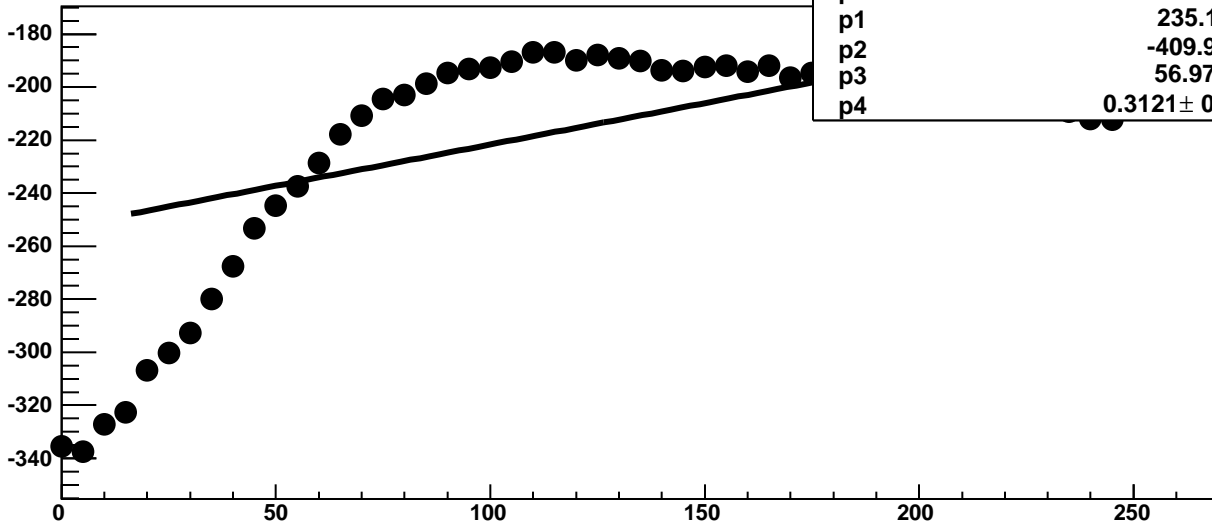
Chip 6, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold

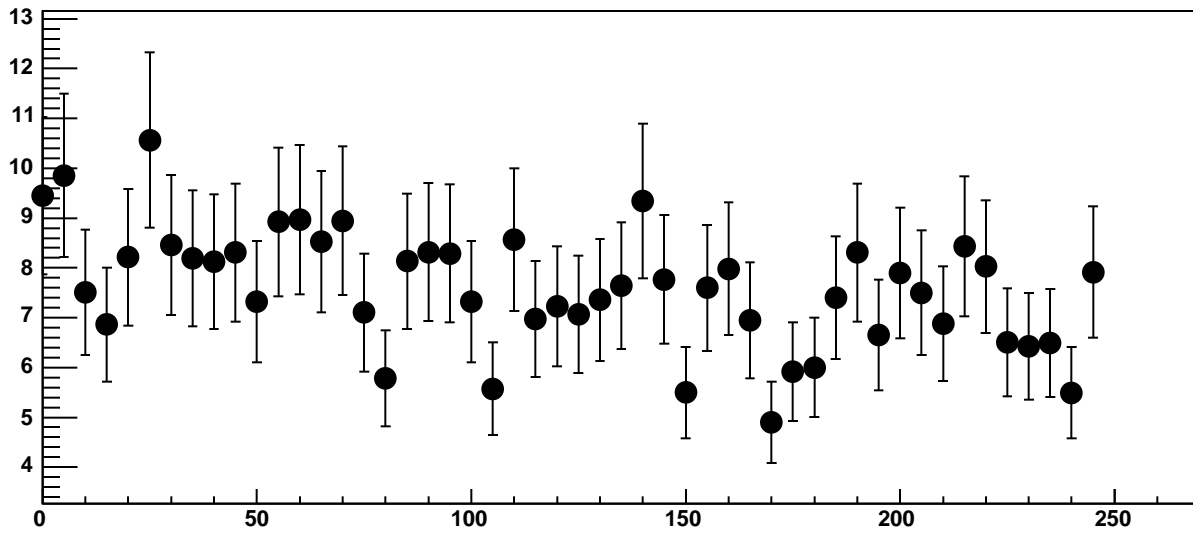


Chip 6, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold

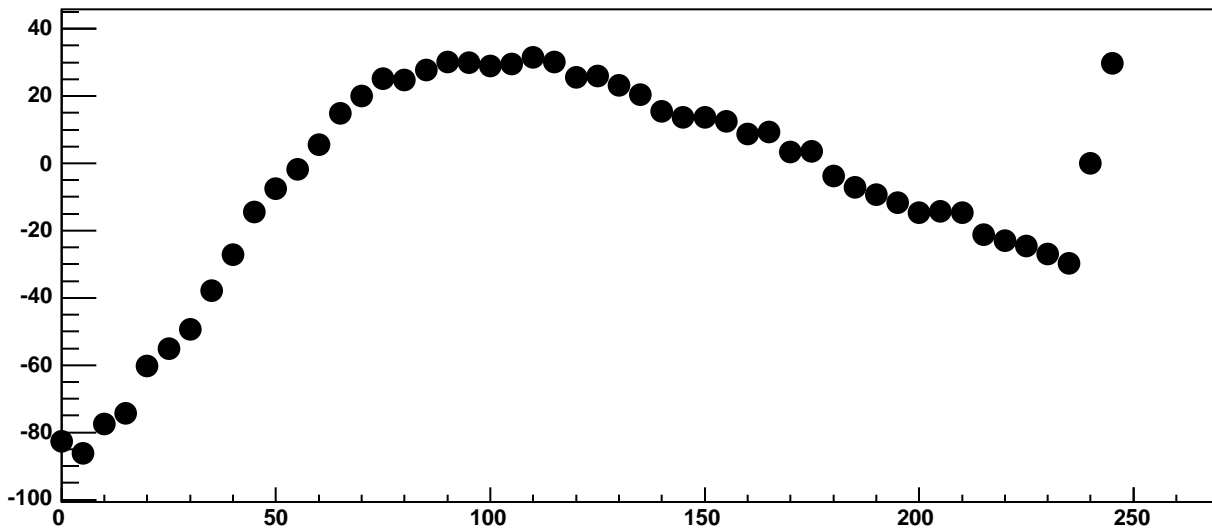


χ^2 / ndf	1.08e+04 / 41
p0	-179.5 ± 0.9053
p1	235.1 ± 2.493
p2	-409.9 ± 192.8
p3	56.97 ± 23.09
p4	0.3121 ± 0.003943

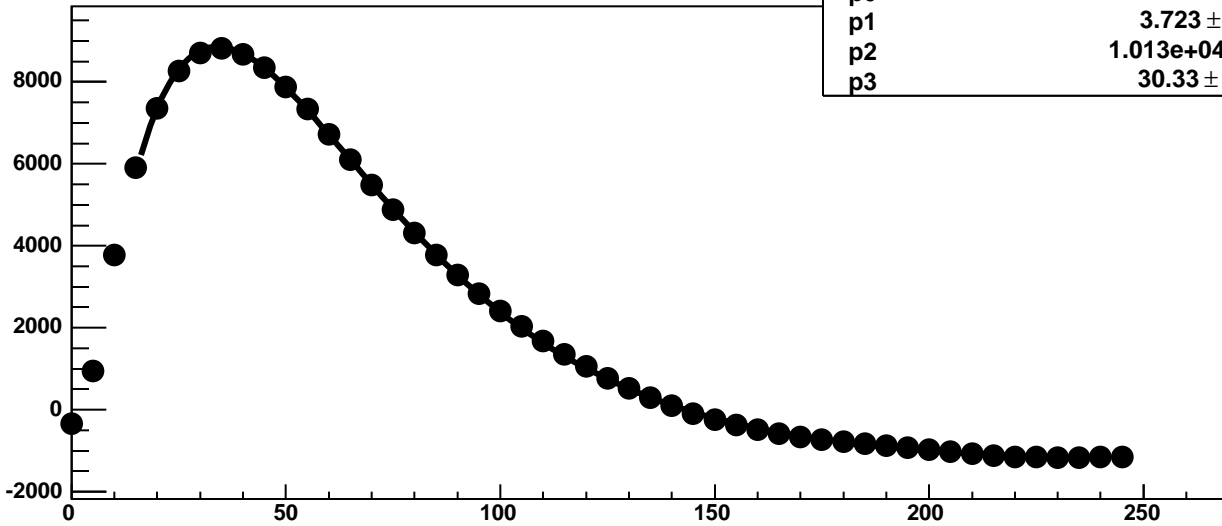
Chip 6, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold

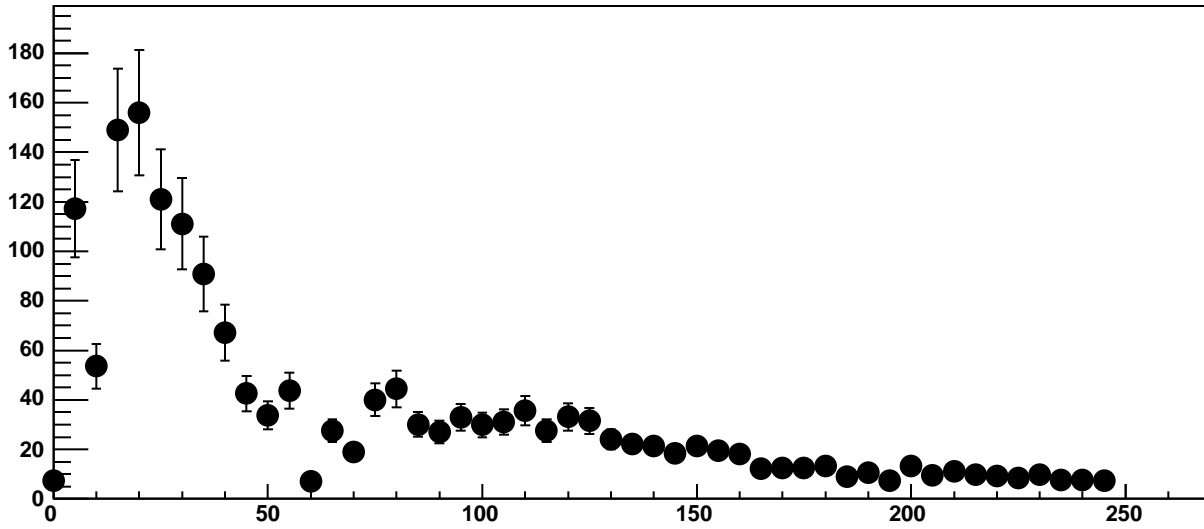


Chip 6, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold

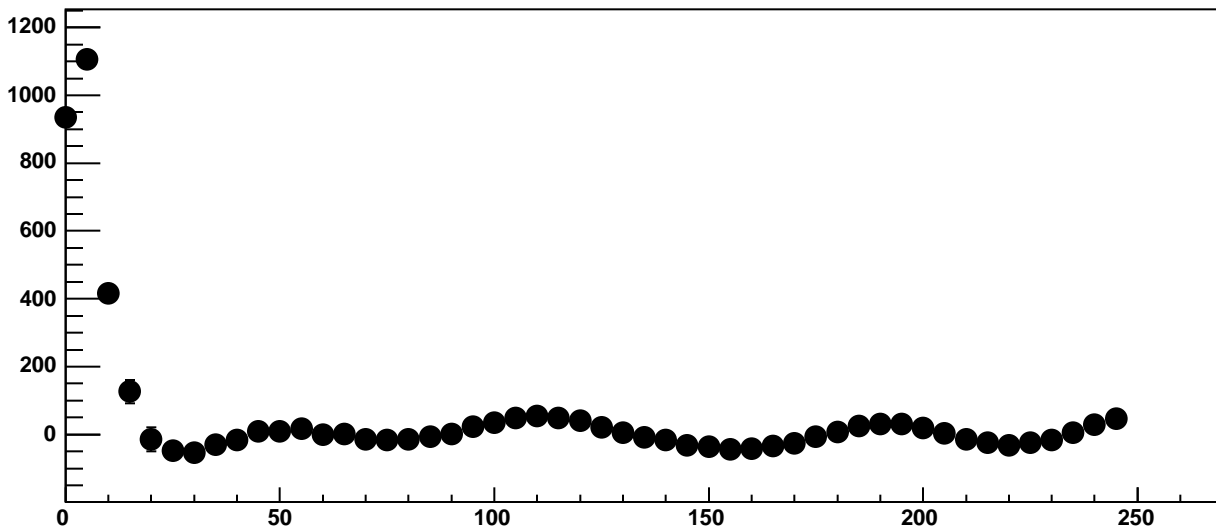


χ^2 / ndf	2368 / 42
p0	-1271 ± 0.8754
p1	3.723 ± 0.05181
p2	1.013e+04 ± 5.517
p3	30.33 ± 0.01739

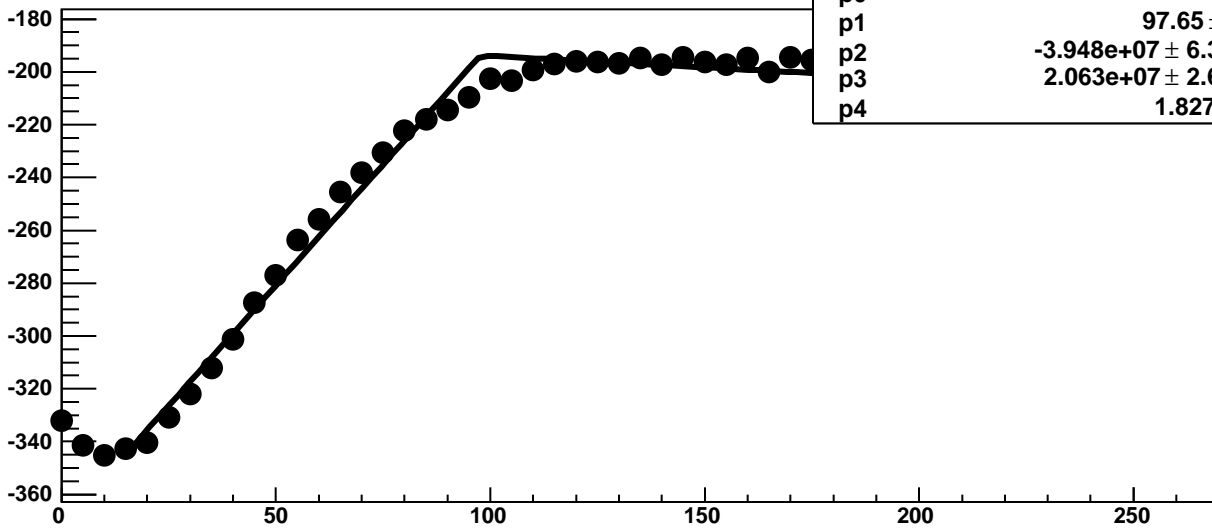
Chip 6, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold

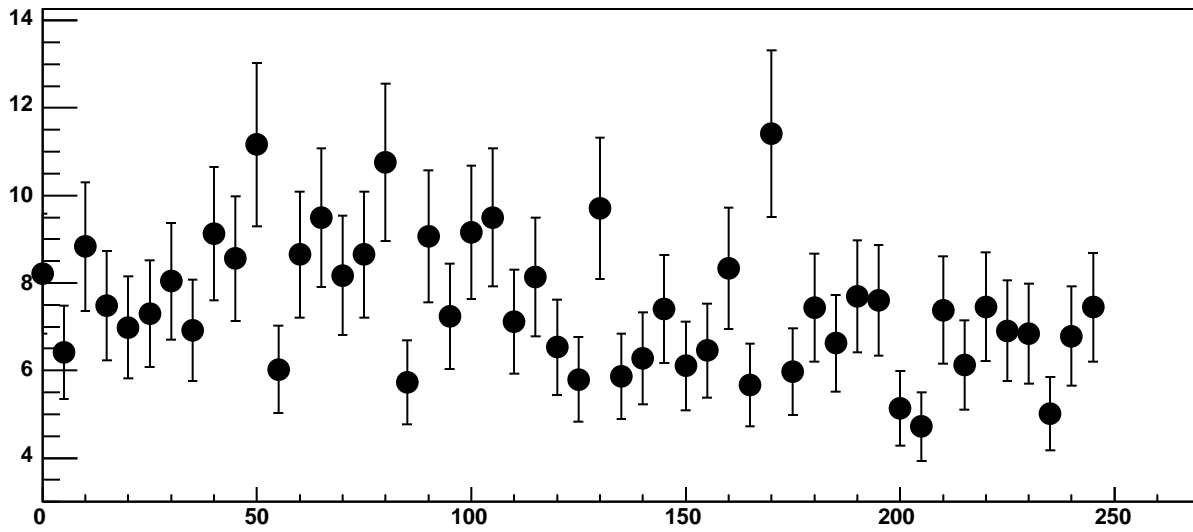


Chip 6, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

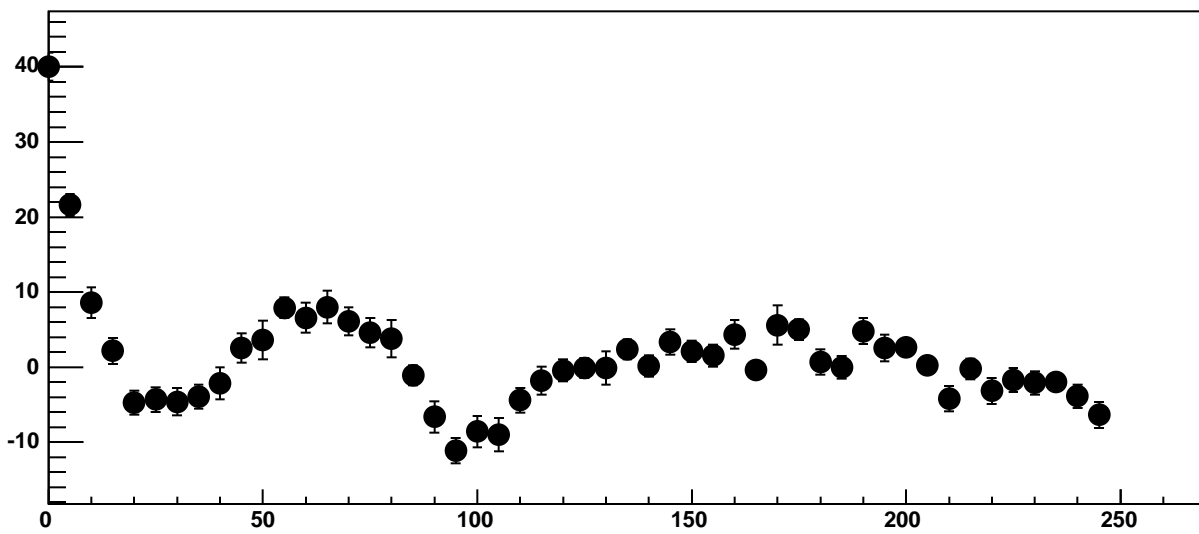


χ^2 / ndf	277.4 / 41
p0	-193.7 ± 0.5859
p1	97.65 ± 0.5487
p2	$-3.948\text{e}+07 \pm 6.371\text{e}+05$
p3	$2.063\text{e}+07 \pm 2.687\text{e}+05$
p4	1.827 ± 0.017

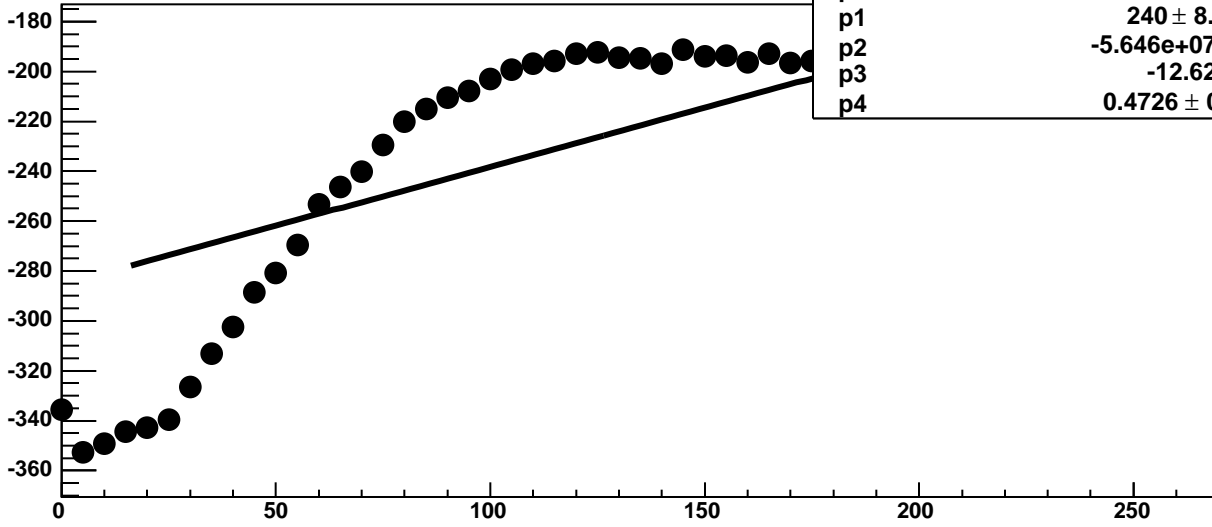
Chip 6, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

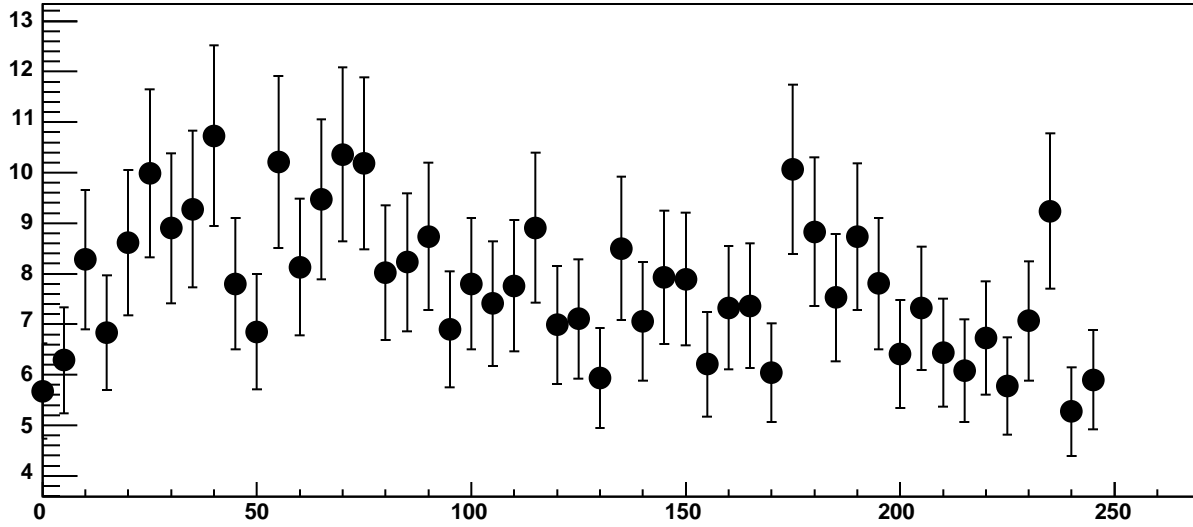


Chip 6, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

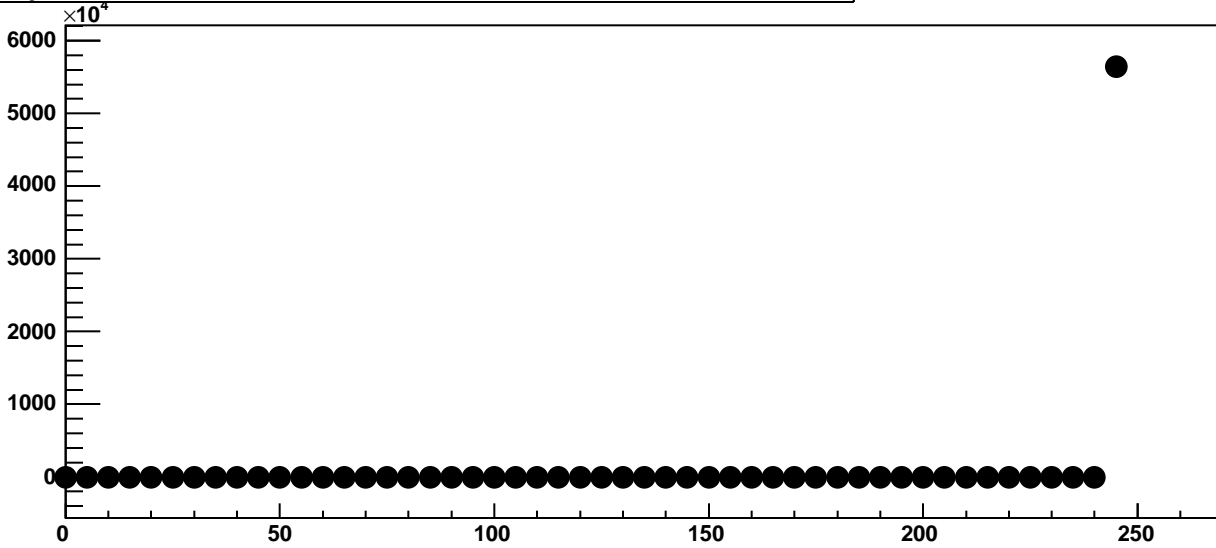


χ^2 / ndf	1.31e+04 / 41
p0	-172.1 ± 0.2551
p1	240 ± 8.401e-10
p2	-5.646e+07 ± 84.85
p3	-12.62 ± 1.414
p4	0.4726 ± 0.002121

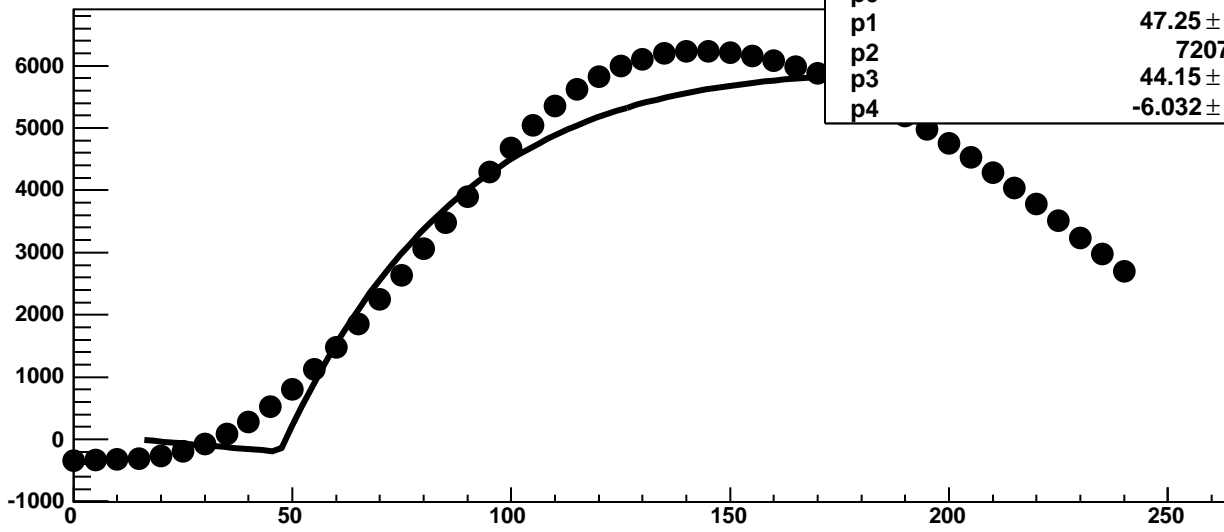
Chip 6, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

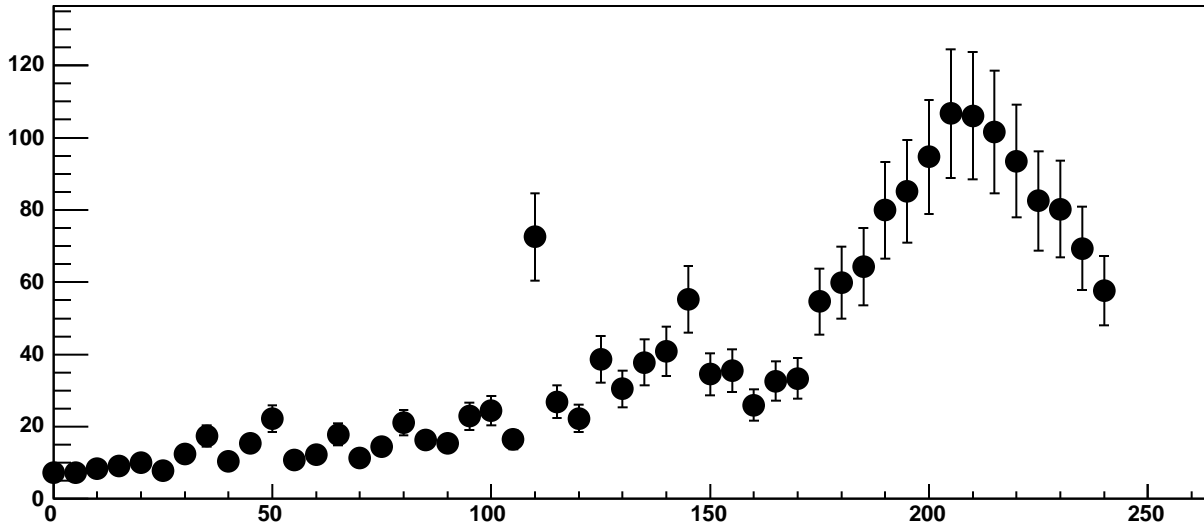


Chip 6, Channel 6, Enable 5, DAC=4000, ADC Mean vs Hold

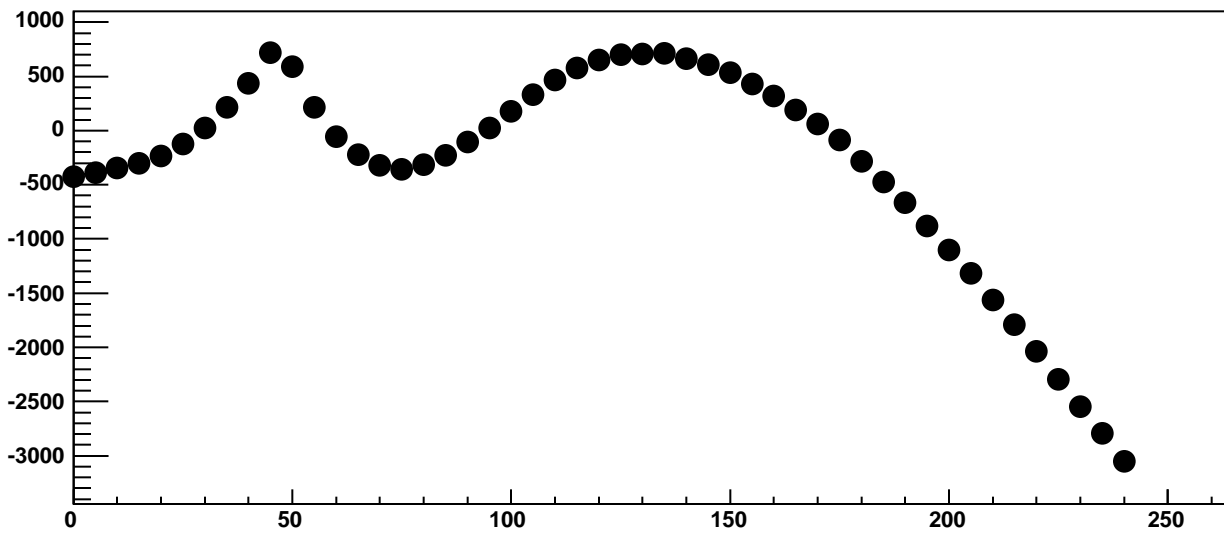


χ^2 / ndf	3.972e+05 / 41
p0	-202.4 ± 1.58
p1	47.25 ± 0.01945
p2	7207 ± 11.76
p3	44.15 ± 0.07464
p4	-6.032 ± 0.06639

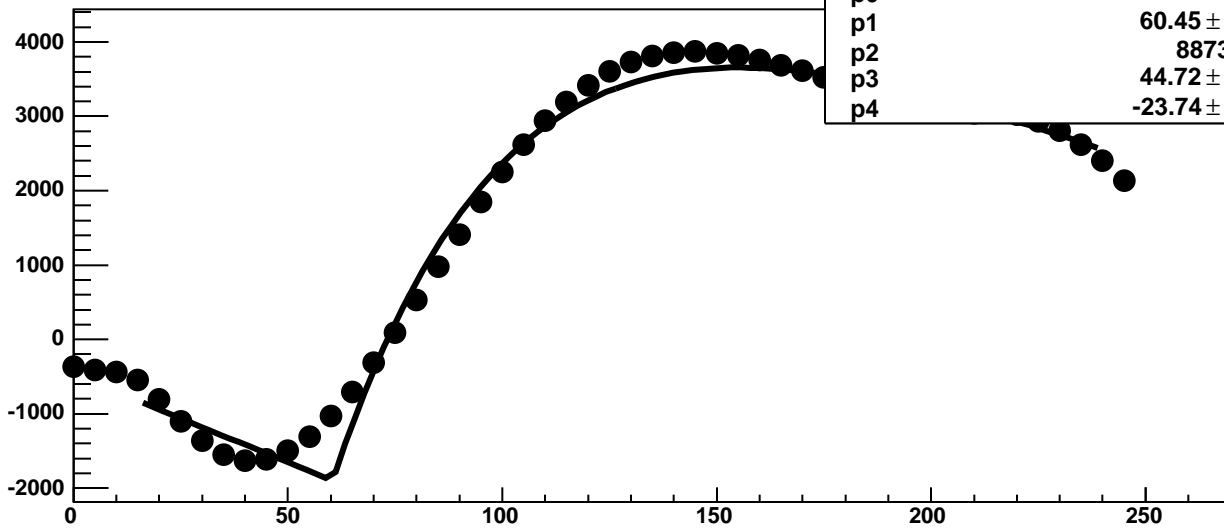
Chip 6, Channel 6, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 6, Enable 5, DAC=4000, ADC Residuals vs Hold

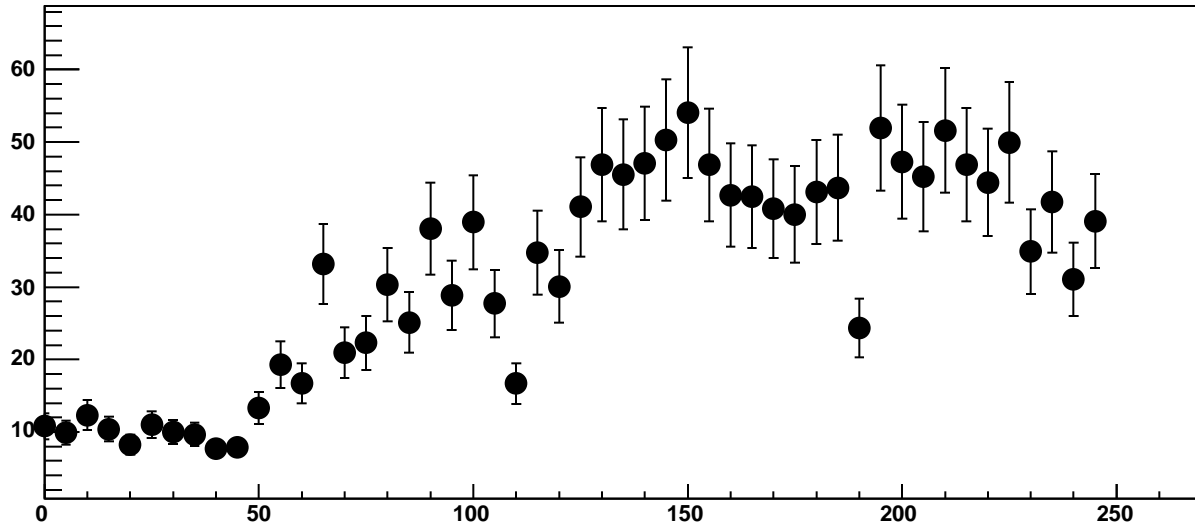


Chip 6, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

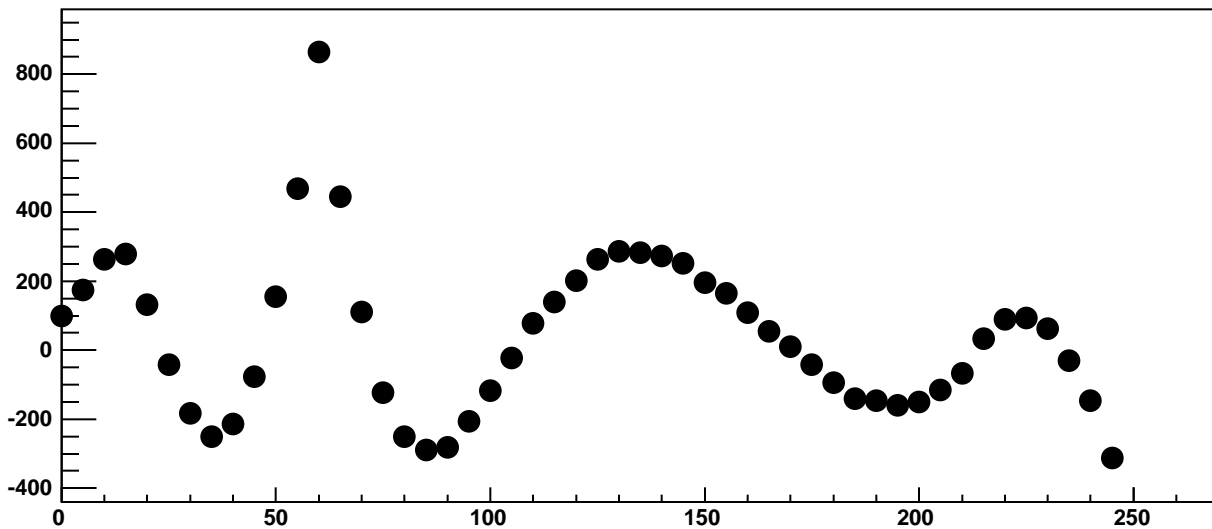


χ^2 / ndf	1.374e+05 / 41
p0	-1902 ± 1.473
p1	60.45 ± 0.02883
p2	8873 ± 11.77
p3	44.72 ± 0.08566
p4	-23.74 ± 0.05534

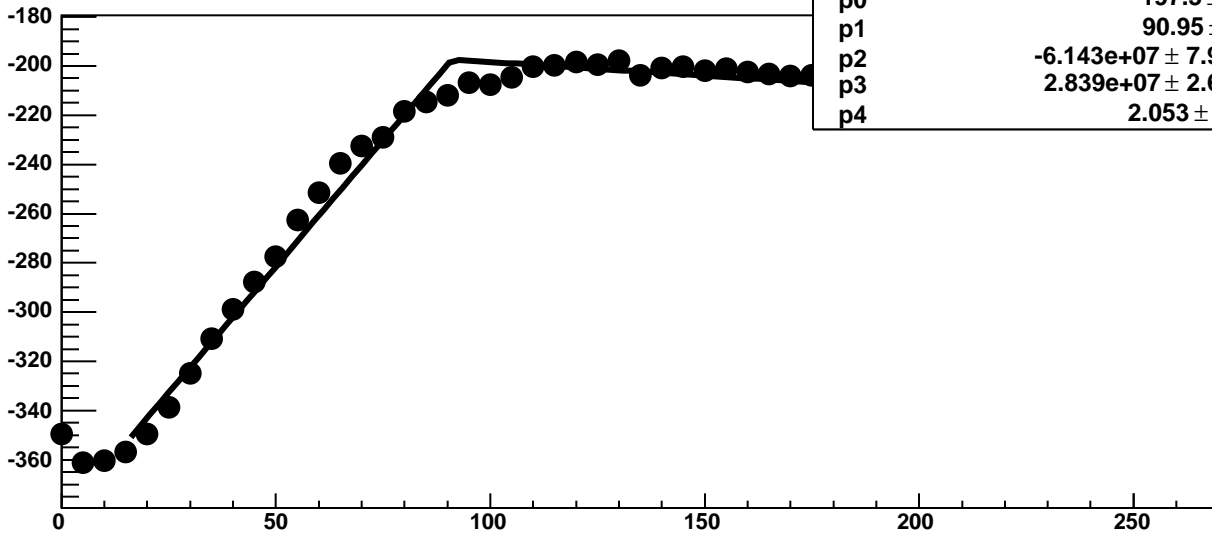
Chip 6, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

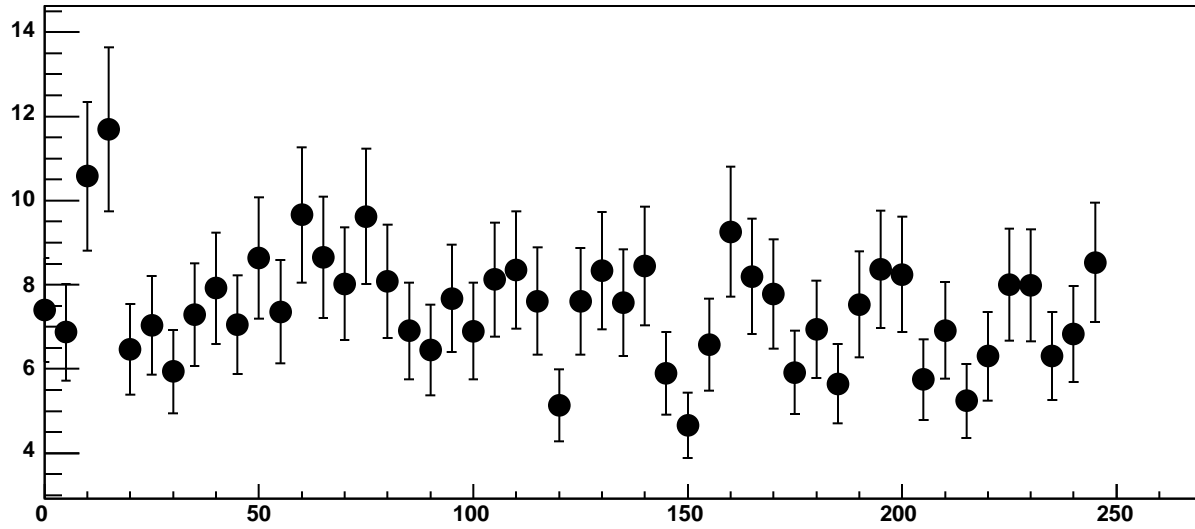


Chip 6, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold

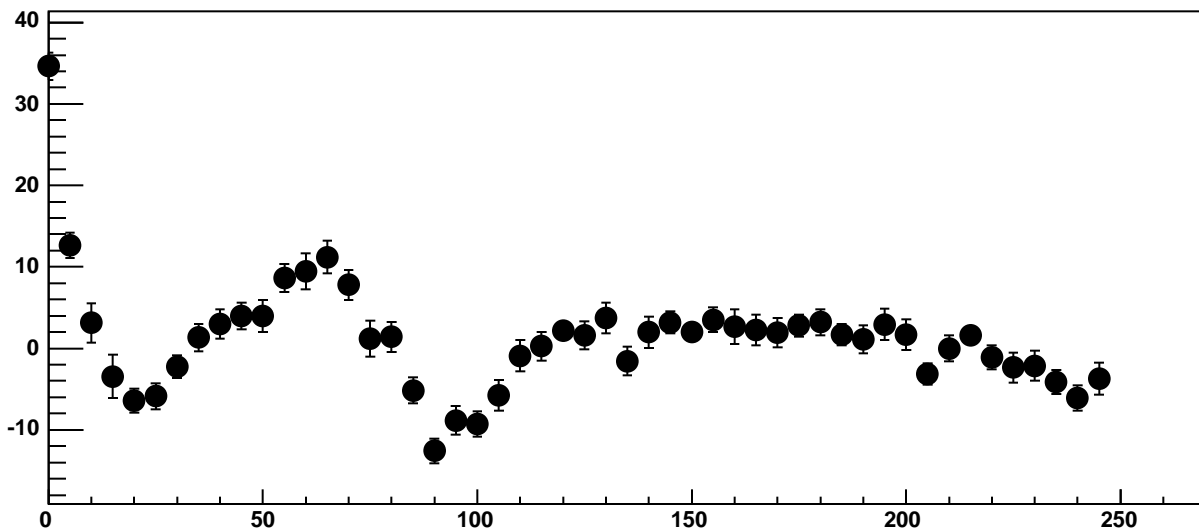


χ^2 / ndf	373.1 / 41
p0	-197.3 ± 0.5726
p1	90.95 ± 0.4783
p2	$-6.143\text{e}+07 \pm 7.971\text{e}+05$
p3	$2.839\text{e}+07 \pm 2.671\text{e}+05$
p4	2.053 ± 0.01843

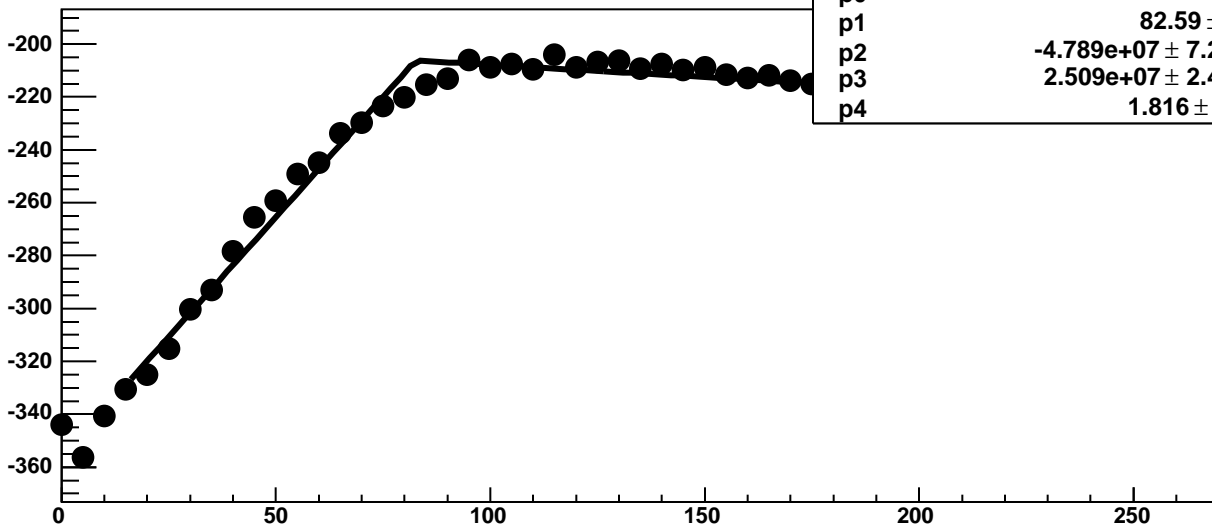
Chip 6, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

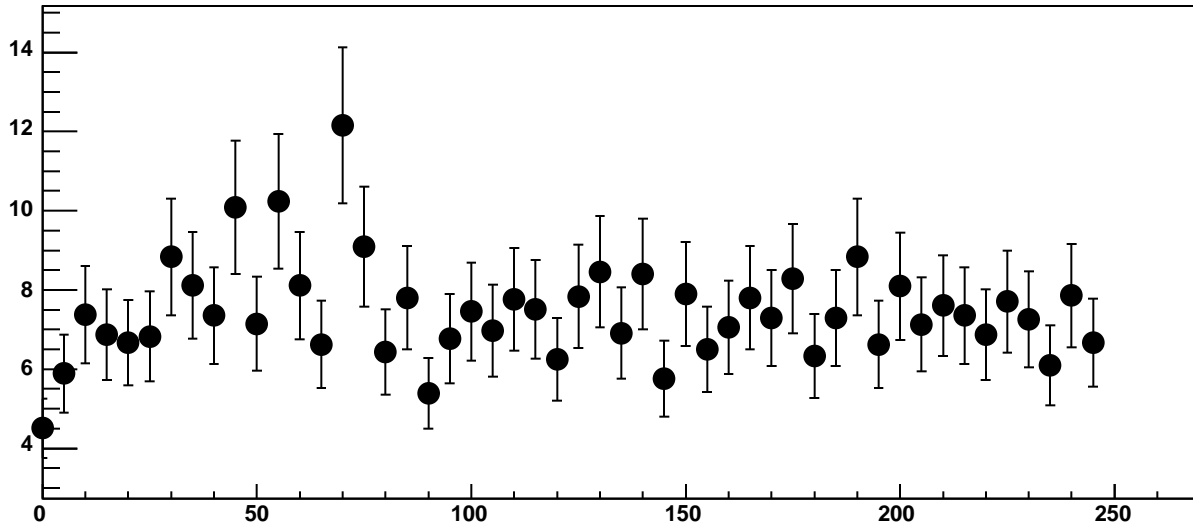


Chip 6, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold

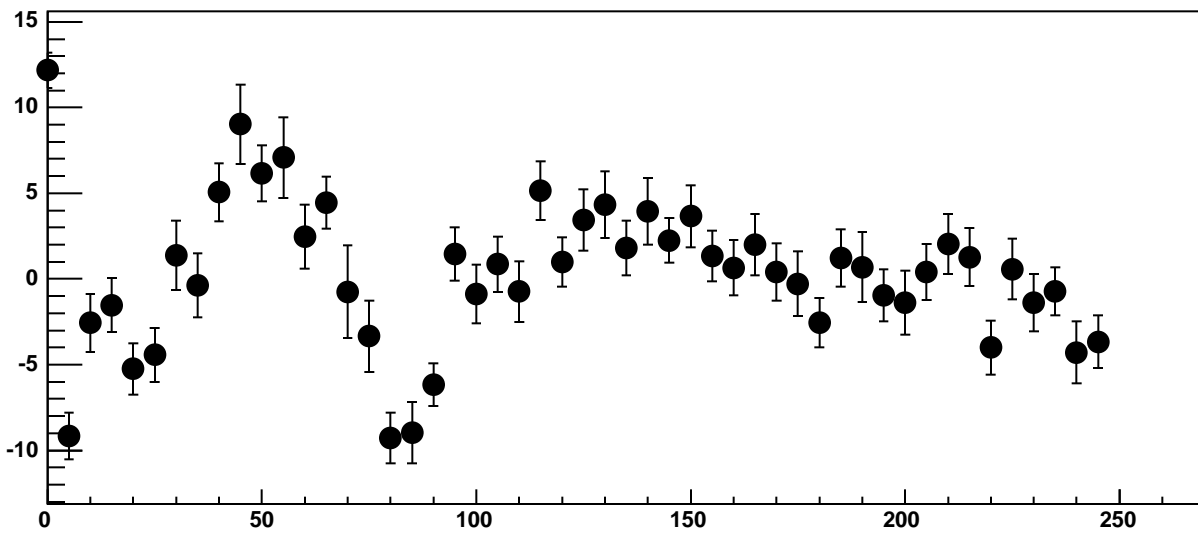


χ^2 / ndf	225.9 / 41
p0	-206.2 ± 0.5381
p1	82.59 ± 0.5785
p2	-4.789e+07 ± 7.287e+05
p3	2.509e+07 ± 2.433e+05
p4	1.816 ± 0.02228

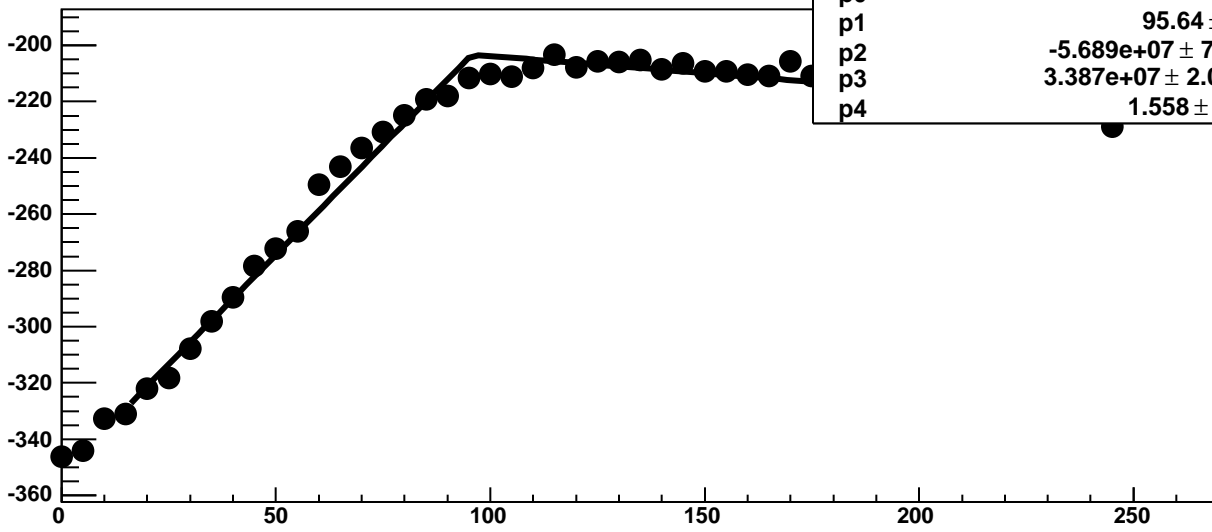
Chip 6, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold

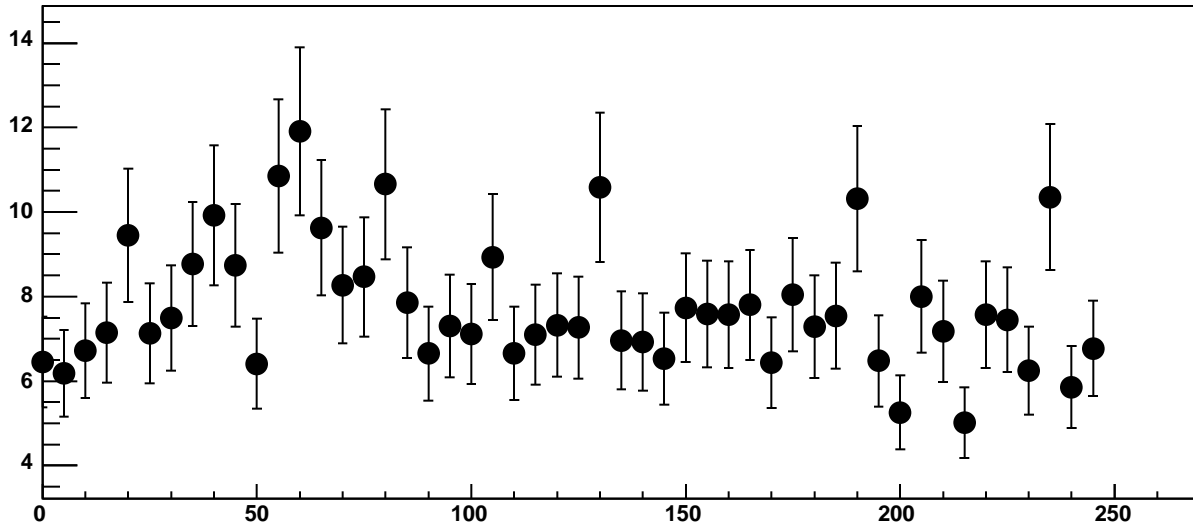


Chip 6, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

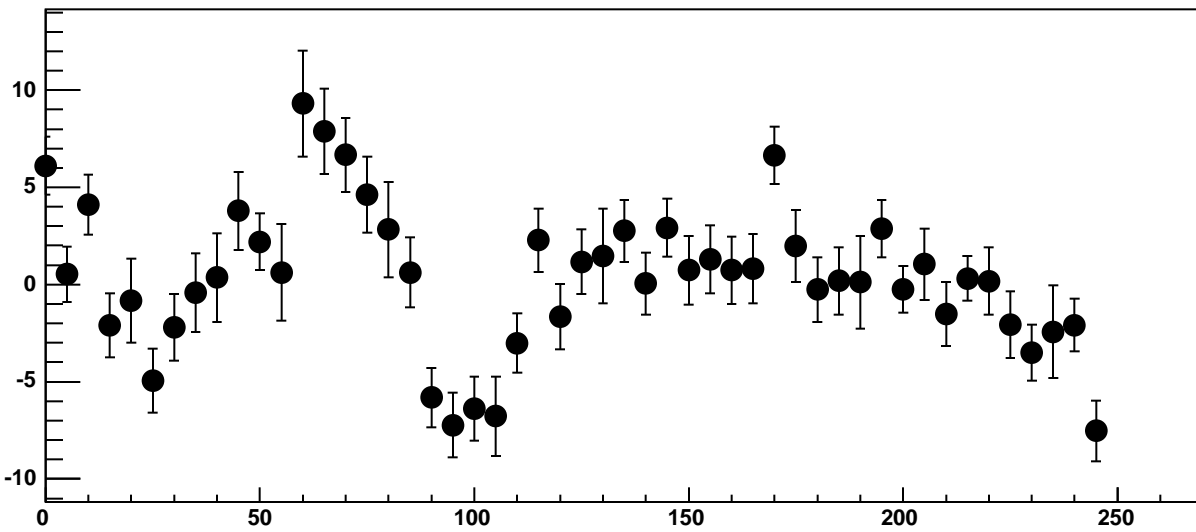


χ^2 / ndf	174.6 / 41
p0	-203.3 ± 0.5948
p1	95.64 ± 0.6313
p2	$-5.689\text{e}+07 \pm 7.28\text{e}+05$
p3	$3.387\text{e}+07 \pm 2.039\text{e}+05$
p4	1.558 ± 0.01755

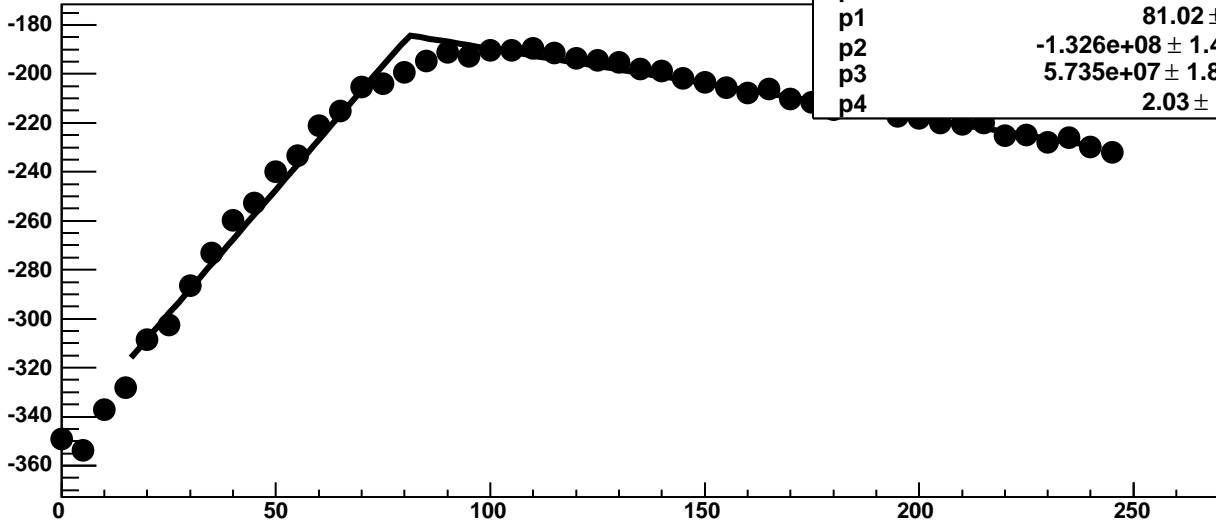
Chip 6, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

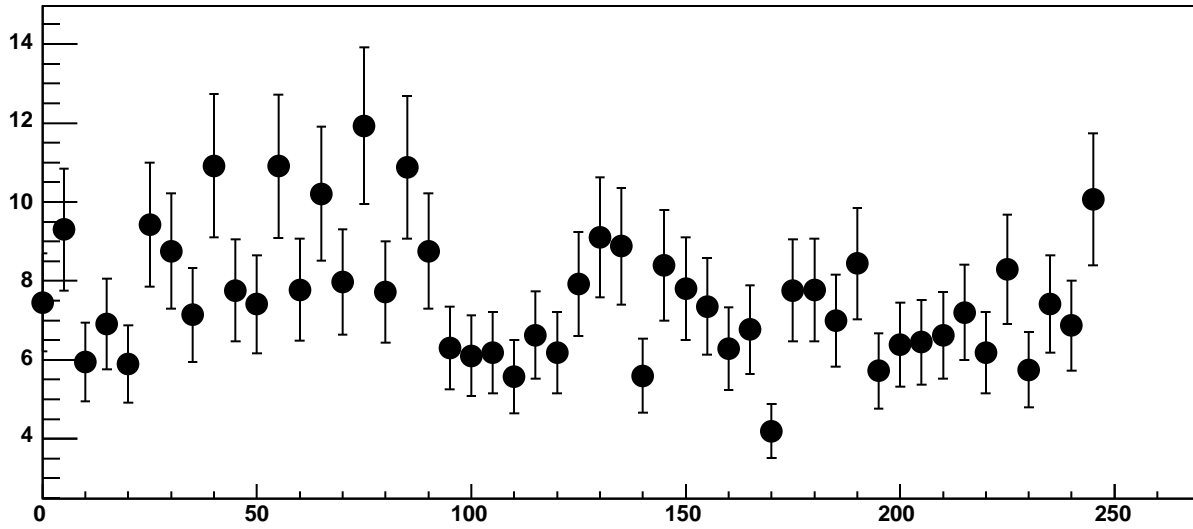


Chip 6, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

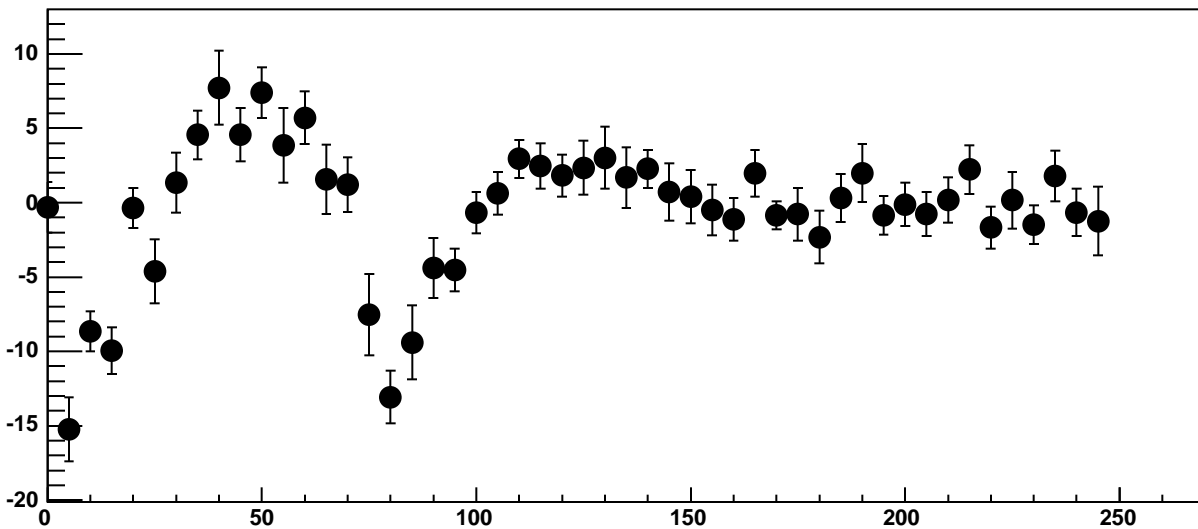


χ^2 / ndf	222 / 41
p0	-184.3 ± 0.5166
p1	81.02 ± 0.5048
p2	-1.326e+08 ± 1.458e+06
p3	5.735e+07 ± 1.853e+05
p4	2.03 ± 0.02394

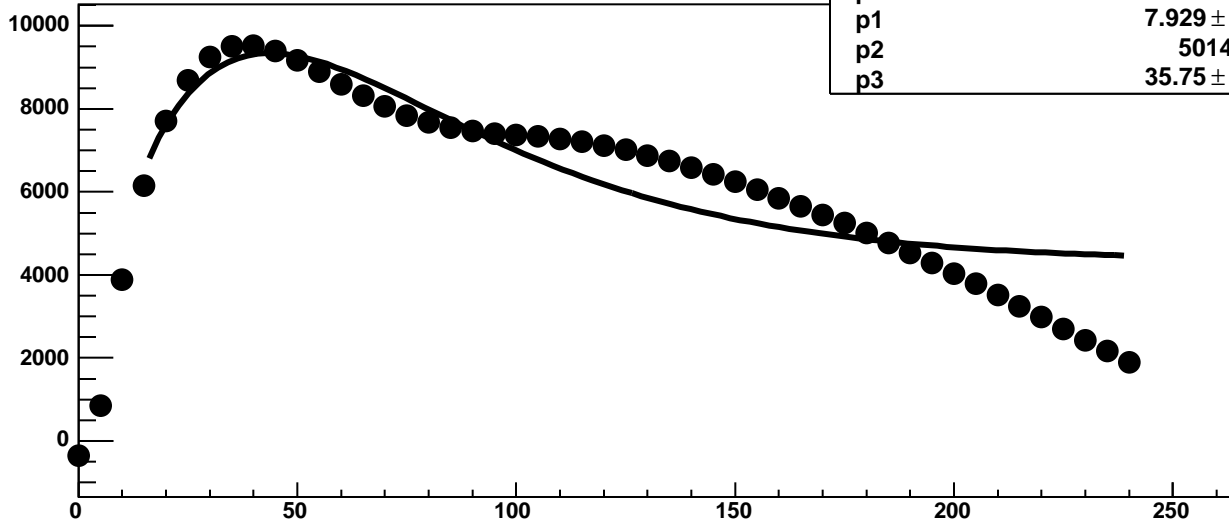
Chip 6, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

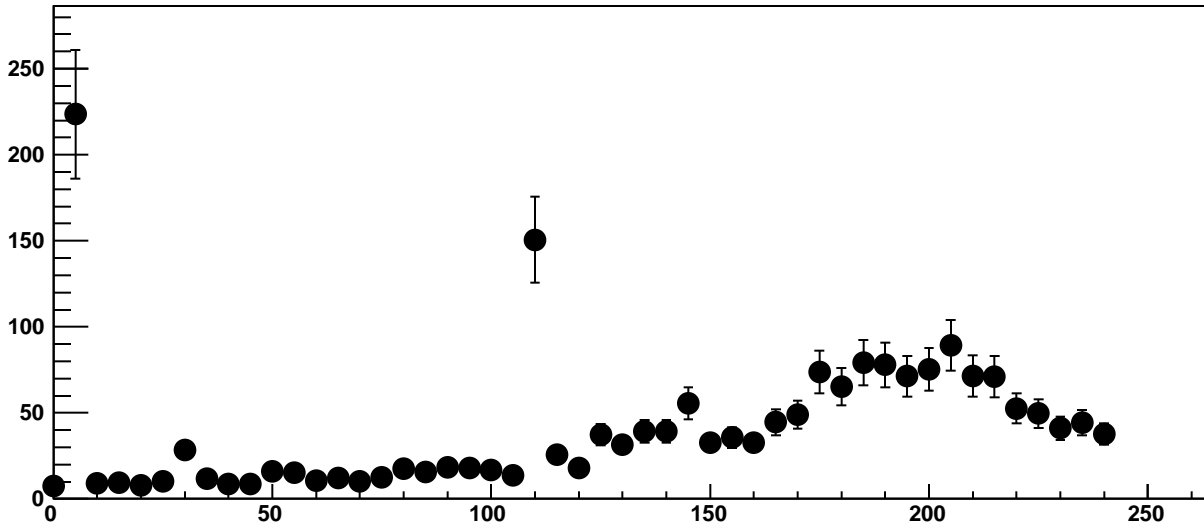


Chip 6, Channel 7, Enable 5!, DAC=4000, ADC Mean vs Hold

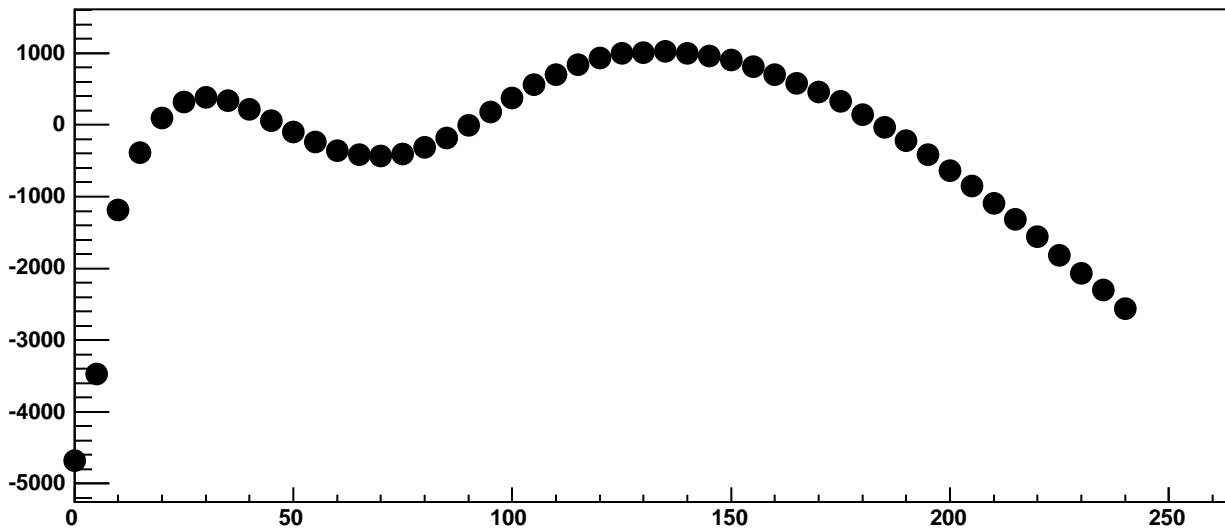


χ^2 / ndf	6.564e+05 / 42
p0	4328 ± 4.565
p1	7.929 ± 0.02248
p2	5014 ± 4.295
p3	35.75 ± 0.03455

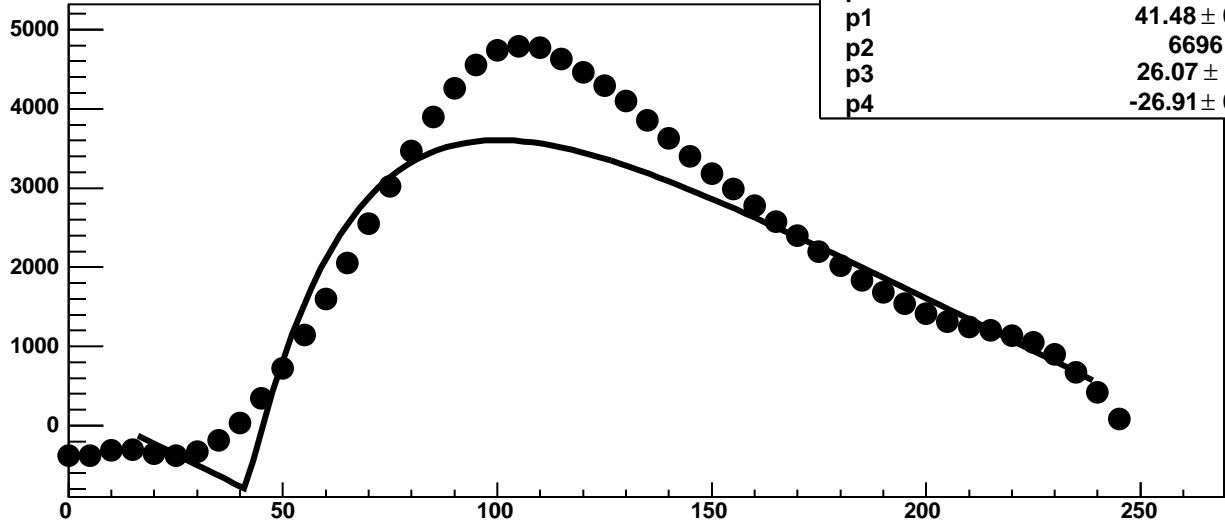
Chip 6, Channel 7, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 7, Enable 5!, DAC=4000, ADC Residuals vs Hold

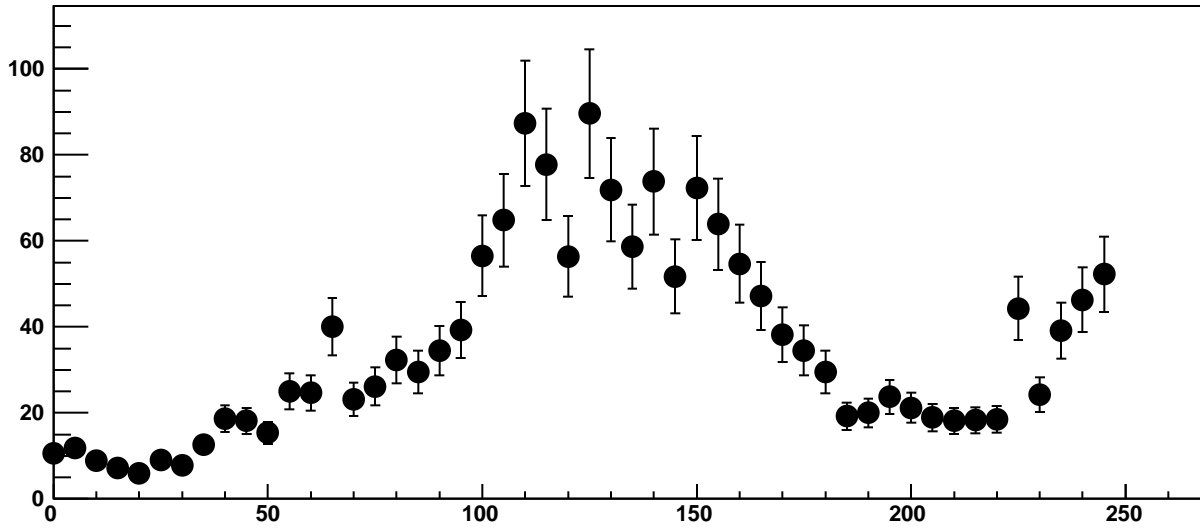


Chip 6, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

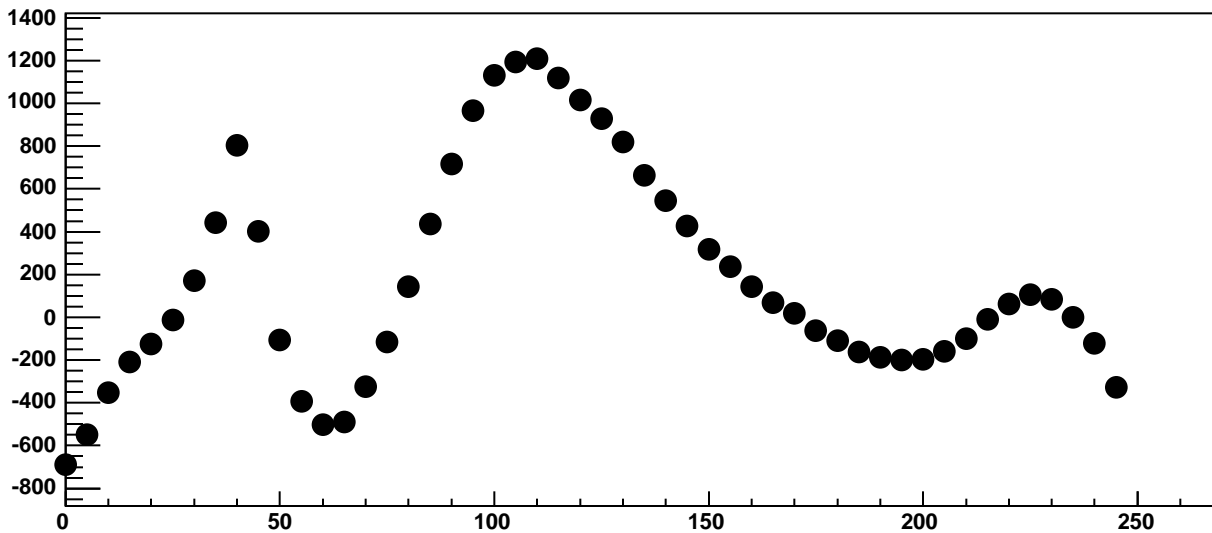


χ^2 / ndf	1.931e+05 / 41
p0	-806.8 ± 1.056
p1	41.48 ± 0.01772
p2	6696 ± 7.869
p3	26.07 ± 0.05041
p4	-26.91 ± 0.04339

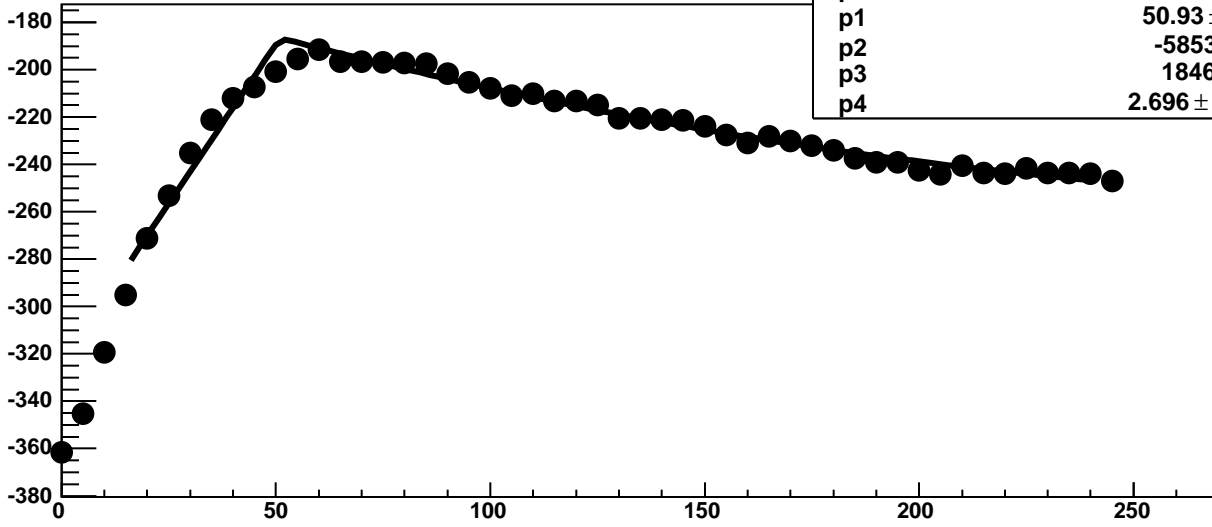
Chip 6, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



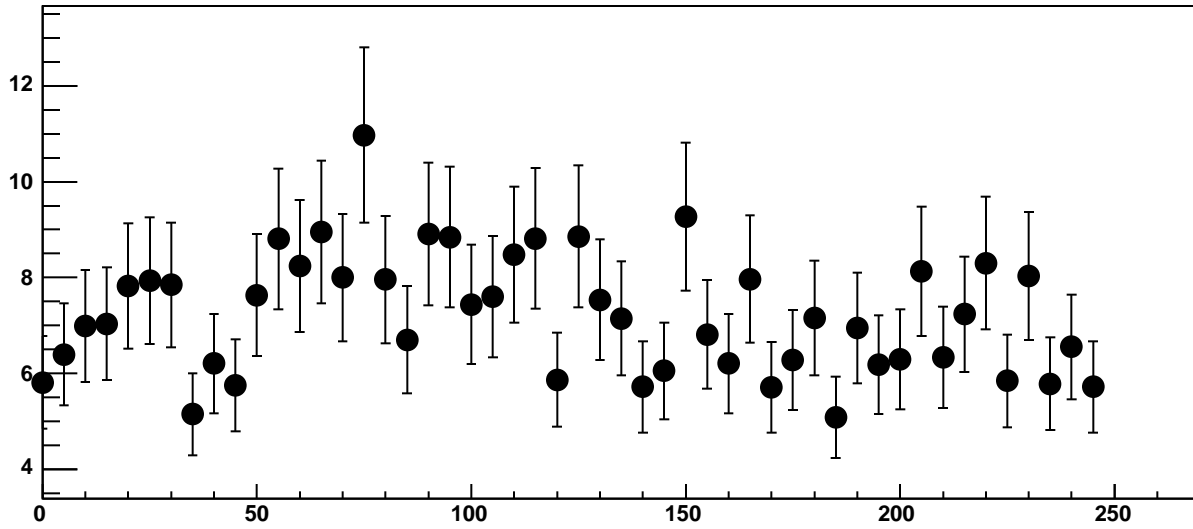
Chip 6, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold



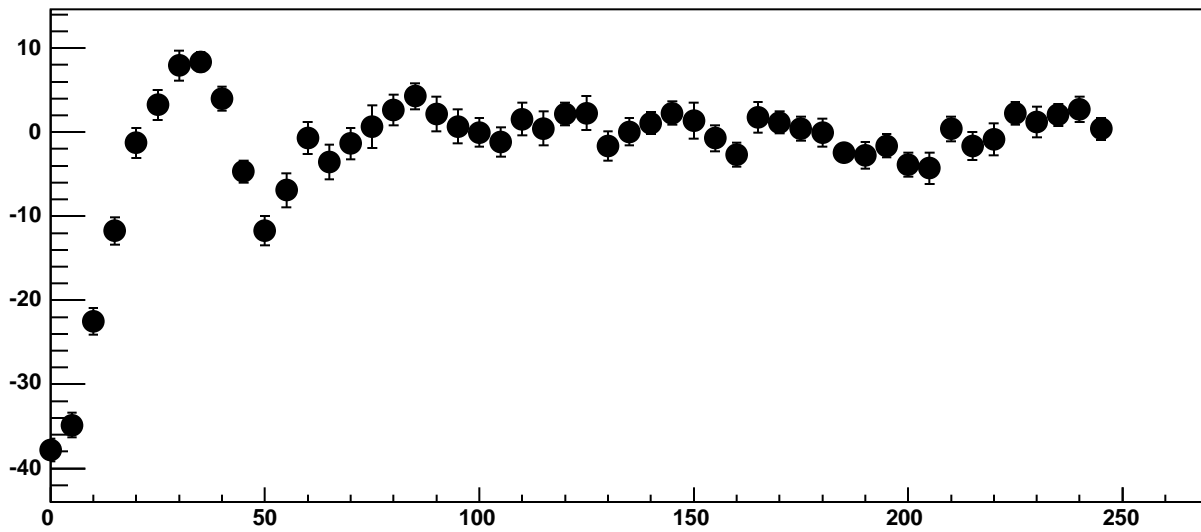
Chip 6, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold



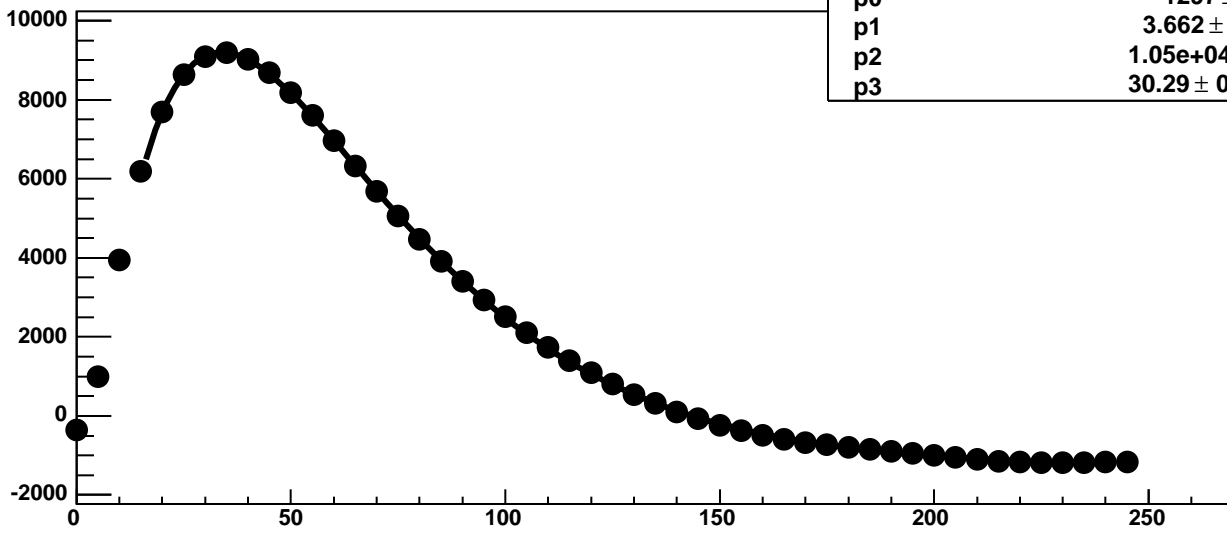
Chip 6, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

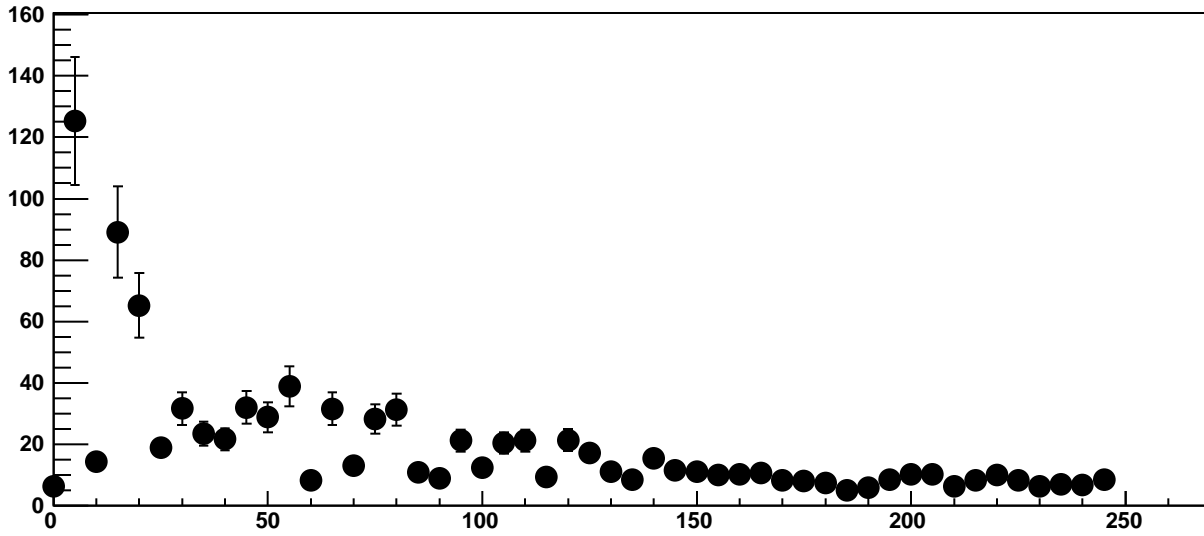


Chip 6, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold

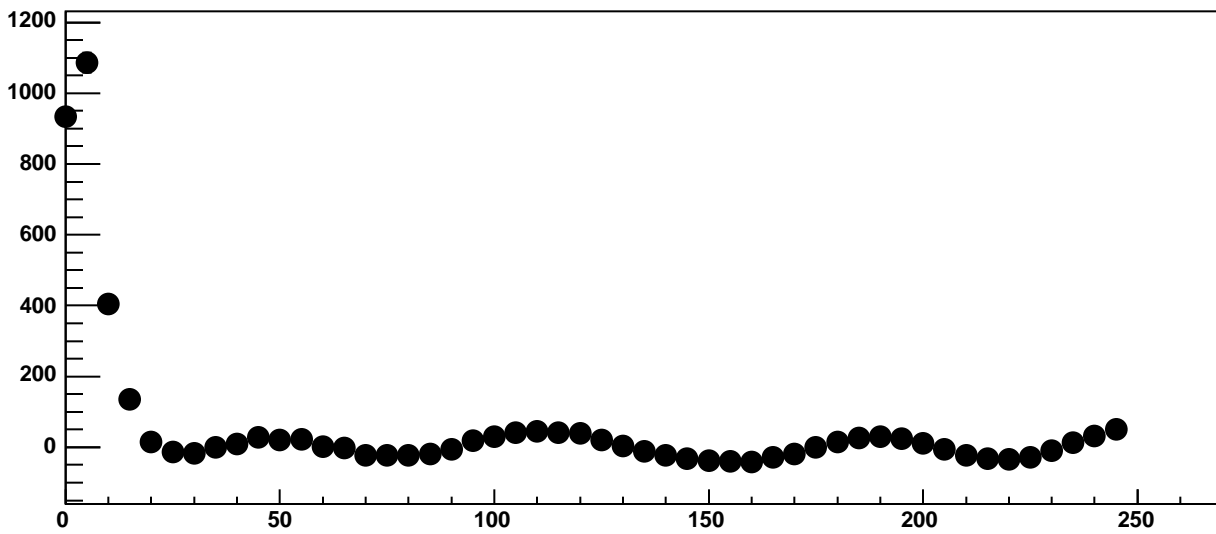


χ^2 / ndf	4958 / 42
p0	-1297 ± 0.6386
p1	3.662 ± 0.02178
p2	$1.05\text{e}+04 \pm 1.974$
p3	30.29 ± 0.009724

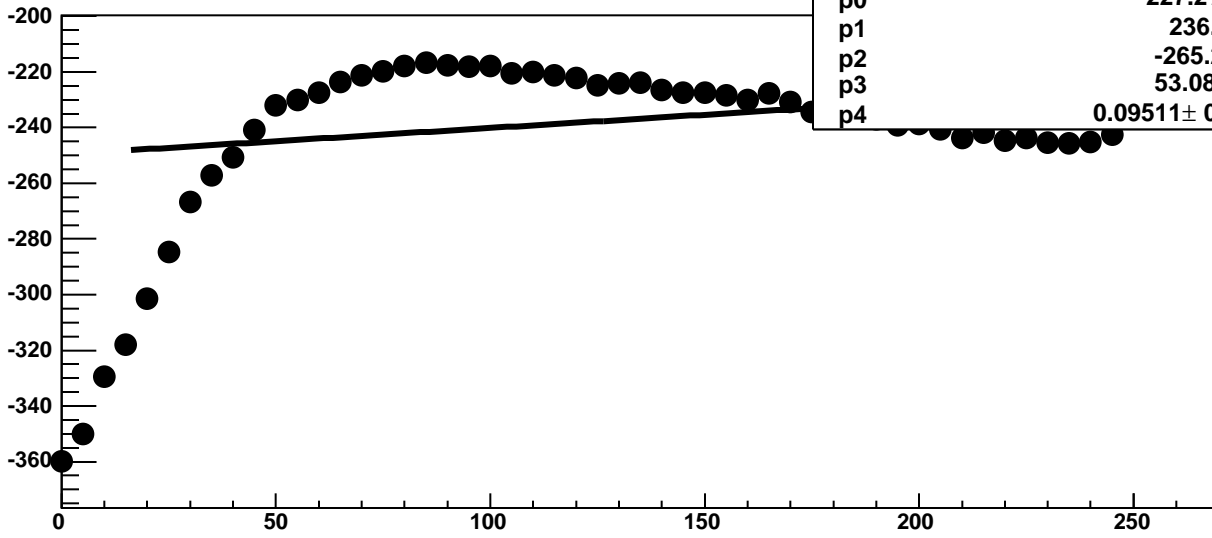
Chip 6, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold

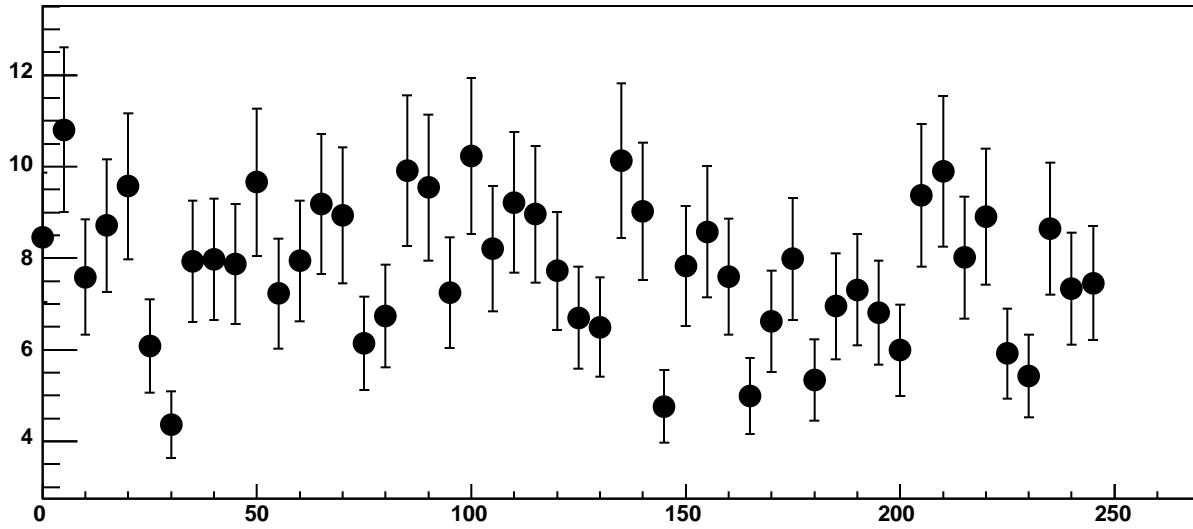


Chip 6, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

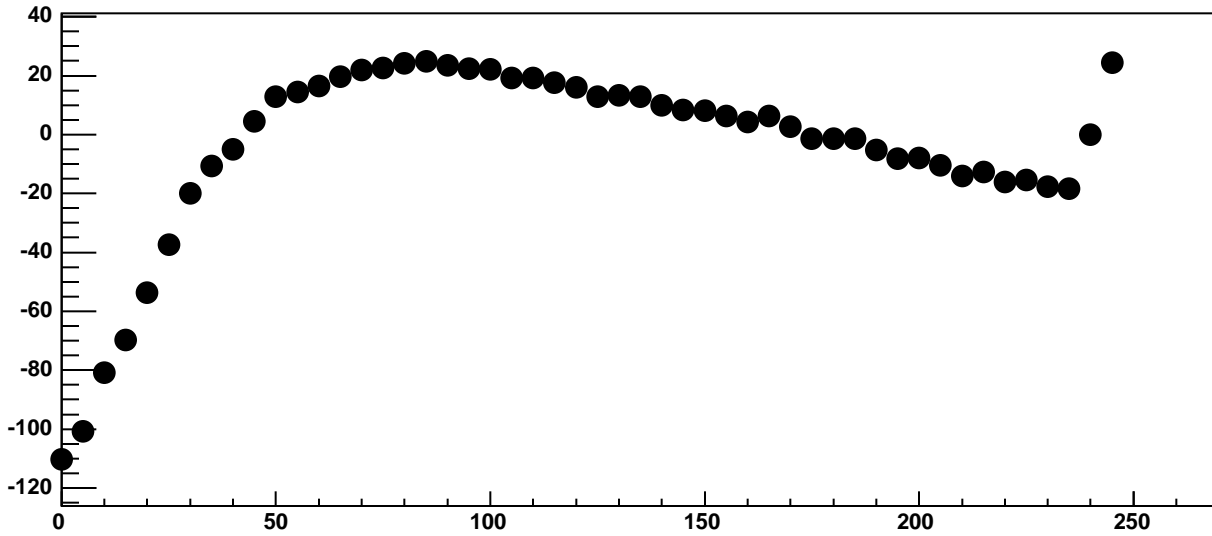


χ^2 / ndf	5713 / 41
p0	-227.2 ± 0.5741
p1	236.2 ± 3.5
p2	-265.2 ± 229
p3	53.08 ± 39.23
p4	0.09511 ± 0.003732

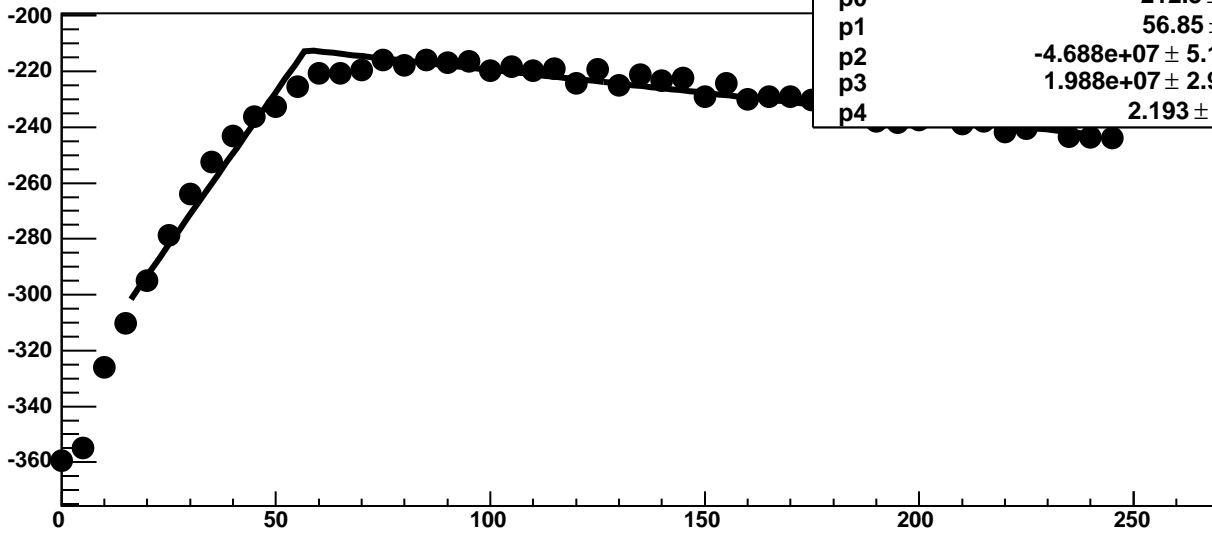
Chip 6, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

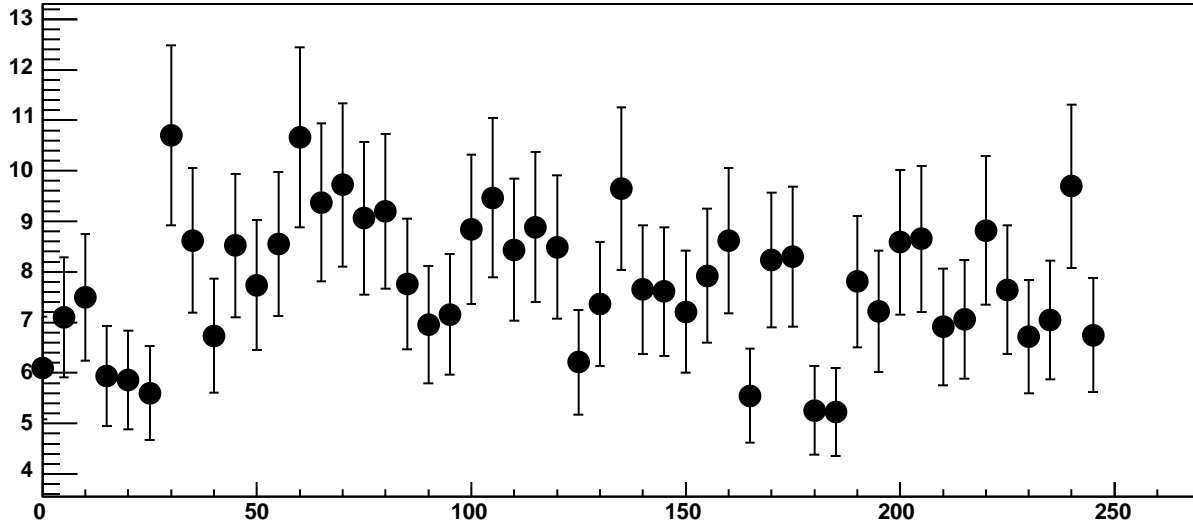


Chip 6, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

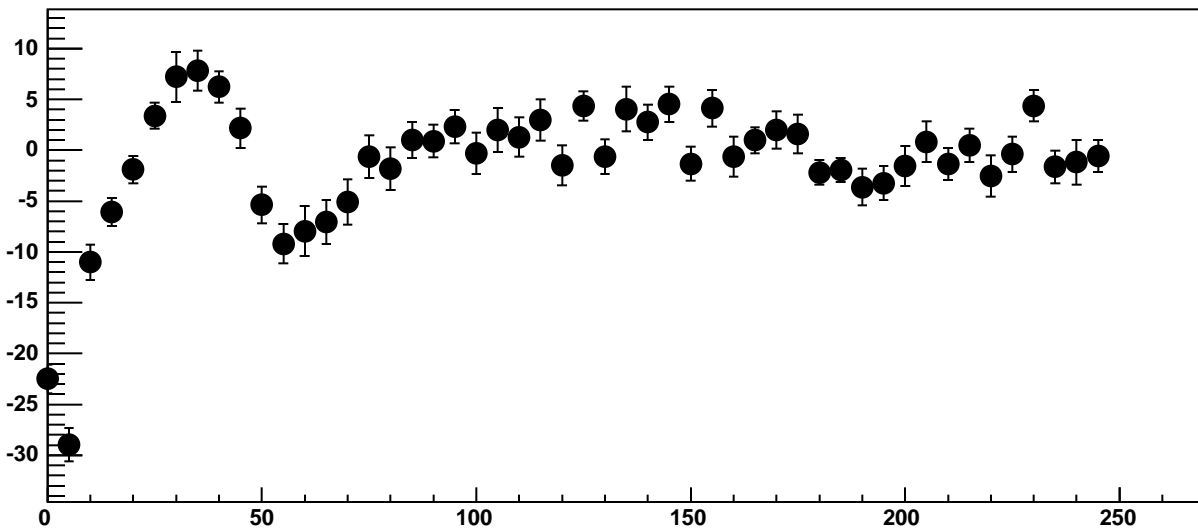


χ^2 / ndf	192.7 / 41
p0	-212.3 ± 0.5824
p1	56.85 ± 0.4133
p2	$-4.688\text{e}+07 \pm 5.176\text{e}+05$
p3	$1.988\text{e}+07 \pm 2.944\text{e}+05$
p4	2.193 ± 0.02163

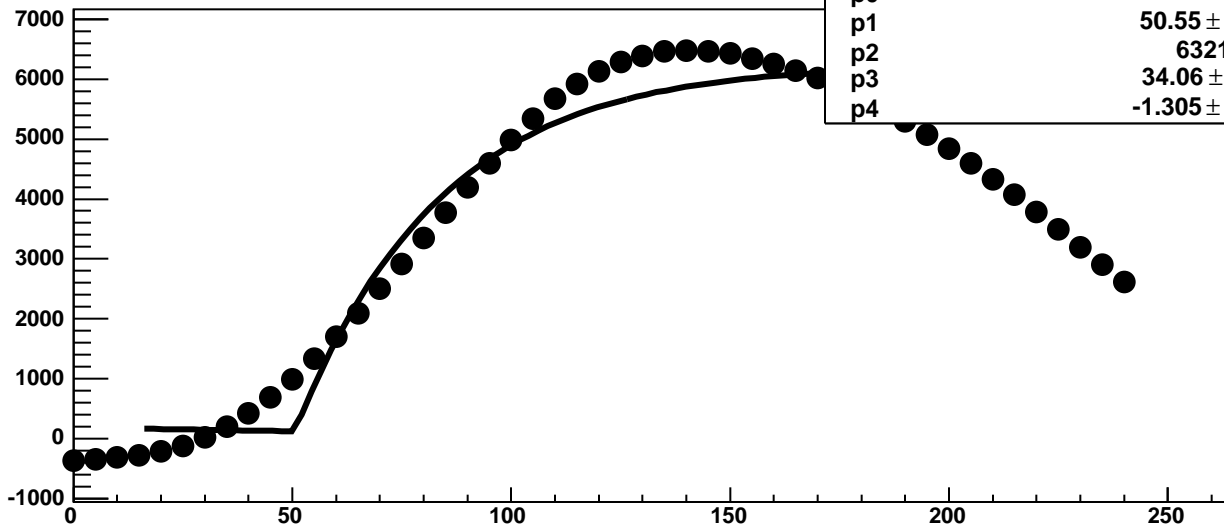
Chip 6, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

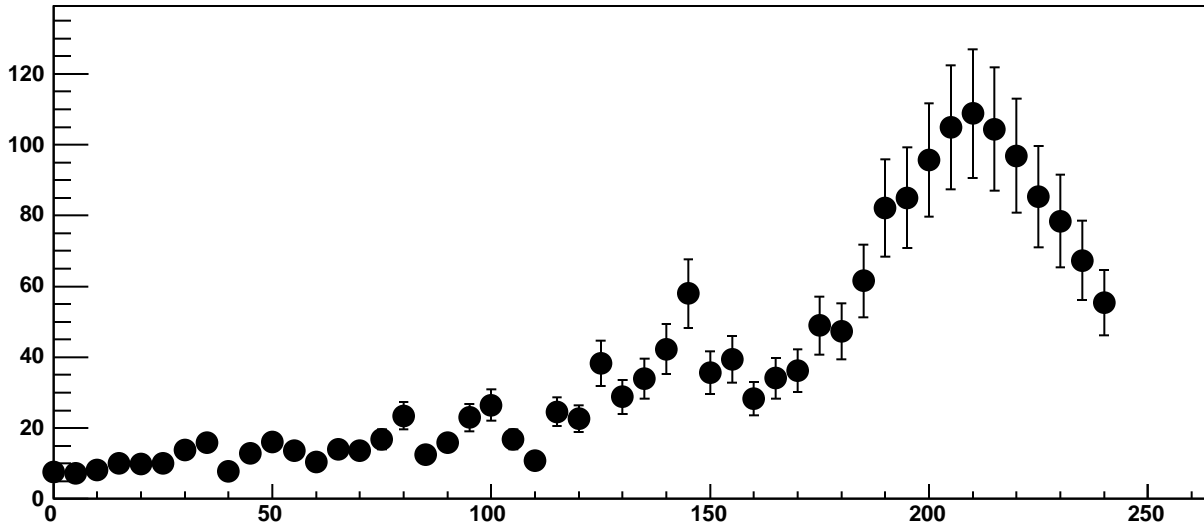


Chip 6, Channel 8, Enable 5, DAC=4000, ADC Mean vs Hold

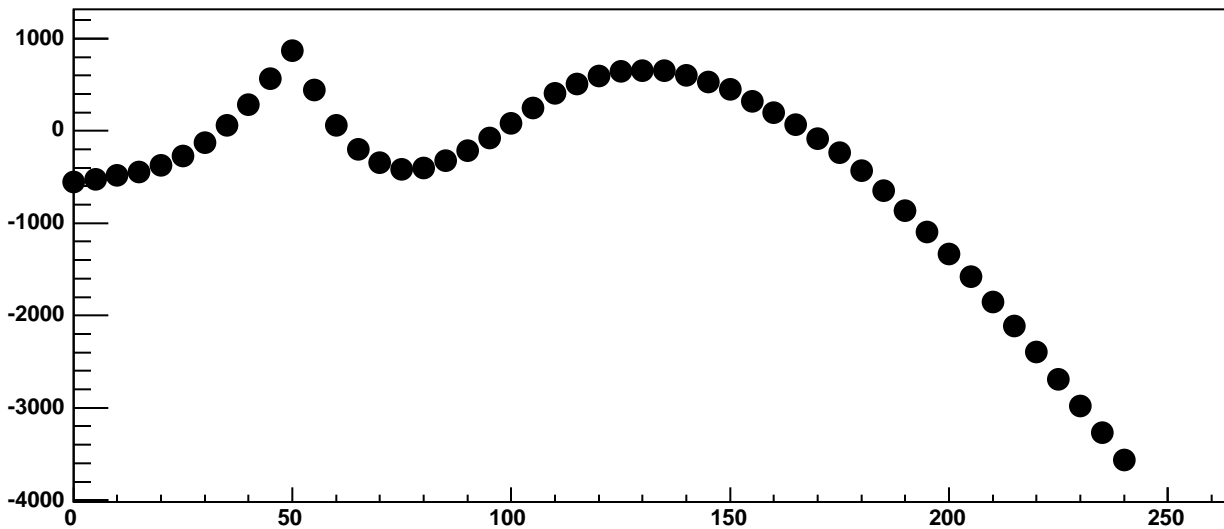


χ^2 / ndf	5.649e+05 / 41
p0	125.3 ± 1.384
p1	50.55 ± 0.01704
p2	6321 ± 8.048
p3	34.06 ± 0.05251
p4	-1.305 ± 0.05284

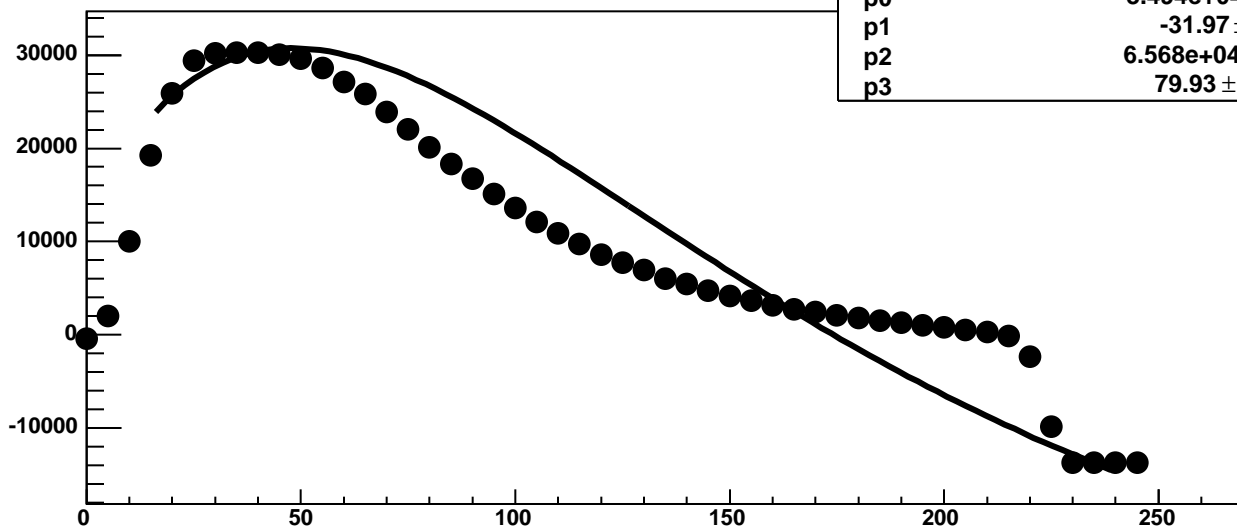
Chip 6, Channel 8, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 8, Enable 5, DAC=4000, ADC Residuals vs Hold

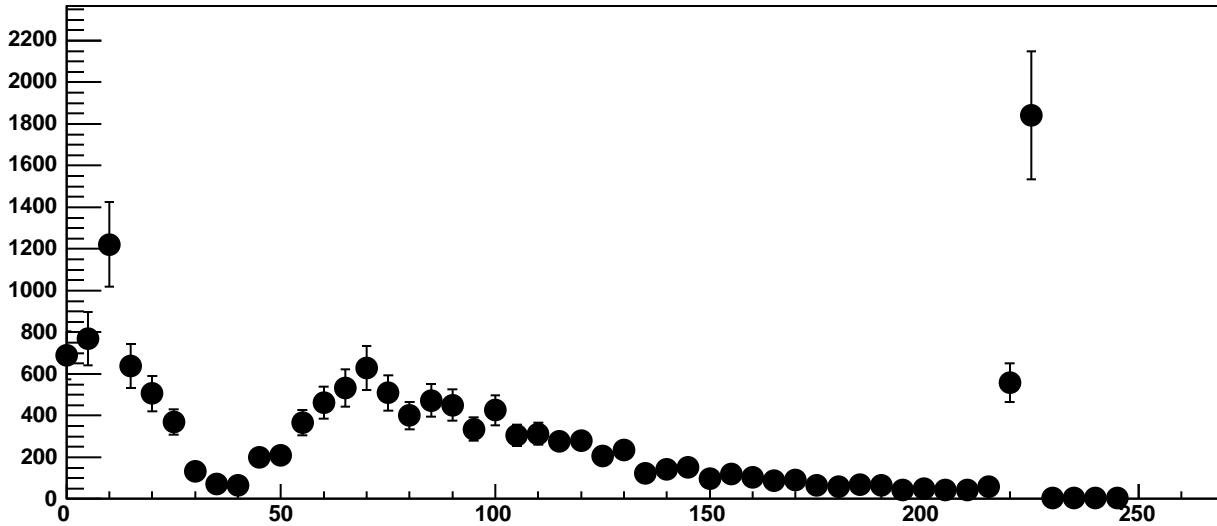


Chip 6, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold

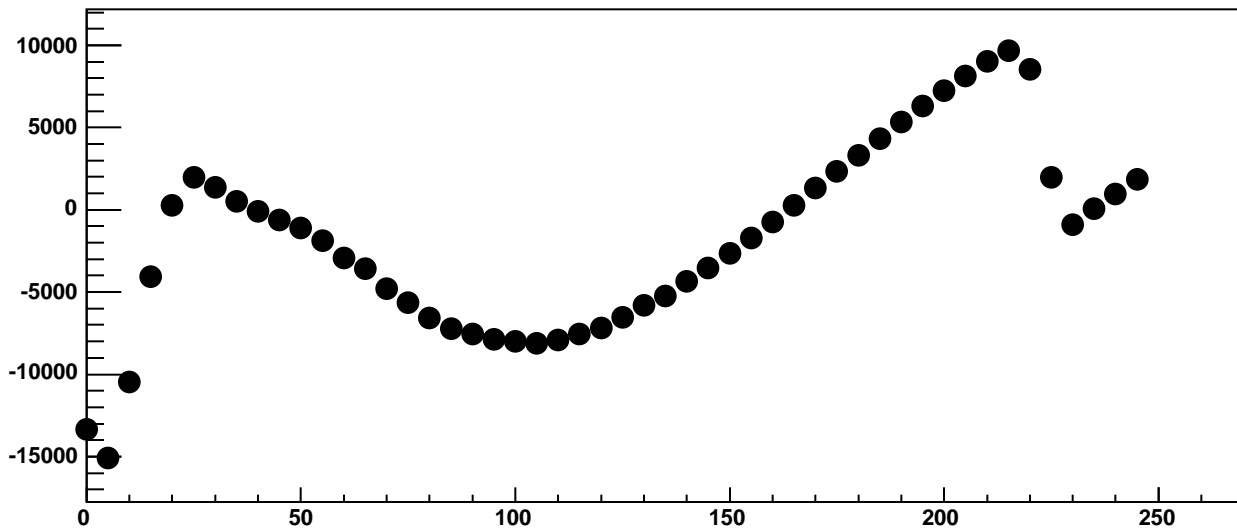


χ^2 / ndf	5.275e+06 / 42
p0	-3.494e+04 ± 31.51
p1	-31.97 ± 0.1237
p2	6.568e+04 ± 26.47
p3	79.93 ± 0.07331

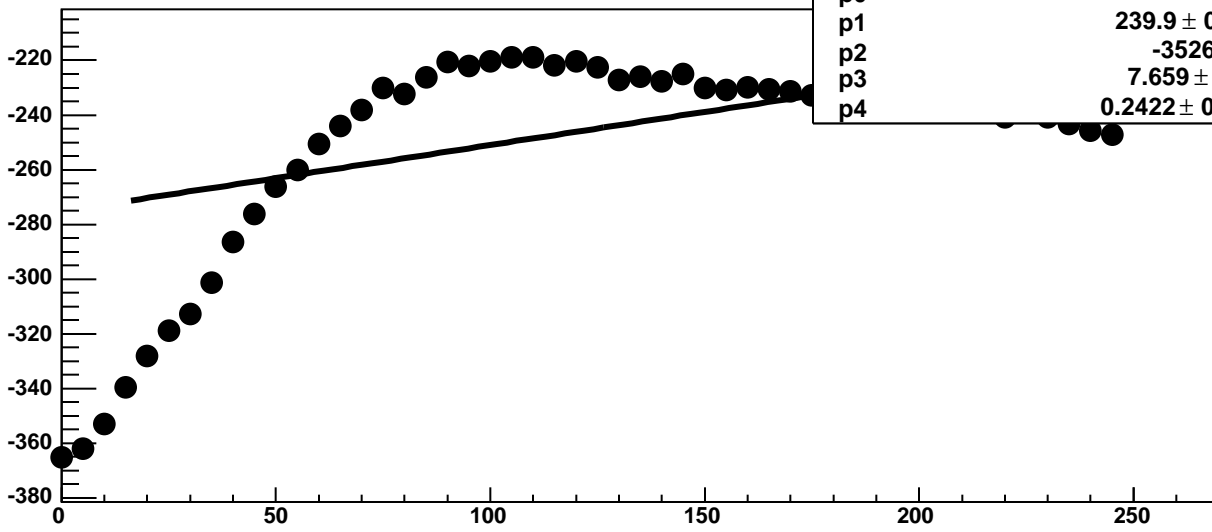
Chip 6, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold

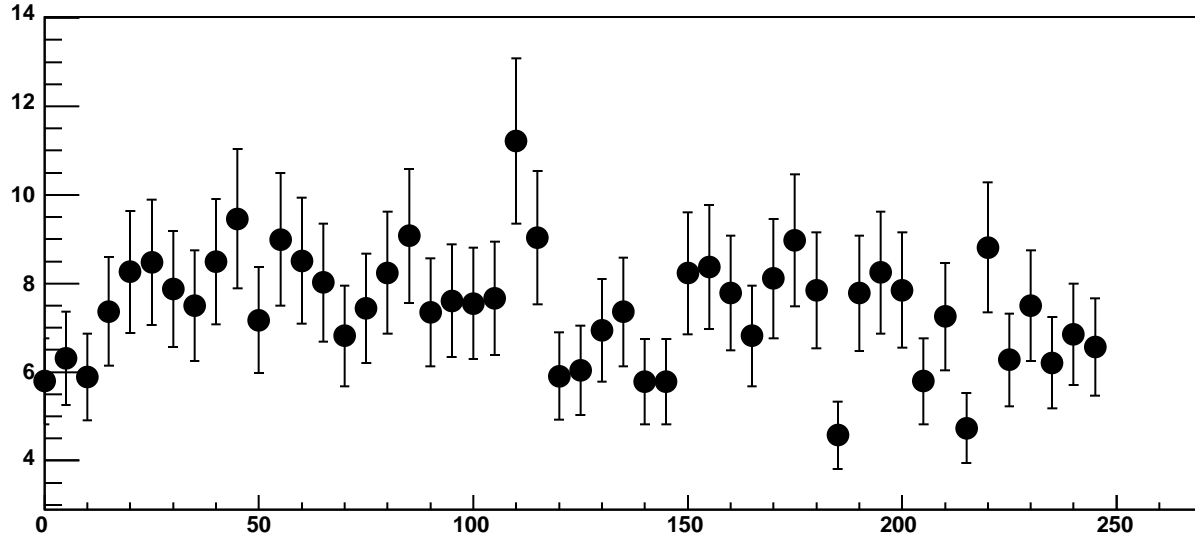


Chip 6, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

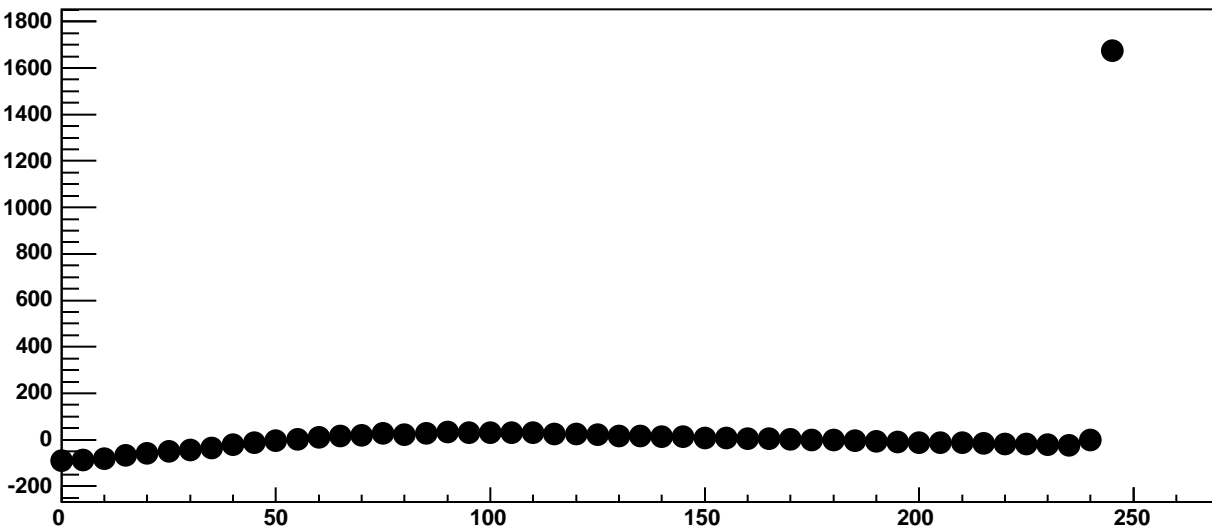


χ^2 / ndf	8993 / 41
p0	-217.1 ± 0.472
p1	239.9 ± 0.003842
p2	-3526 ± 10.09
p3	7.659 ± 0.04682
p4	0.2422 ± 0.003834

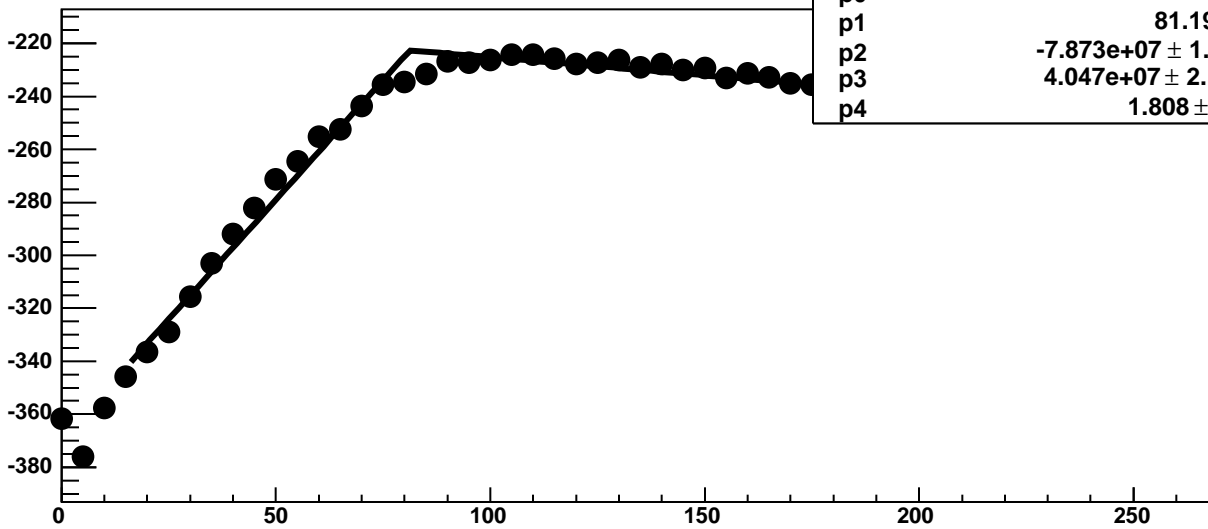
Chip 6, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

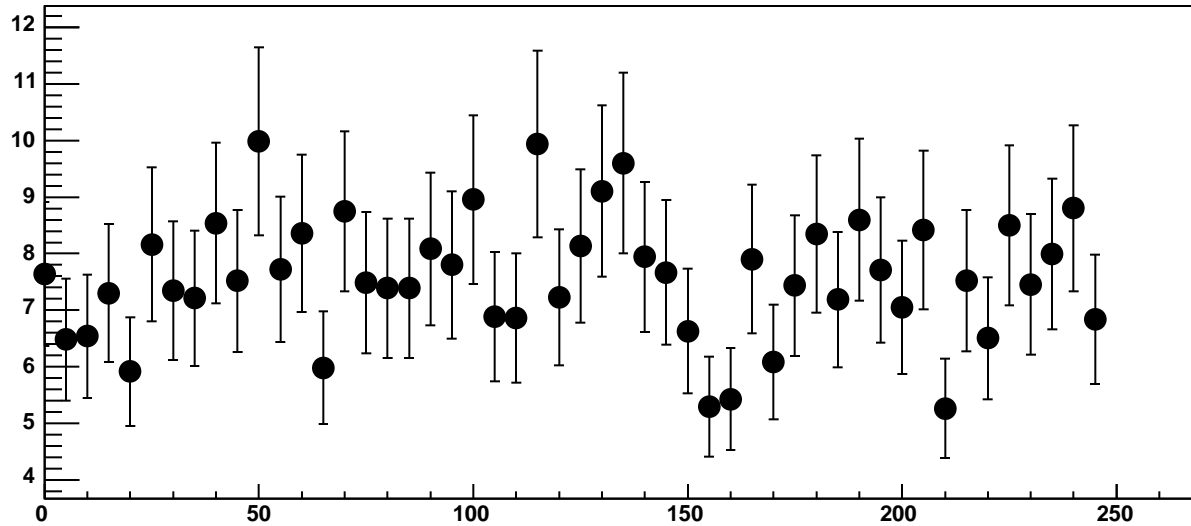


Chip 6, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

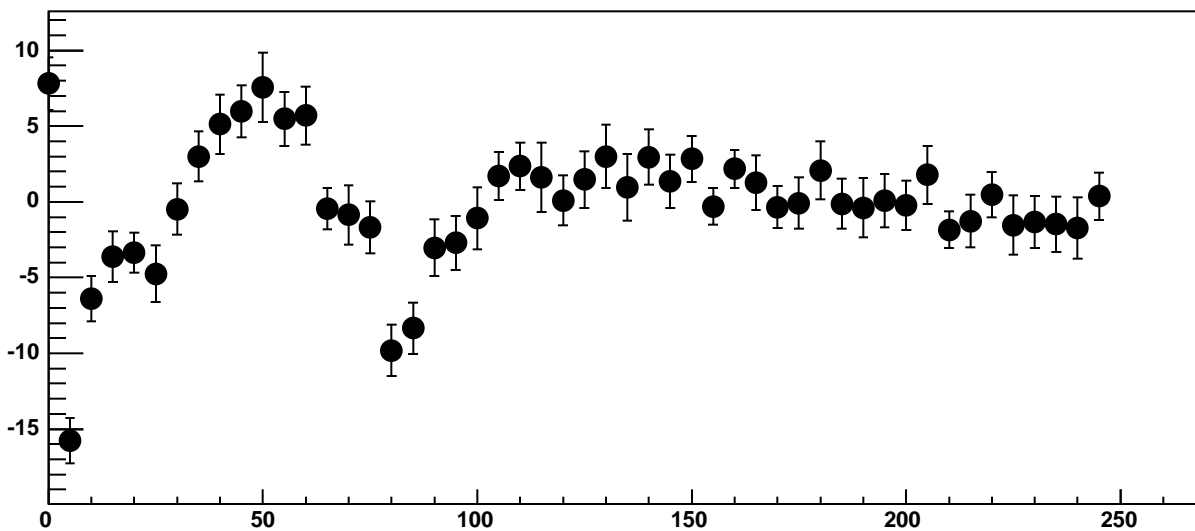


χ^2 / ndf	158.4 / 41
p0	-222.6 ± 0.5914
p1	81.19 ± 0.558
p2	$-7.873\text{e}+07 \pm 1.003\text{e}+06$
p3	$4.047\text{e}+07 \pm 2.138\text{e}+05$
p4	1.808 ± 0.02175

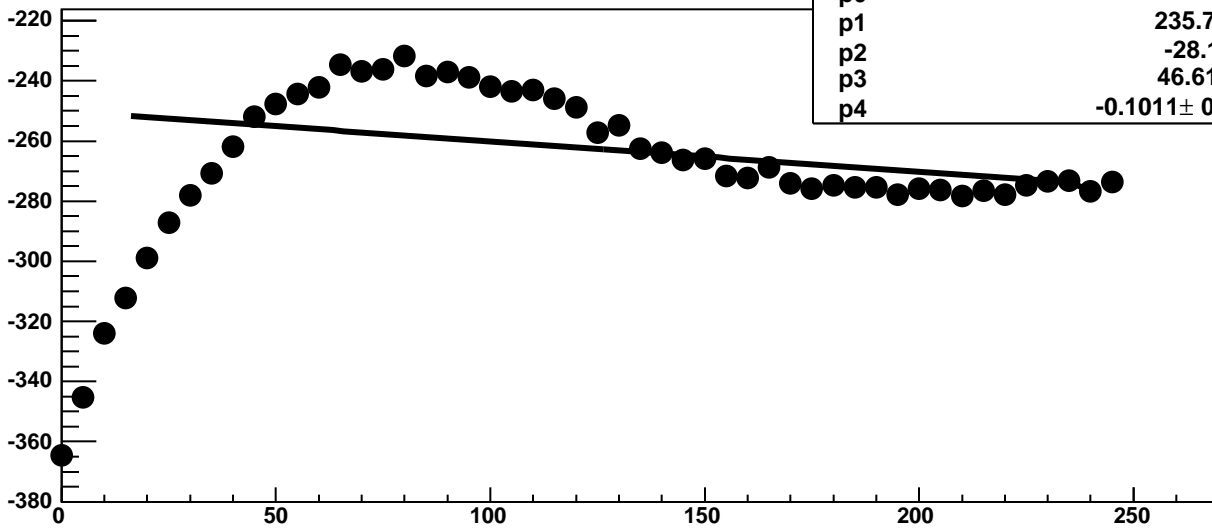
Chip 6, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold

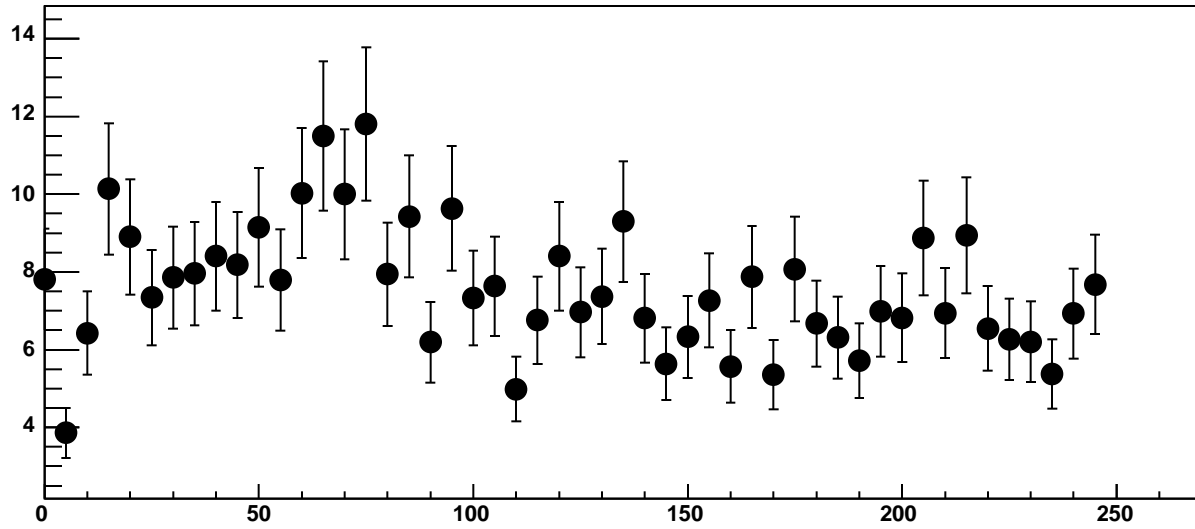


Chip 6, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold

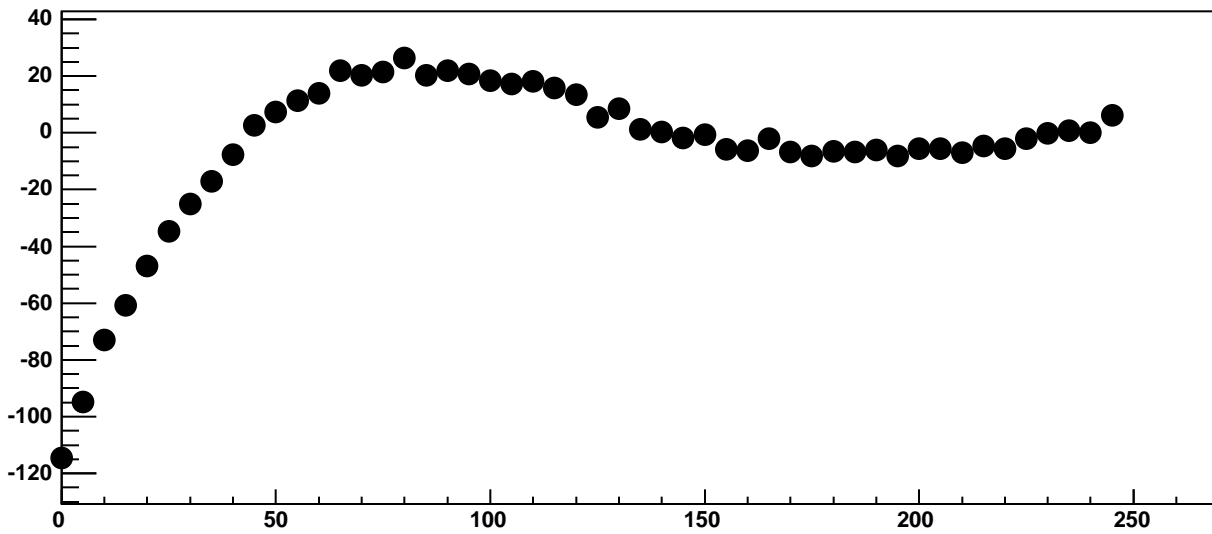


χ^2 / ndf	3755 / 41
p0	-273.9 ± 1.387
p1	235.7 ± 12.84
p2	-28.1 ± 75.58
p3	46.61 ± 106.6
p4	-0.1011 ± 0.003948

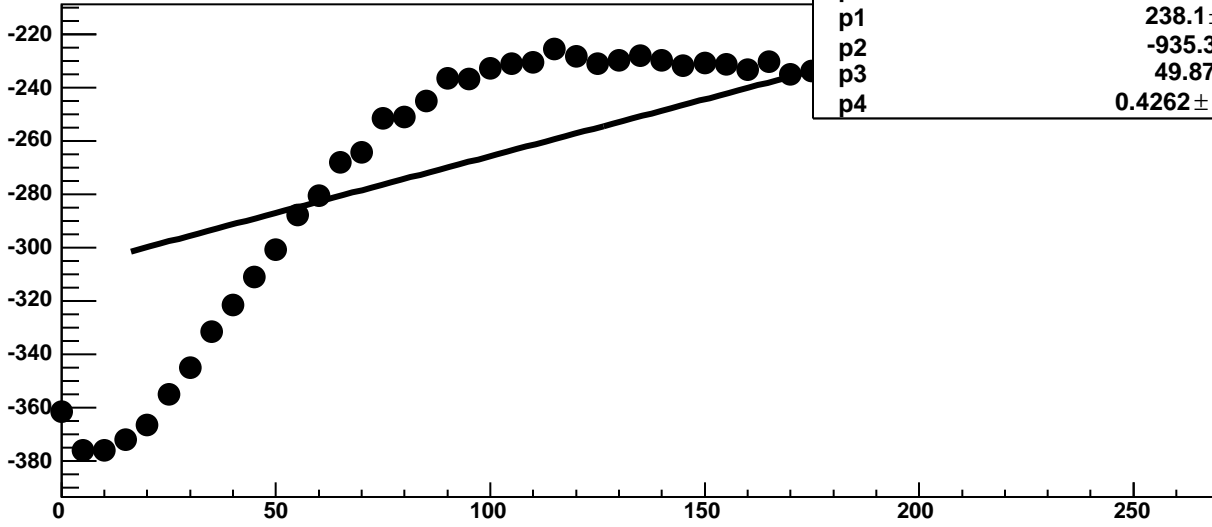
Chip 6, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold

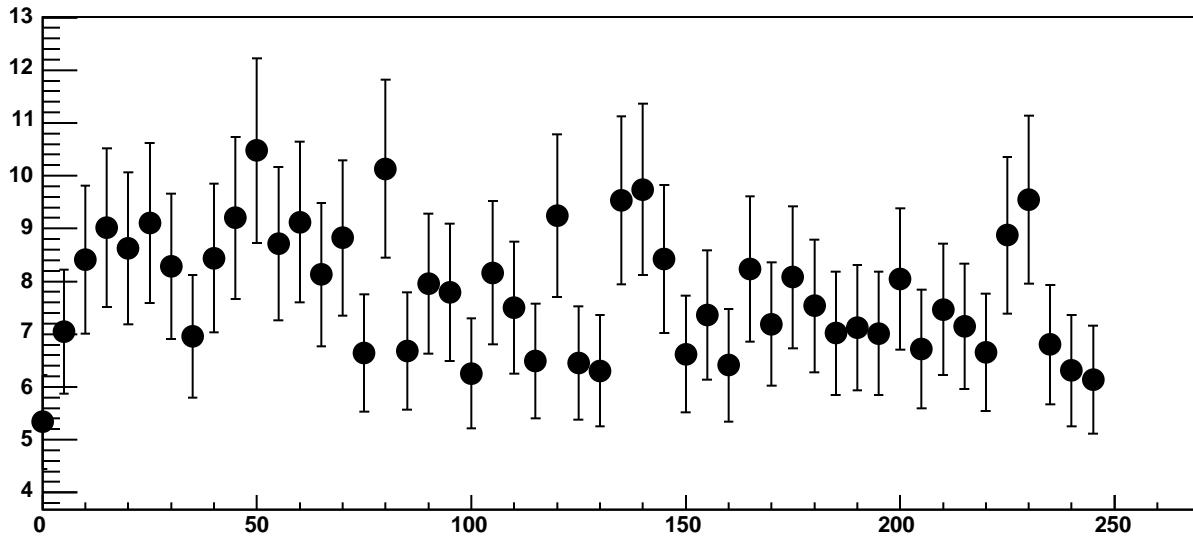


Chip 6, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

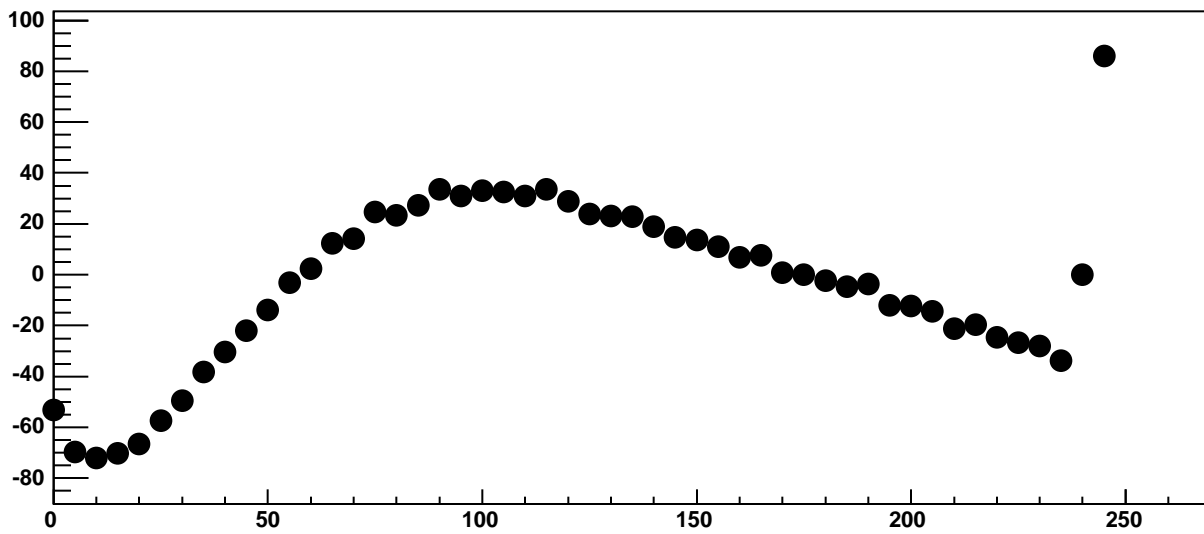


χ^2 / ndf	1.043e+04 / 41
p0	-206.8 ± 0.6645
p1	238.1 ± 0.9898
p2	-935.3 ± 463.4
p3	49.87 ± 20.68
p4	0.4262 ± 0.00418

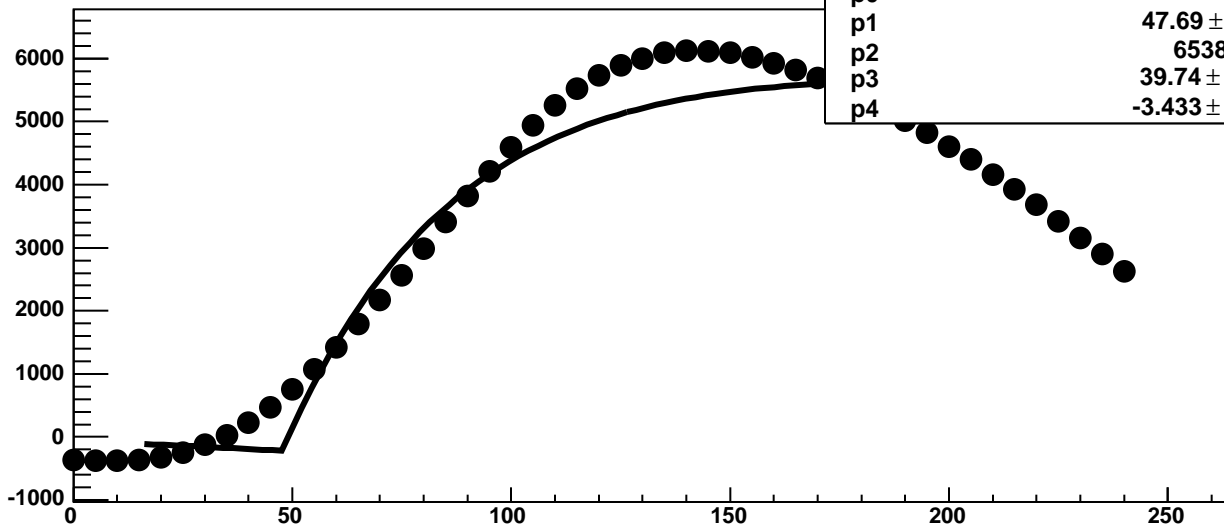
Chip 6, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



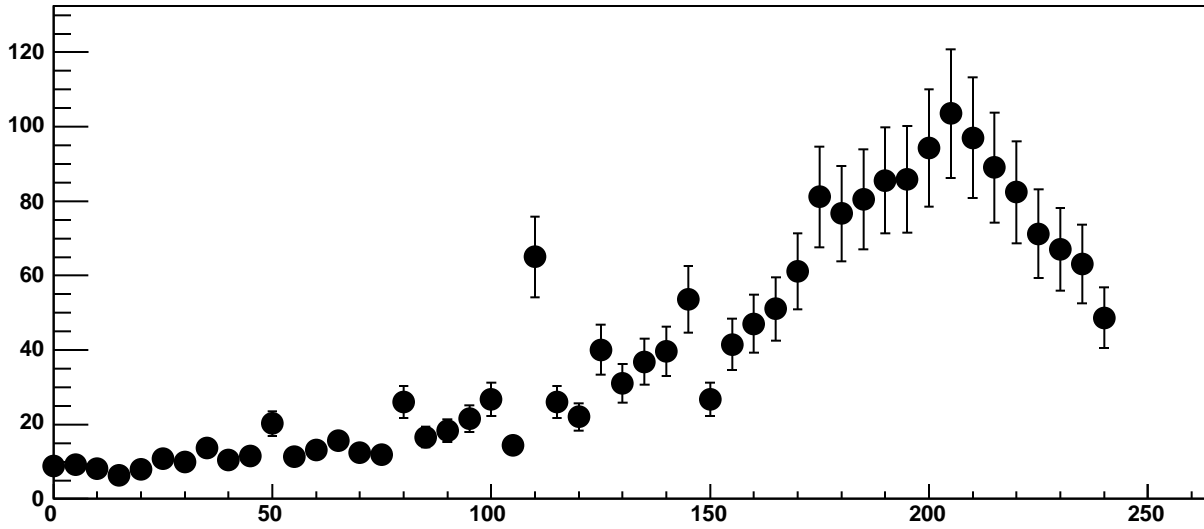
Chip 6, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold



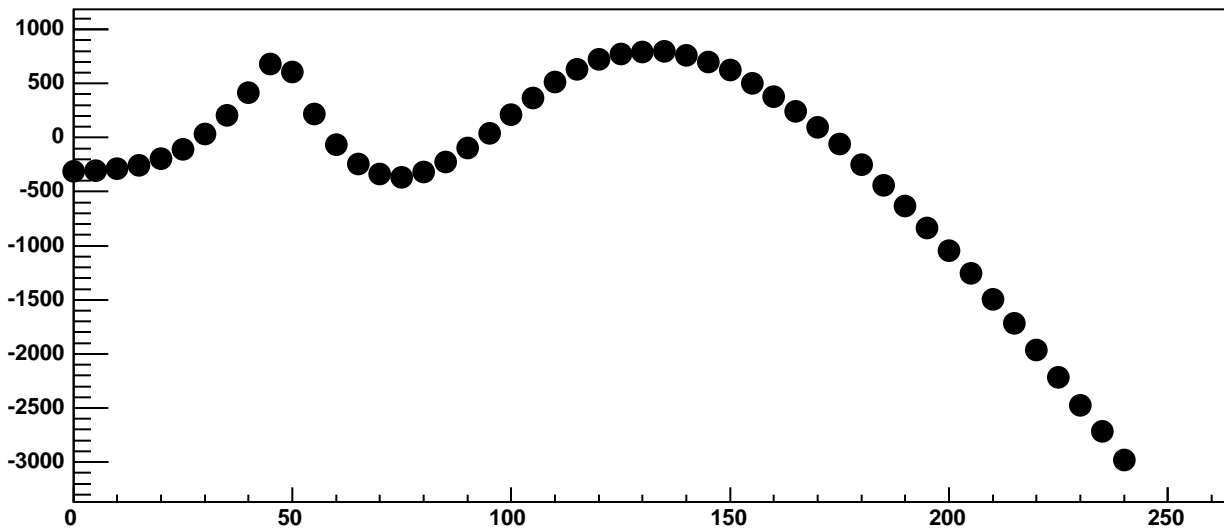
Chip 6, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold



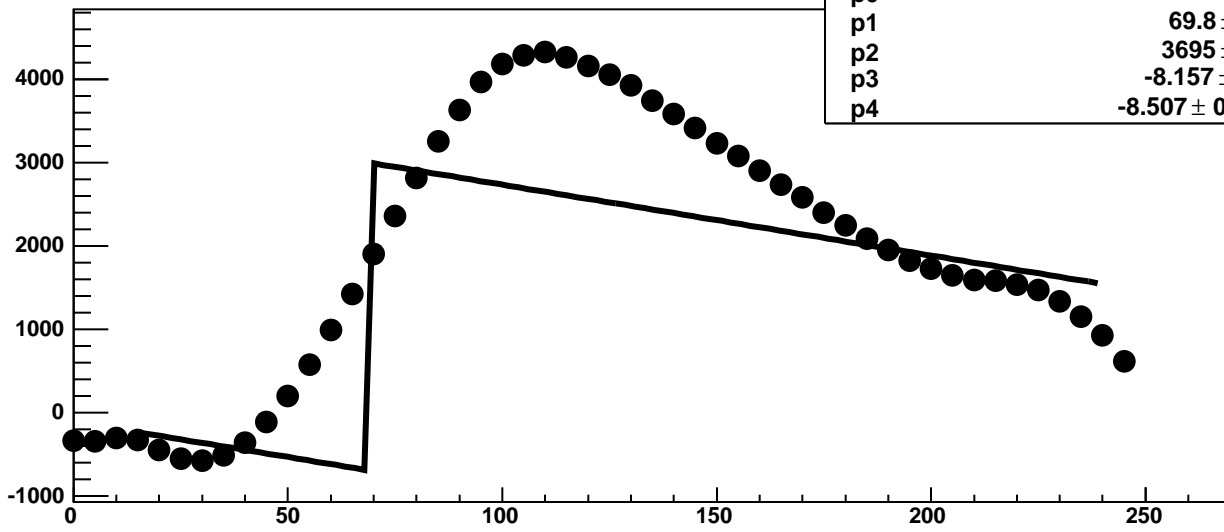
Chip 6, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold

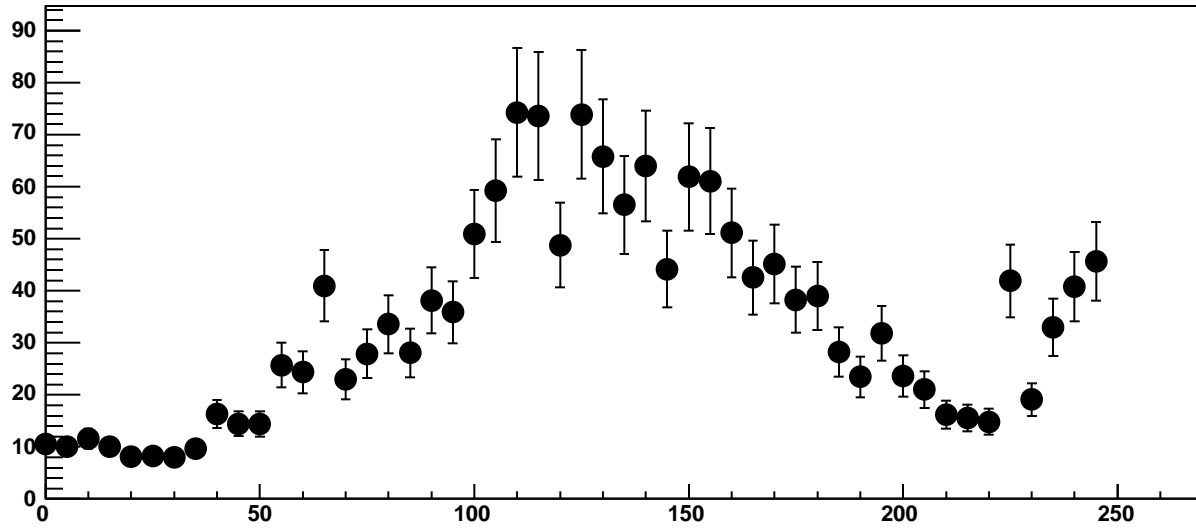


Chip 6, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold

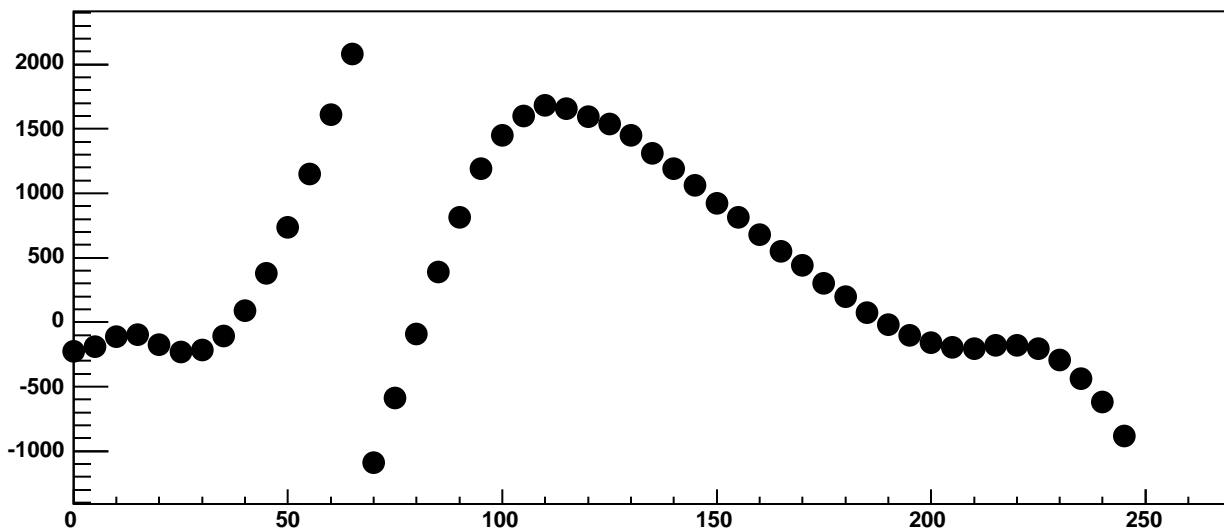


χ^2 / ndf	5.137e+05 / 41
p0	-699.6 ± 0.7997
p1	69.8 ± 0.5667
p2	3695 ± 0.8887
p3	-8.157 ± 0.3255
p4	-8.507 ± 0.003006

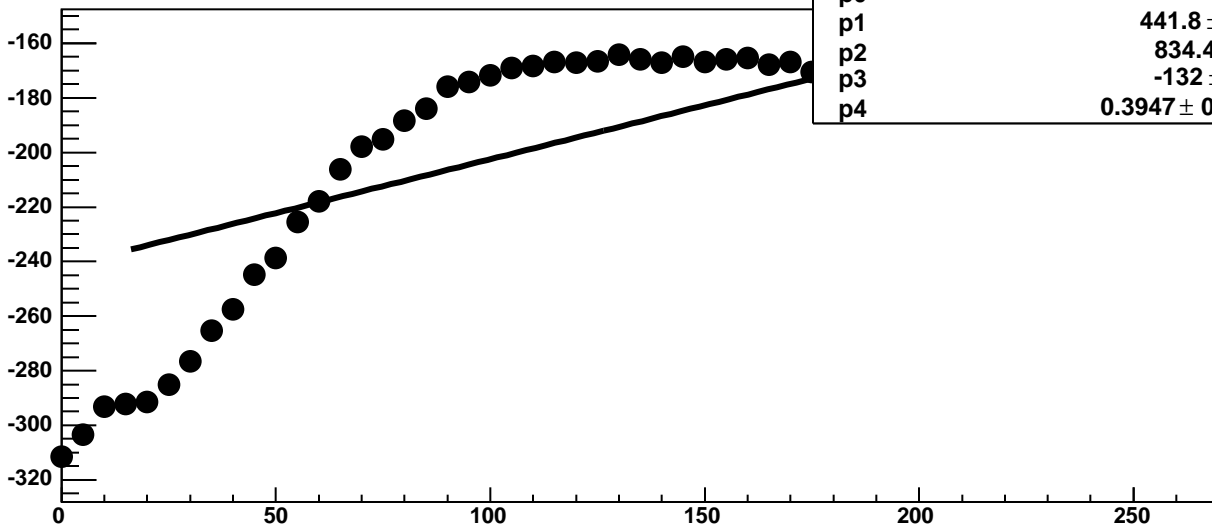
Chip 6, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold

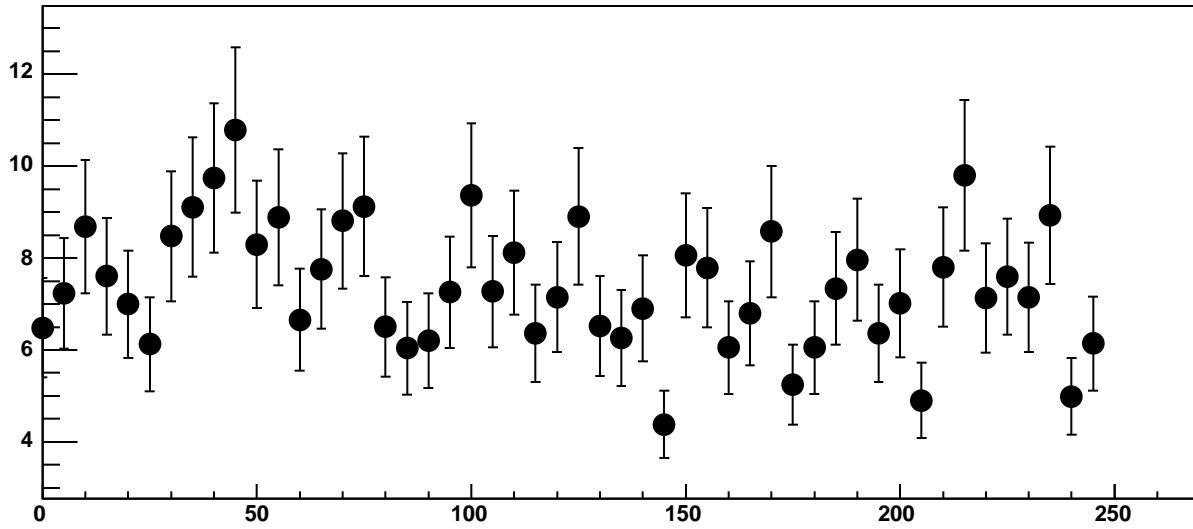


Chip 6, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

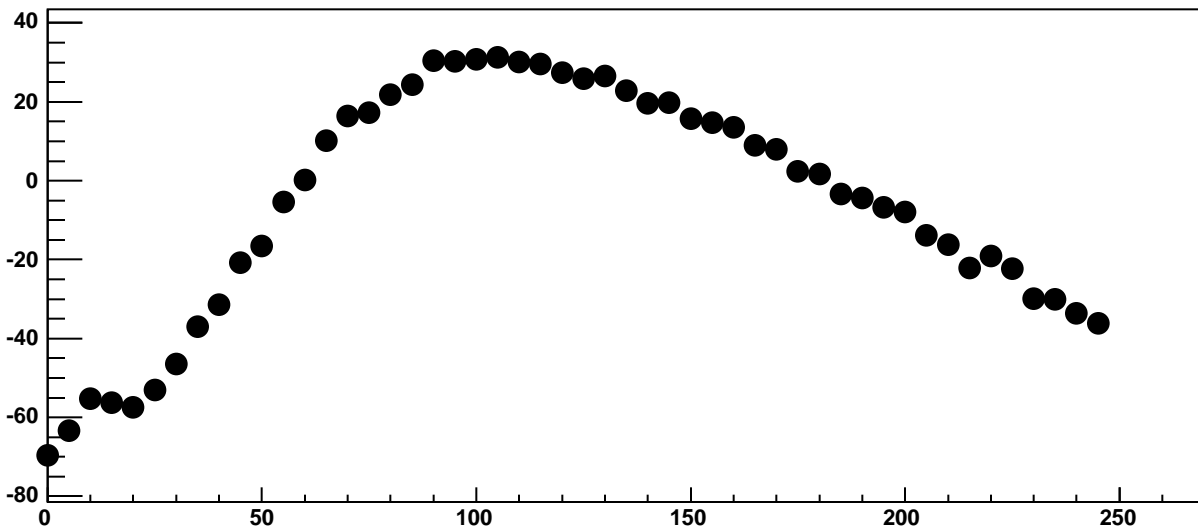


χ^2 / ndf	1.163e+04 / 41
p0	-67.59 ± 0.8949
p1	441.8 ± 0.6825
p2	834.4 ± 5.097
p3	-132 ± 0.4119
p4	0.3947 ± 0.003693

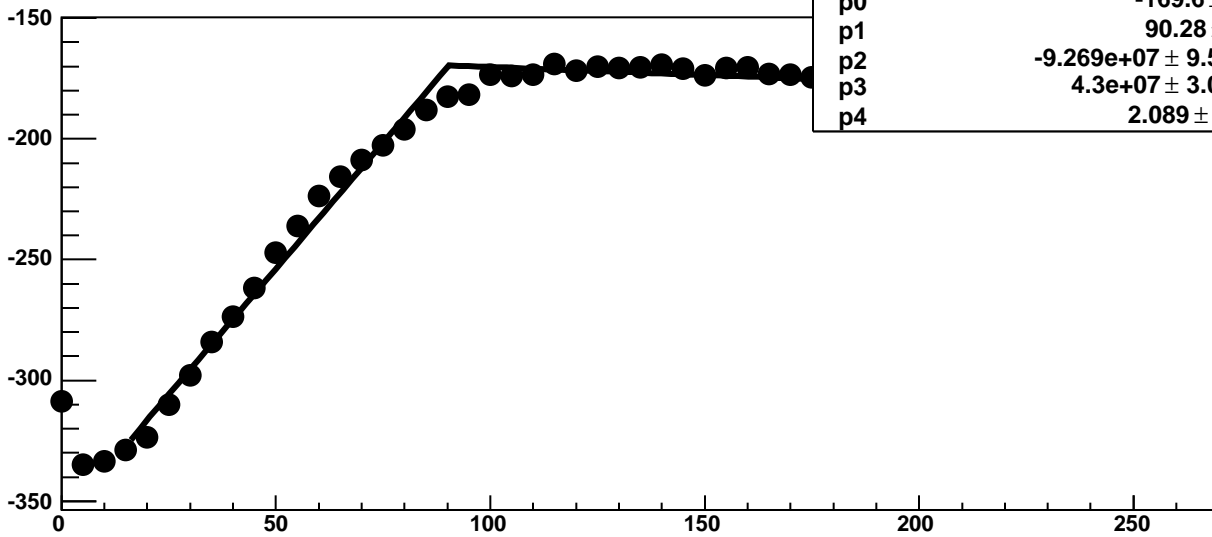
Chip 6, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

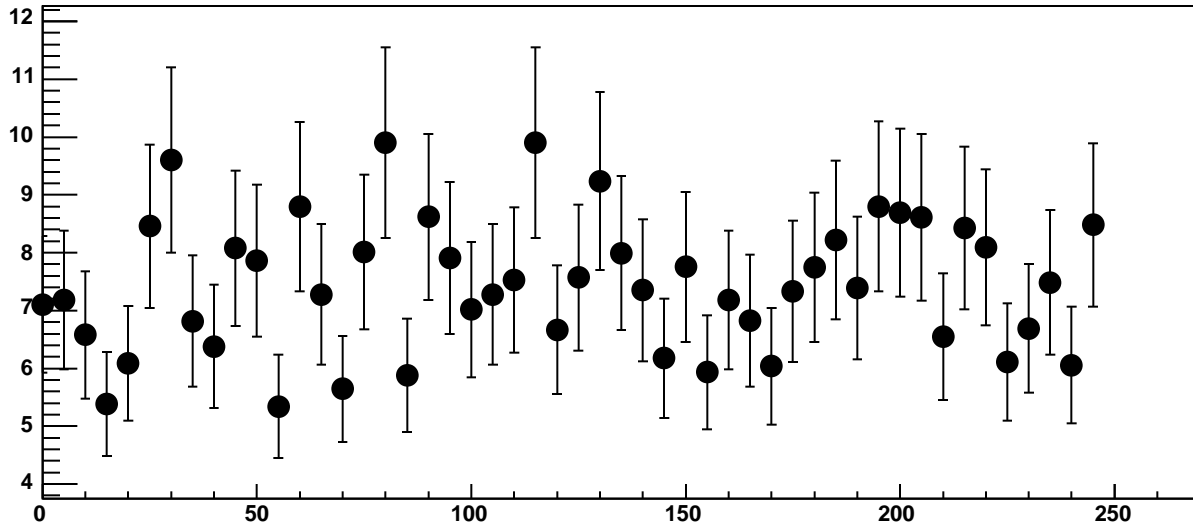


Chip 6, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

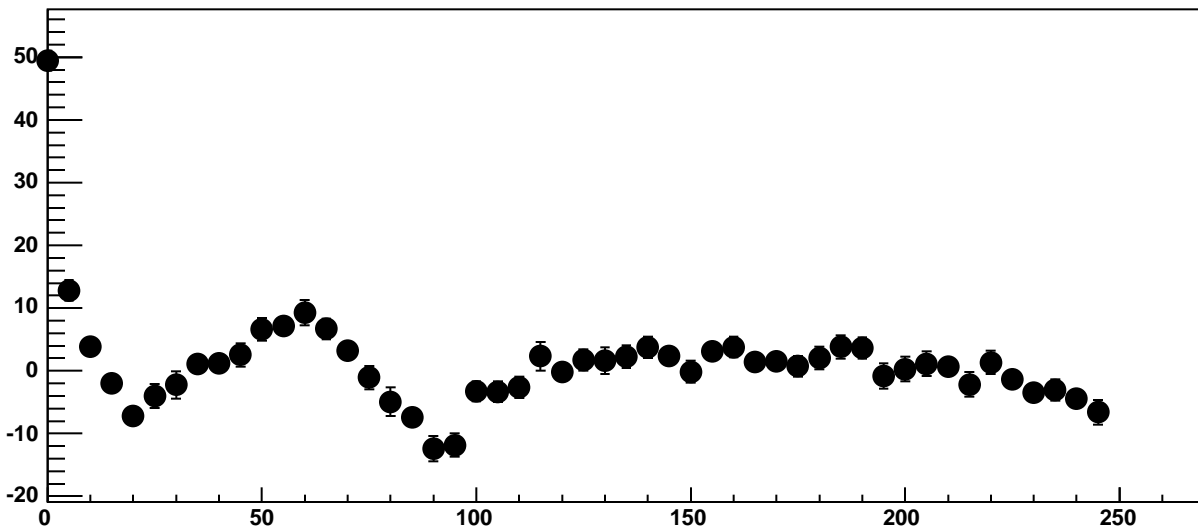


χ^2 / ndf	314 / 41
p0	-169.6 ± 0.6179
p1	90.28 ± 0.4701
p2	$-9.269\text{e}+07 \pm 9.563\text{e}+05$
p3	$4.3\text{e}+07 \pm 3.021\text{e}+05$
p4	2.089 ± 0.01697

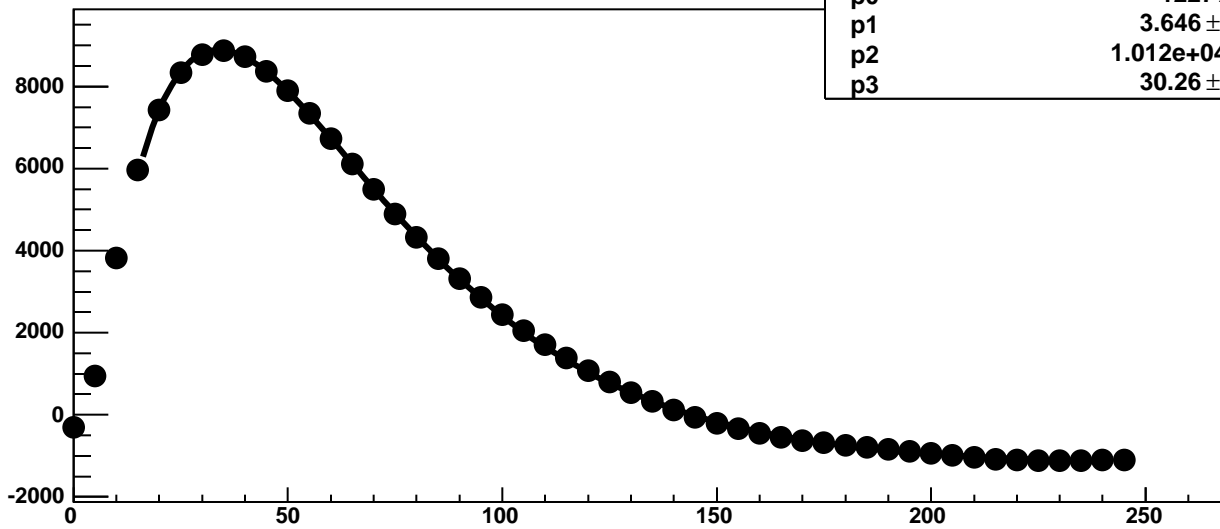
Chip 6, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

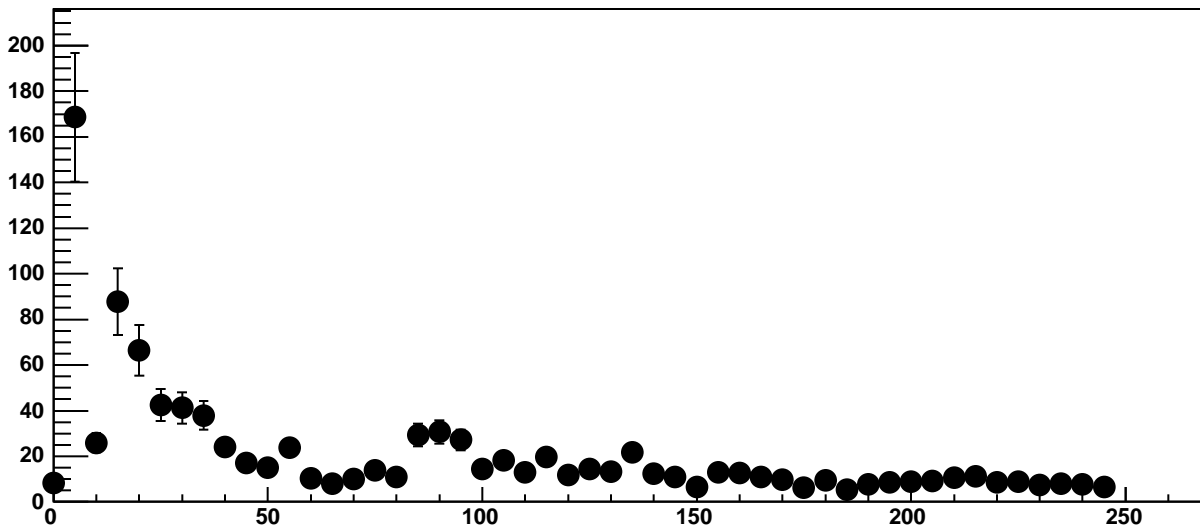


Chip 6, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold

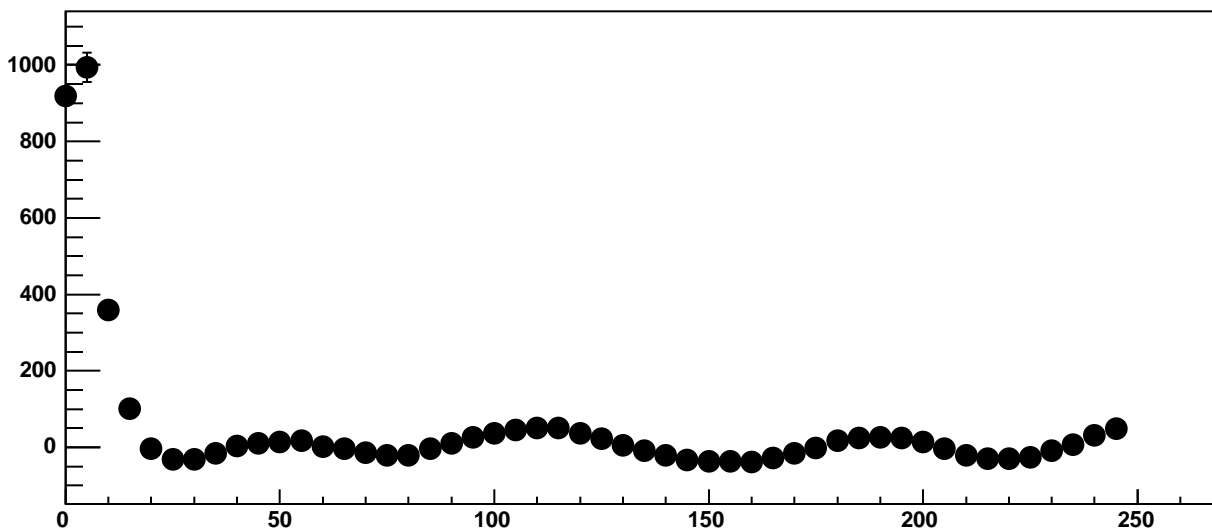


χ^2 / ndf	4315 / 42
p0	-1227 ± 0.7076
p1	3.646 ± 0.02735
p2	$1.012\text{e}+04 \pm 2.603$
p3	30.26 ± 0.01073

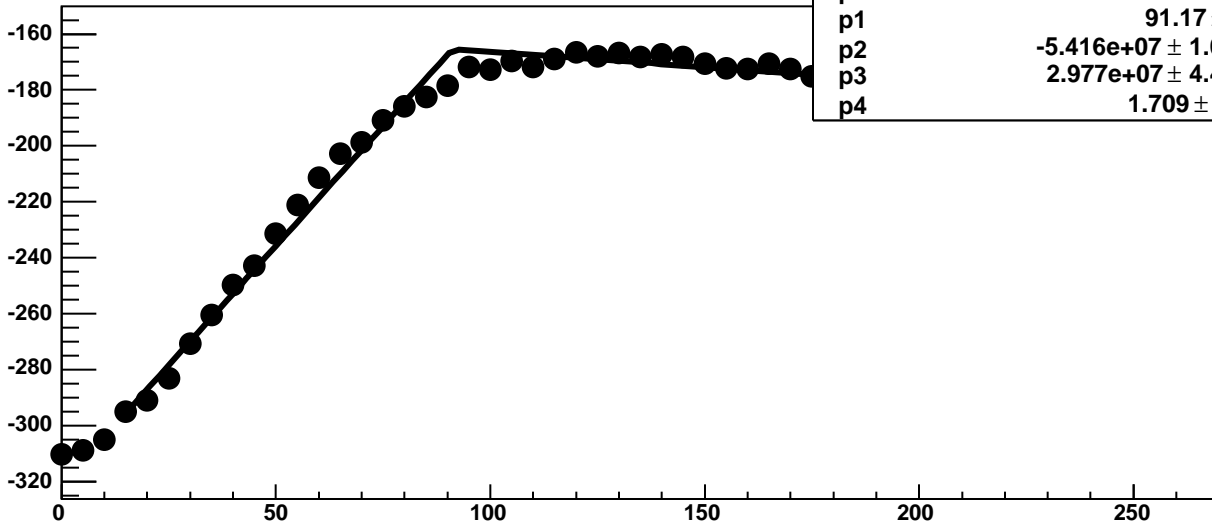
Chip 6, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold

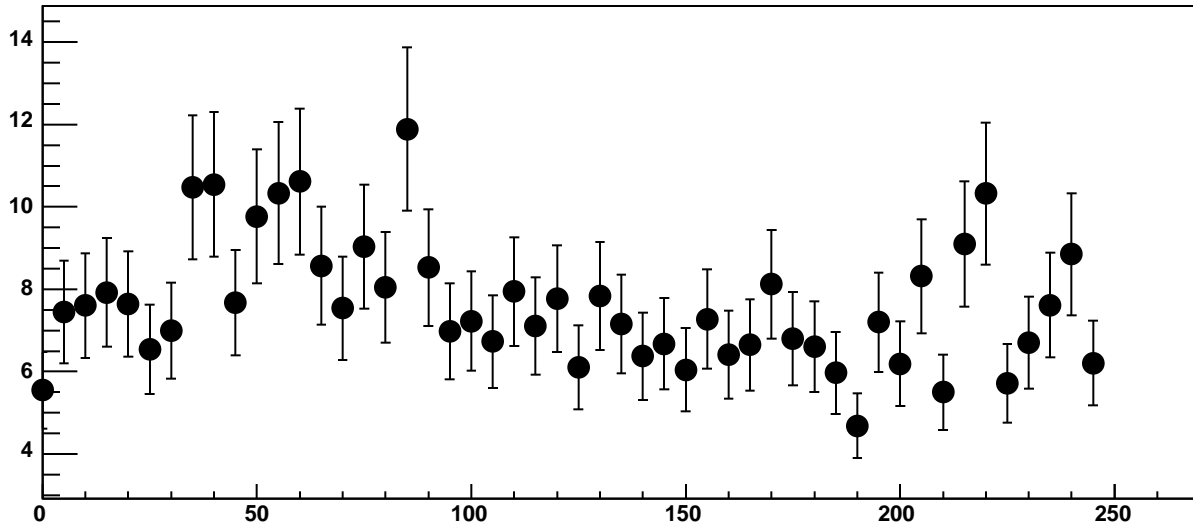


Chip 6, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

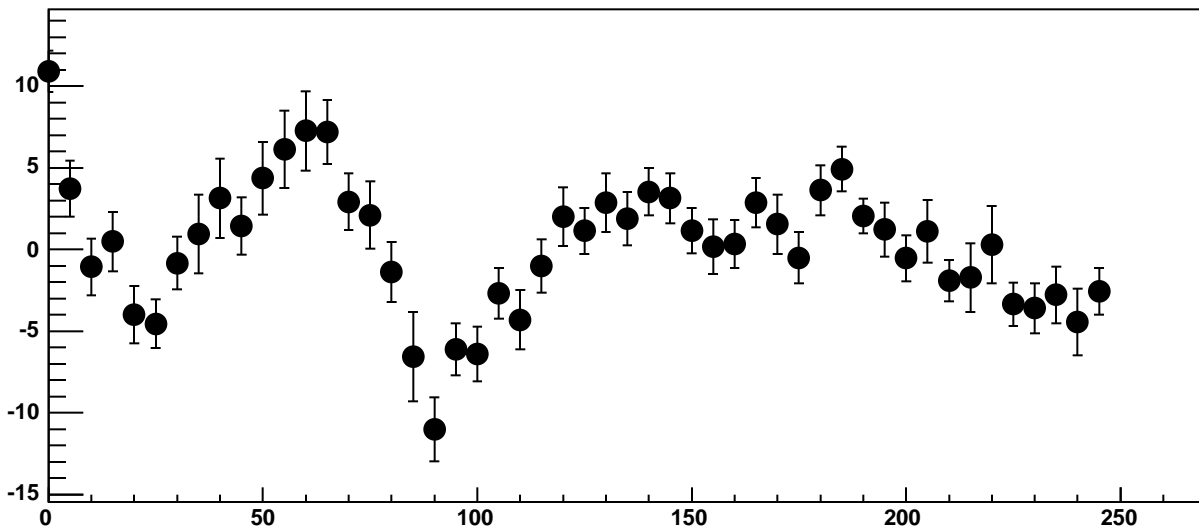


χ^2 / ndf	196.9 / 41
p0	-165.4 ± 0.5674
p1	91.17 ± 0.6351
p2	$-5.416\text{e}+07 \pm 1.016\text{e}+06$
p3	$2.977\text{e}+07 \pm 4.411\text{e}+05$
p4	1.709 ± 0.02056

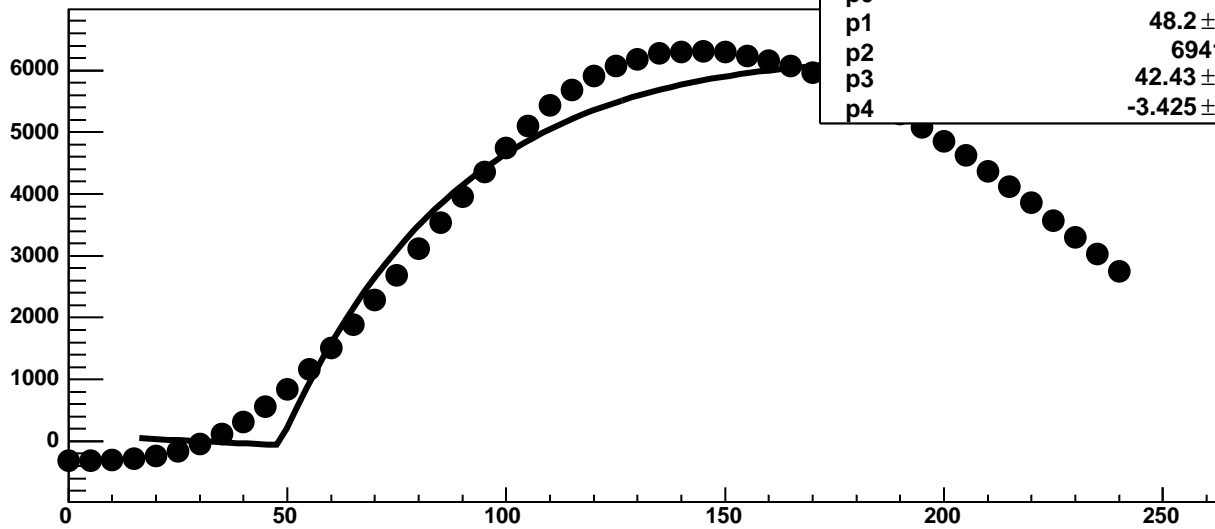
Chip 6, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

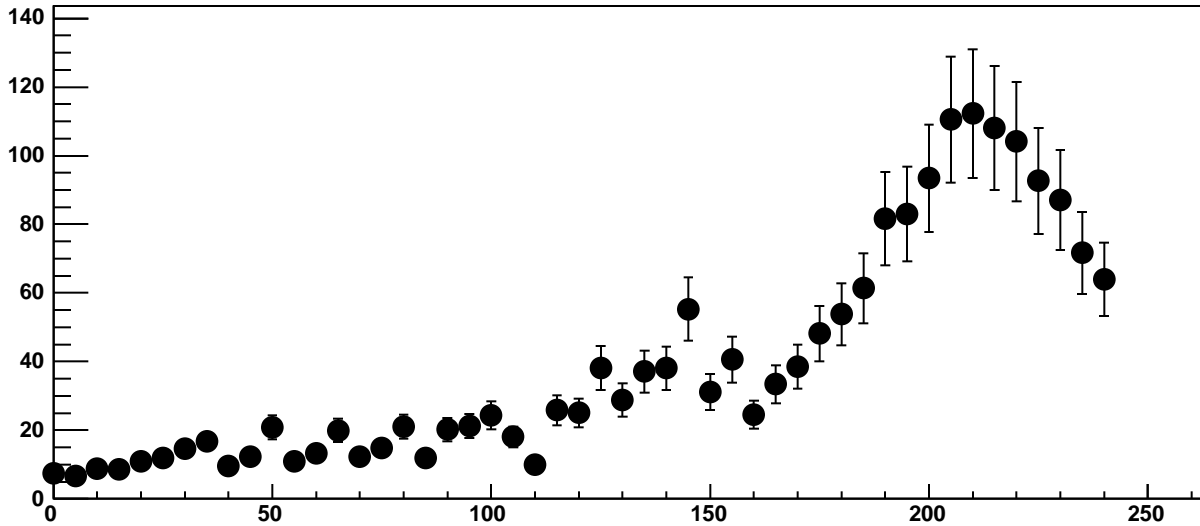


Chip 6, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold

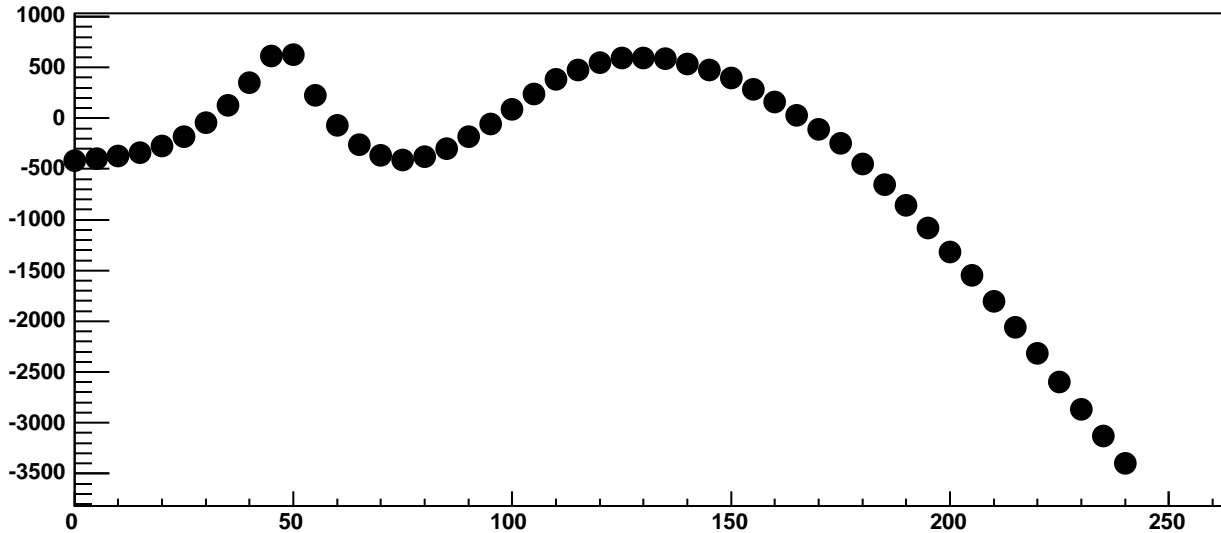


χ^2 / ndf	4.364e+05 / 41
p0	-60.7 ± 1.543
p1	48.2 ± 0.01796
p2	6941 ± 10.38
p3	42.43 ± 0.06617
p4	-3.425 ± 0.06177

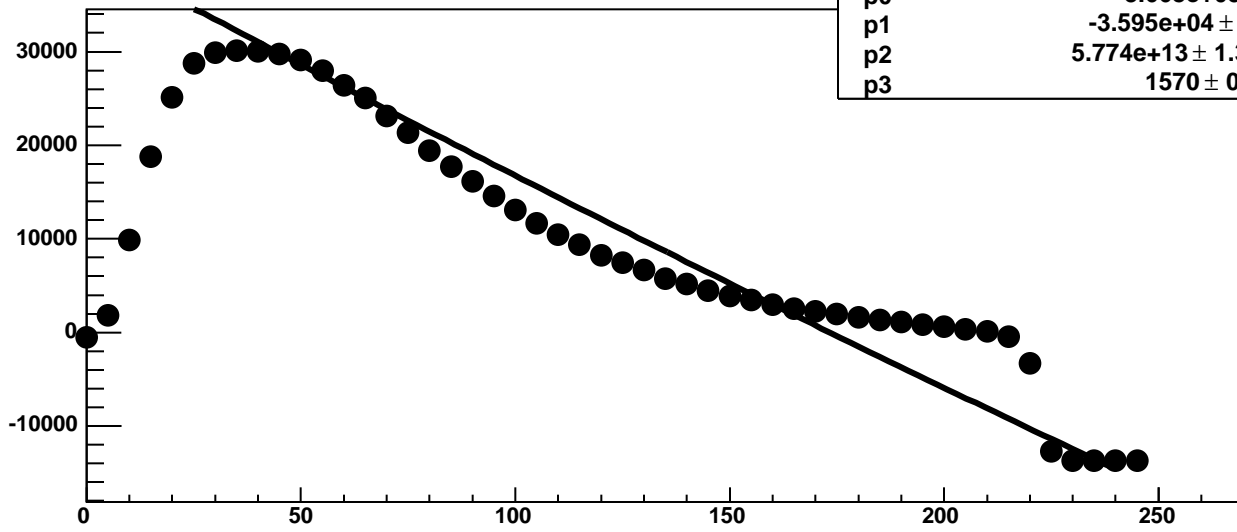
Chip 6, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

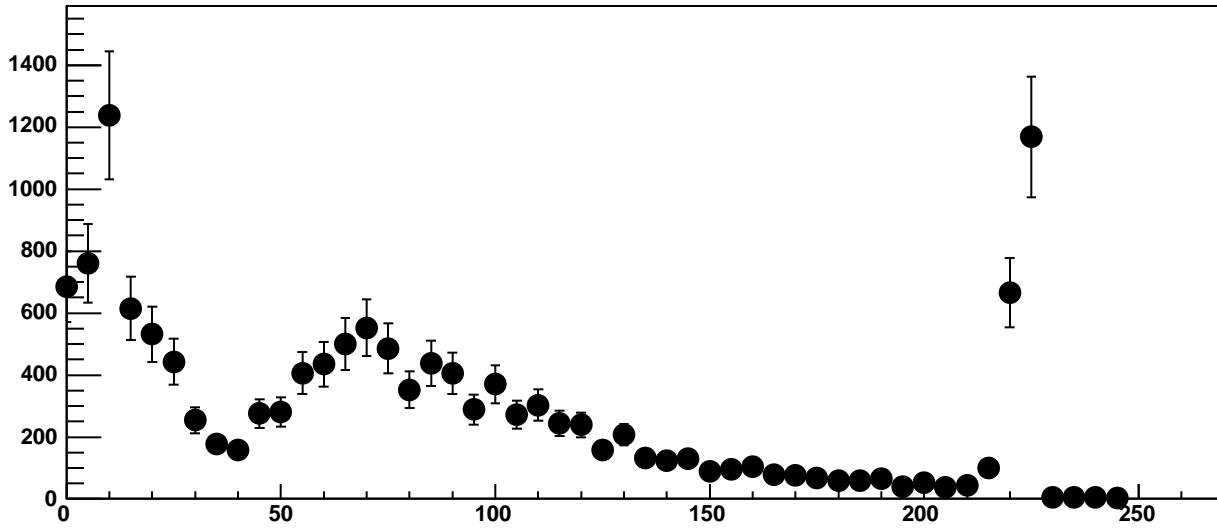


Chip 6, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold

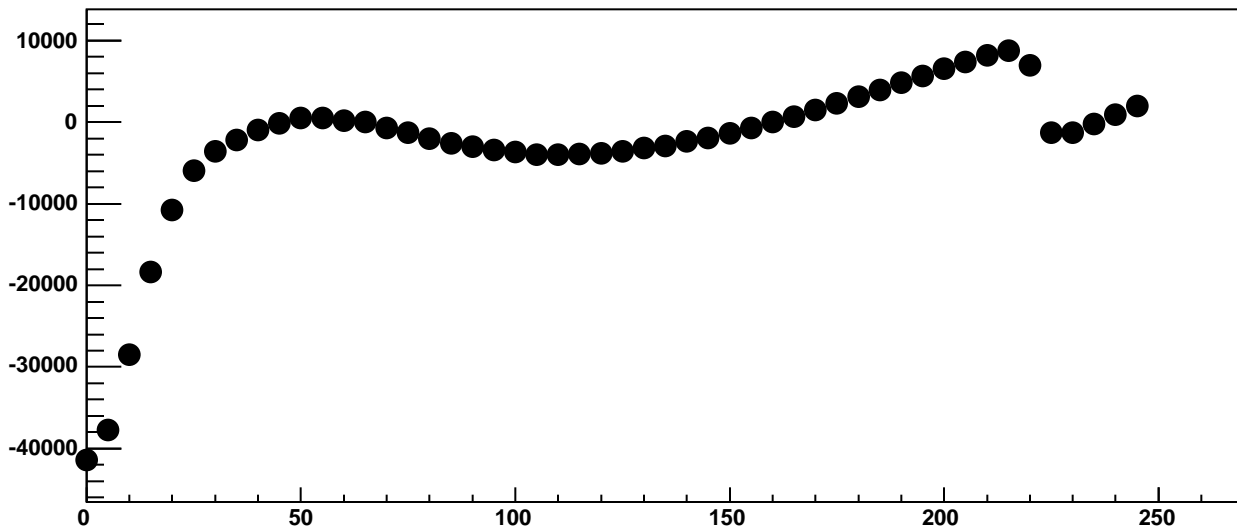


χ^2 / ndf	4.825e+06 / 42
p0	-3.668e+05 ± 8.394
p1	-3.595e+04 ± 0.03927
p2	5.774e+13 ± 1.379e+09
p3	1570 ± 0.001704

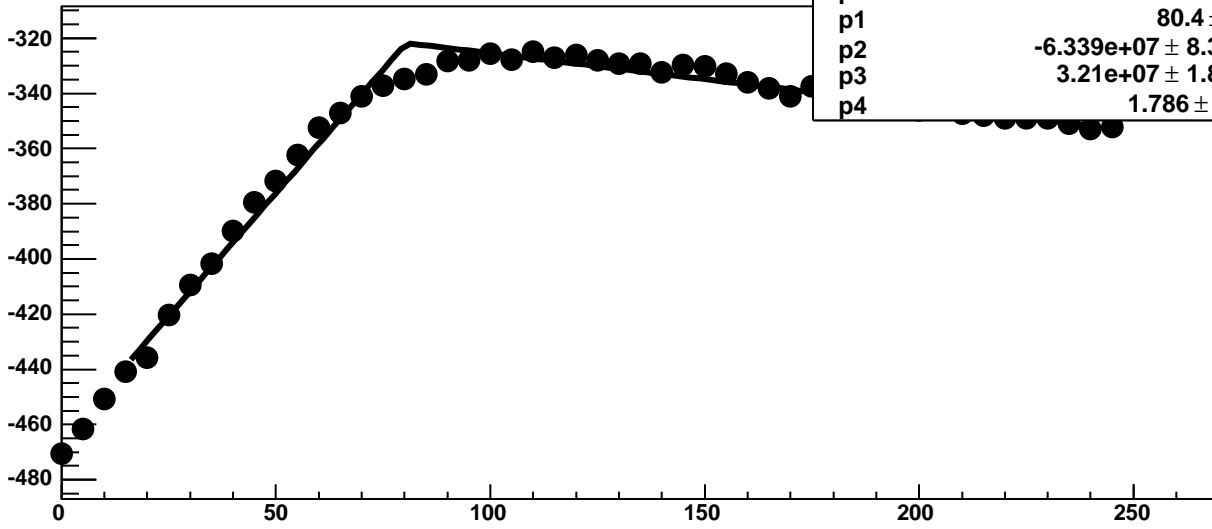
Chip 6, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold

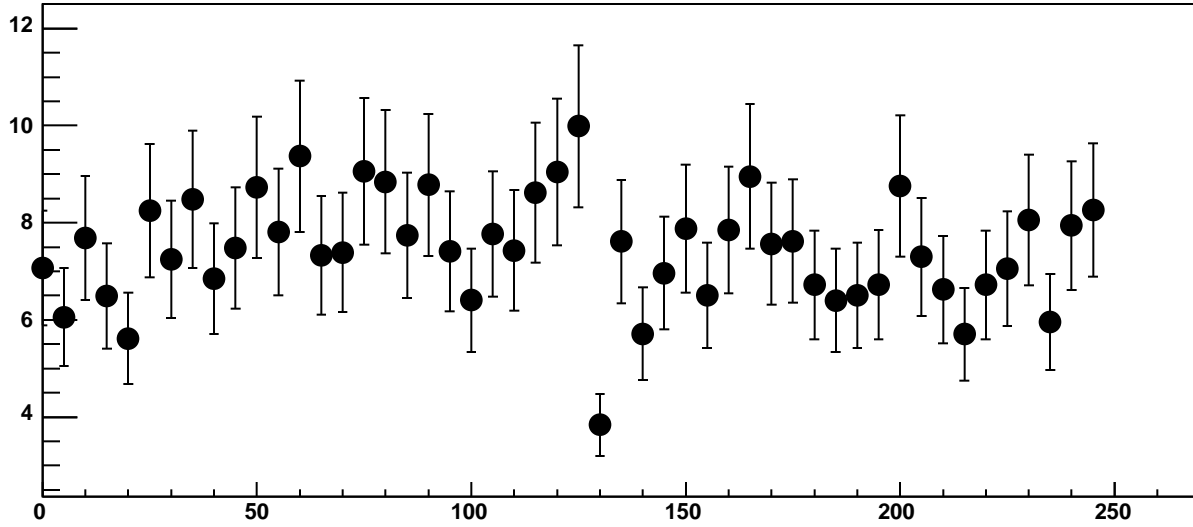


Chip 6, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

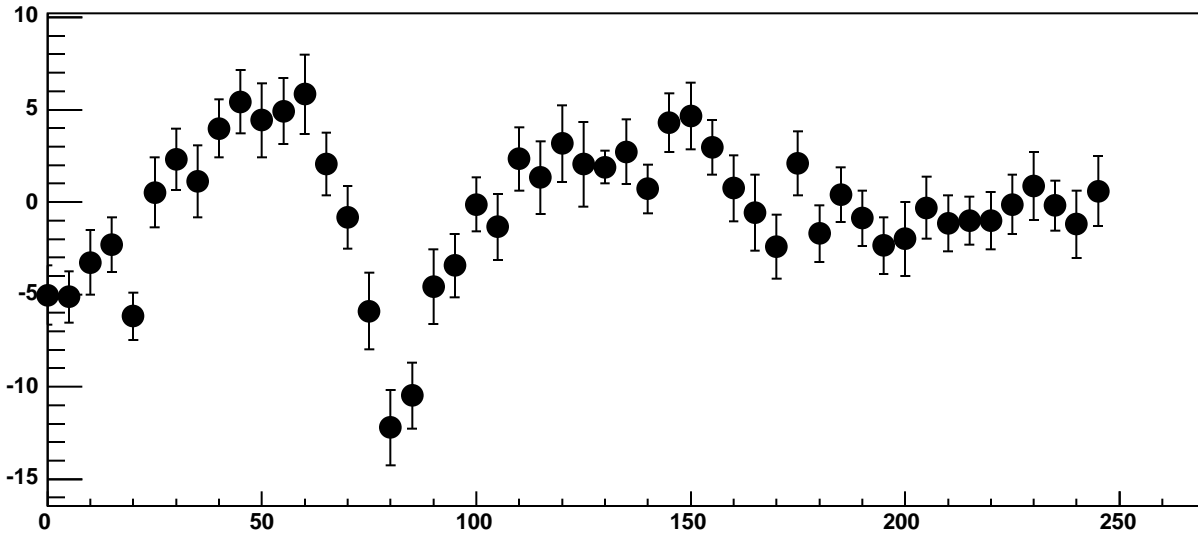


χ^2 / ndf	196.1 / 41
p0	-321.8 ± 0.5468
p1	80.4 ± 0.5659
p2	$-6.339\text{e}+07 \pm 8.314\text{e}+05$
p3	$3.21\text{e}+07 \pm 1.855\text{e}+05$
p4	1.786 ± 0.02252

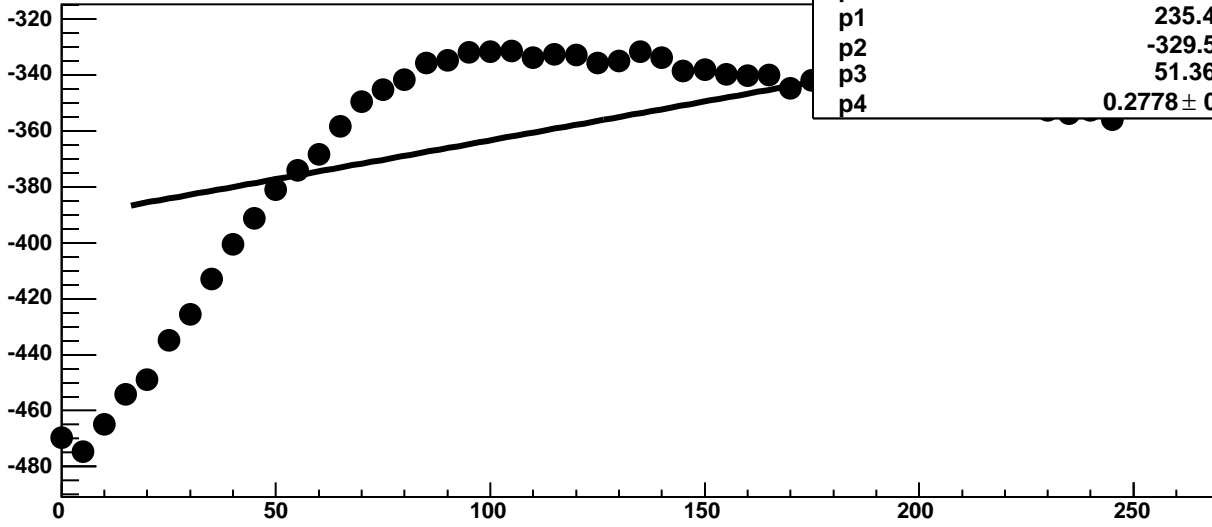
Chip 6, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold

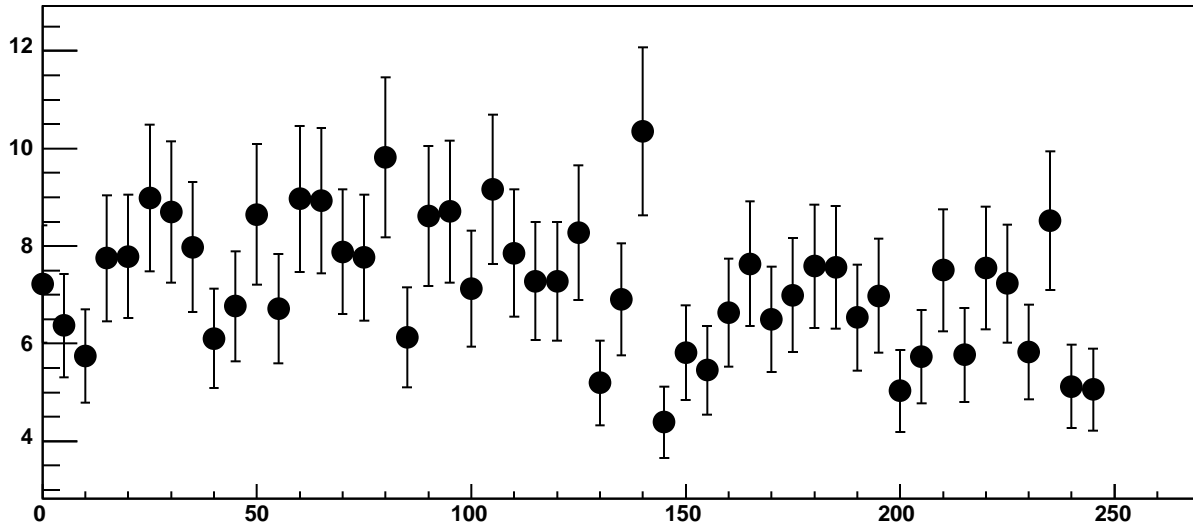


Chip 6, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

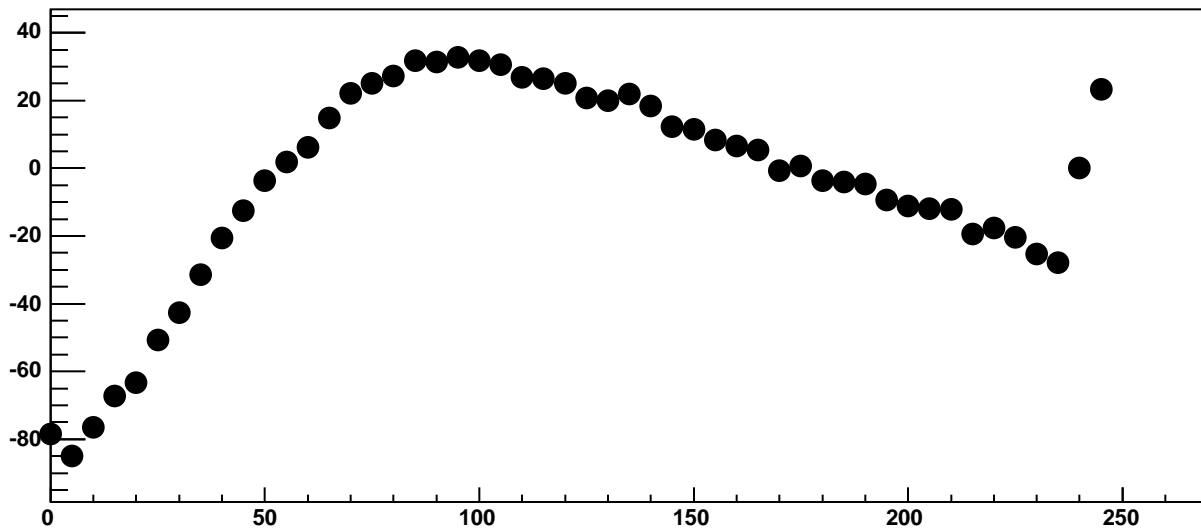


χ^2 / ndf	9532 / 41
p0	-325.7 ± 0.8335
p1	235.4 ± 2.546
p2	-329.5 ± 169.6
p3	51.36 ± 22.83
p4	0.2778 ± 0.003782

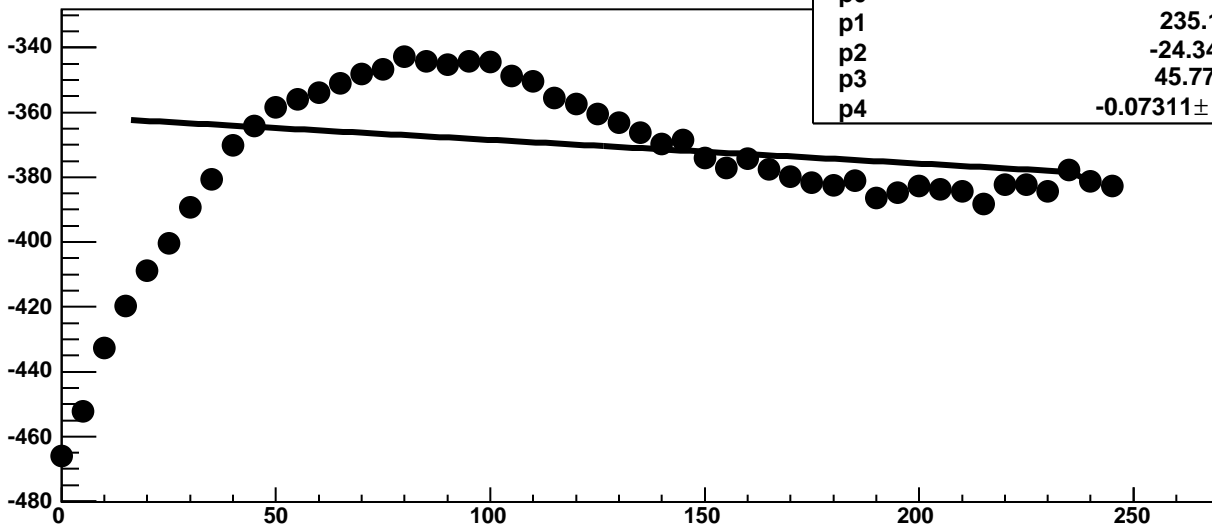
Chip 6, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

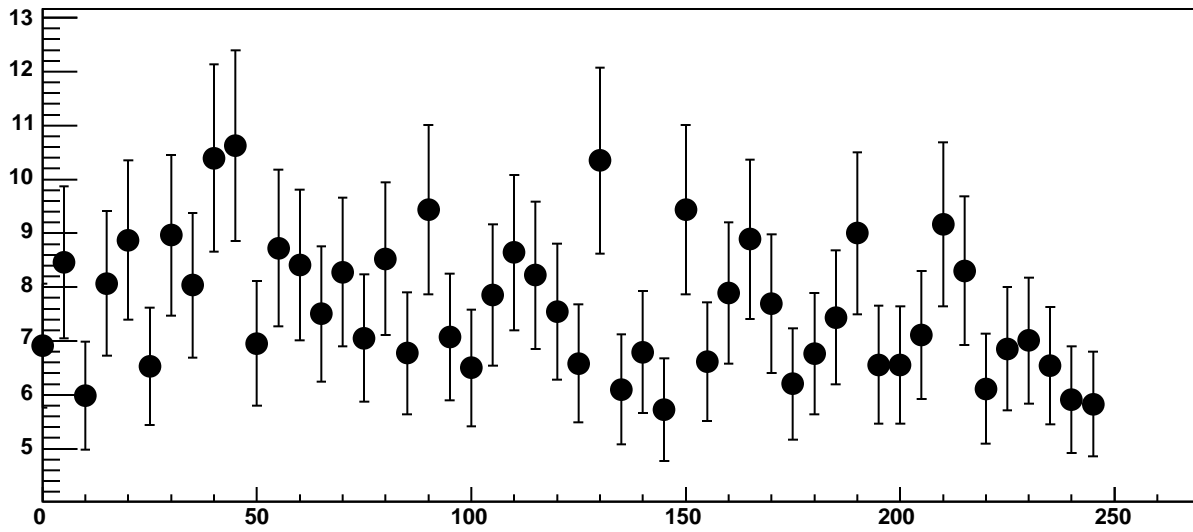


Chip 6, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold

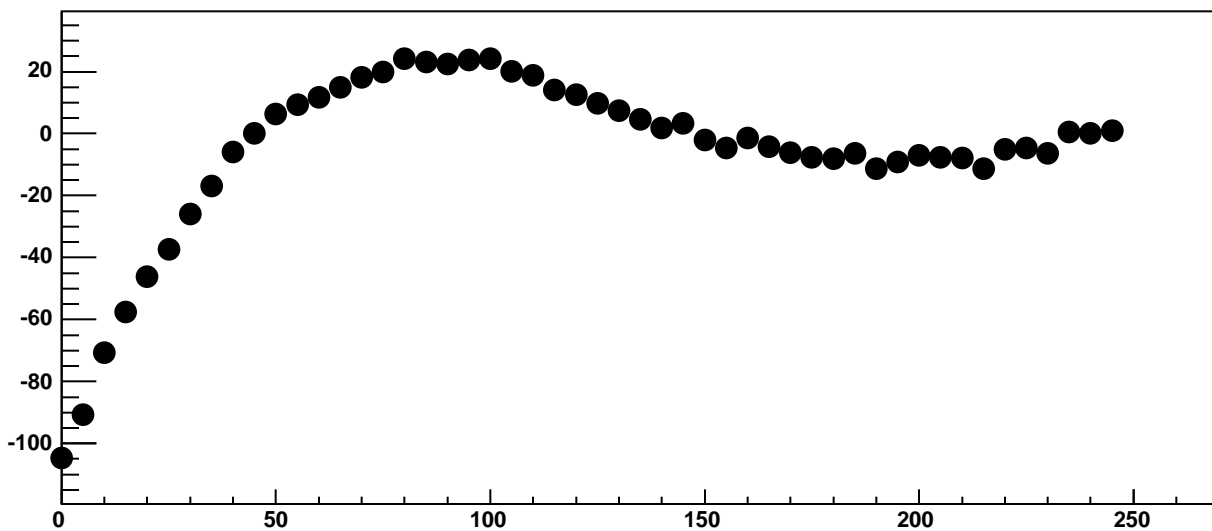


χ^2 / ndf	4392 / 41
p0	-378.4 ± 0.5046
p1	235.1 ± 1.881
p2	-24.34 ± 41.71
p3	45.77 ± 82.03
p4	-0.07311 ± 0.00399

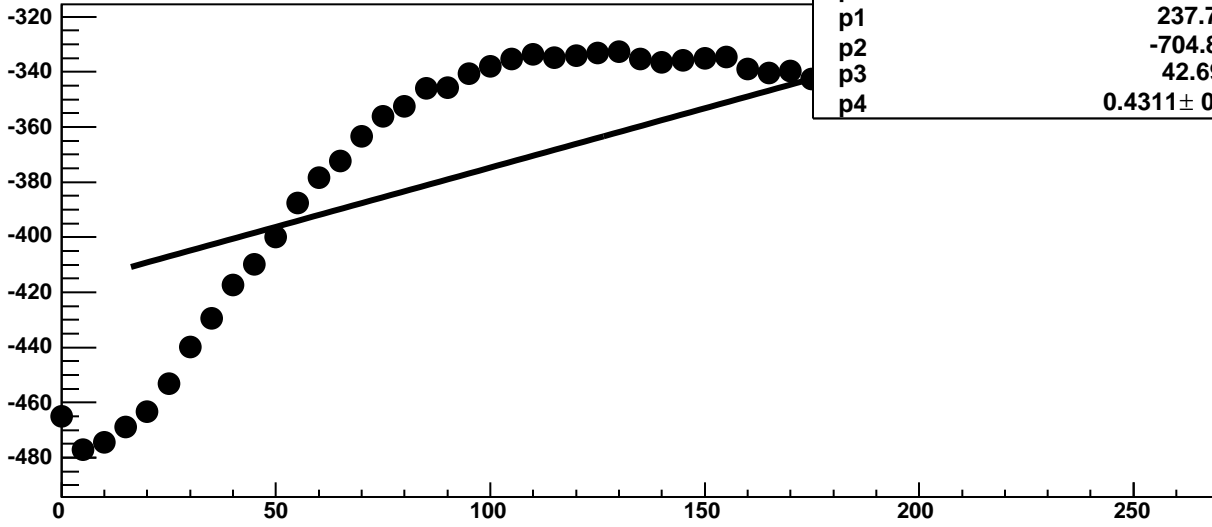
Chip 6, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold

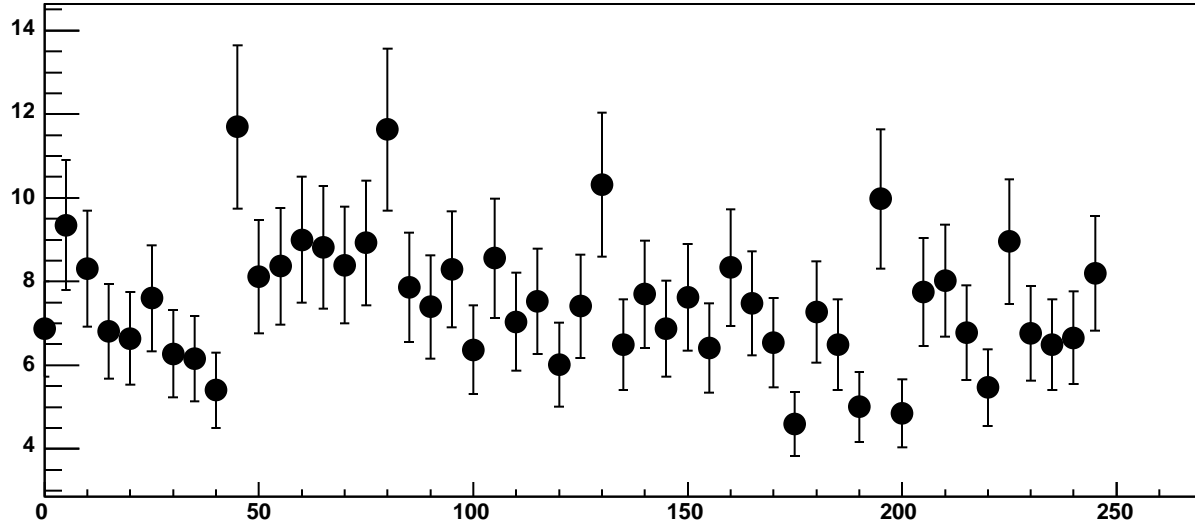


Chip 6, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

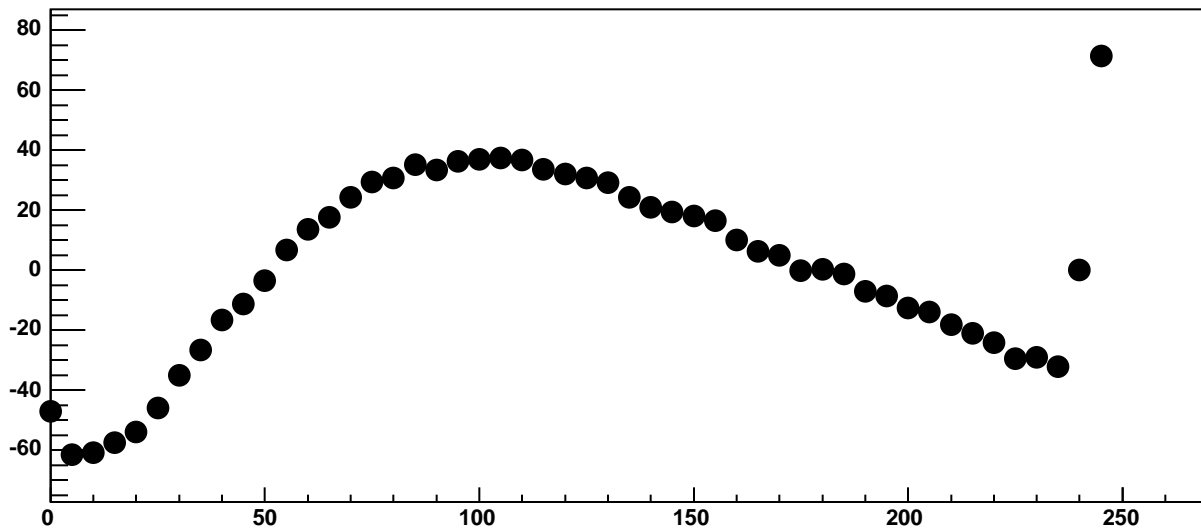


χ^2 / ndf	1.189e+04 / 41
p0	-315.4 ± 0.6677
p1	237.7 ± 1.151
p2	-704.8 ± 344.1
p3	42.69 ± 17.6
p4	0.4311 ± 0.003618

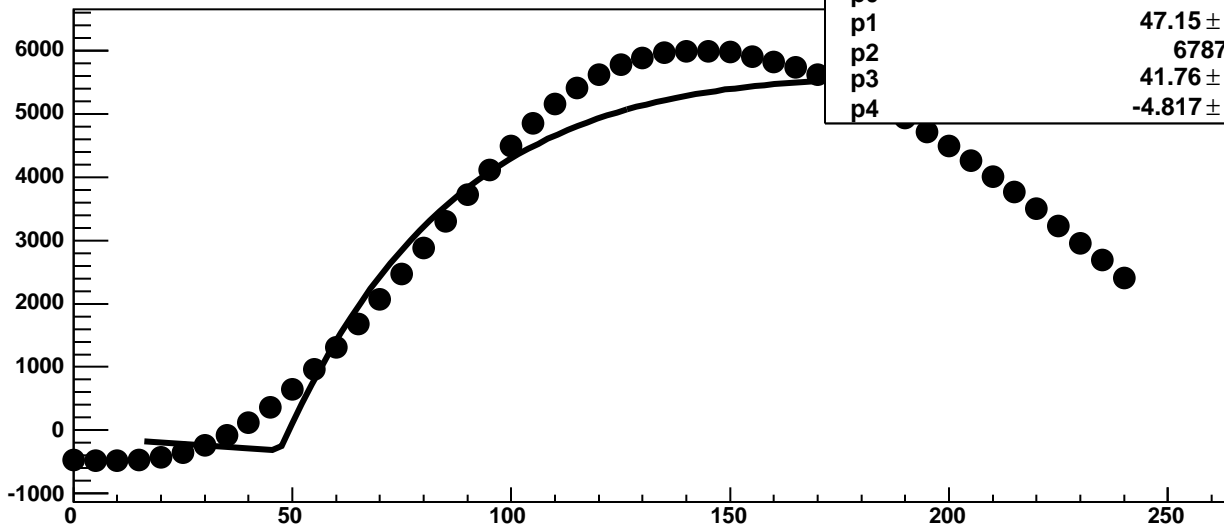
Chip 6, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

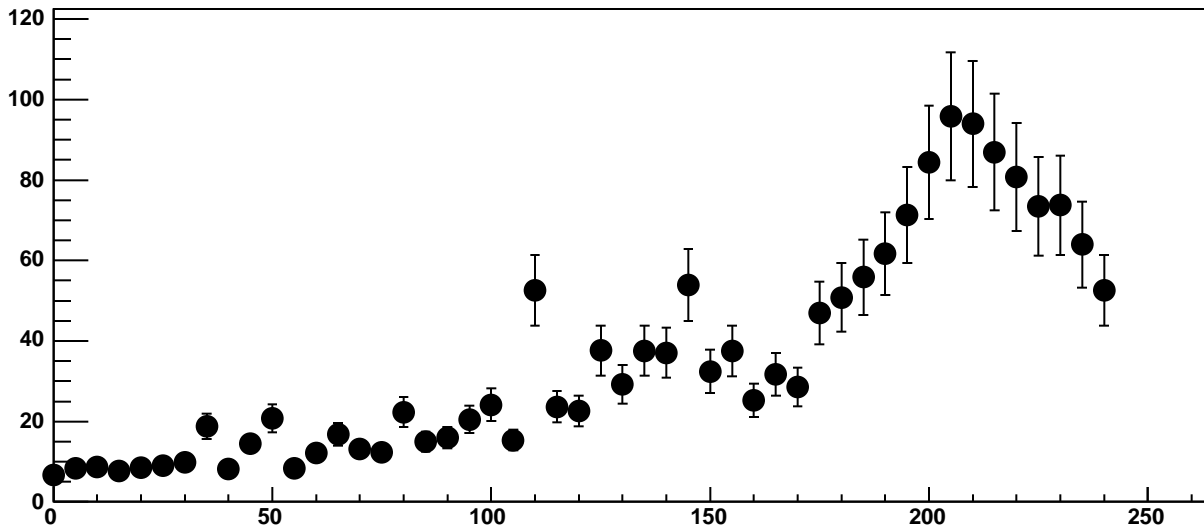


Chip 6, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold

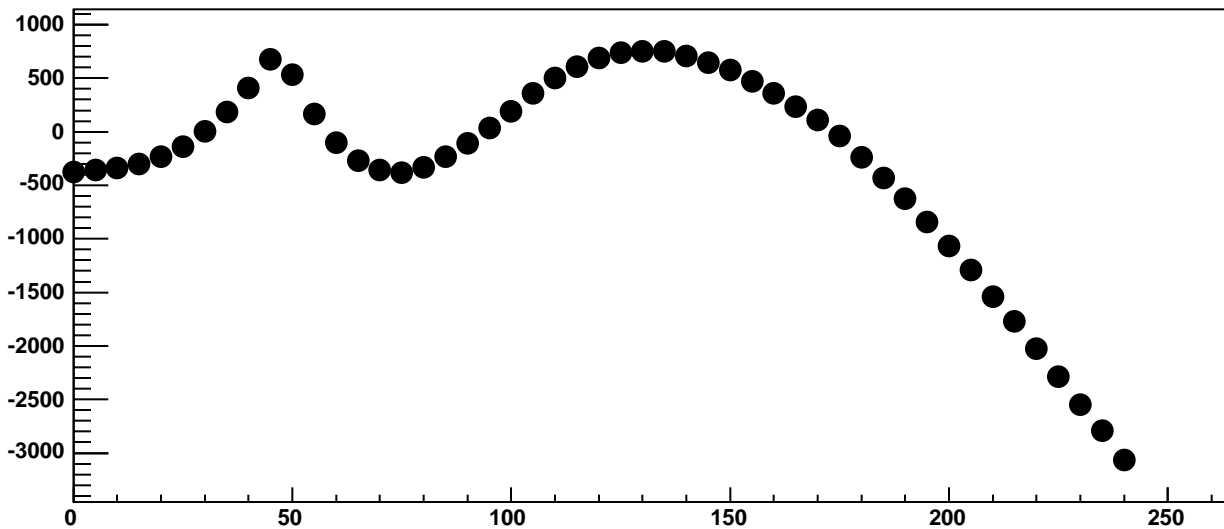


χ^2 / ndf	4.793e+05 / 41
p0	-322.9 ± 1.372
p1	47.15 ± 0.01717
p2	6787 ± 9.825
p3	41.76 ± 0.06483
p4	-4.817 ± 0.05704

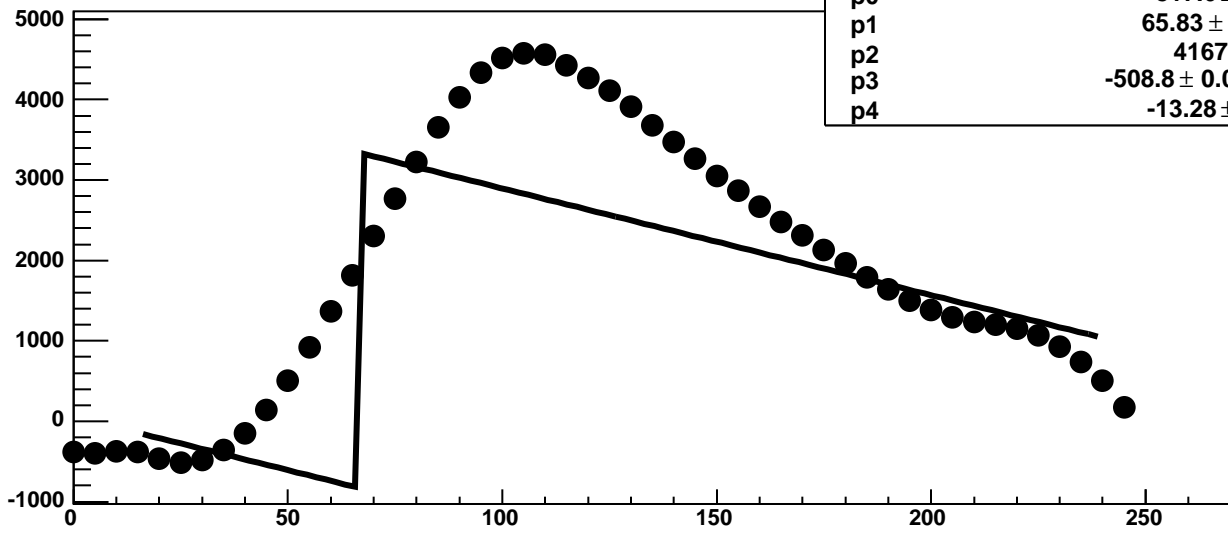
Chip 6, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

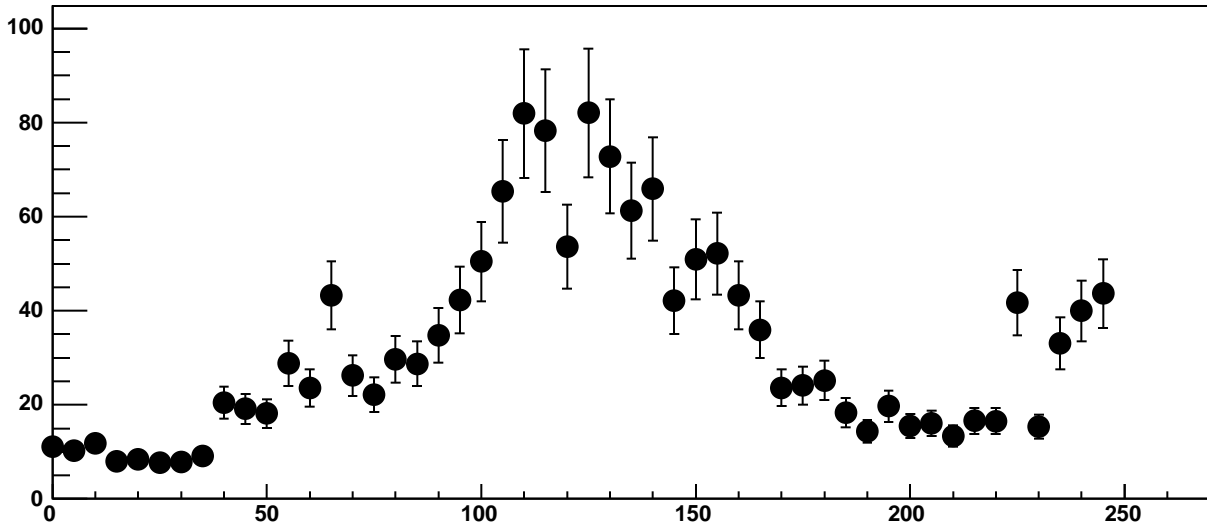


Chip 6, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

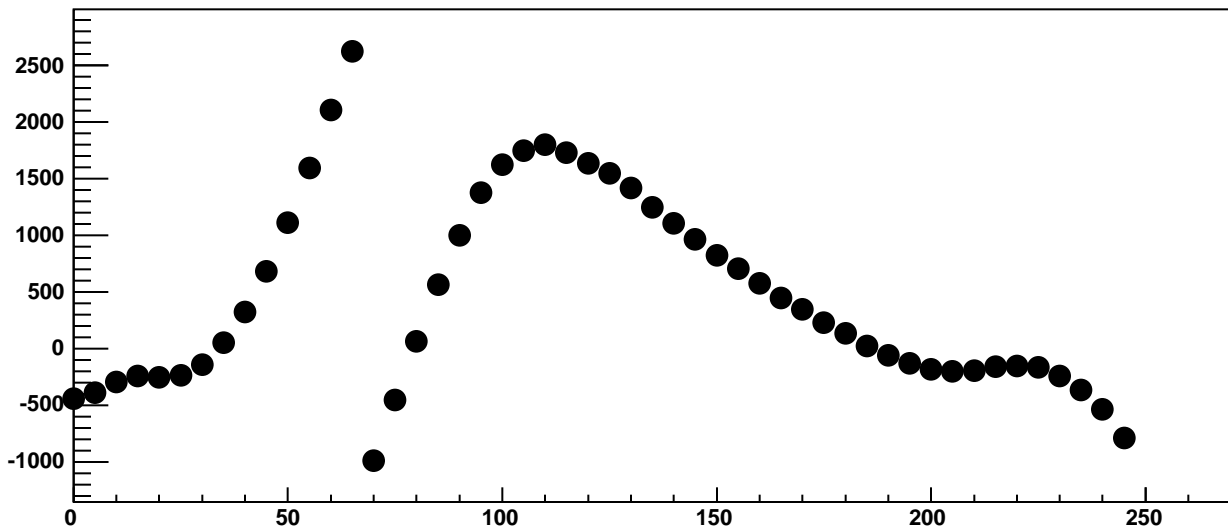


χ^2 / ndf	6.696e+05 / 41
p0	-817.6 ± 0.6837
p1	65.83 ± 0.05151
p2	4167 ± 1.092
p3	-508.8 ± 0.0001147
p4	-13.28 ± 0.0082

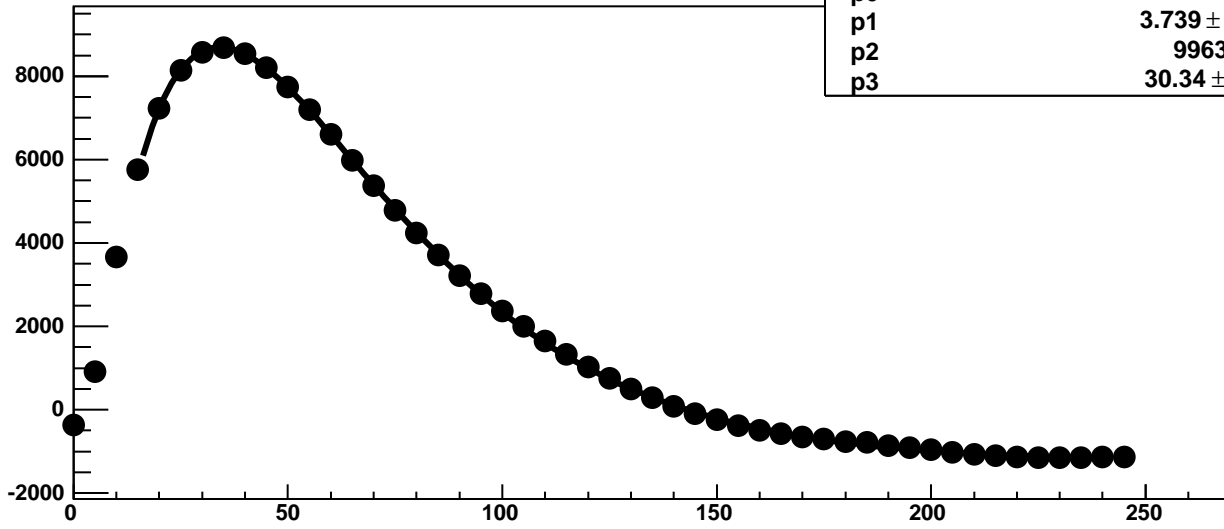
Chip 6, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



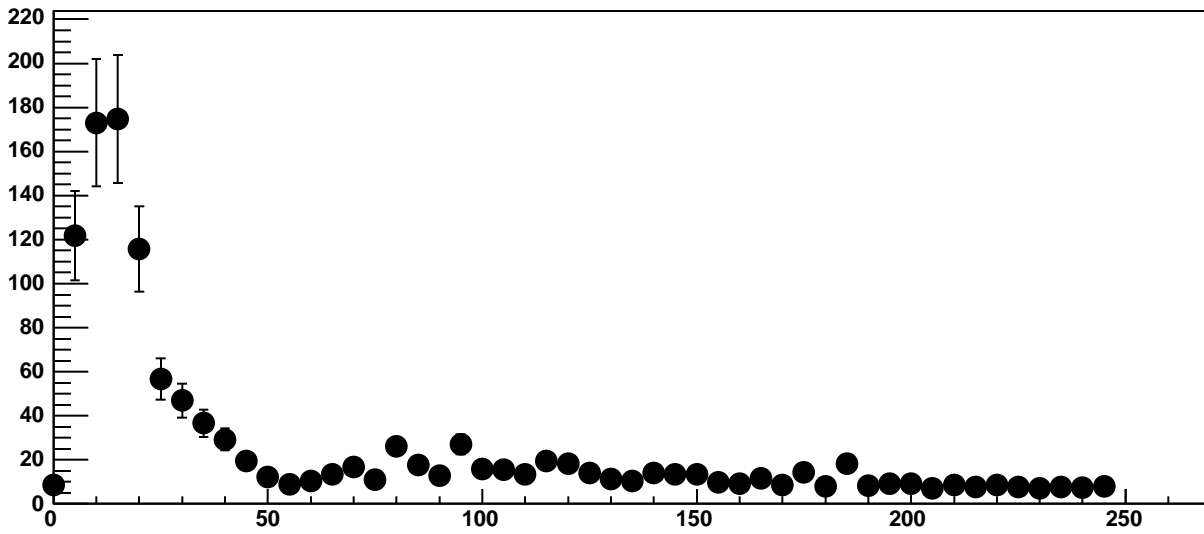
Chip 6, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold



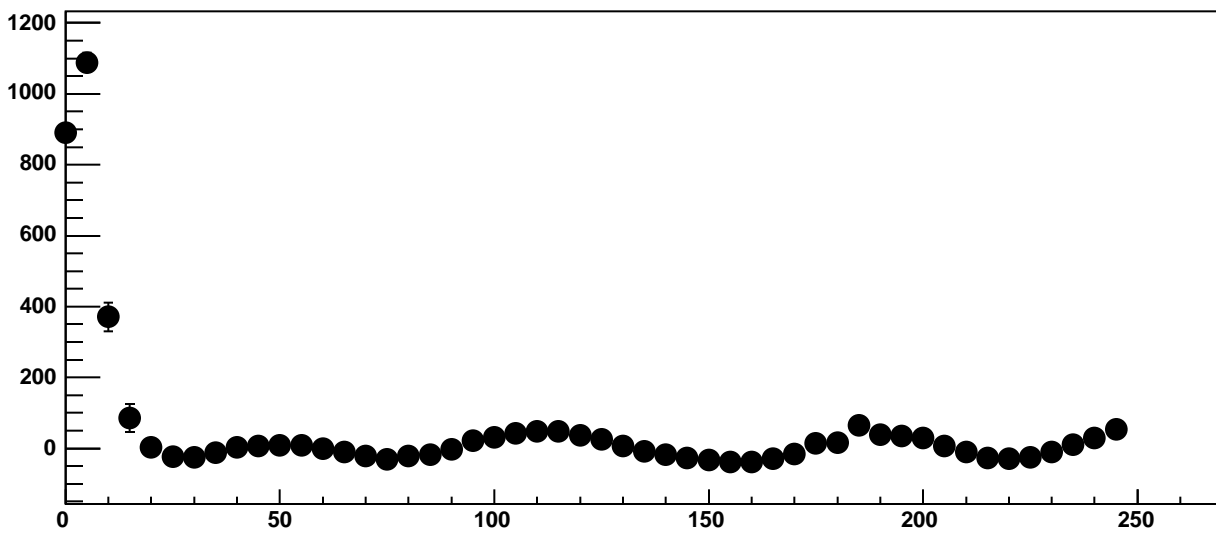
Chip 6, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold



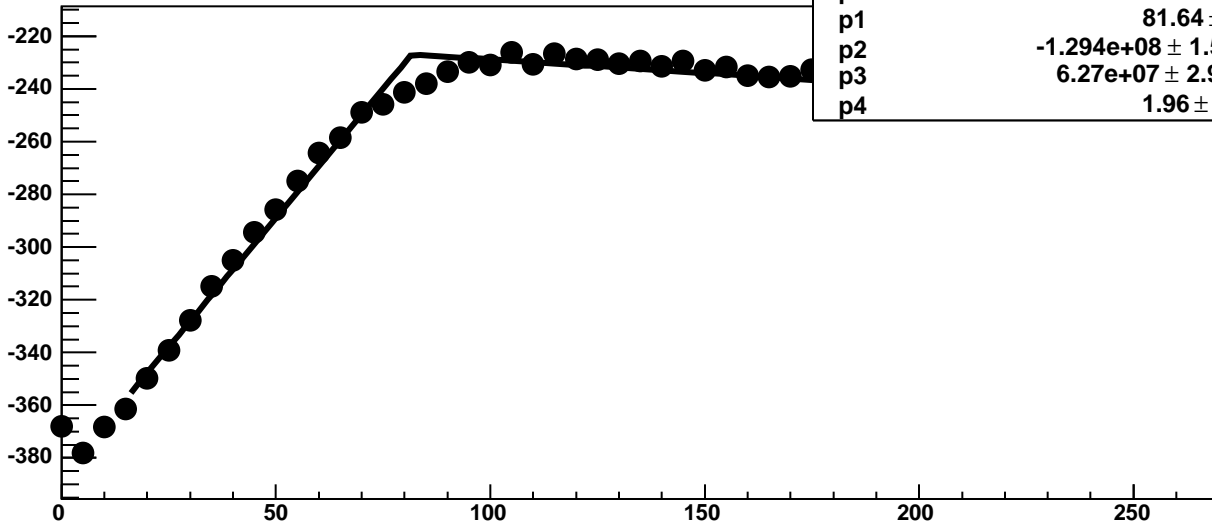
Chip 6, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold

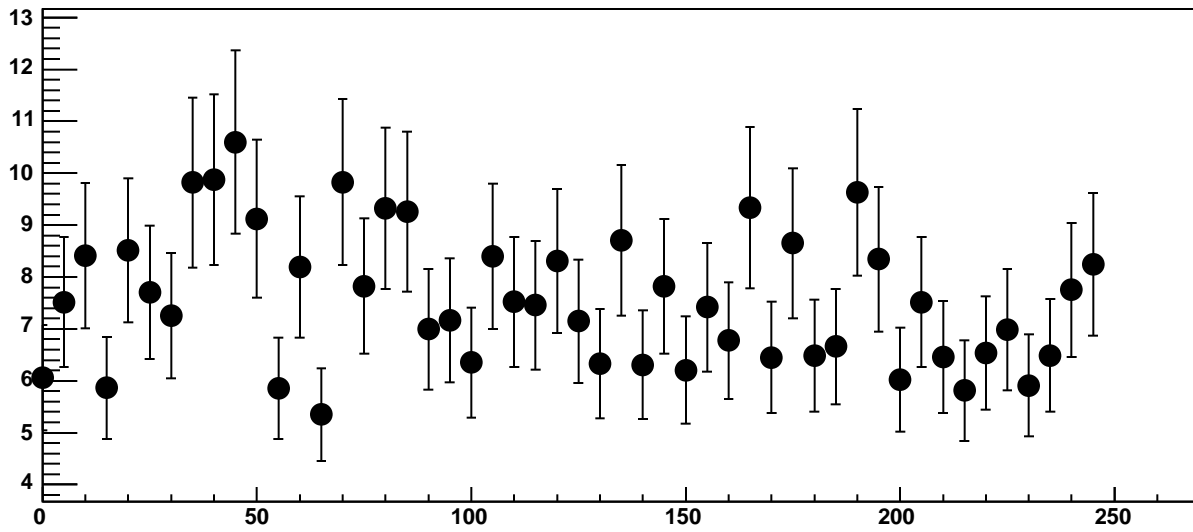


Chip 6, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

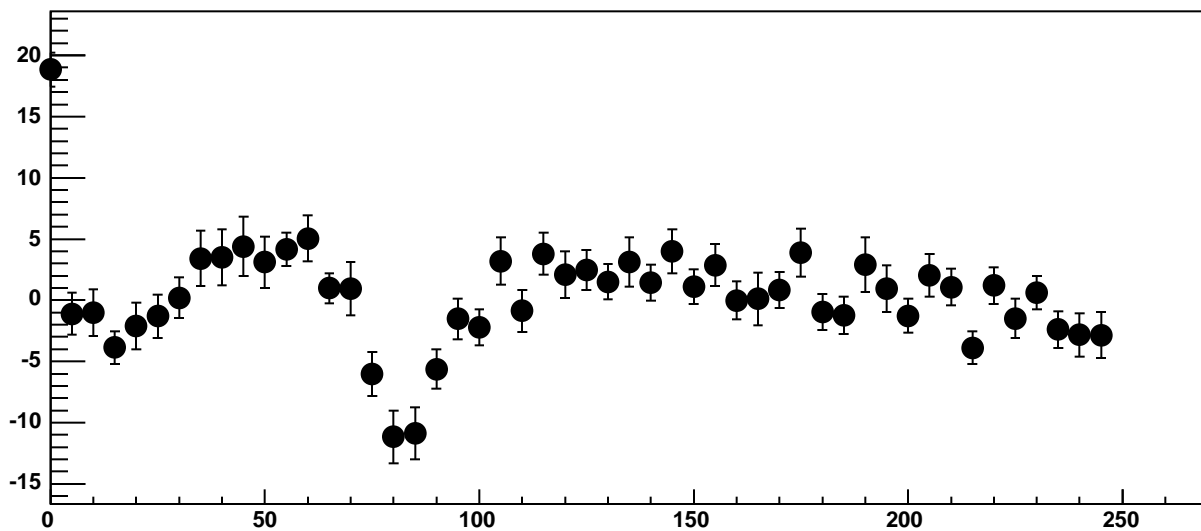


χ^2 / ndf	167.6 / 41
p0	-226.9 ± 0.5696
p1	81.64 ± 0.5307
p2	$-1.294\text{e}+08 \pm 1.553\text{e}+06$
p3	$6.27\text{e}+07 \pm 2.928\text{e}+05$
p4	1.96 ± 0.02256

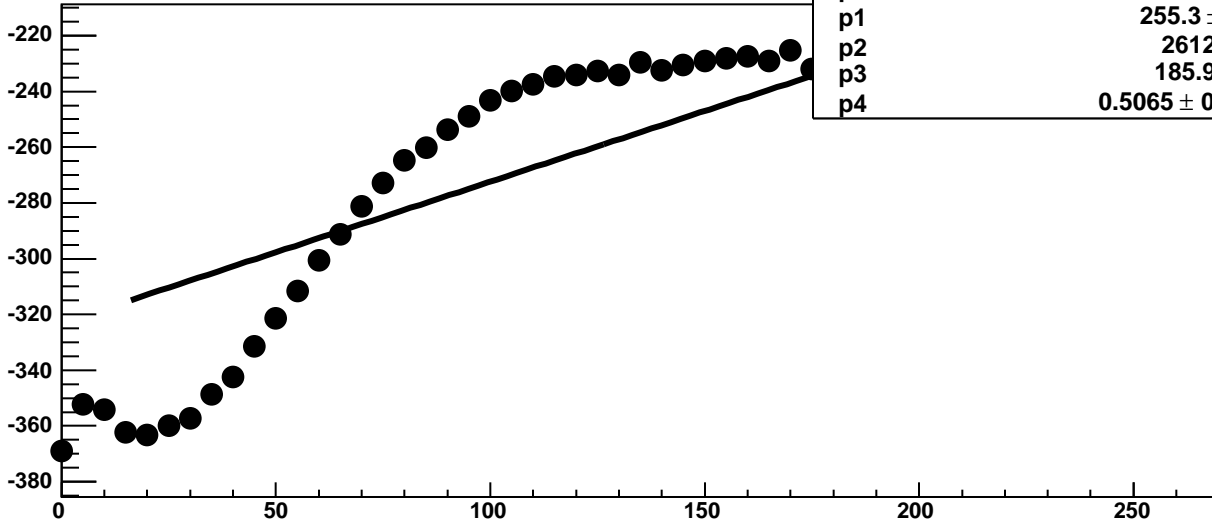
Chip 6, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



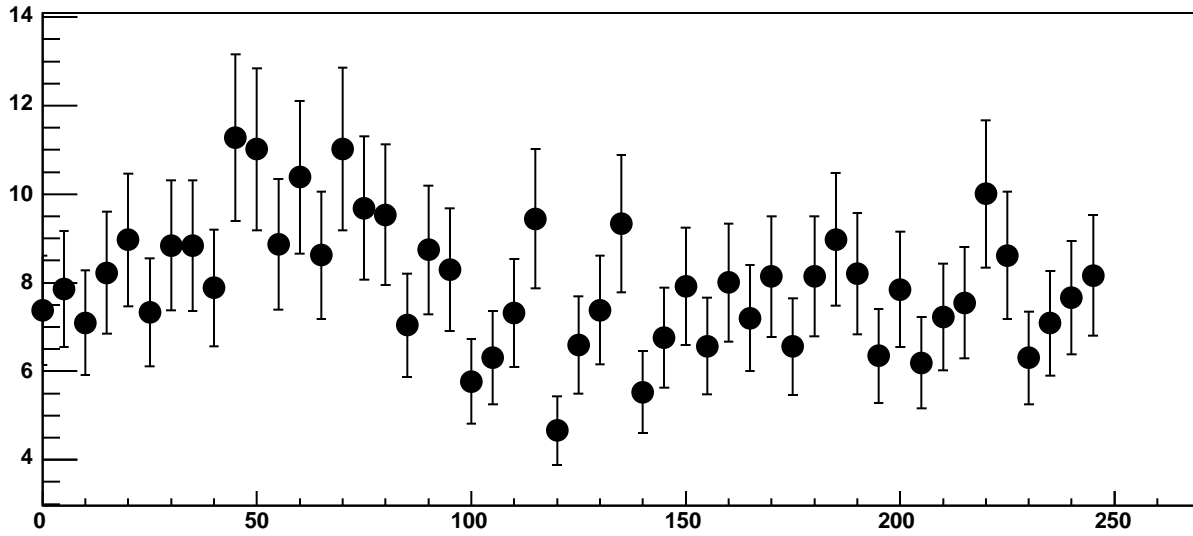
Chip 6, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold



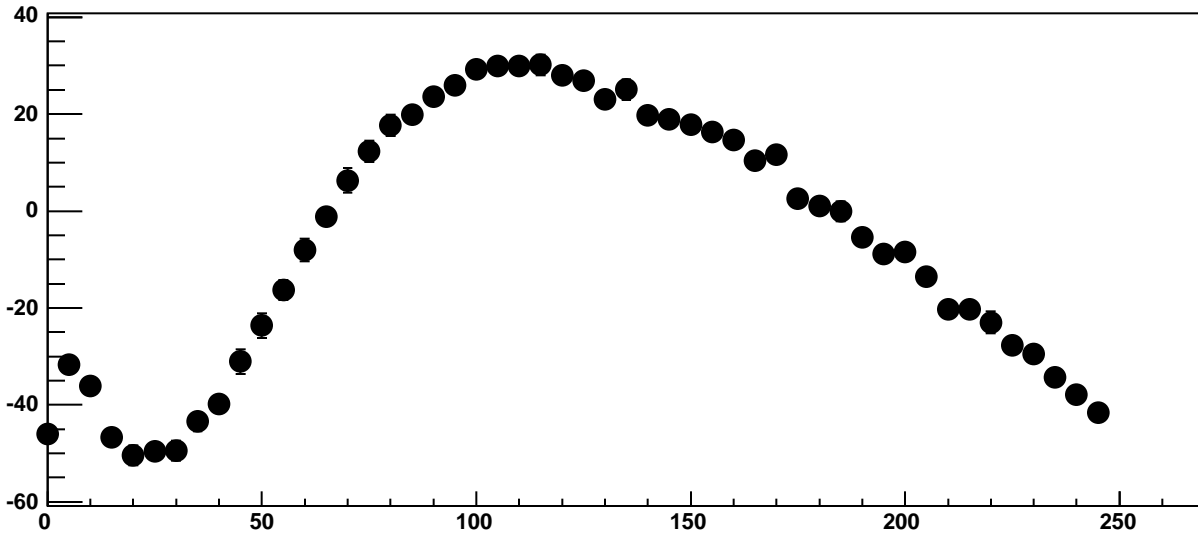
Chip 6, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold



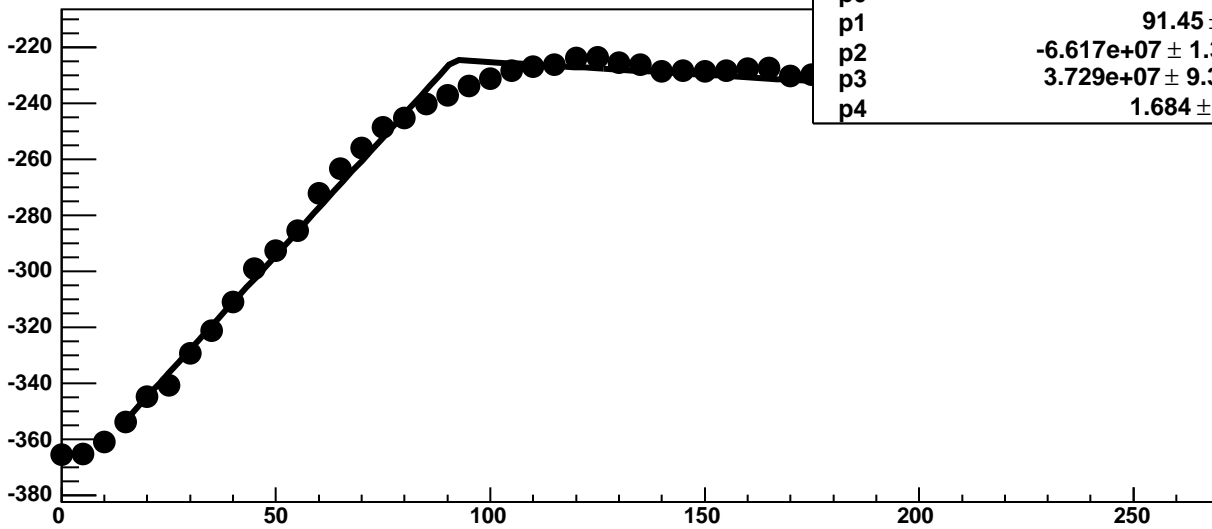
Chip 6, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

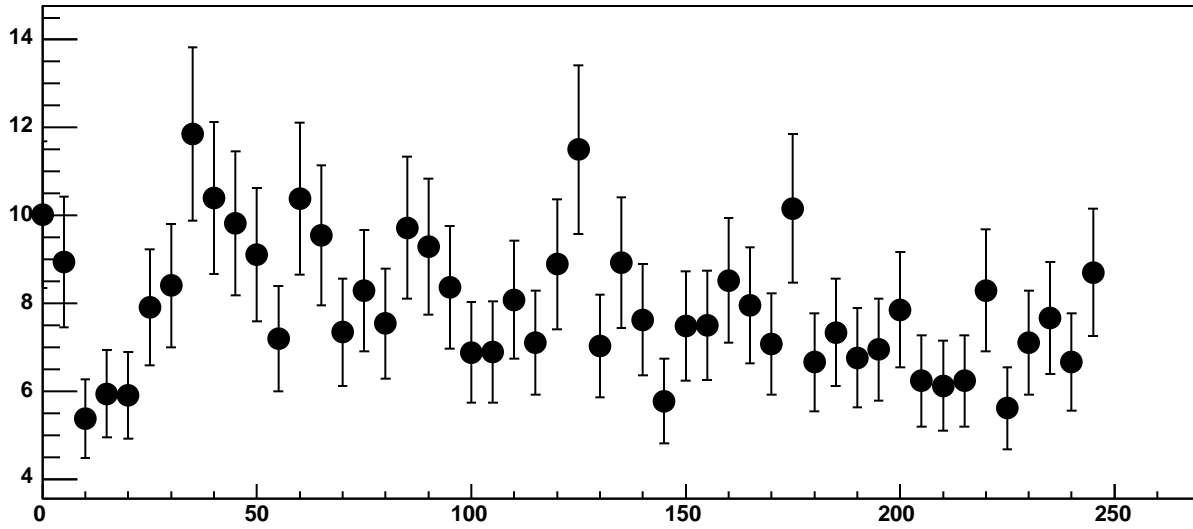


Chip 6, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold

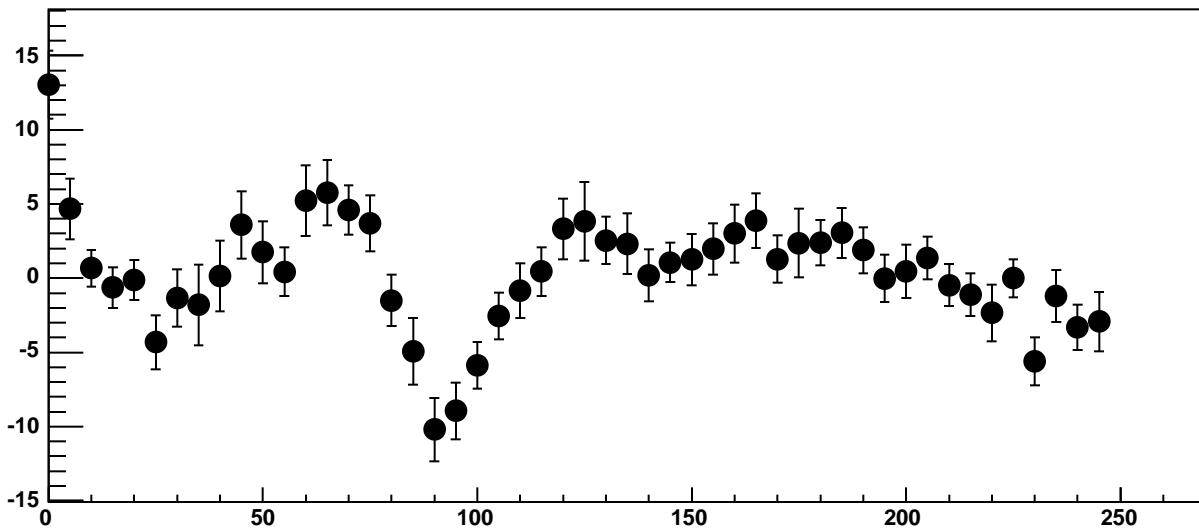


χ^2 / ndf	146.9 / 41
p0	-224.4 ± 0.5949
p1	91.45 ± 0.5607
p2	-6.617e+07 ± 1.327e+06
p3	3.729e+07 ± 9.308e+05
p4	1.684 ± 0.01641

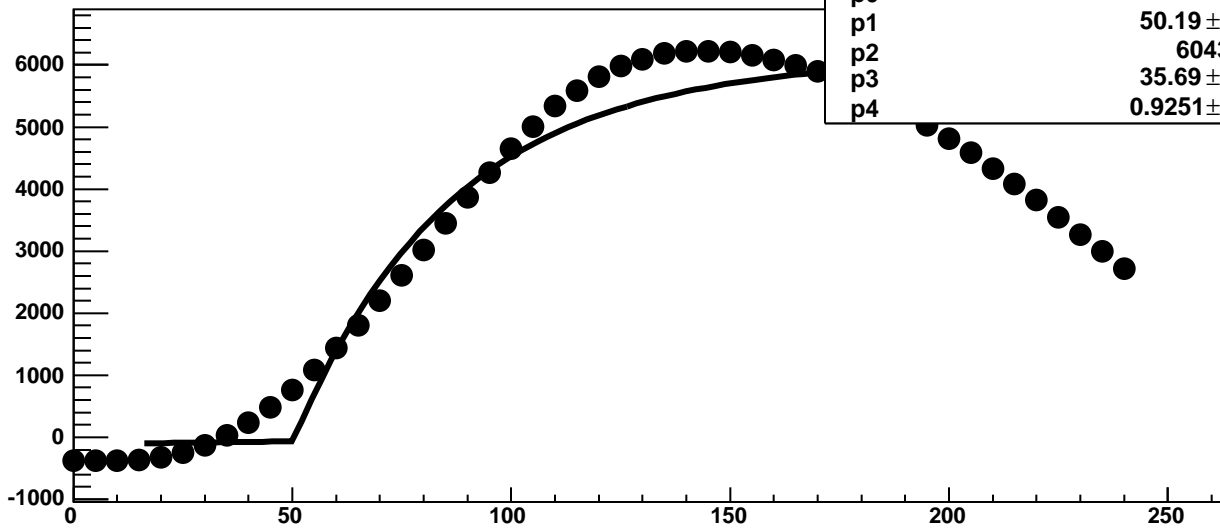
Chip 6, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold

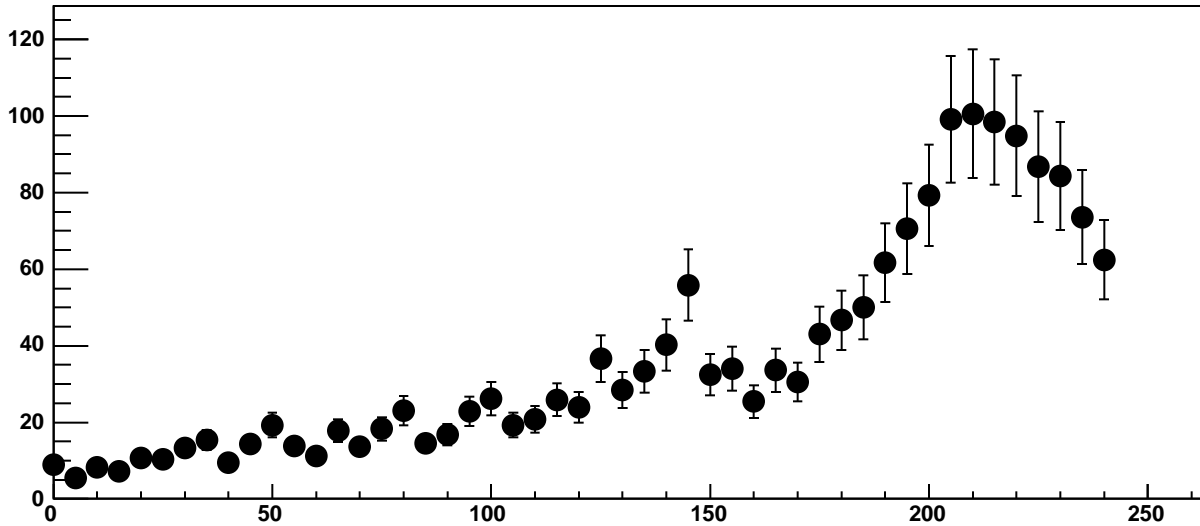


Chip 6, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

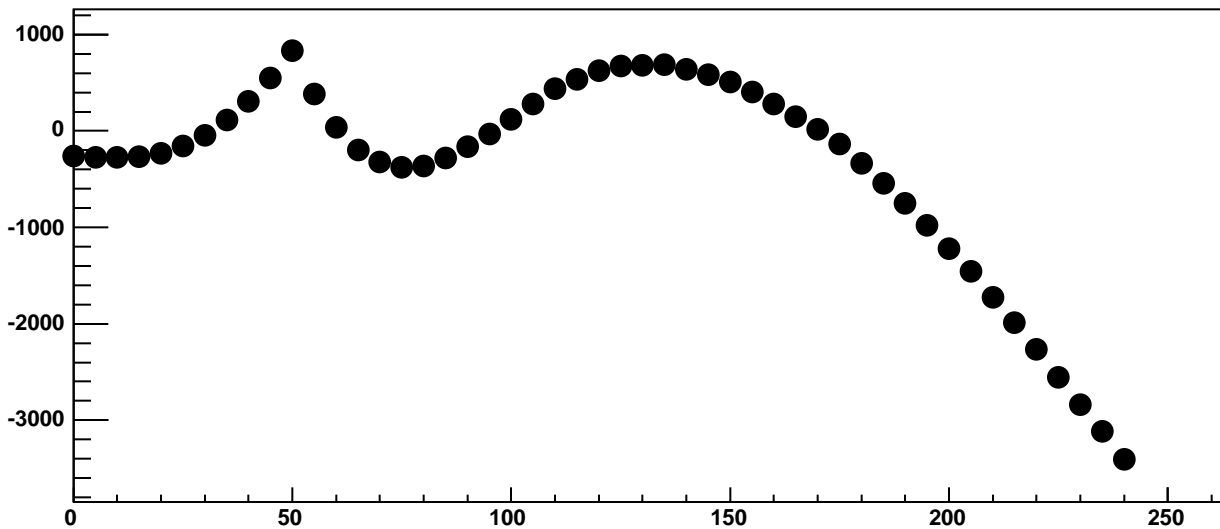


χ^2 / ndf	4.21e+05 / 41
p0	-65.43 ± 1.544
p1	50.19 ± 0.01998
p2	6043 ± 8.861
p3	35.69 ± 0.06116
p4	0.9251 ± 0.05509

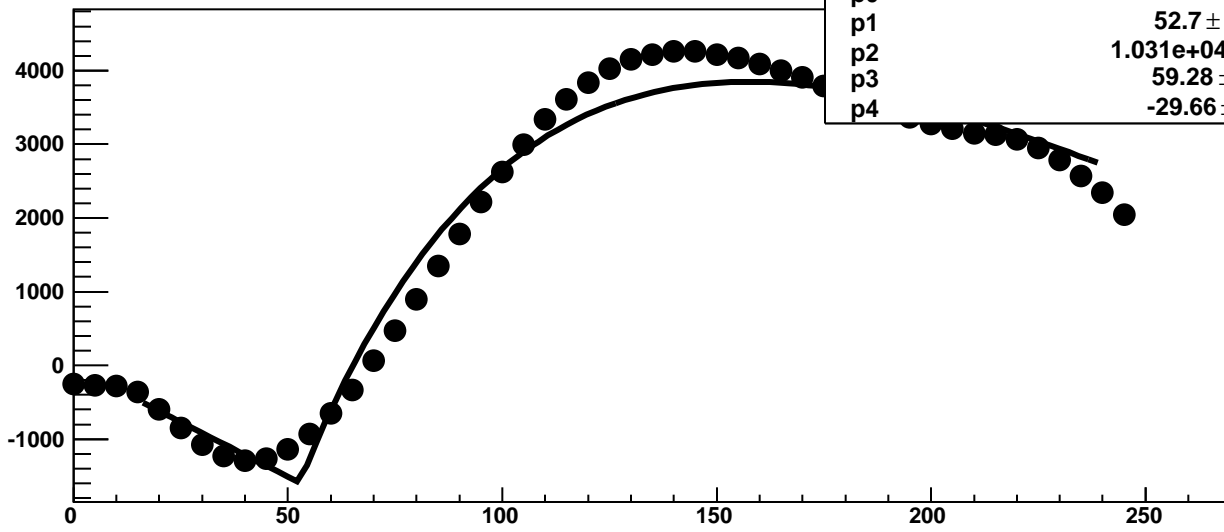
Chip 6, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold



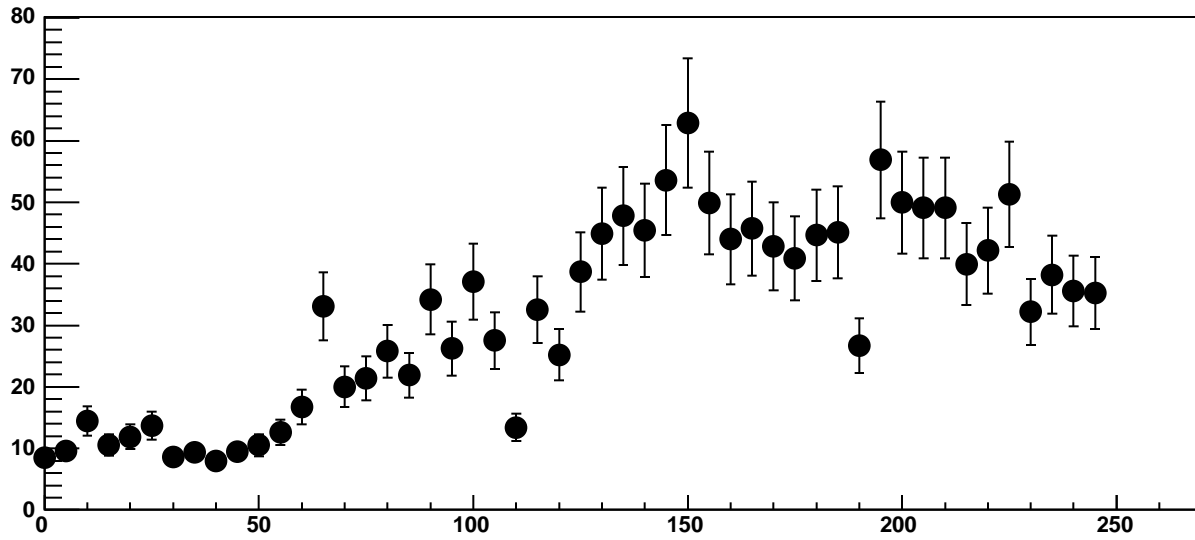
Chip 6, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold



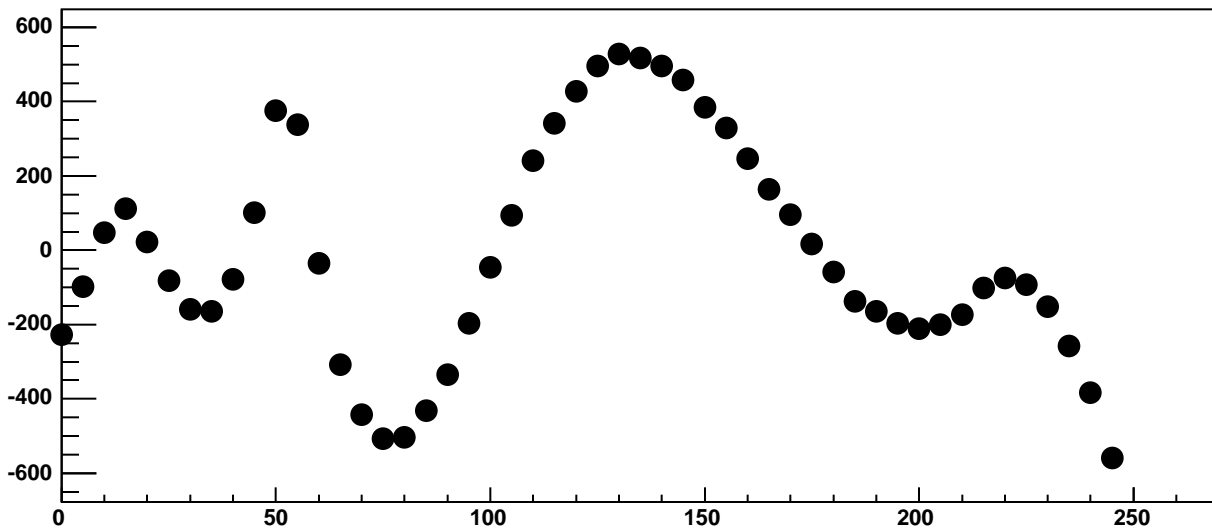
Chip 6, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold



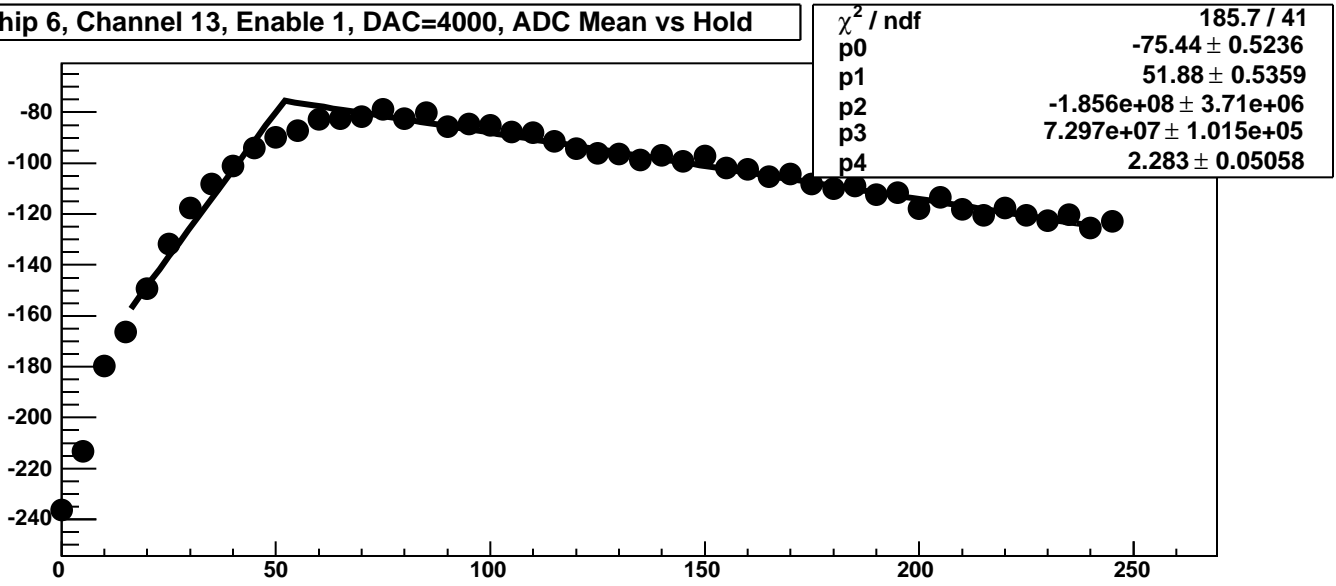
Chip 6, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



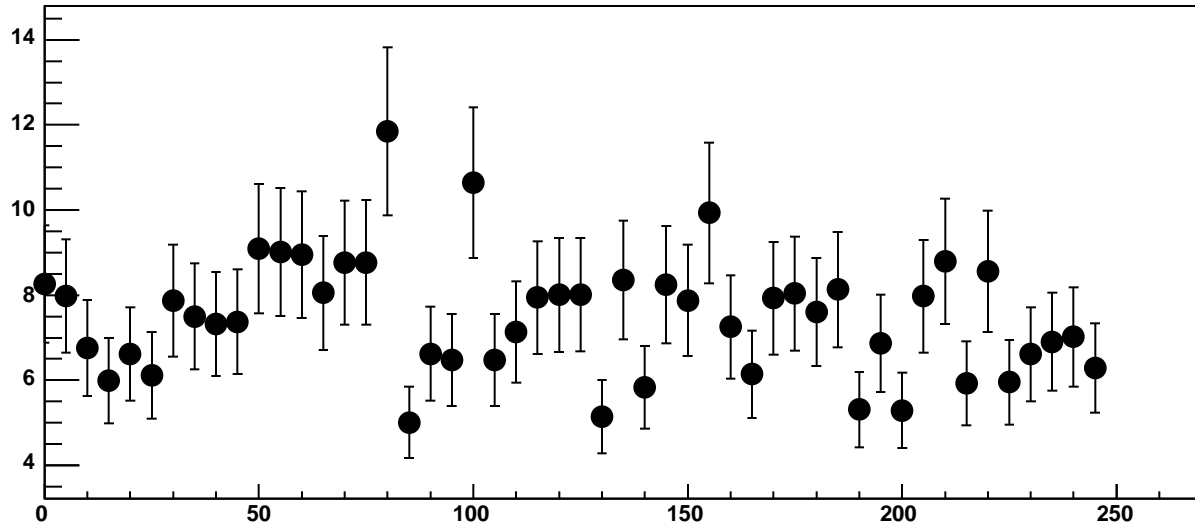
Chip 6, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold



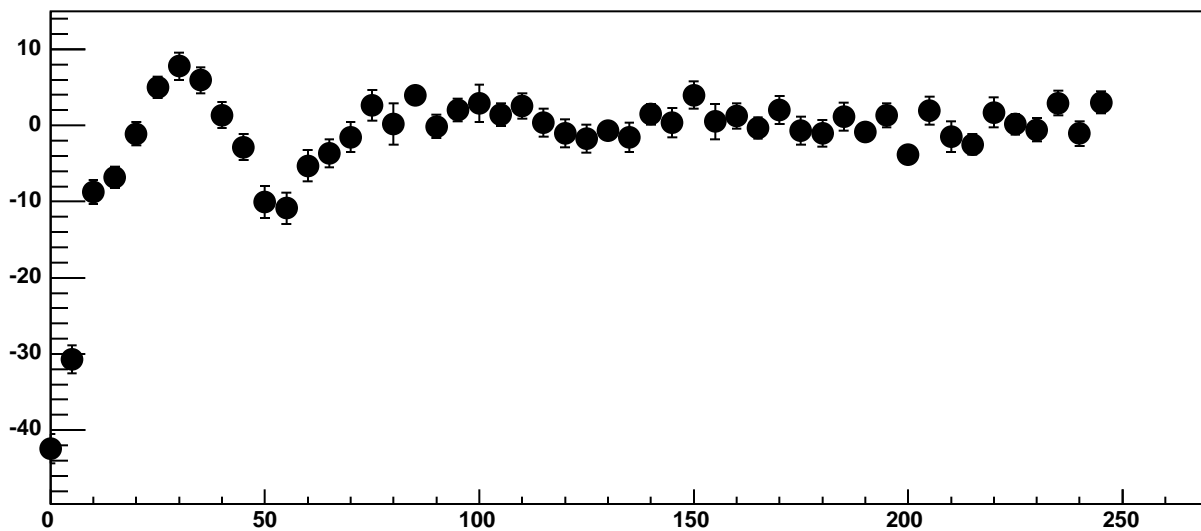
Chip 6, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold



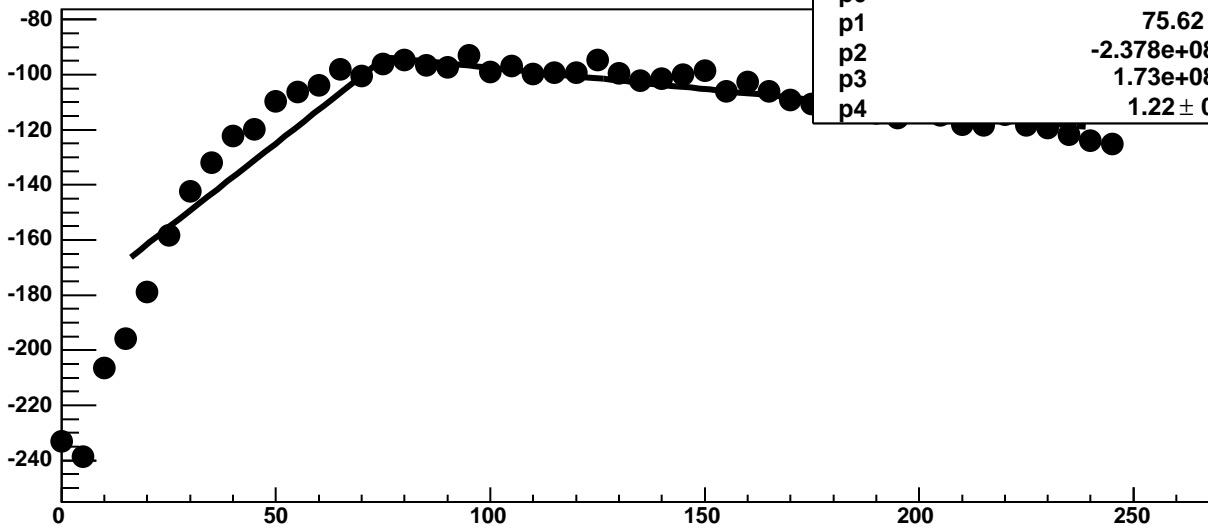
Chip 6, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold

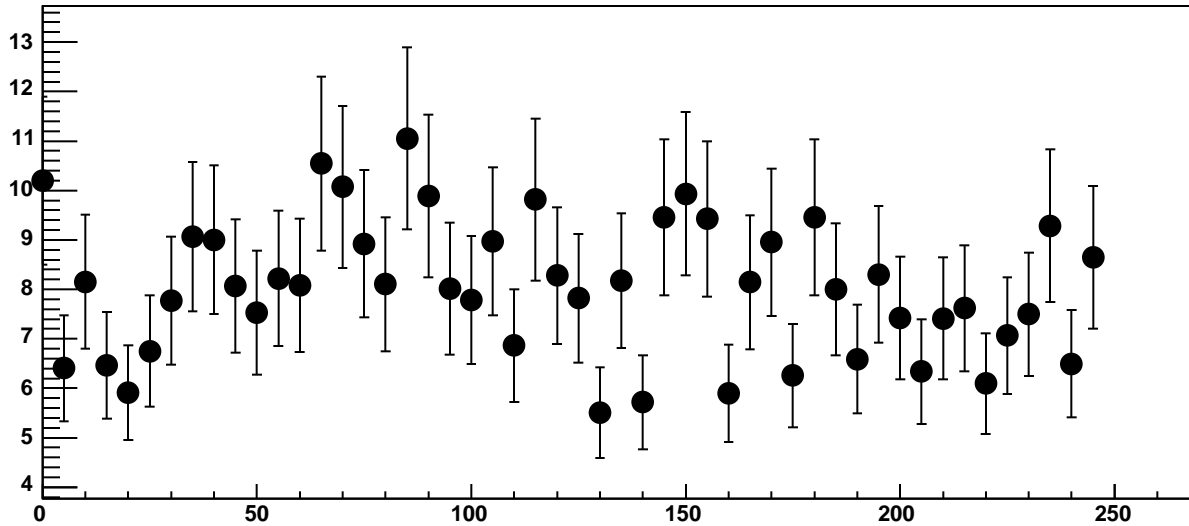


Chip 6, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

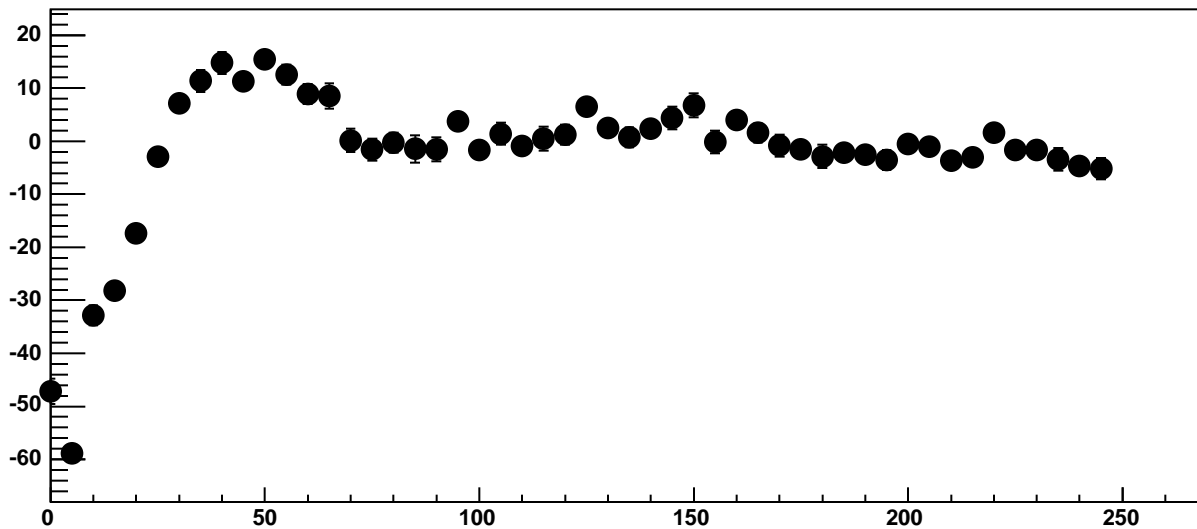


χ^2 / ndf	915.1 / 41
p0	-93.71 ± 0.5486
p1	75.62 ± 0.7138
p2	$-2.378e+08 \pm 1.414$
p3	$1.73e+08 \pm 1.414$
p4	1.22 ± 0.006324

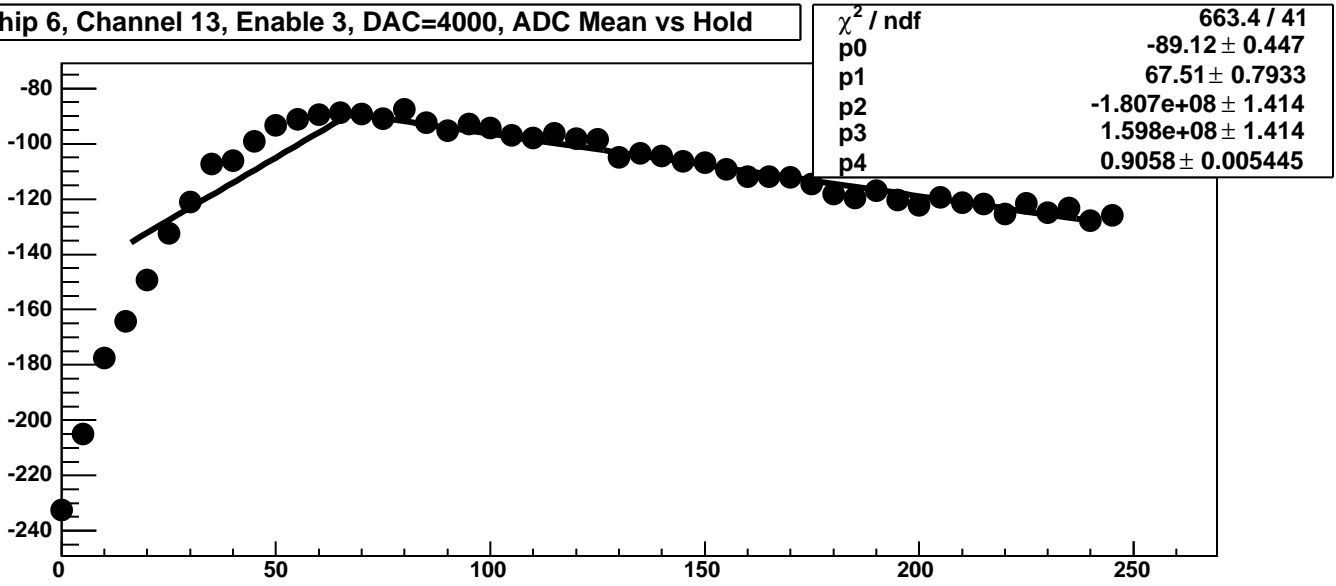
Chip 6, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



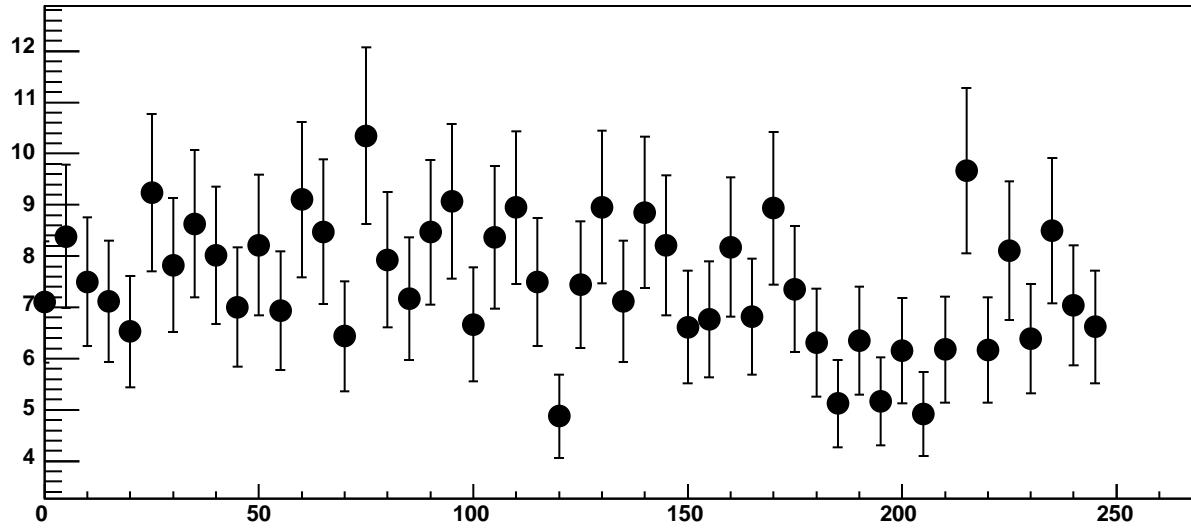
Chip 6, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



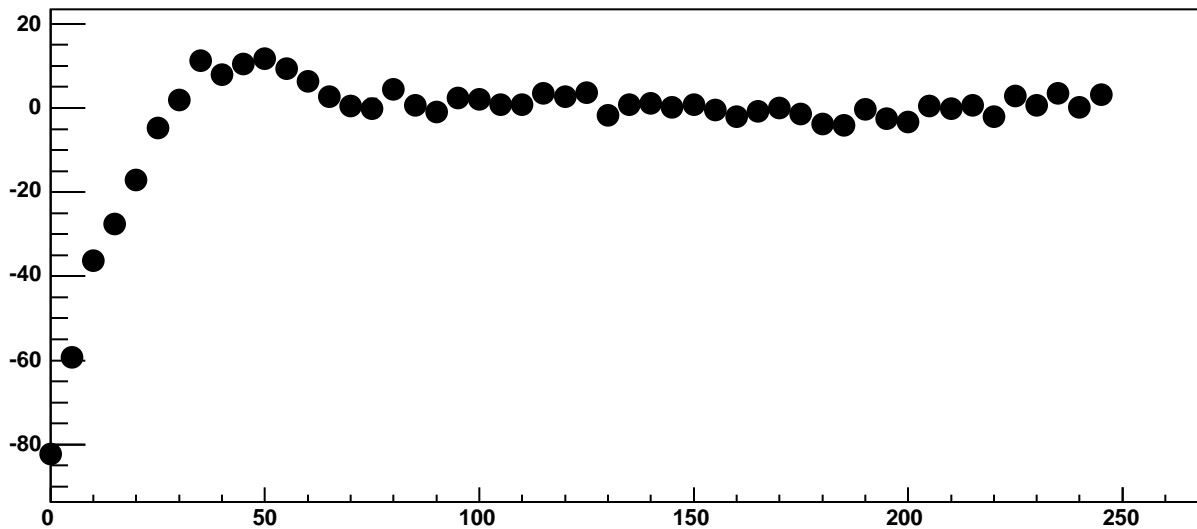
Chip 6, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold



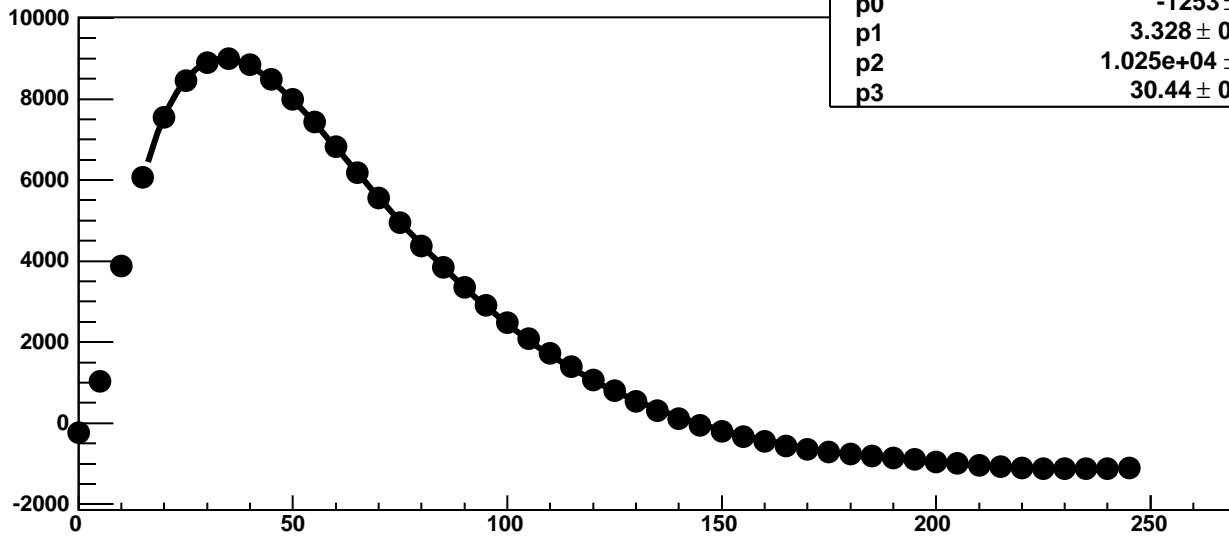
Chip 6, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

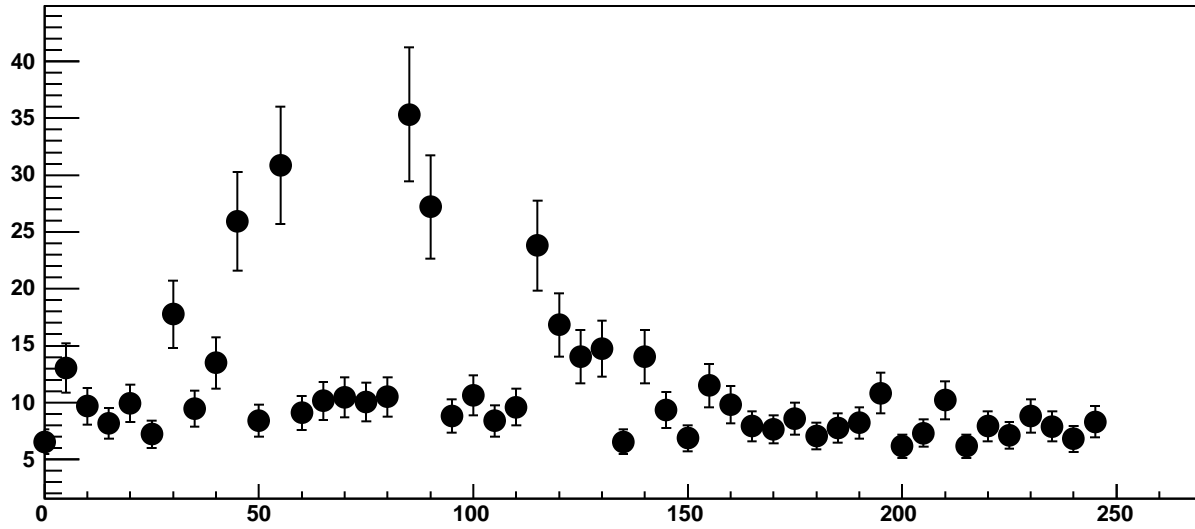


Chip 6, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold

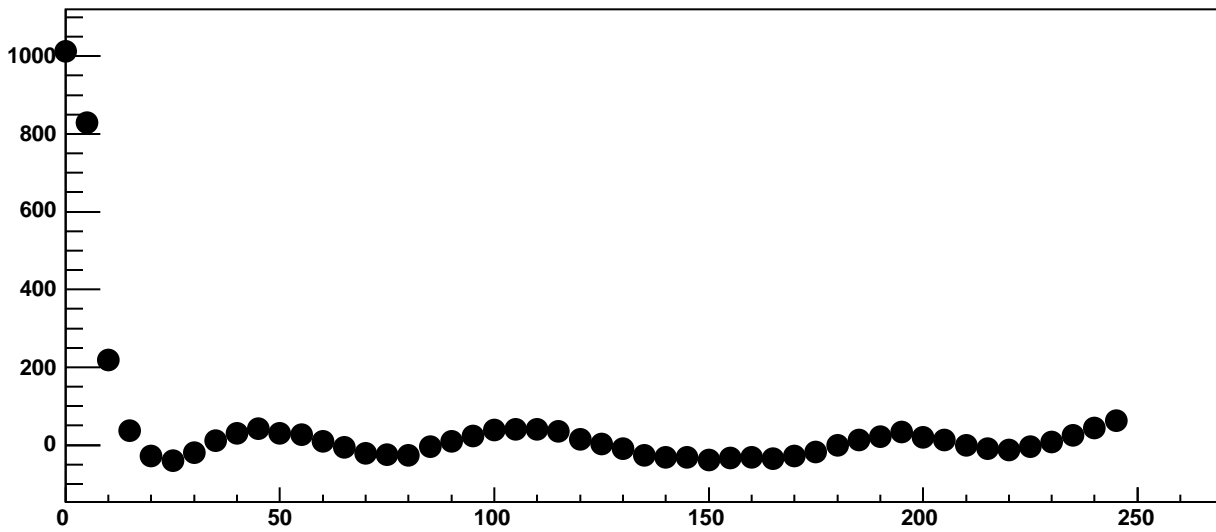


χ^2 / ndf	7146 / 42
p0	-1253 ± 0.5566
p1	3.328 ± 0.006483
p2	$1.025\text{e}+04 \pm 0.9894$
p3	30.44 ± 0.005457

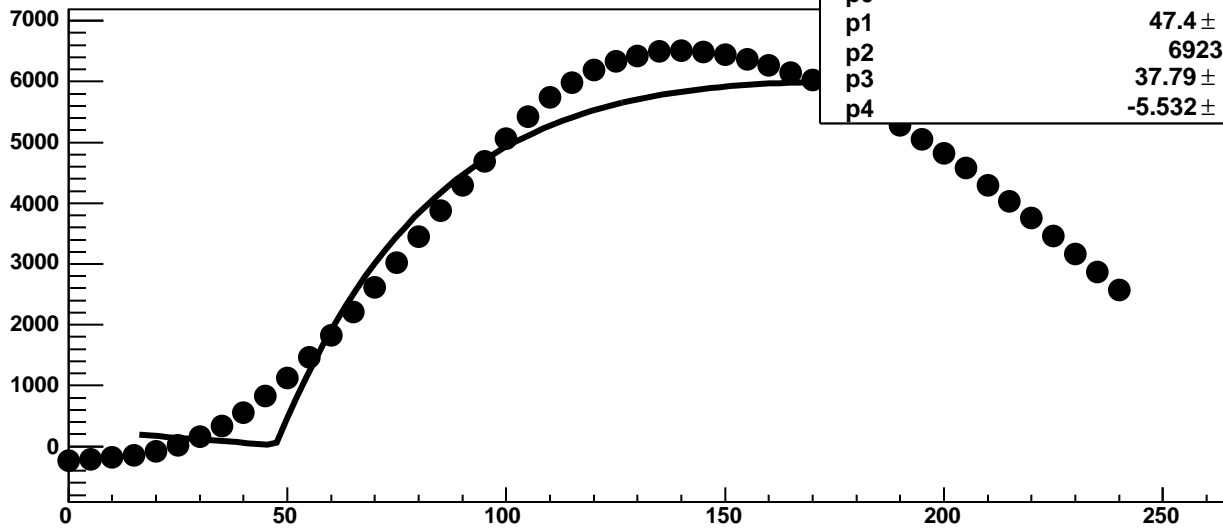
Chip 6, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold

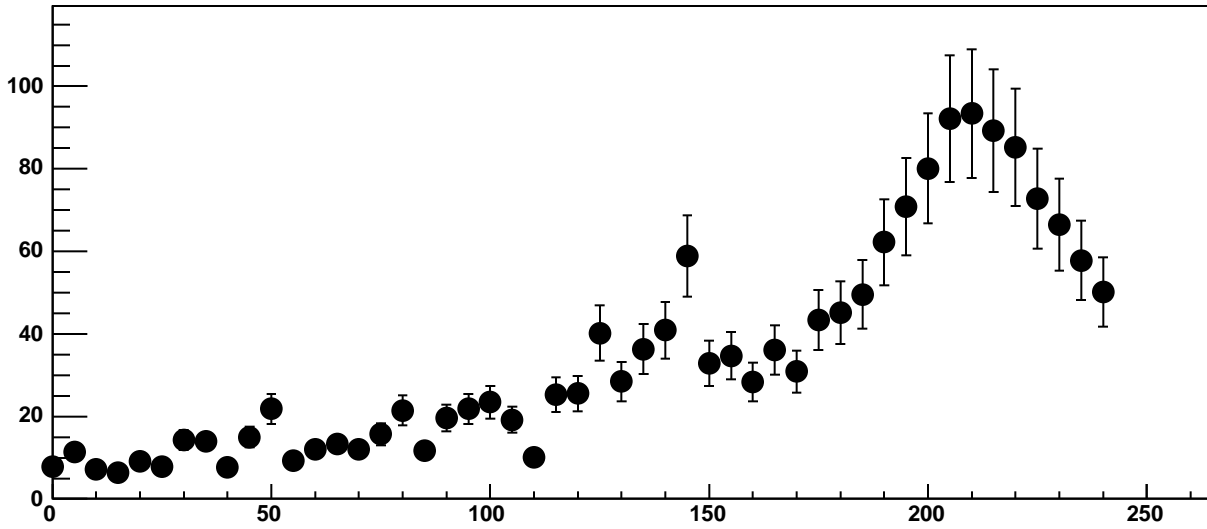


Chip 6, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

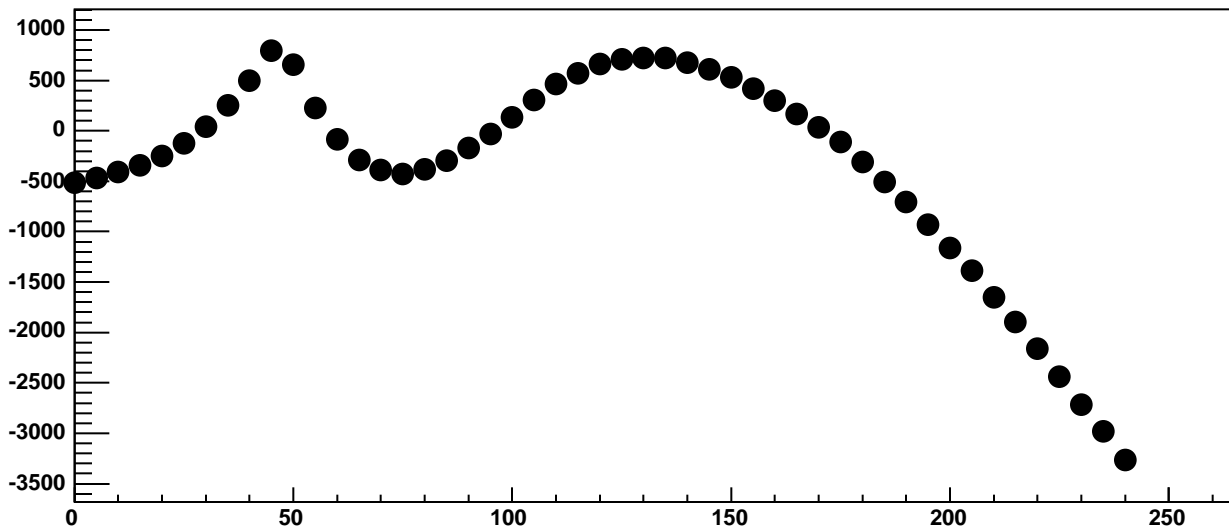


χ^2 / ndf	6.45e+05 / 41
p0	18.78 ± 1.282
p1	47.4 ± 0.01572
p2	6923 ± 8.234
p3	37.79 ± 0.05167
p4	-5.532 ± 0.05022

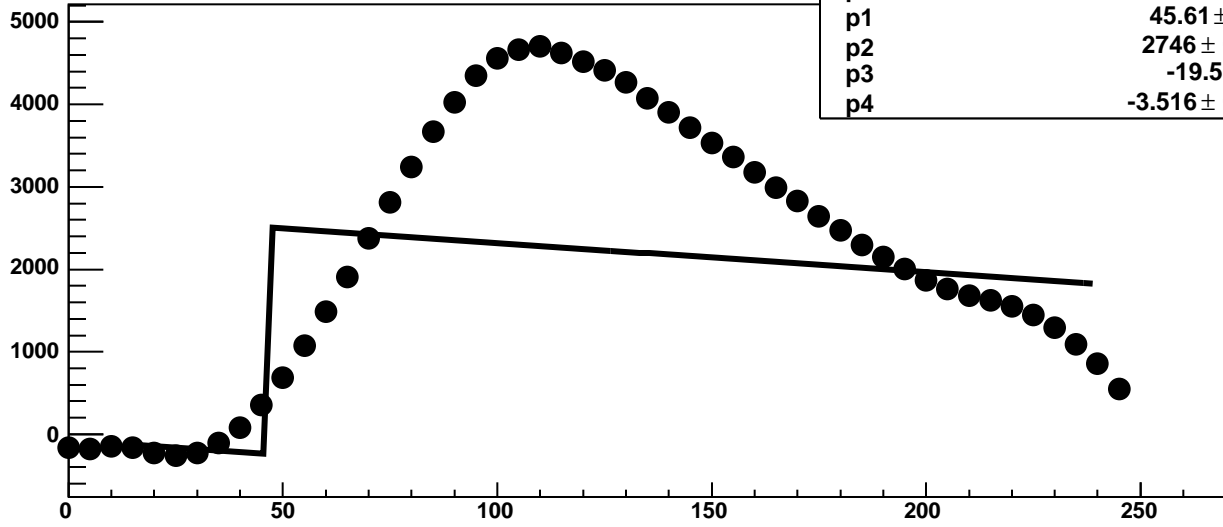
Chip 6, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold

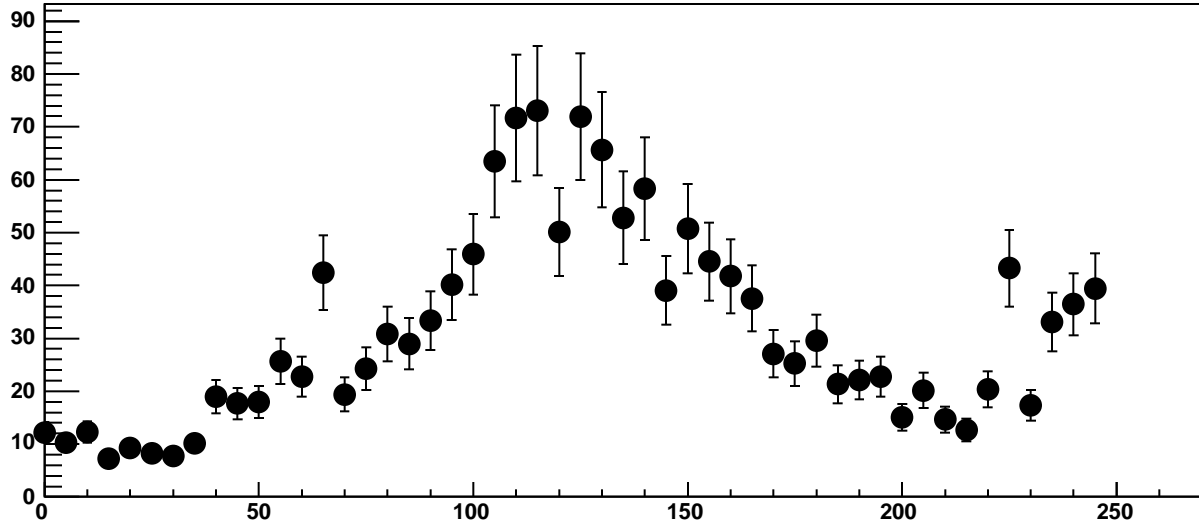


Chip 6, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

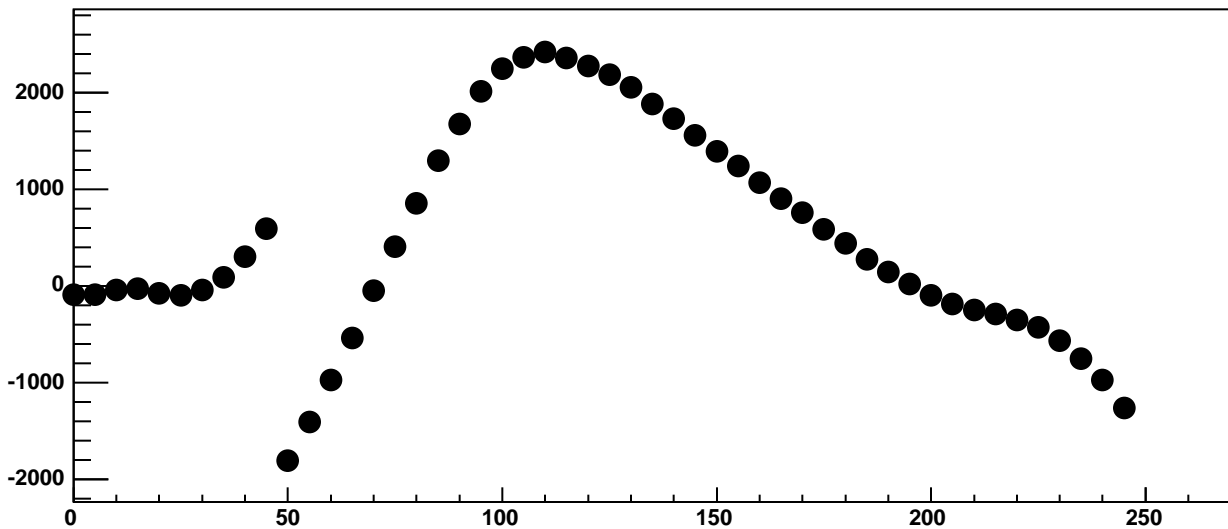


χ^2 / ndf	8.857e+05 / 41
p0	-236.3 ± 0.04862
p1	45.61 ± 0.4446
p2	2746 ± 0.04269
p3	-19.5 ± 4.615
p4	-3.516 ± 0.00136

Chip 6, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold

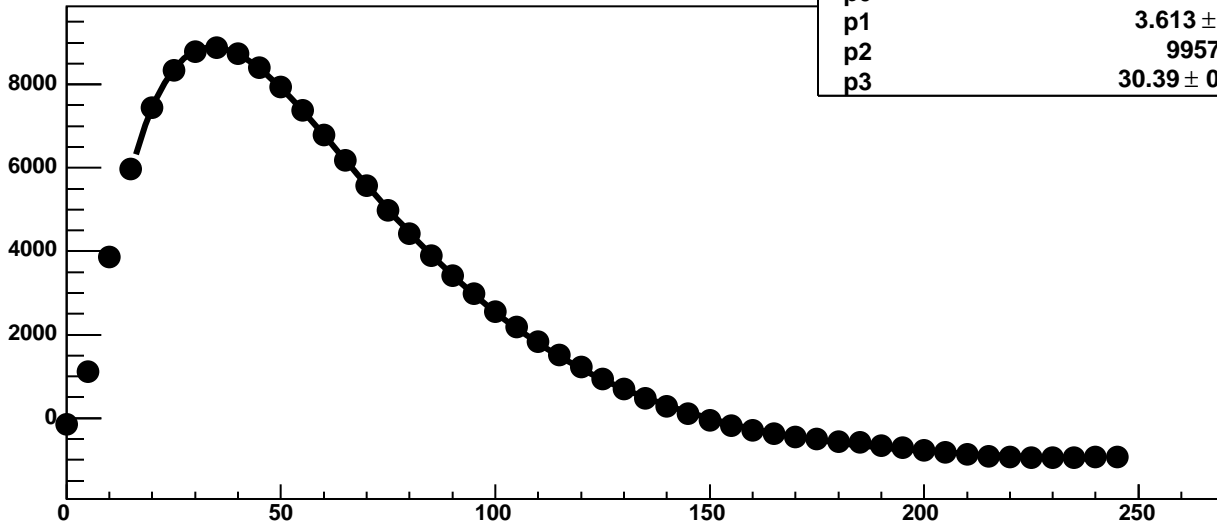


Chip 6, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

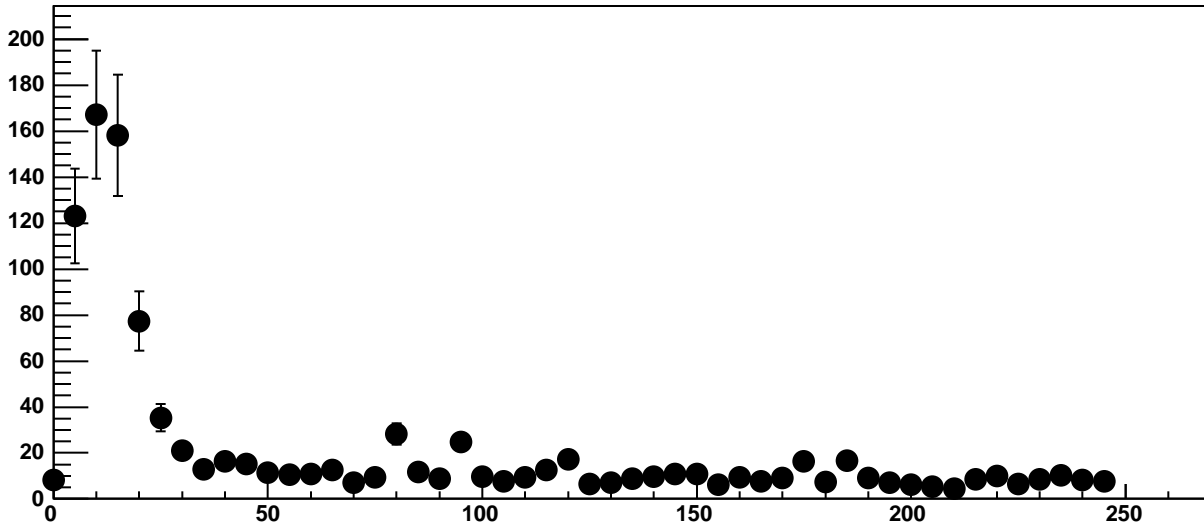


Chip 6, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold

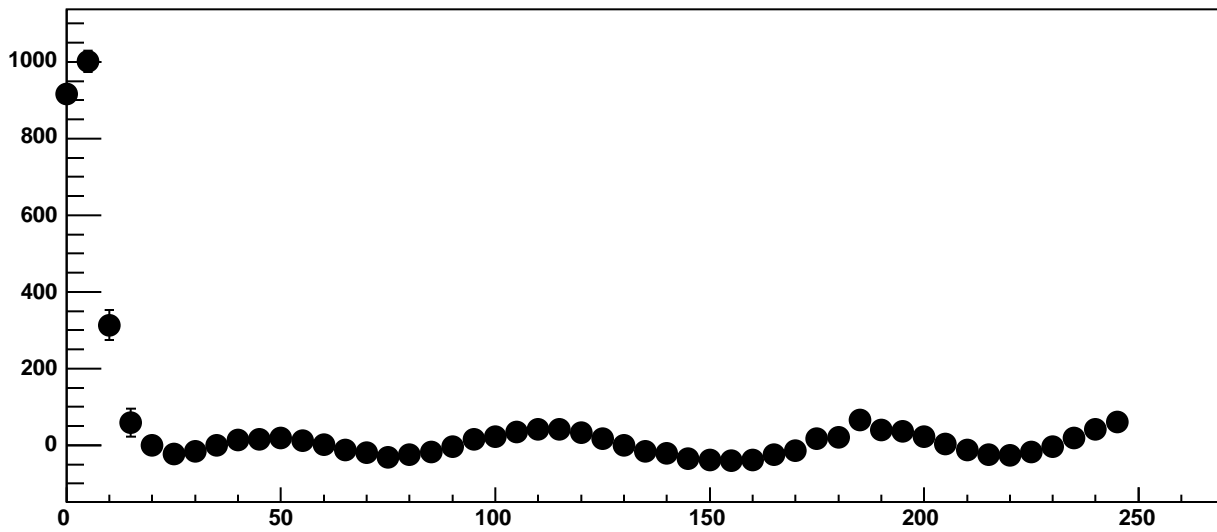
χ^2 / ndf	6420 / 42
p0	-1064 ± 0.6016
p1	3.613 ± 0.02451
p2	9957 ± 1.882
p3	30.39 ± 0.008887



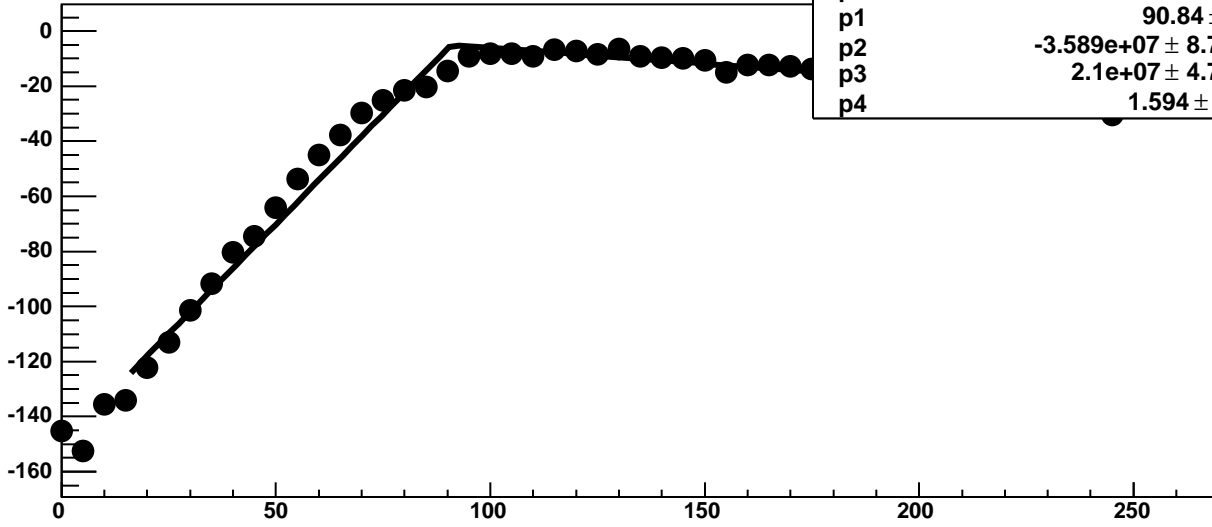
Chip 6, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold

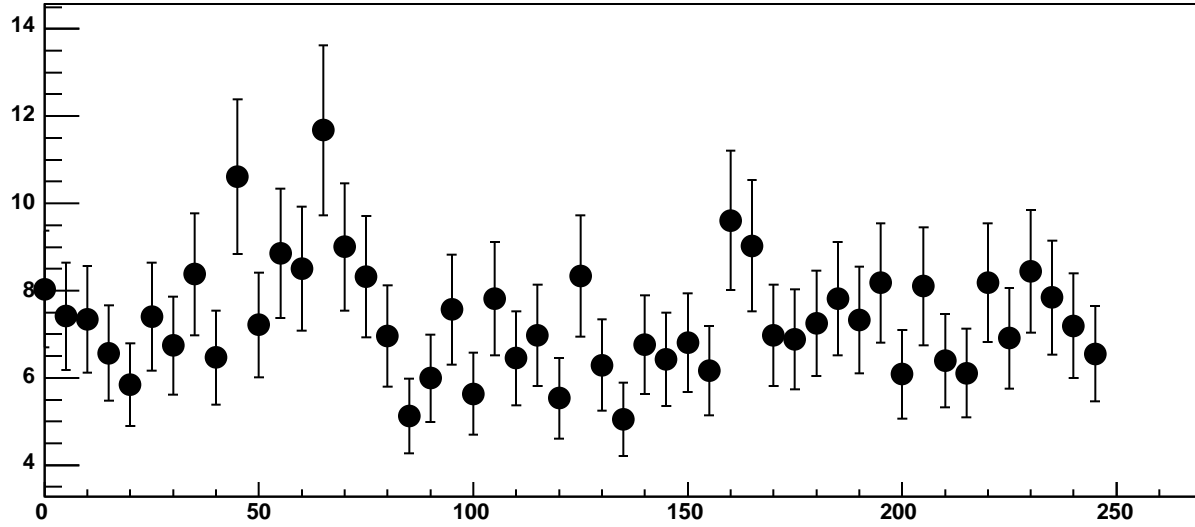


Chip 6, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

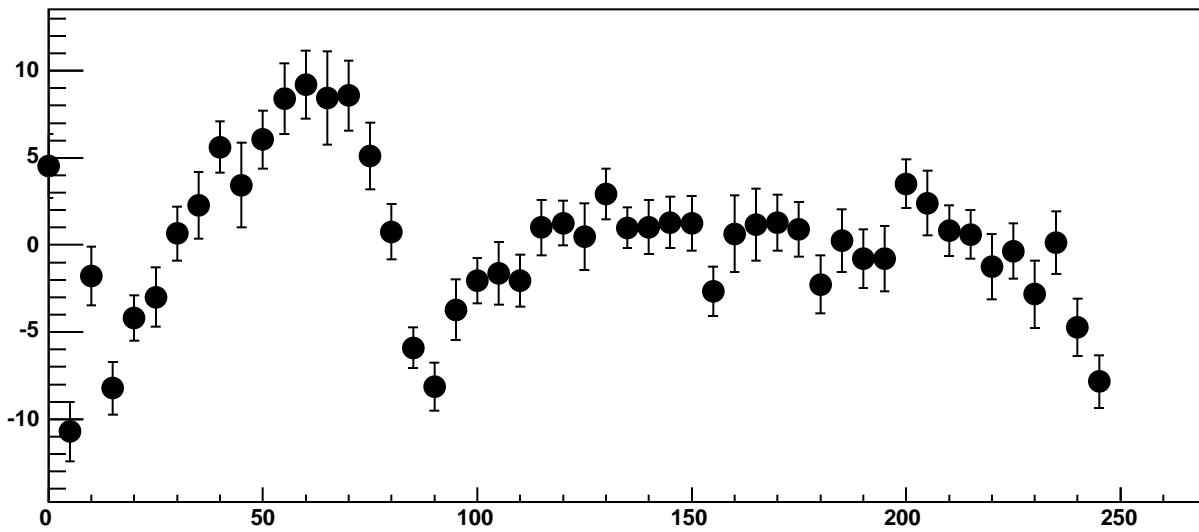


χ^2 / ndf	253.8 / 41
p0	-5.008 ± 0.5311
p1	90.84 ± 0.5448
p2	$-3.589\text{e}+07 \pm 8.769\text{e}+05$
p3	$2.1\text{e}+07 \pm 4.792\text{e}+05$
p4	1.594 ± 0.01595

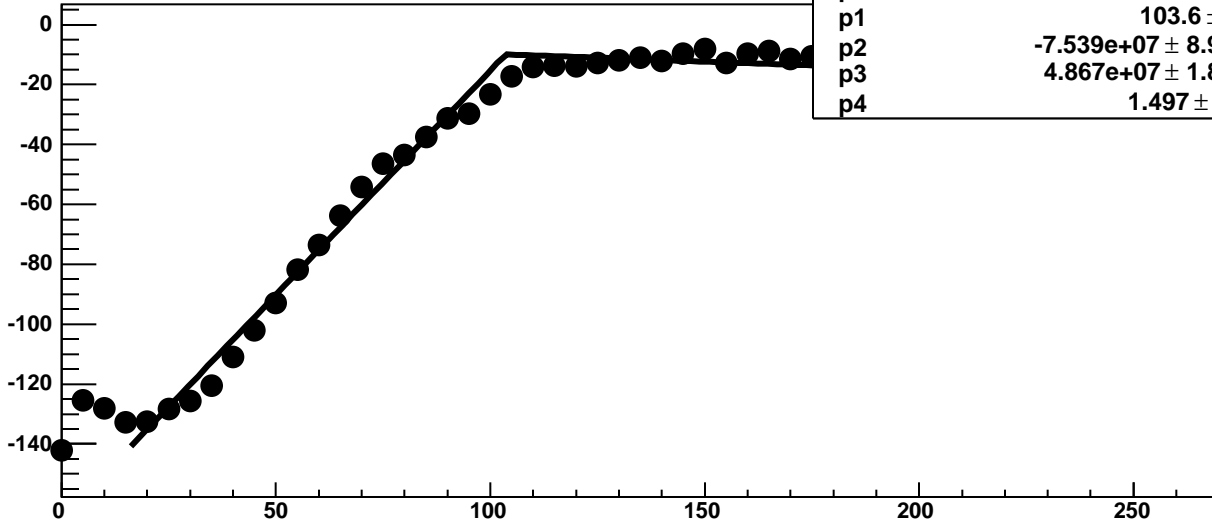
Chip 6, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

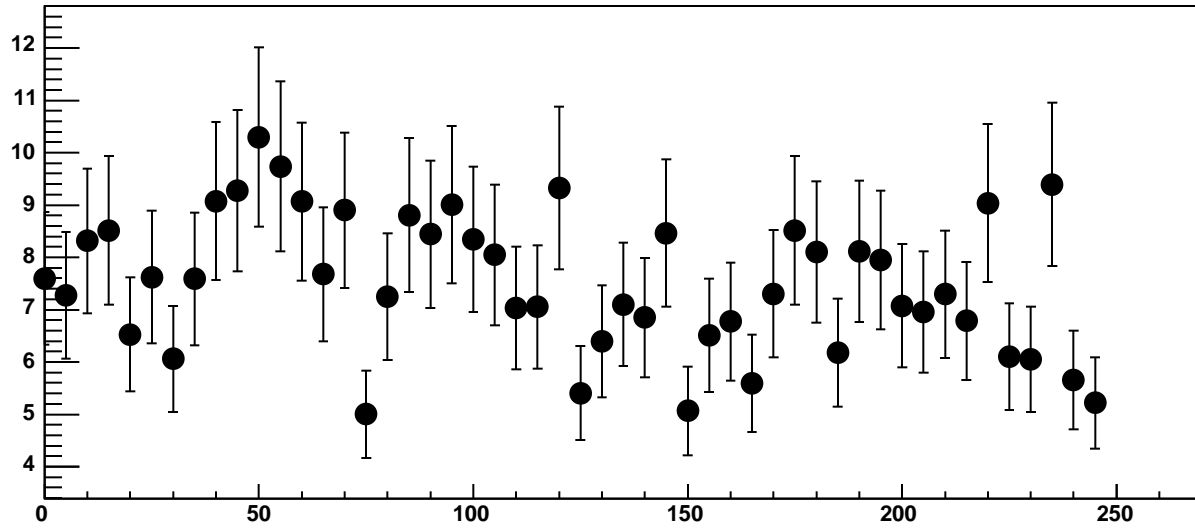


Chip 6, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

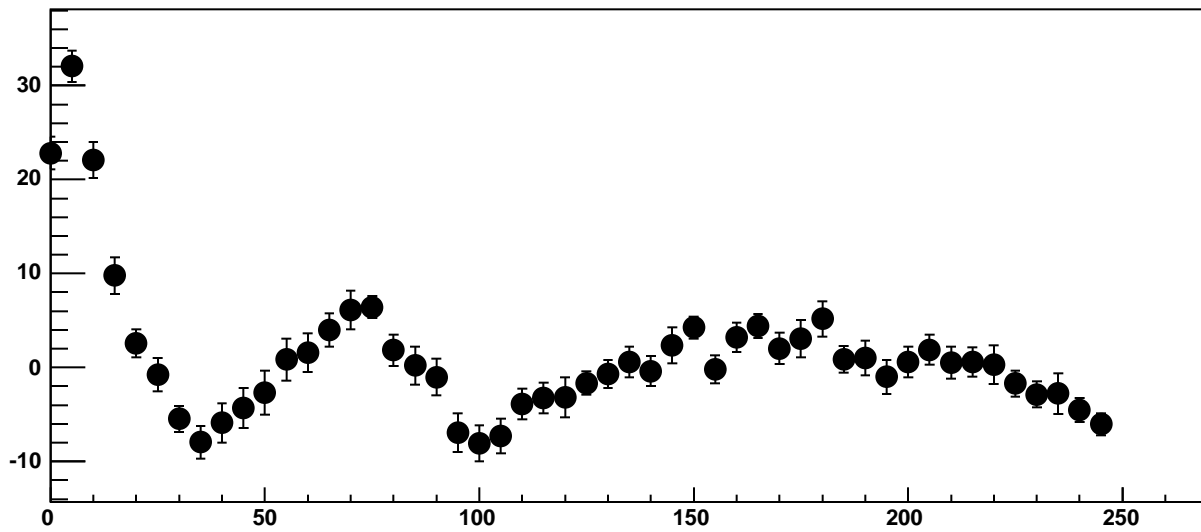


χ^2 / ndf	249.2 / 41
p0	-9.949 ± 0.5698
p1	103.6 ± 0.6775
p2	$-7.539\text{e}+07 \pm 8.986\text{e}+05$
p3	$4.867\text{e}+07 \pm 1.808\text{e}+05$
p4	1.497 ± 0.01609

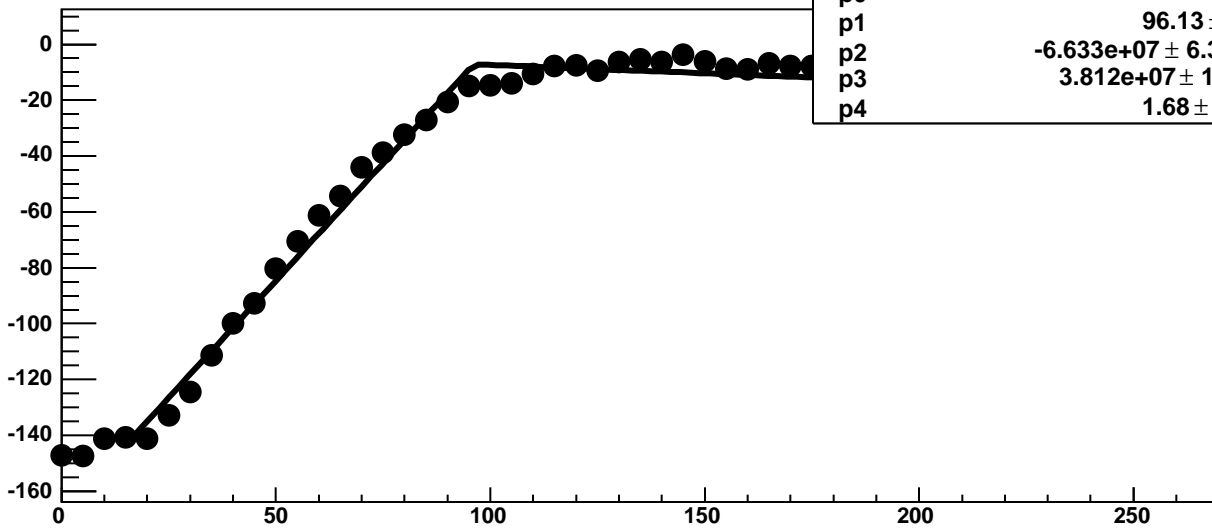
Chip 6, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

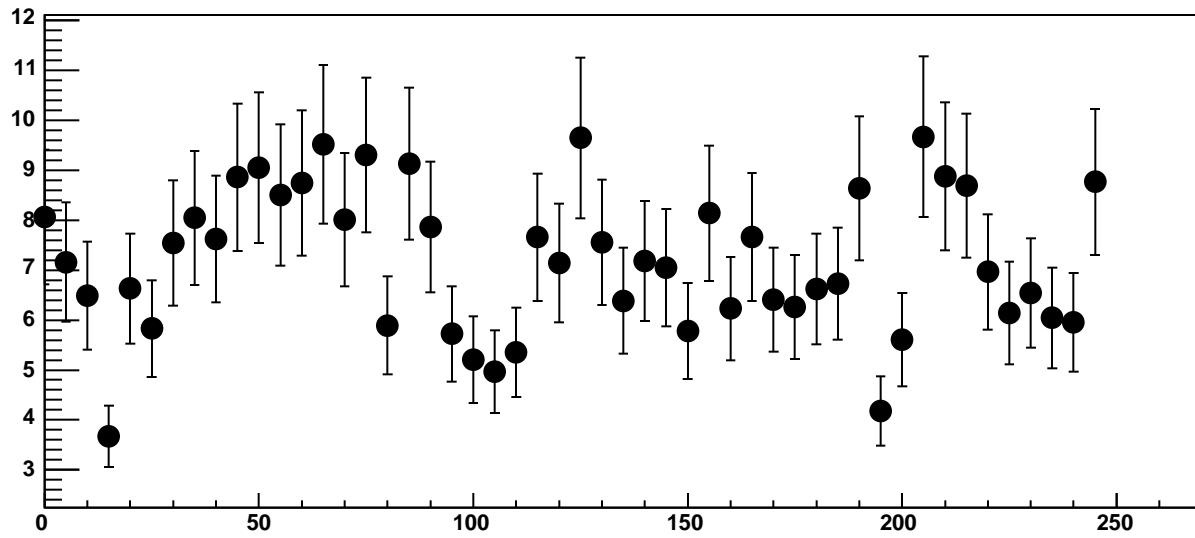


Chip 6, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold

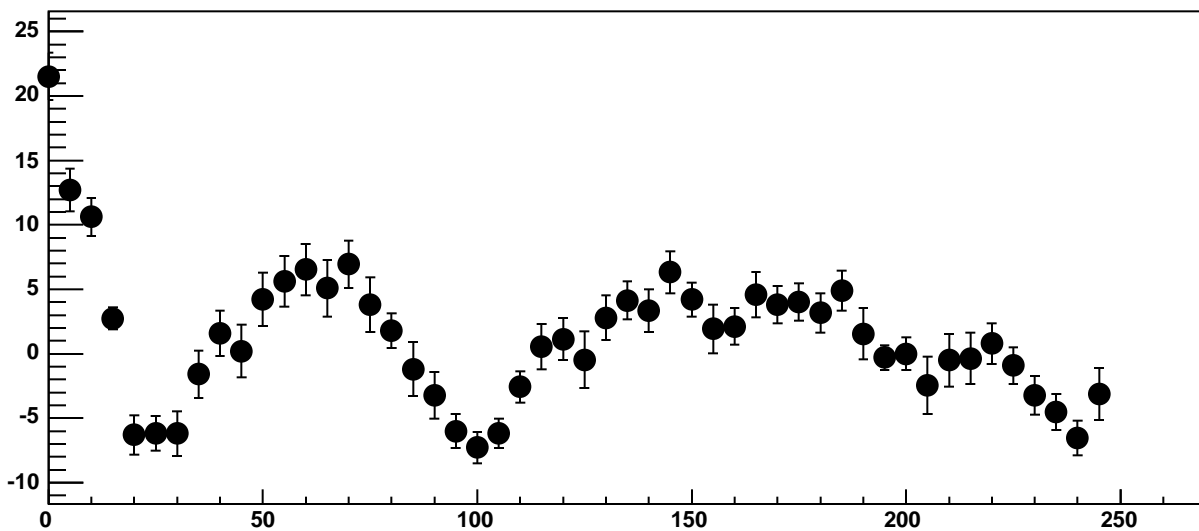


χ^2 / ndf	326.9 / 41
p0	-7.112 ± 0.5142
p1	96.13 ± 0.5212
p2	$-6.633\text{e}+07 \pm 6.359\text{e}+05$
p3	$3.812\text{e}+07 \pm 1.95\text{e}+05$
p4	1.68 ± 0.01334

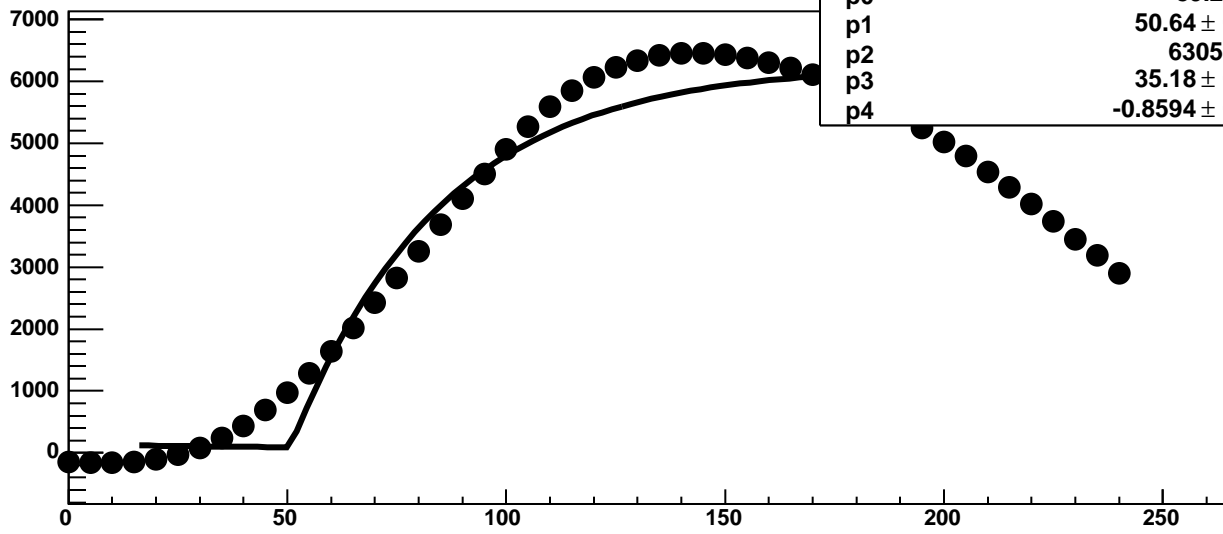
Chip 6, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold

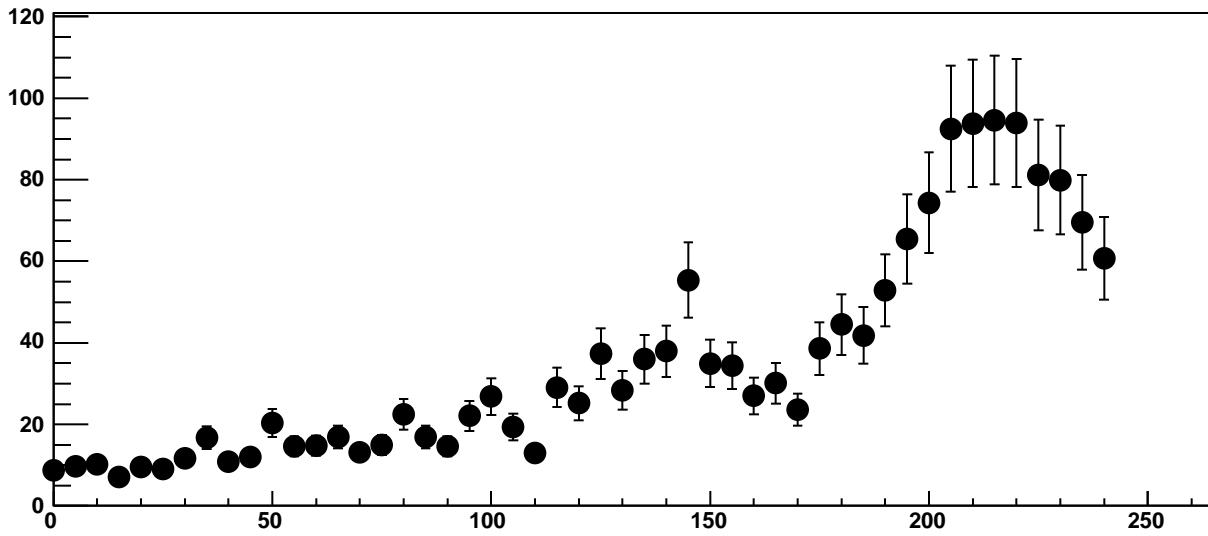


Chip 6, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

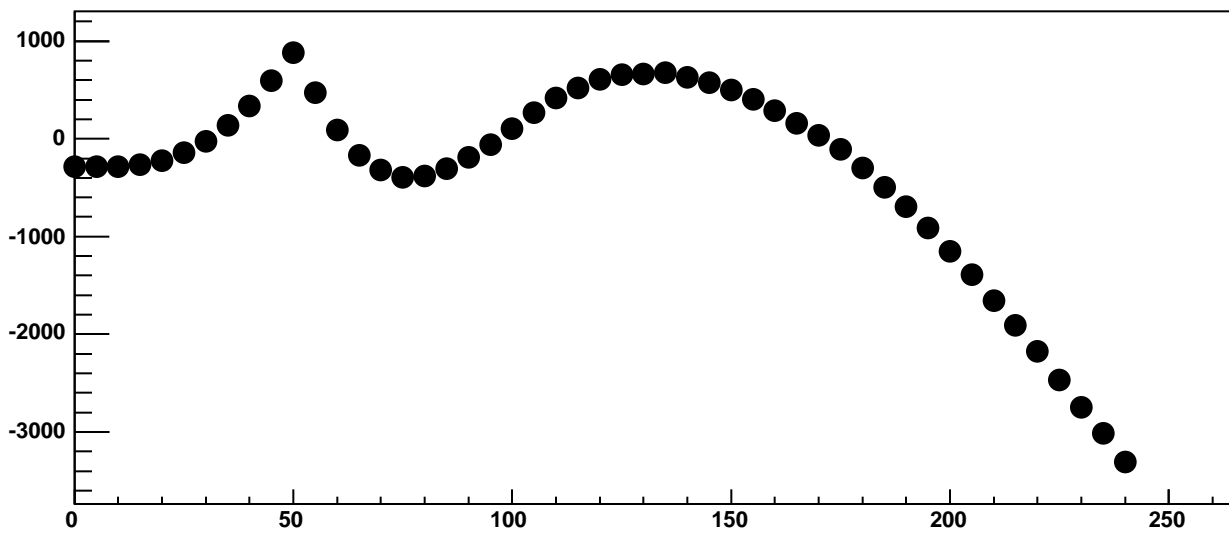


χ^2 / ndf	4.603e+05 / 41
p0	89.2 ± 1.502
p1	50.64 ± 0.01956
p2	6305 ± 8.556
p3	35.18 ± 0.05586
p4	-0.8594 ± 0.05302

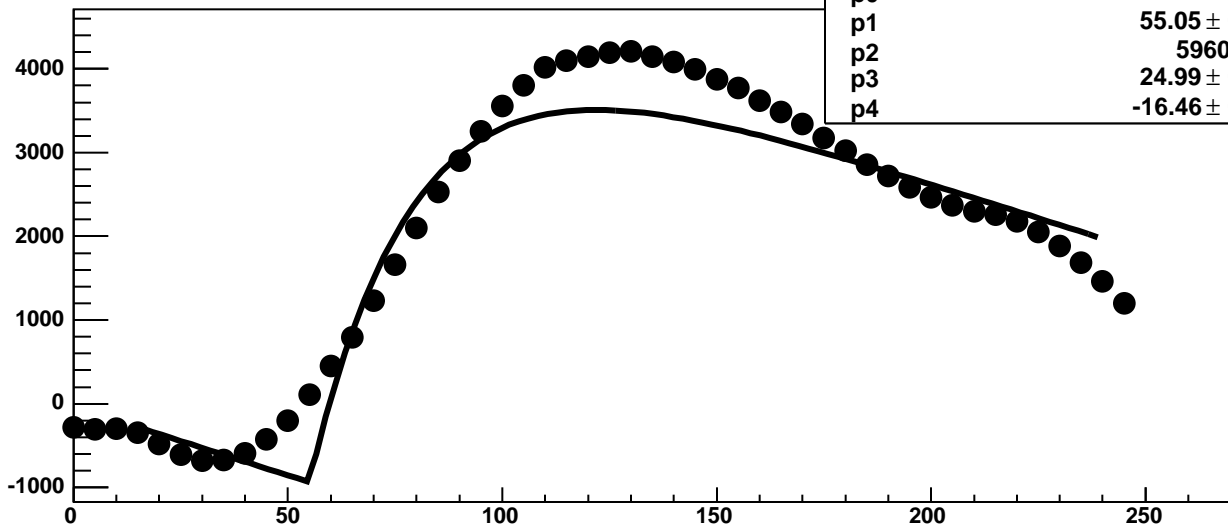
Chip 6, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

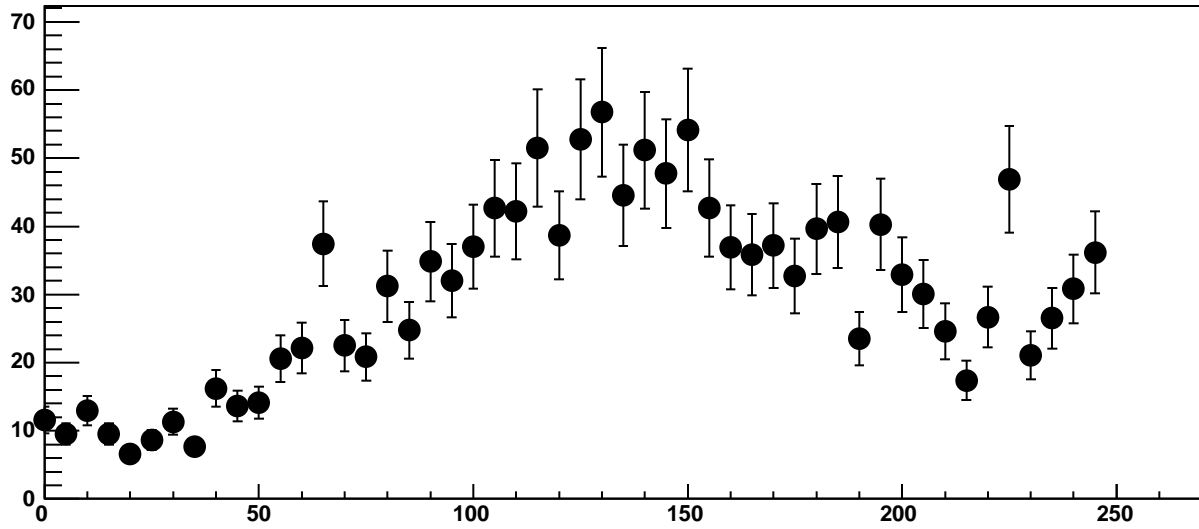


Chip 6, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

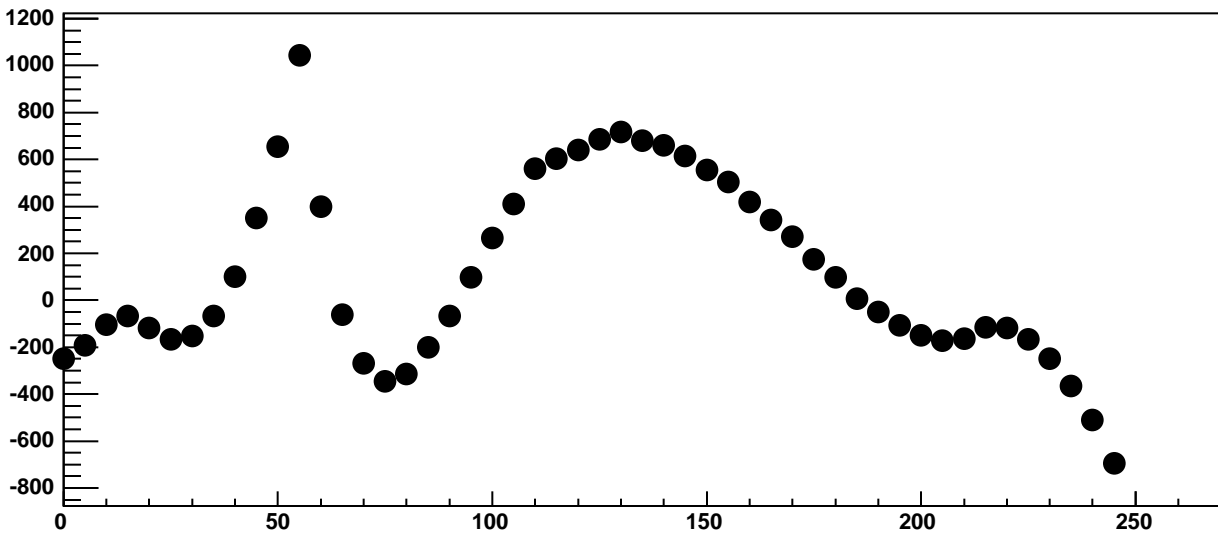


χ^2 / ndf	1.959e+05 / 41
p0	-938.4 ± 1.272
p1	55.05 ± 0.02726
p2	5960 ± 7.261
p3	24.99 ± 0.05343
p4	-16.46 ± 0.04068

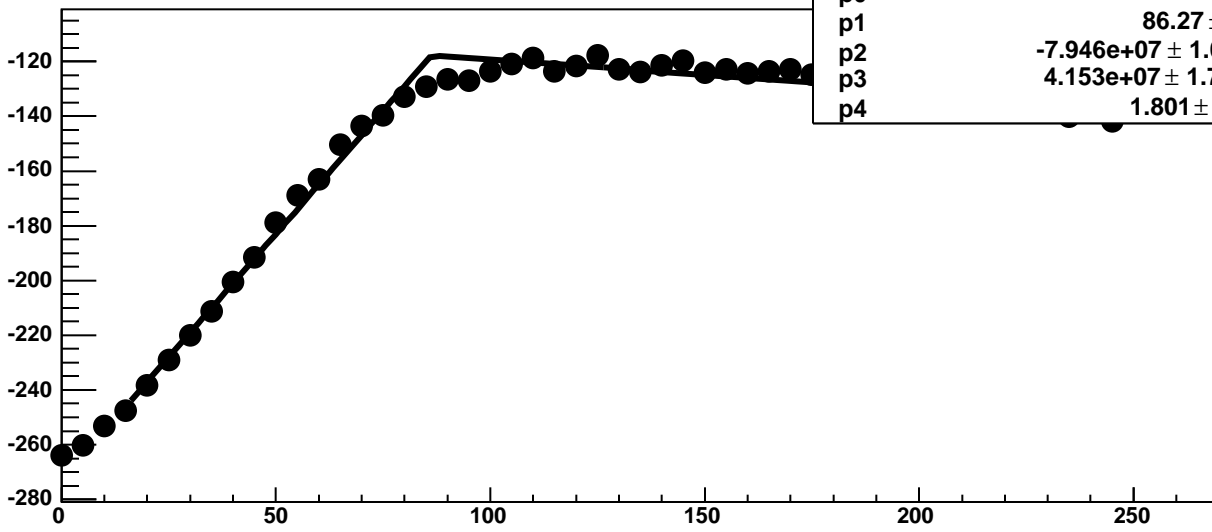
Chip 6, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold

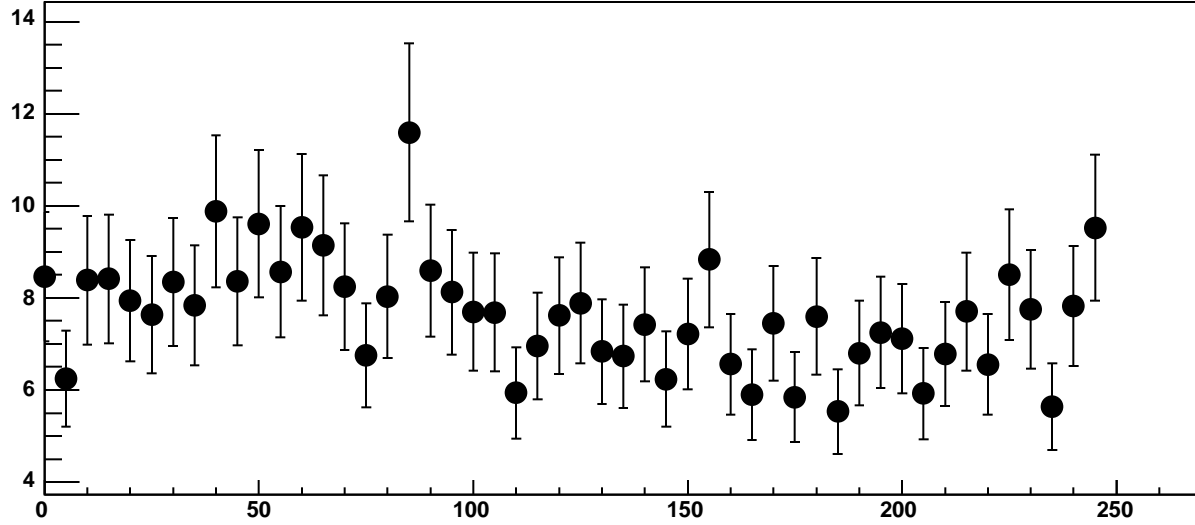


Chip 6, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

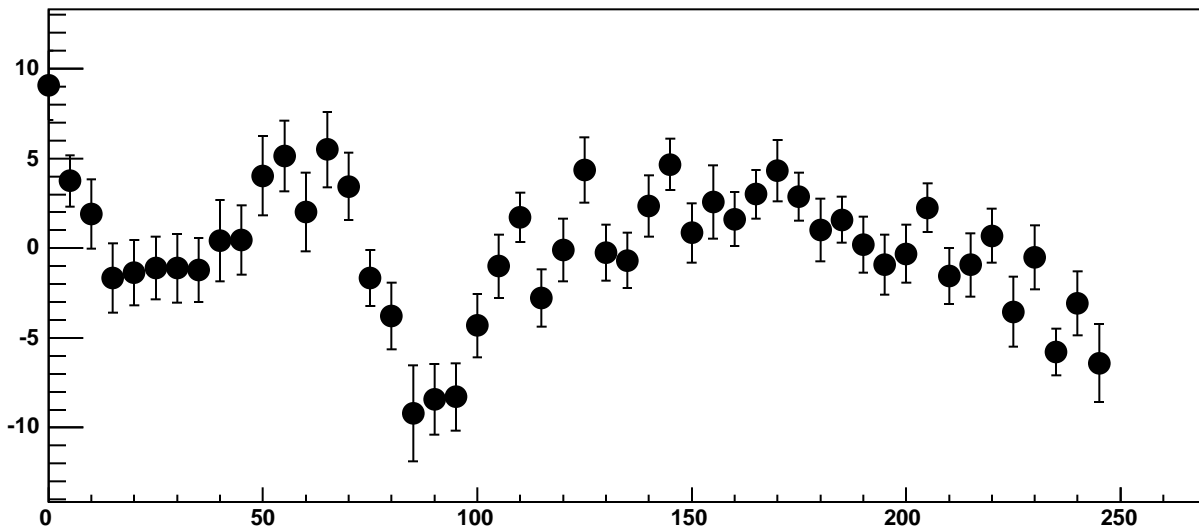


χ^2 / ndf	160.3 / 41
p0	-117.7 ± 0.577
p1	86.27 ± 0.6059
p2	$-7.946\text{e}+07 \pm 1.027\text{e}+06$
p3	$4.153\text{e}+07 \pm 1.729\text{e}+05$
p4	1.801 ± 0.02294

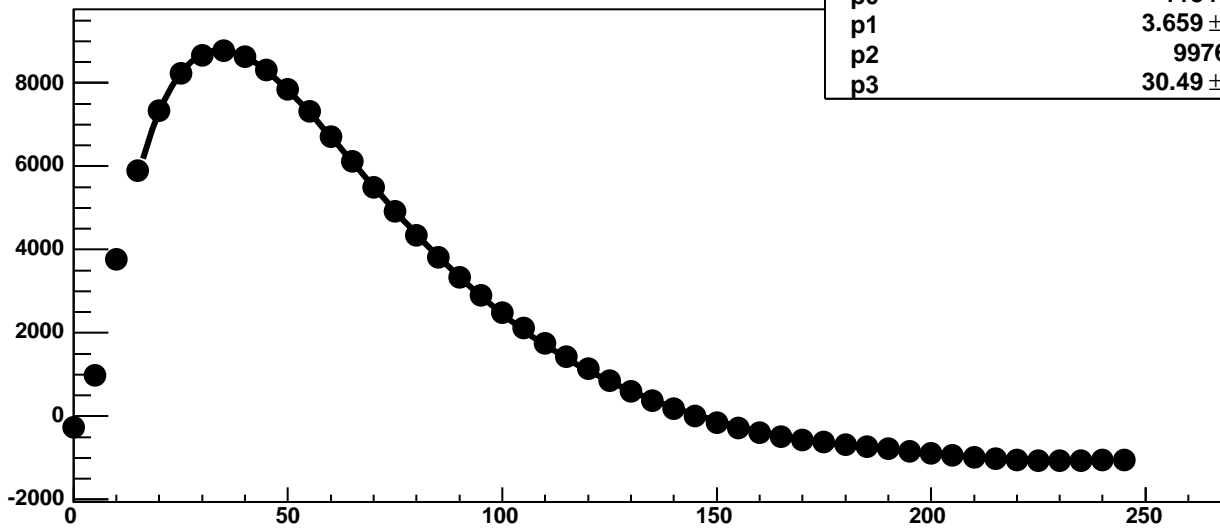
Chip 6, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

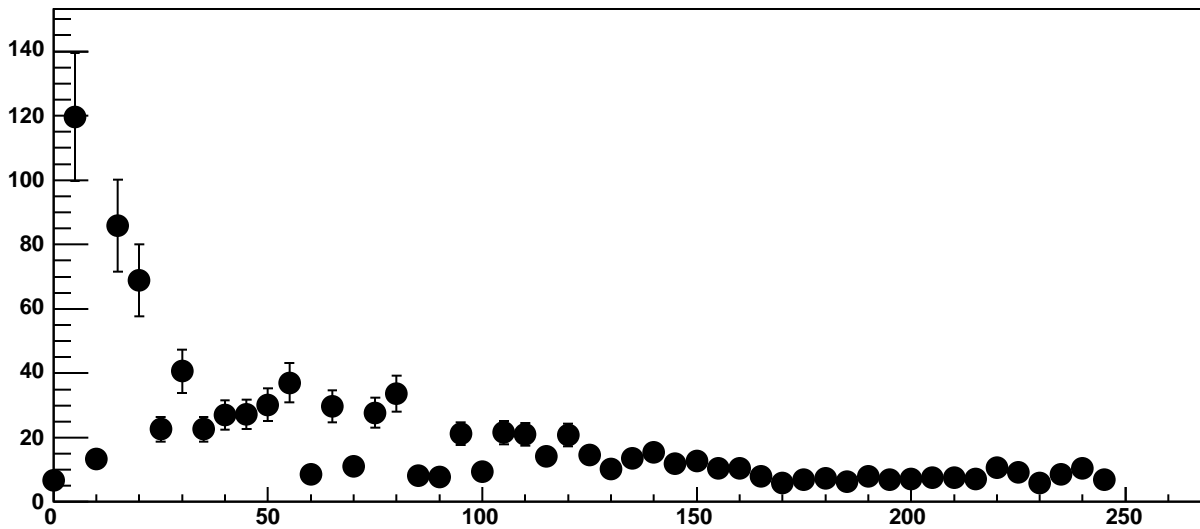


Chip 6, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold

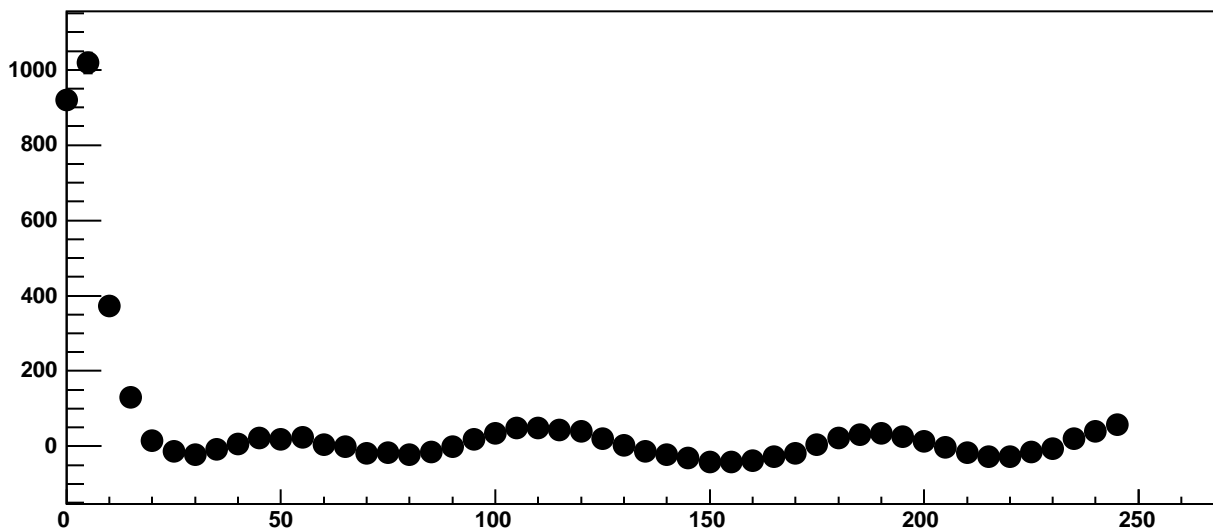


χ^2 / ndf	4522 / 42
p0	-1184 ± 0.6629
p1	3.659 ± 0.02441
p2	9976 ± 2.153
p3	30.49 ± 0.01061

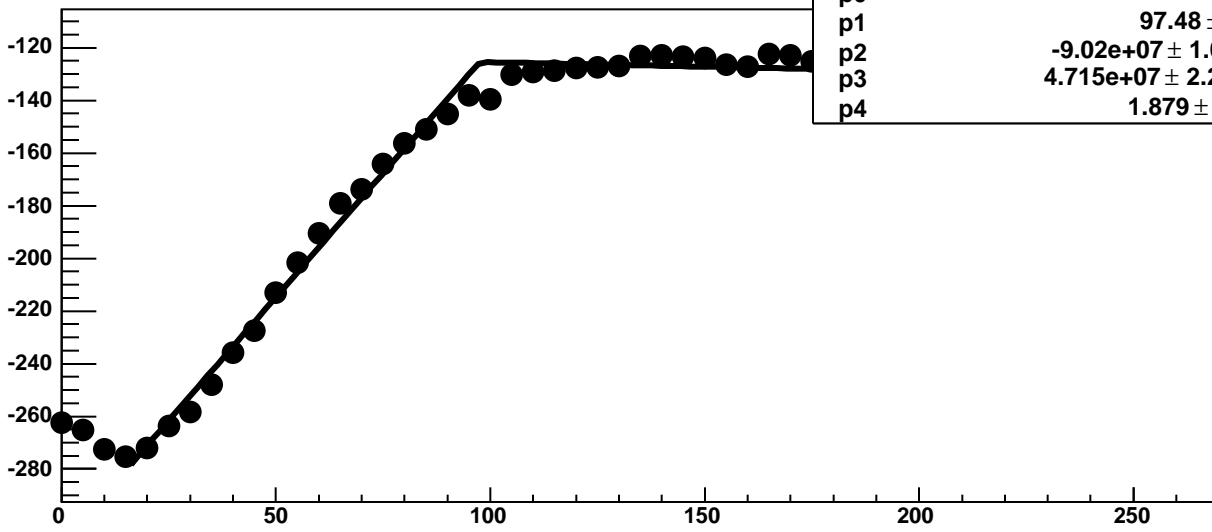
Chip 6, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold

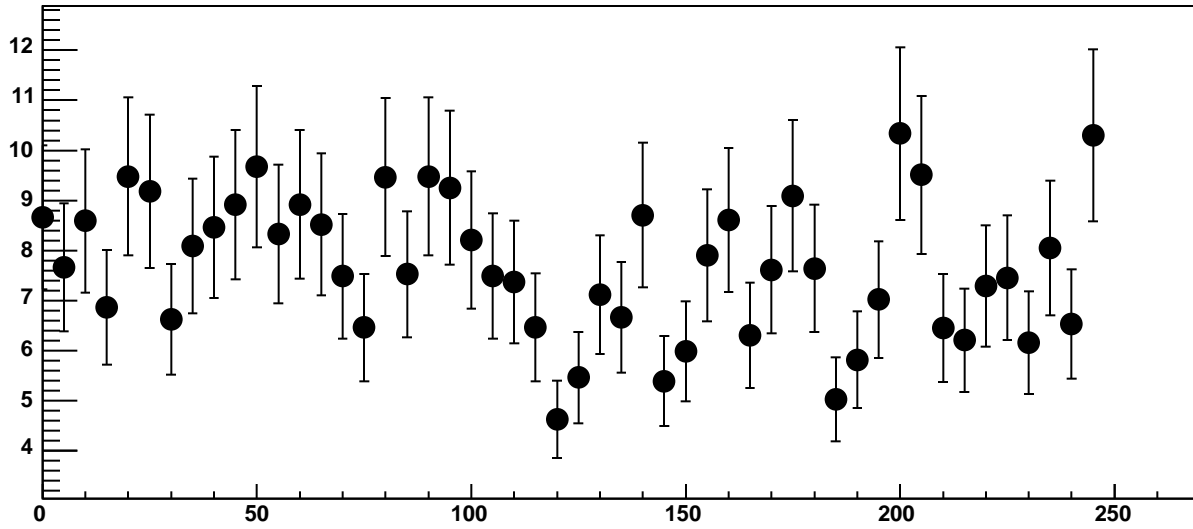


Chip 6, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

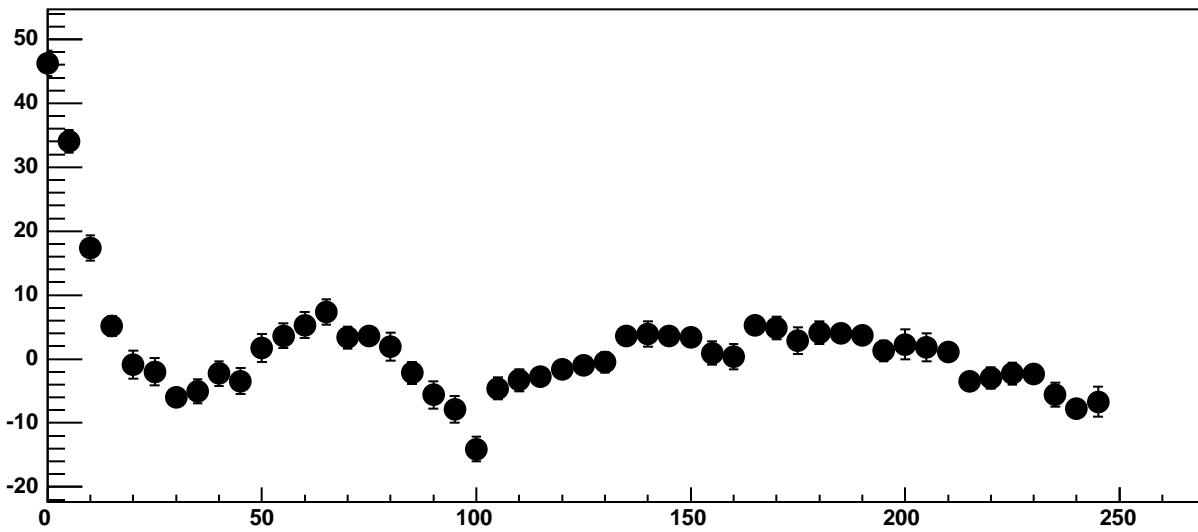


χ^2 / ndf	295.4 / 41
p0	-125.4 ± 0.5525
p1	97.48 ± 0.5664
p2	$-9.02\text{e}+07 \pm 1.021\text{e}+06$
p3	$4.715\text{e}+07 \pm 2.282\text{e}+05$
p4	1.879 ± 0.01852

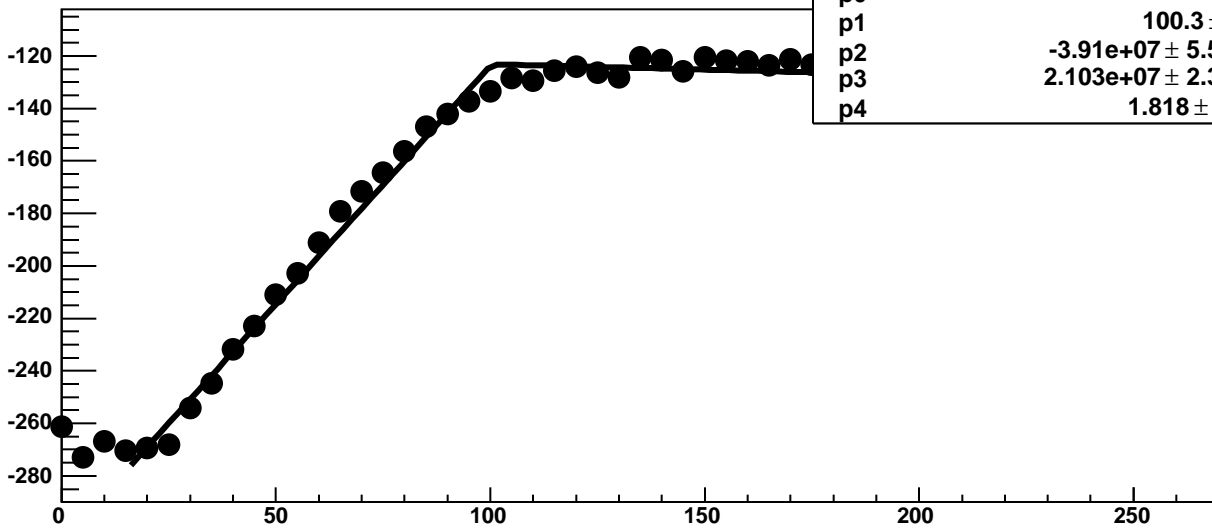
Chip 6, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

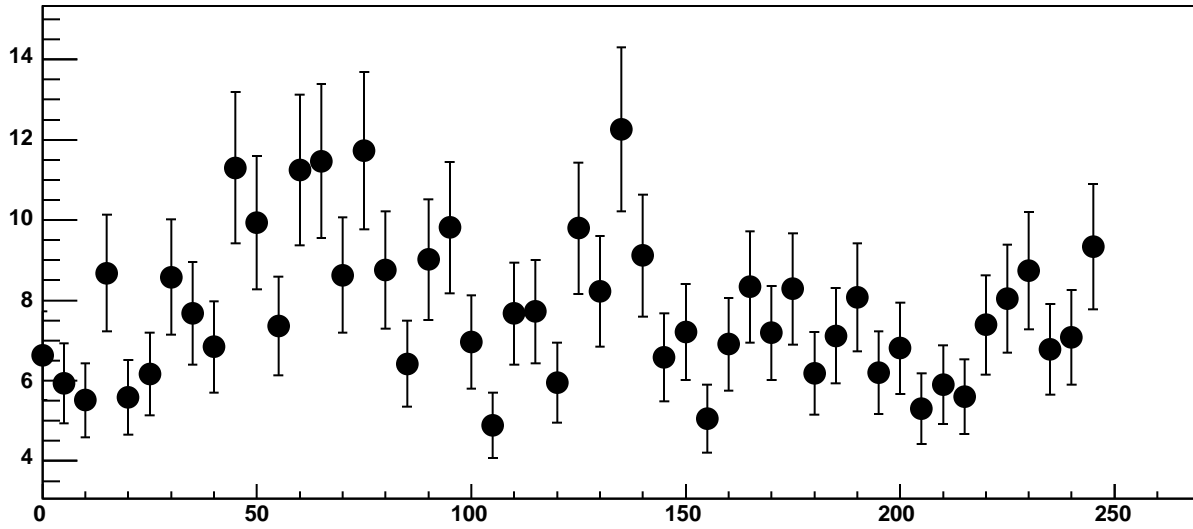


Chip 6, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

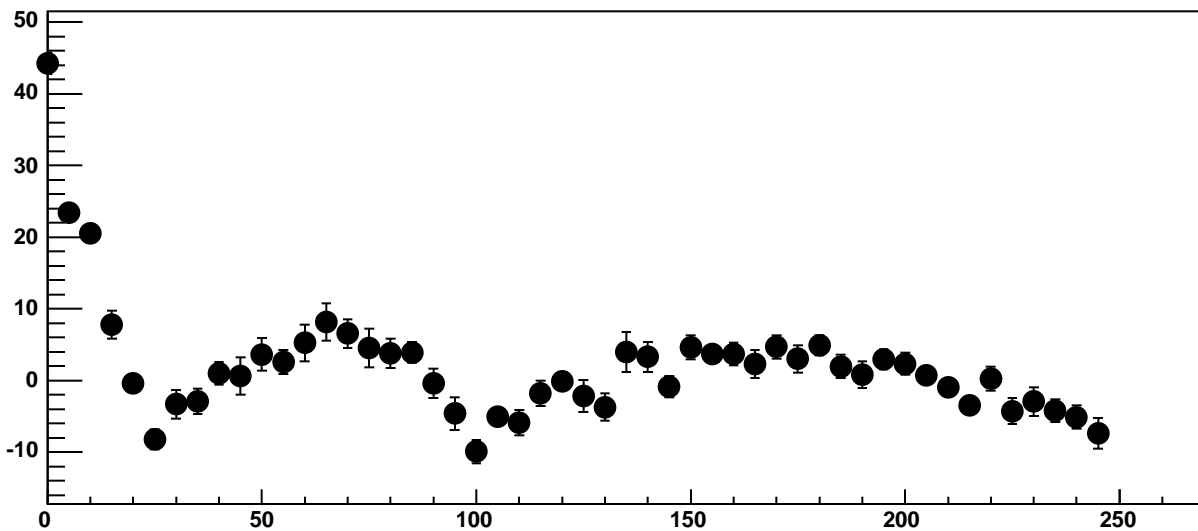


χ^2 / ndf	272.6 / 41
p0	-123.1 ± 0.5961
p1	100.3 ± 0.5609
p2	-3.91e+07 ± 5.598e+05
p3	2.103e+07 ± 2.396e+05
p4	1.818 ± 0.01563

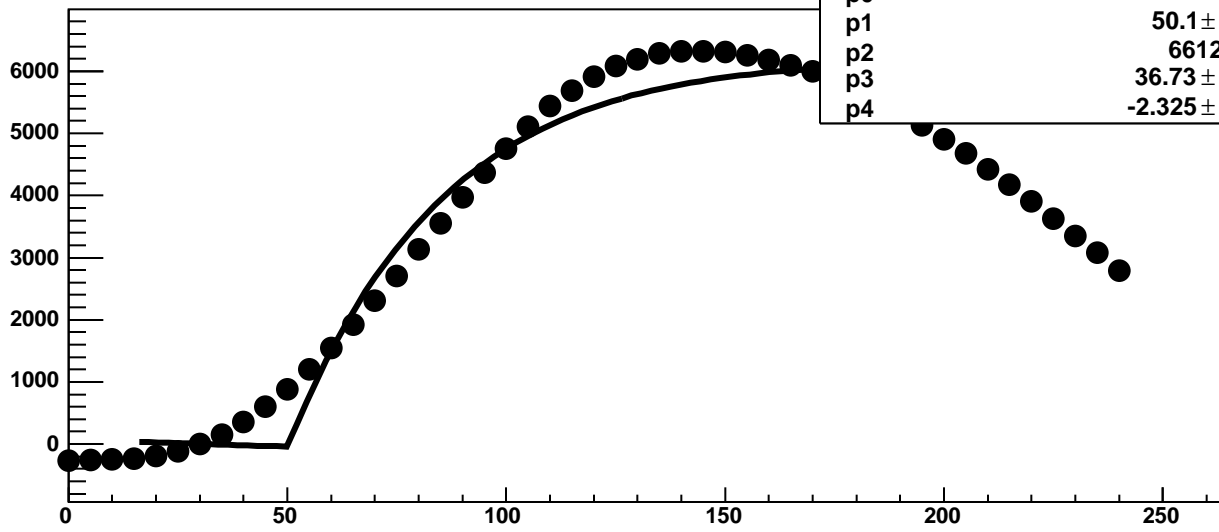
Chip 6, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

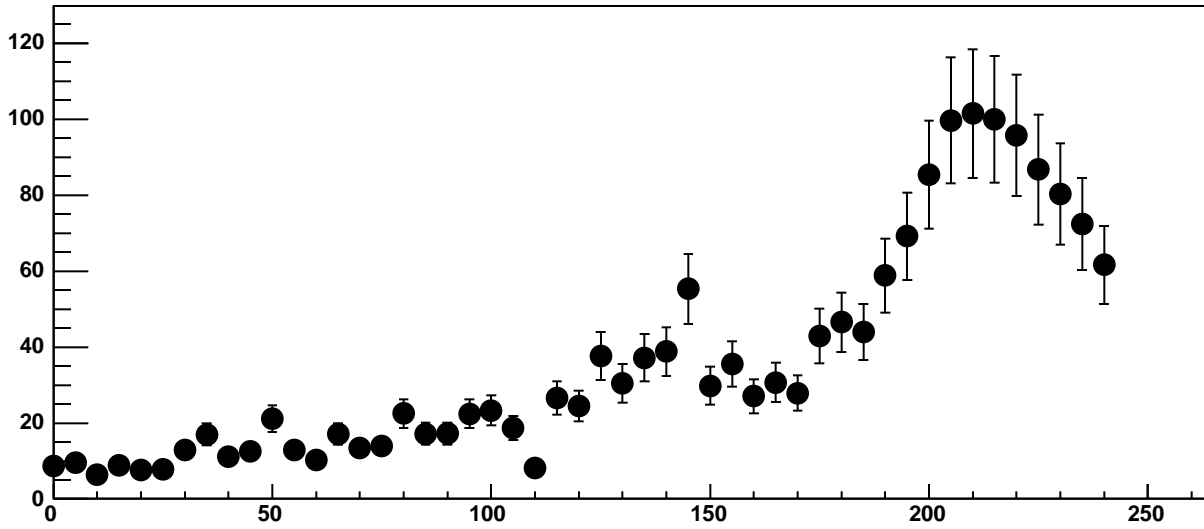


Chip 6, Channel 15, Enable 5, DAC=4000, ADC Mean vs Hold

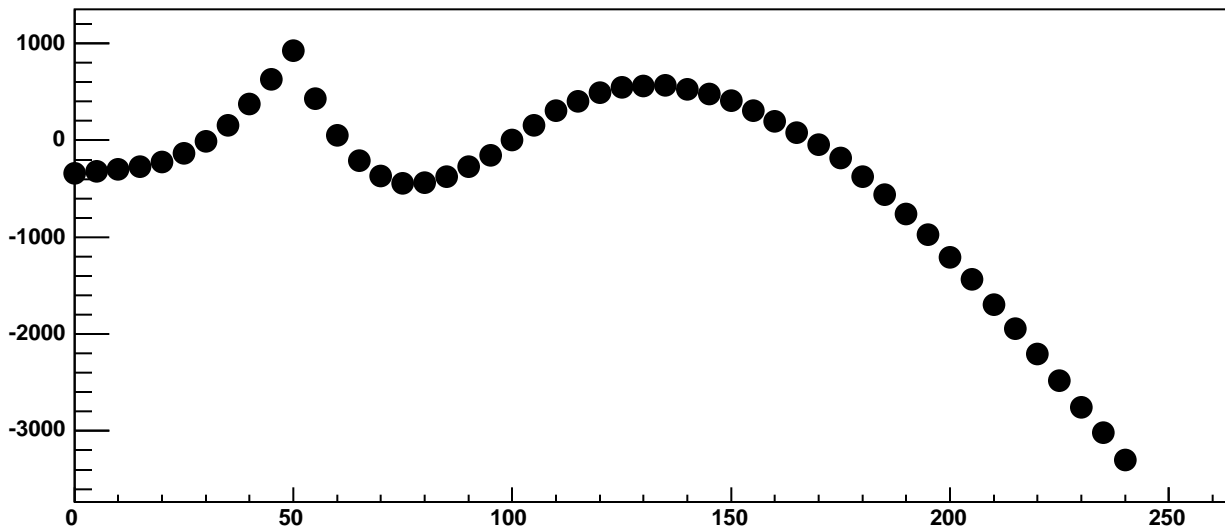


χ^2 / ndf	4.612e+05 / 41
p0	-41.98 ± 1.489
p1	50.1 ± 0.01762
p2	6612 ± 8.867
p3	36.73 ± 0.05669
p4	-2.325 ± 0.05468

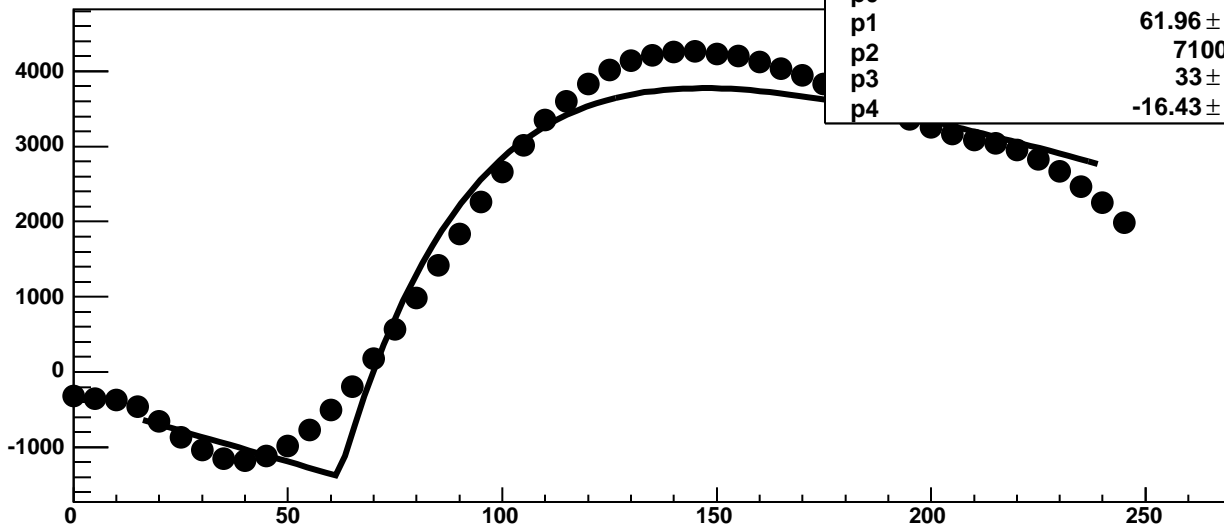
Chip 6, Channel 15, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 15, Enable 5, DAC=4000, ADC Residuals vs Hold

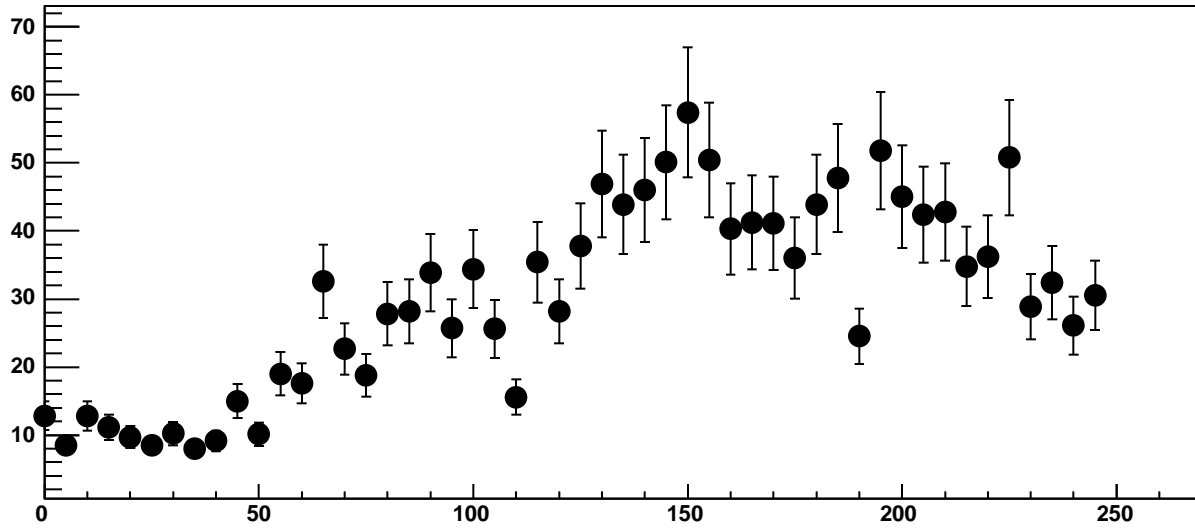


Chip 6, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

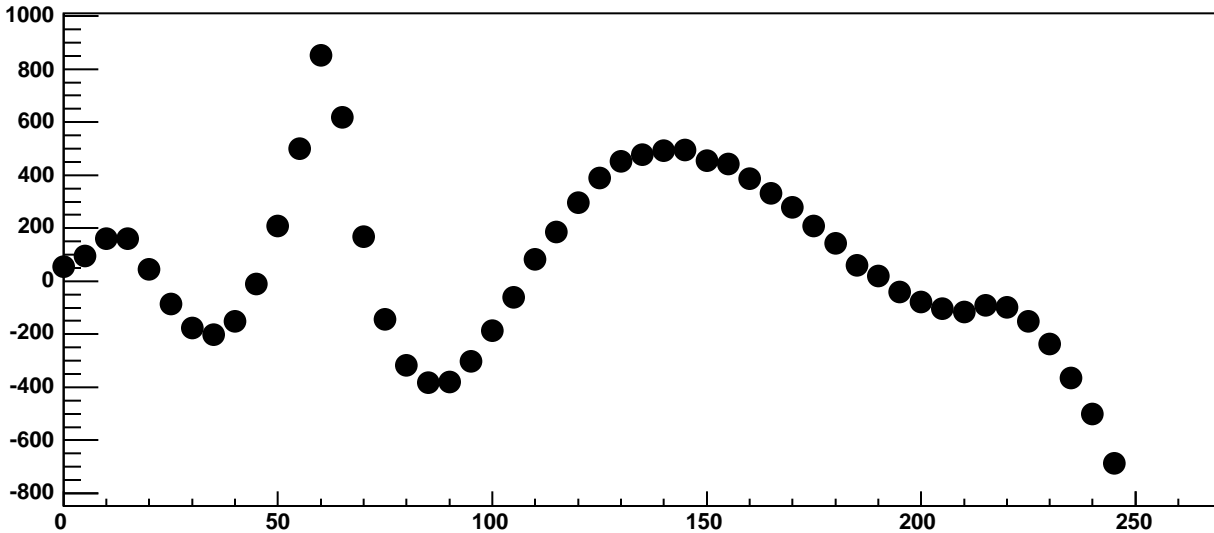


χ^2 / ndf	1.476e+05 / 41
p0	-1389 ± 1.453
p1	61.96 ± 0.02583
p2	7100 ± 9.088
p3	33 ± 0.06362
p4	-16.43 ± 0.04838

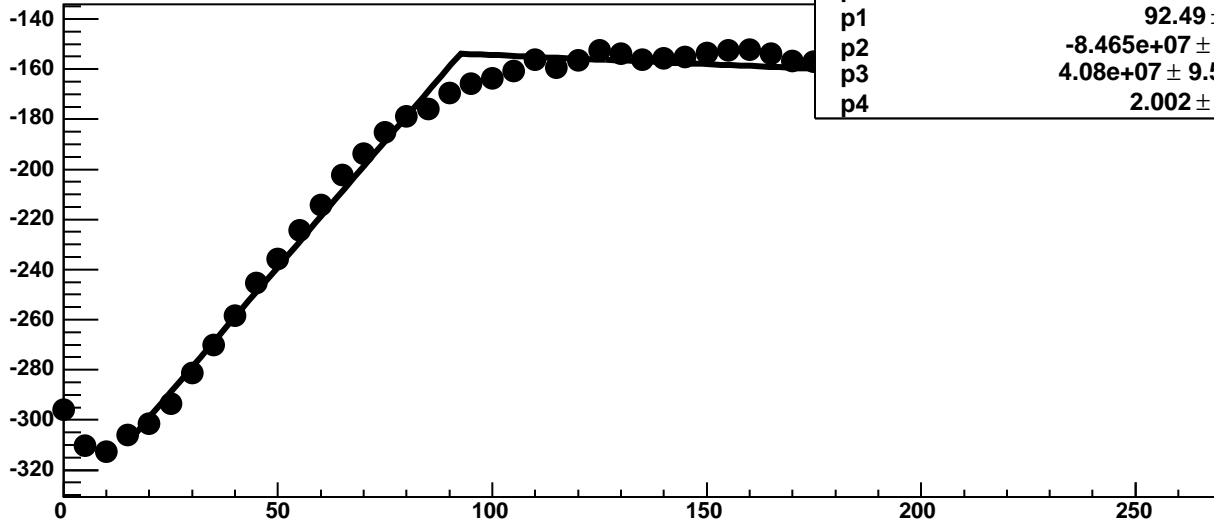
Chip 6, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

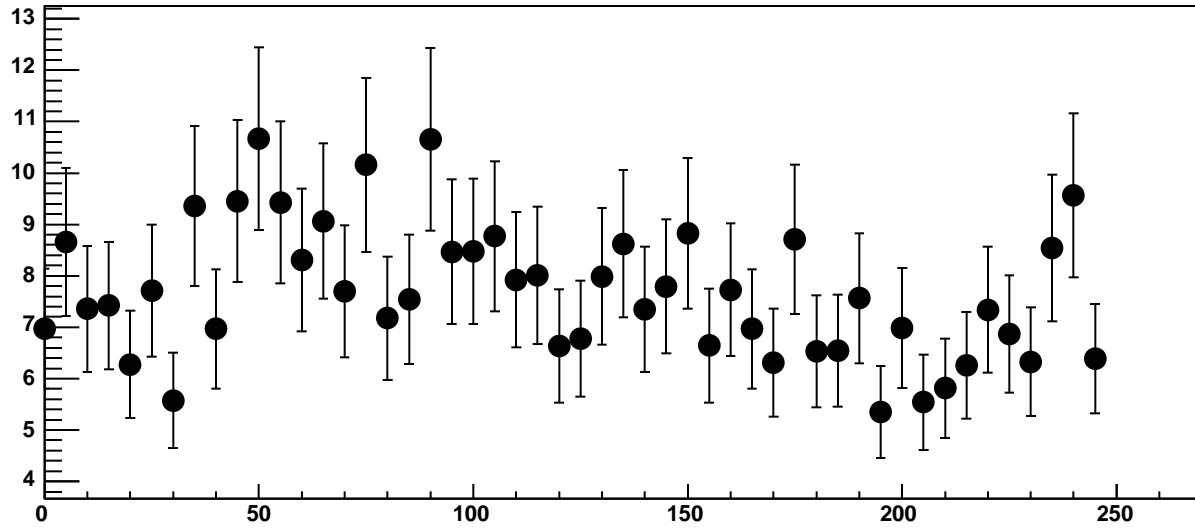


Chip 6, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold

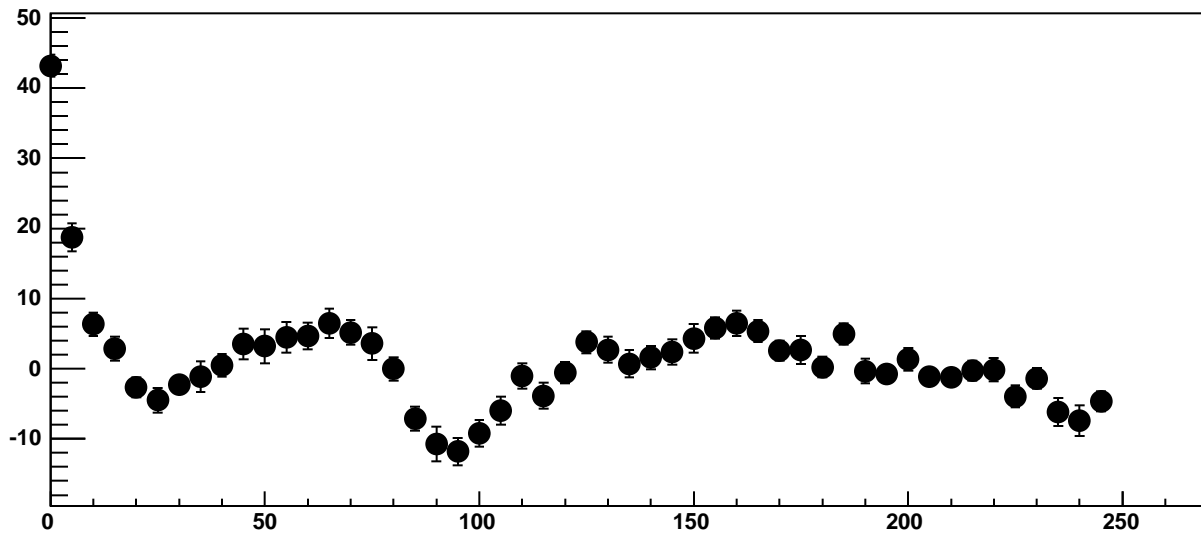


χ^2 / ndf	262.3 / 41
p0	-153.8 ± 0.6827
p1	92.49 ± 0.3782
p2	$-8.465e+07 \pm 1.1e+06$
p3	$4.08e+07 \pm 9.519e+05$
p4	2.002 ± 0.01802

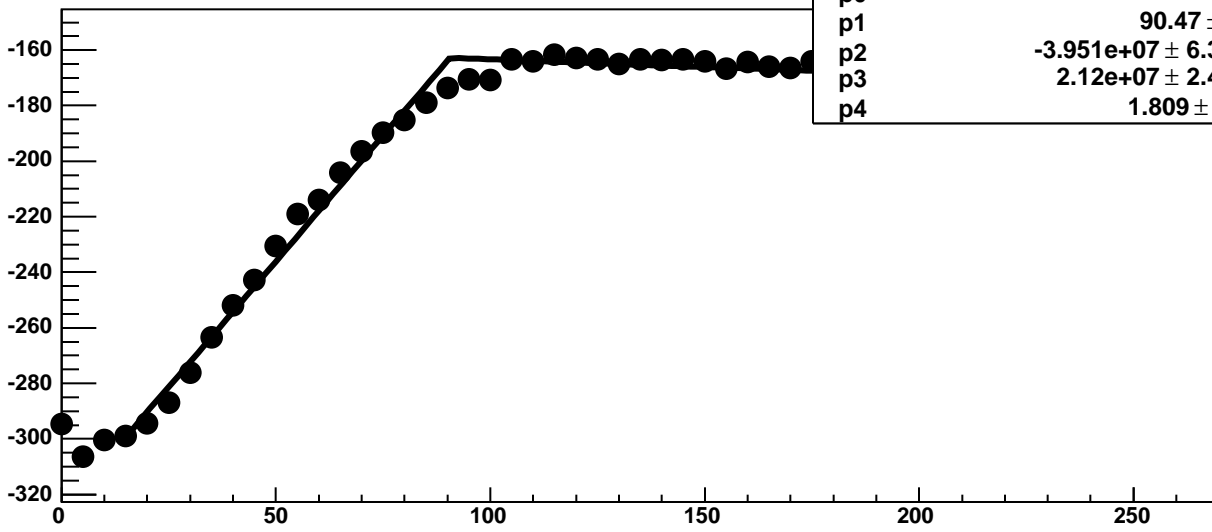
Chip 6, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold

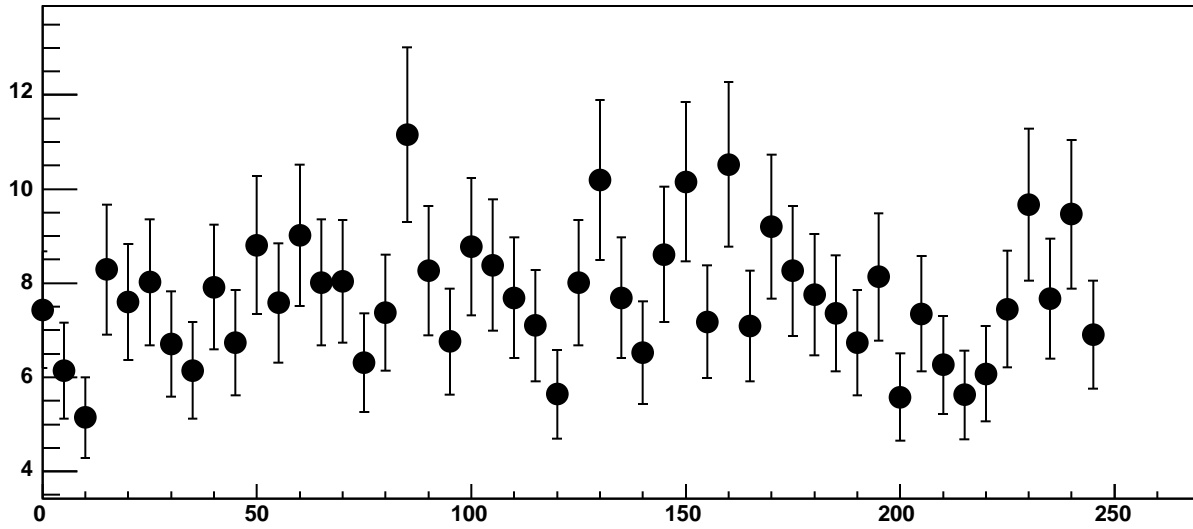


Chip 6, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold

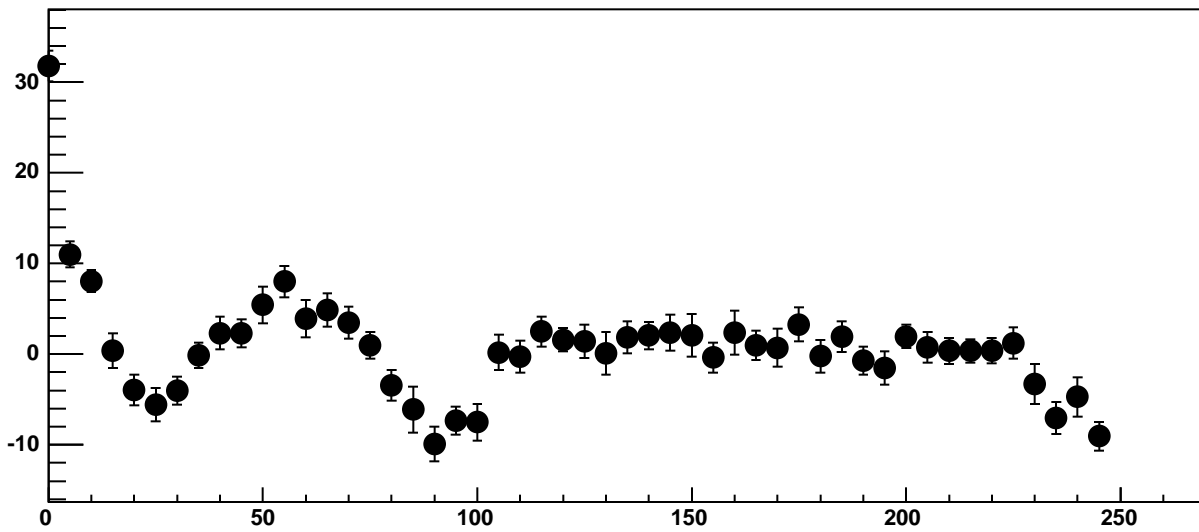


χ^2 / ndf	184.7 / 41
p0	-162.8 ± 0.6281
p1	90.47 ± 0.5884
p2	$-3.951\text{e}+07 \pm 6.321\text{e}+05$
p3	$2.12\text{e}+07 \pm 2.416\text{e}+05$
p4	1.809 ± 0.01953

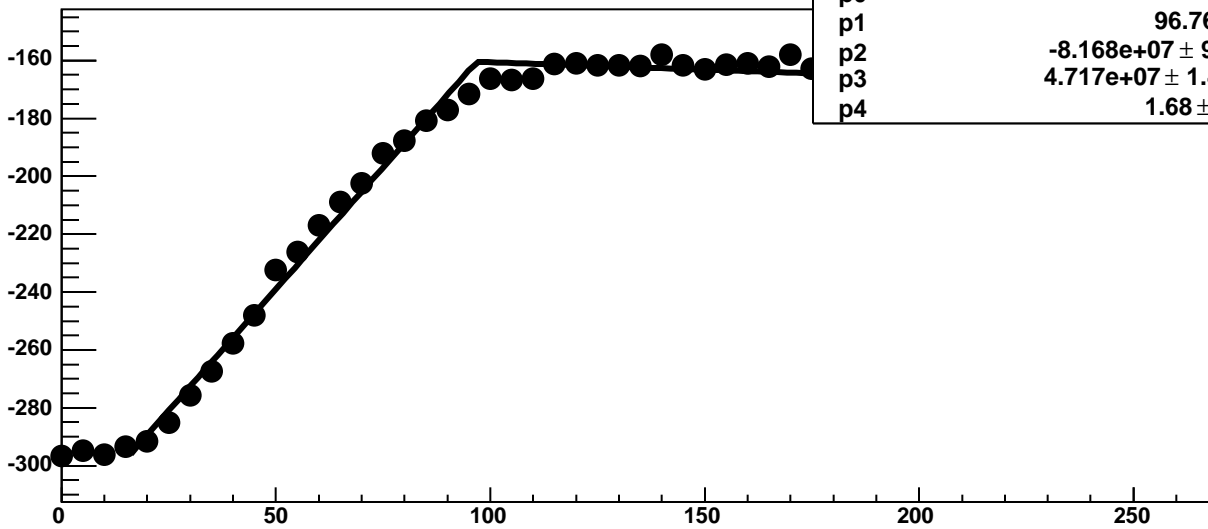
Chip 6, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold

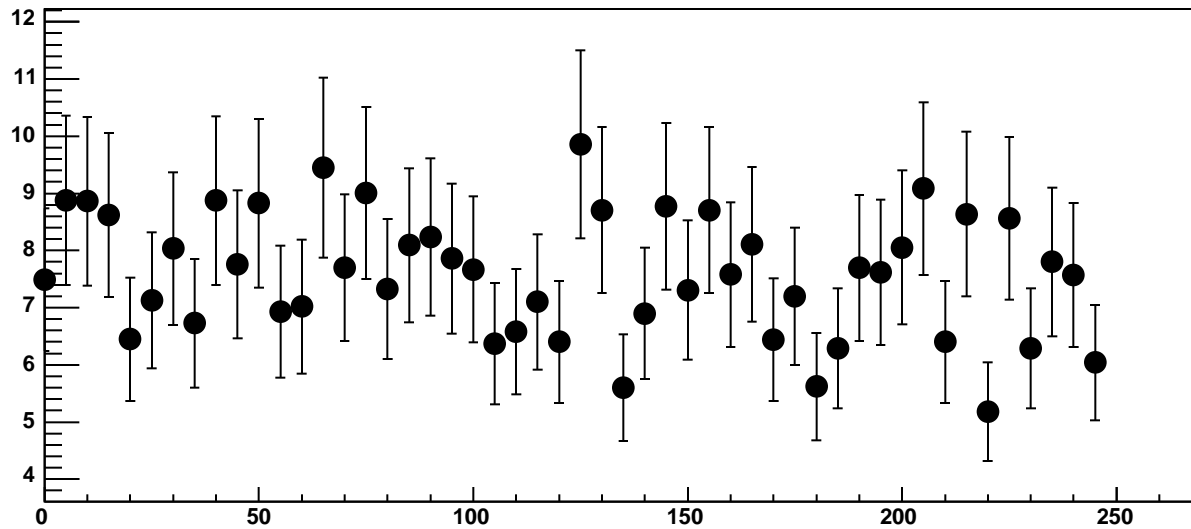


Chip 6, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

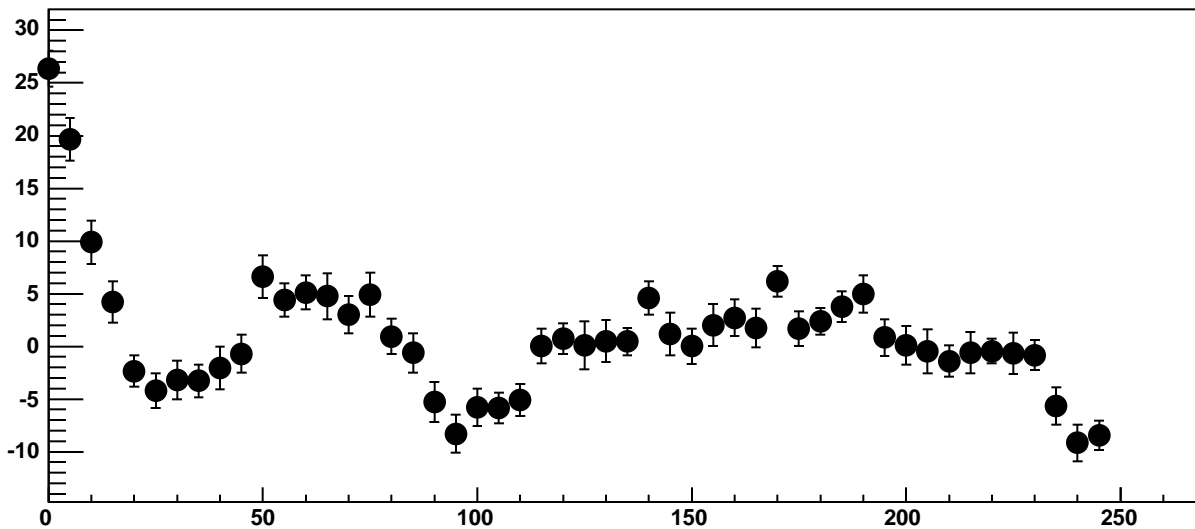


χ^2 / ndf	221.8 / 41
p0	-160.4 ± 0.5912
p1	96.76 ± 0.613
p2	$-8.168\text{e}+07 \pm 9.28\text{e}+05$
p3	$4.717\text{e}+07 \pm 1.494\text{e}+05$
p4	1.68 ± 0.01751

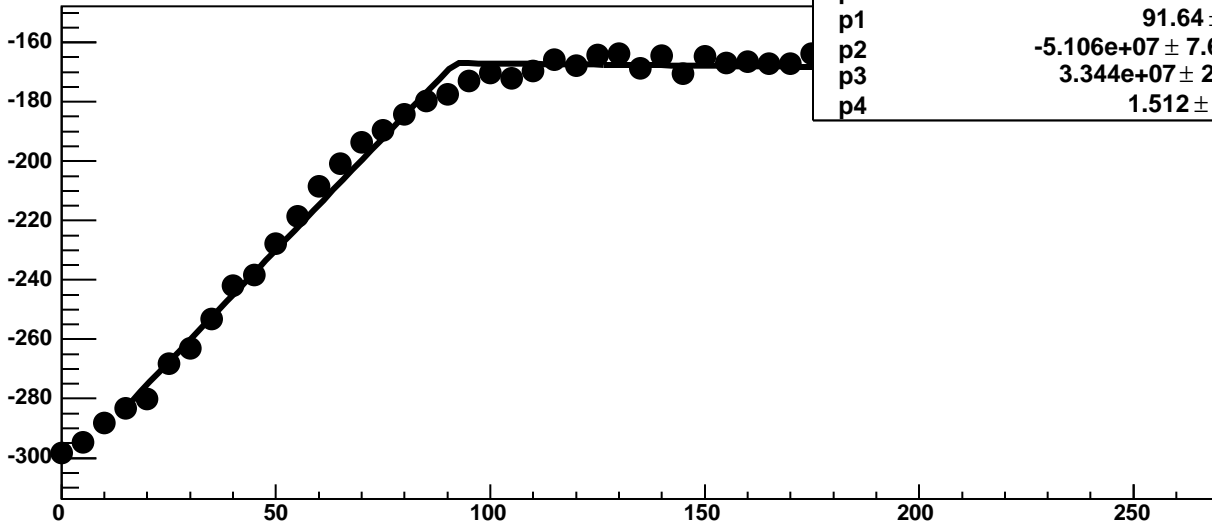
Chip 6, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

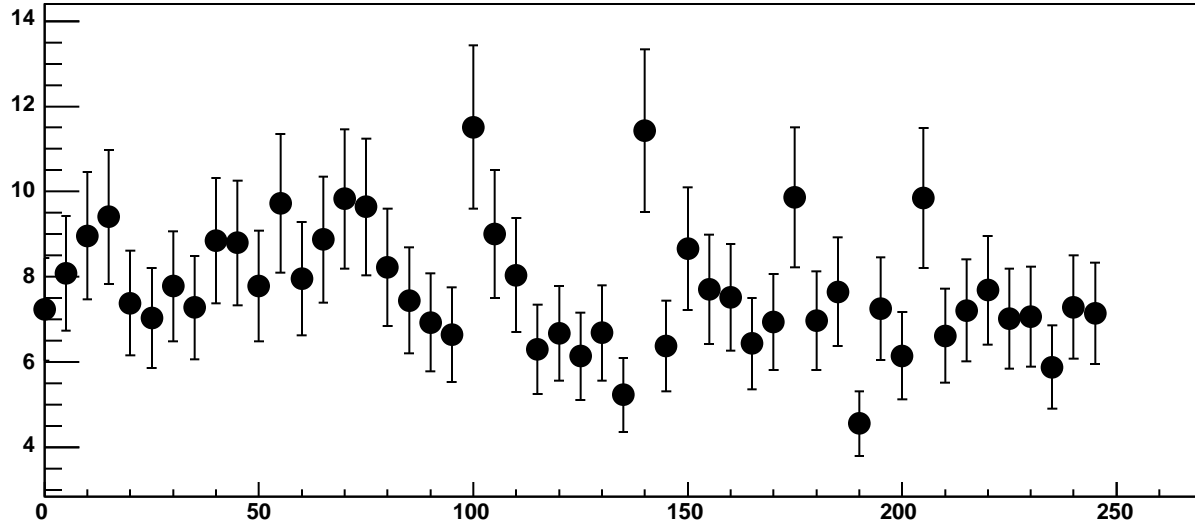


Chip 6, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

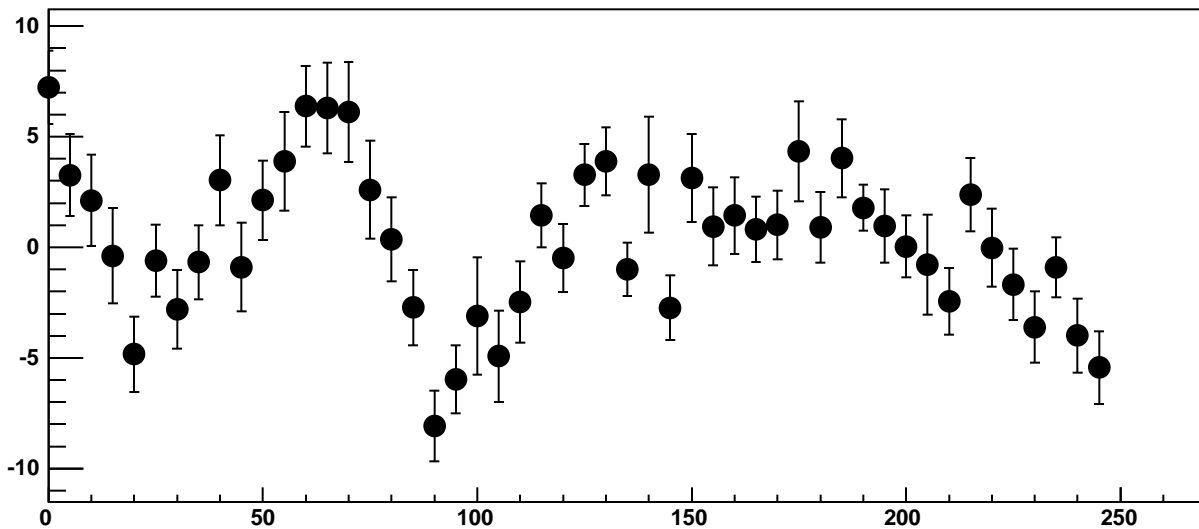


χ^2 / ndf	153.2 / 41
p0	-167 ± 0.6037
p1	91.64 ± 0.7123
p2	$-5.106\text{e}+07 \pm 7.616\text{e}+05$
p3	$3.344\text{e}+07 \pm 2.22\text{e}+05$
p4	1.512 ± 0.01948

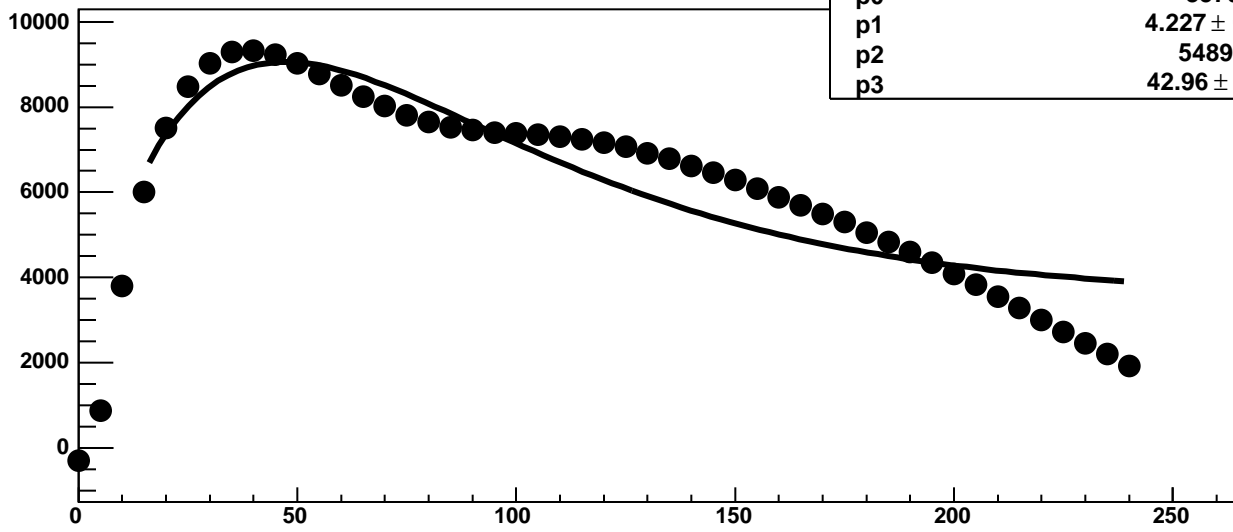
Chip 6, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



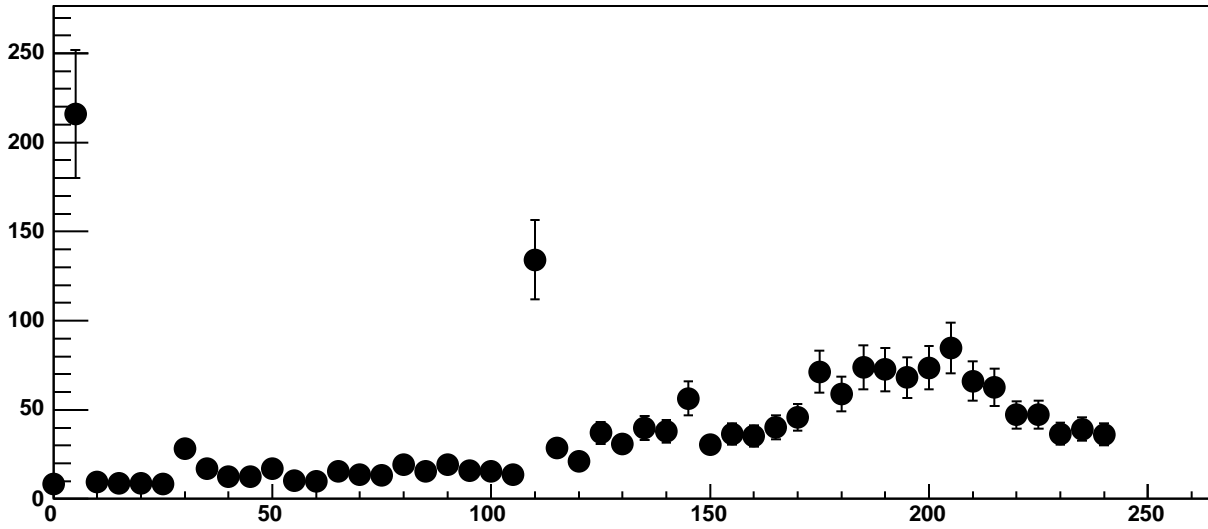
Chip 6, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold



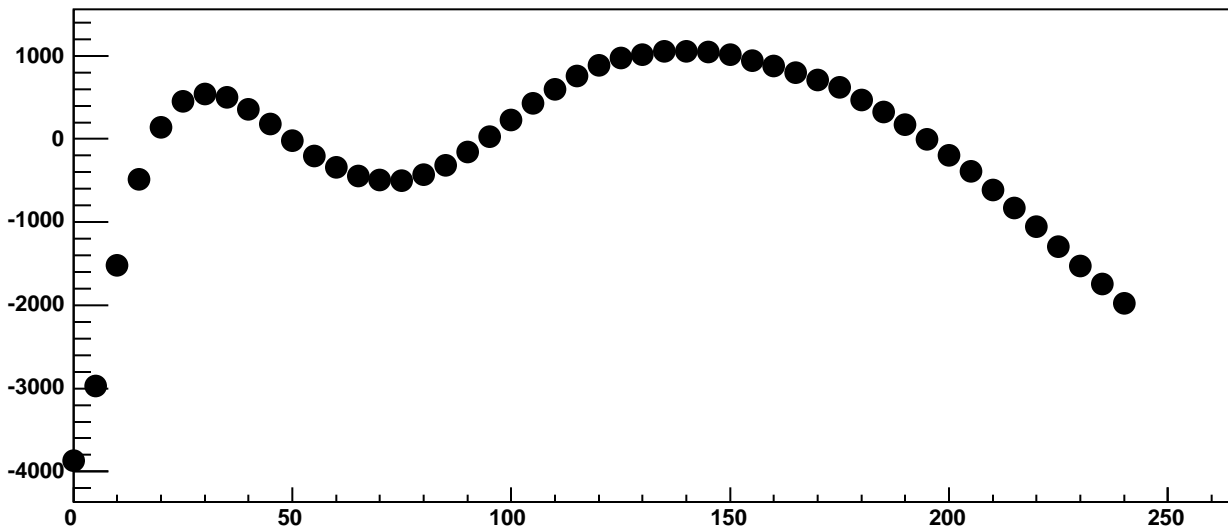
Chip 6, Channel 16, Enable 5!, DAC=4000, ADC Mean vs Hold



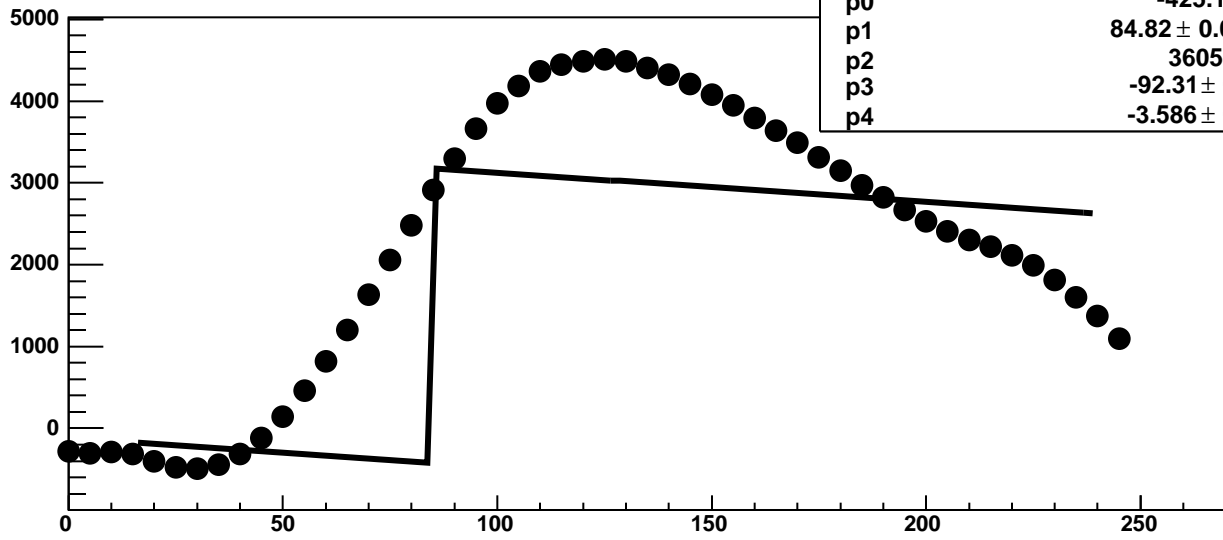
Chip 6, Channel 16, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 16, Enable 5!, DAC=4000, ADC Residuals vs Hold

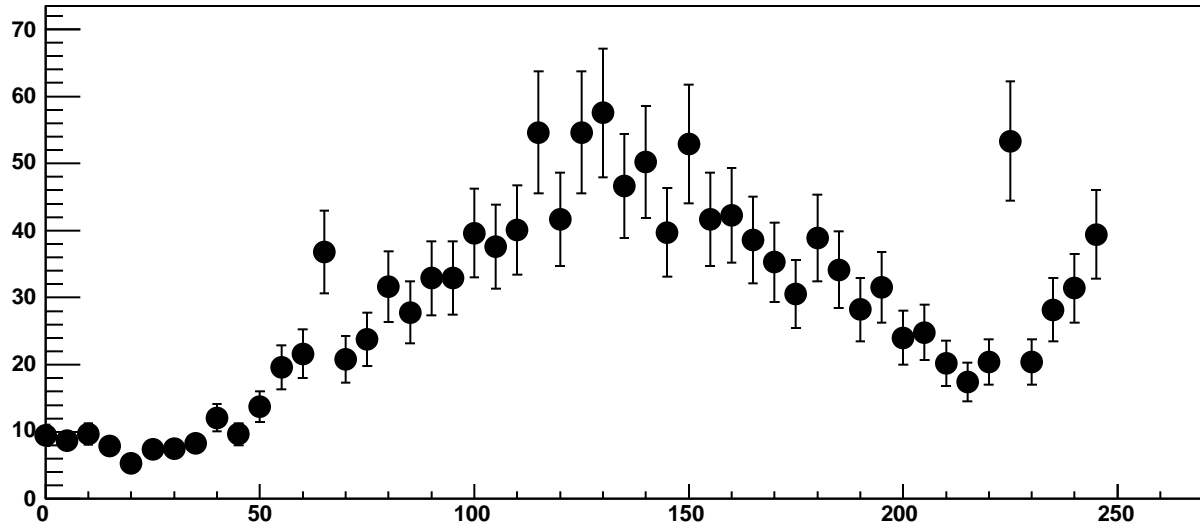


Chip 6, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

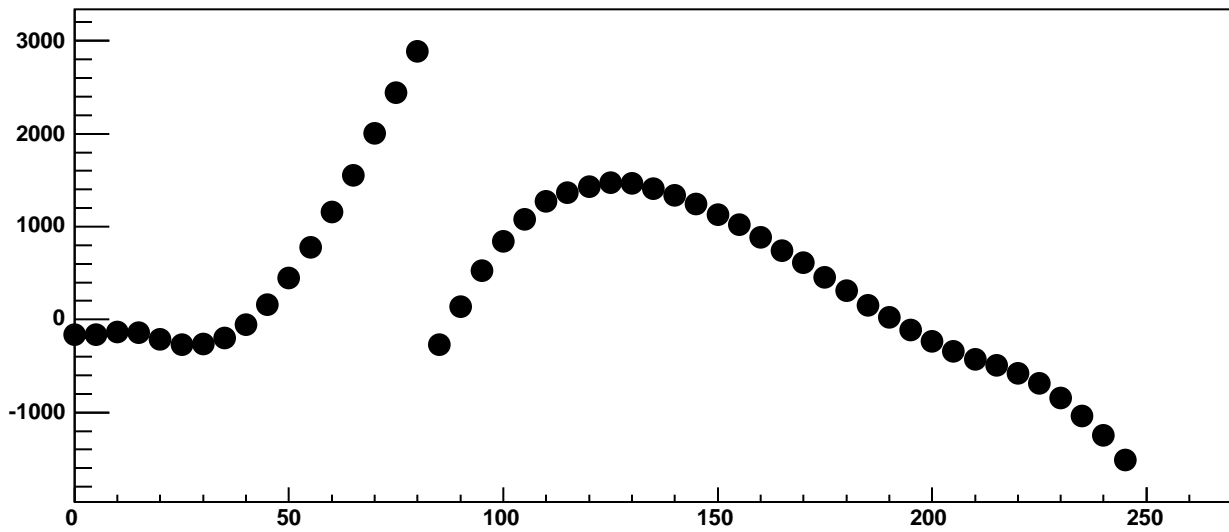


χ^2 / ndf	1.121e+06 / 41
p0	-425.1 ± 1.166
p1	84.82 ± 0.0002171
p2	3605 ± 3.086
p3	-92.31 ± 0.08739
p4	-3.586 ± 0.01922

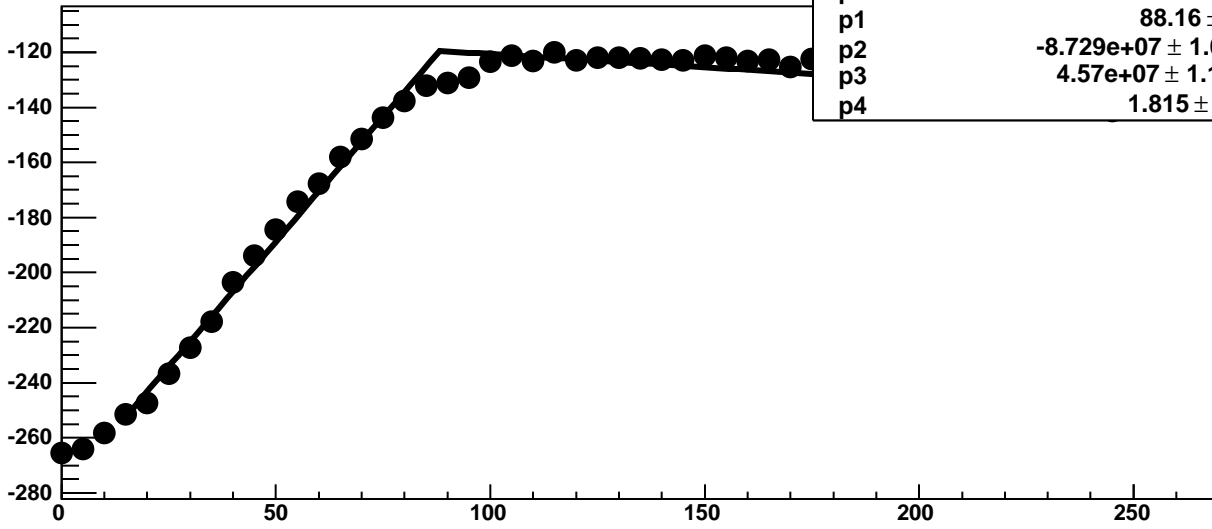
Chip 6, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

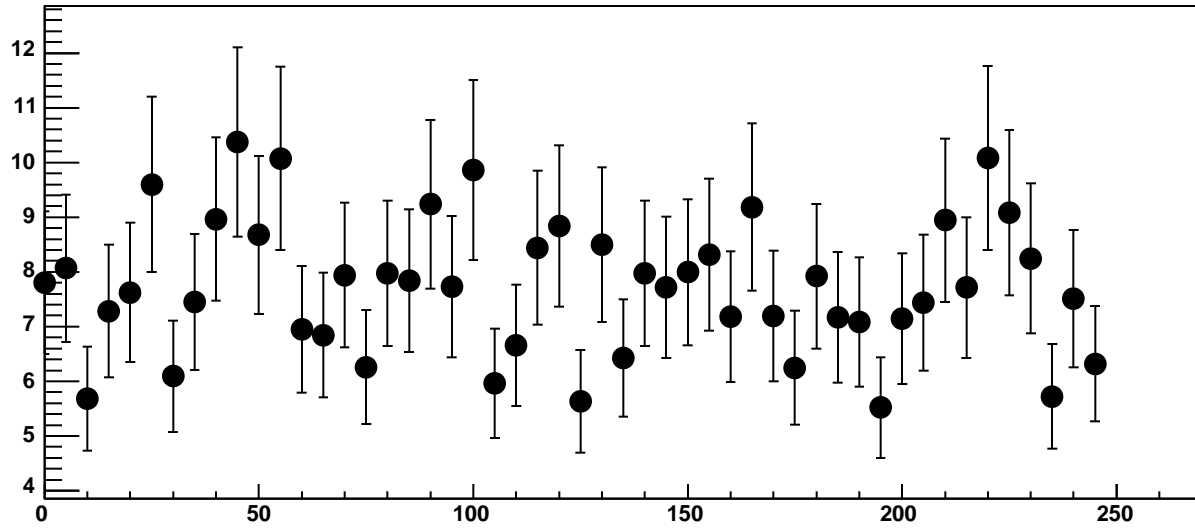


Chip 6, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

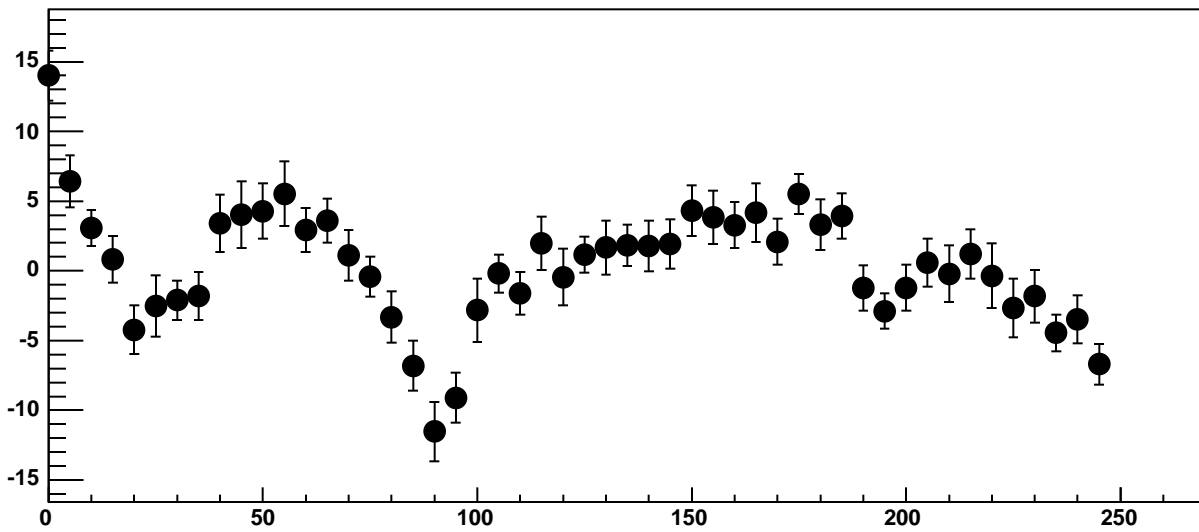


χ^2 / ndf	186.3 / 41
p0	-119.5 ± 0.5885
p1	88.16 ± 0.5686
p2	$-8.729\text{e}+07 \pm 1.007\text{e}+06$
p3	$4.57\text{e}+07 \pm 1.102\text{e}+05$
p4	1.815 ± 0.02049

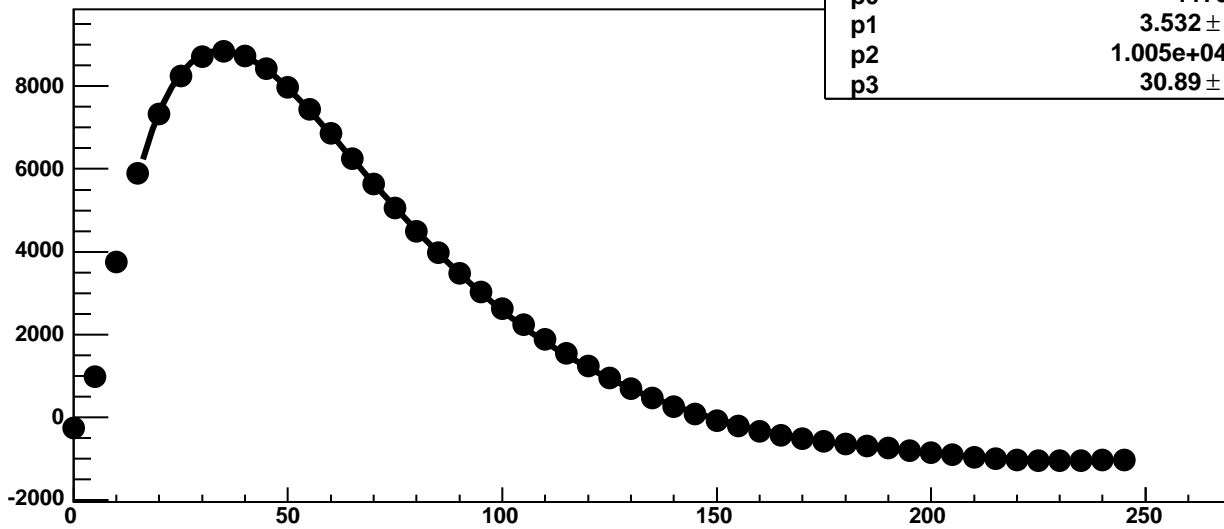
Chip 6, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

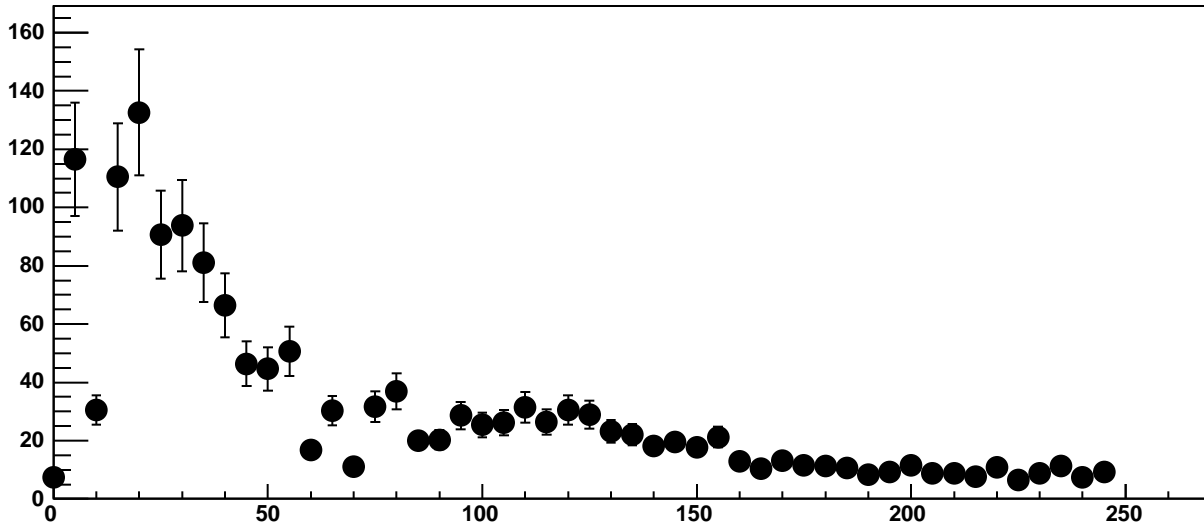


Chip 6, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold

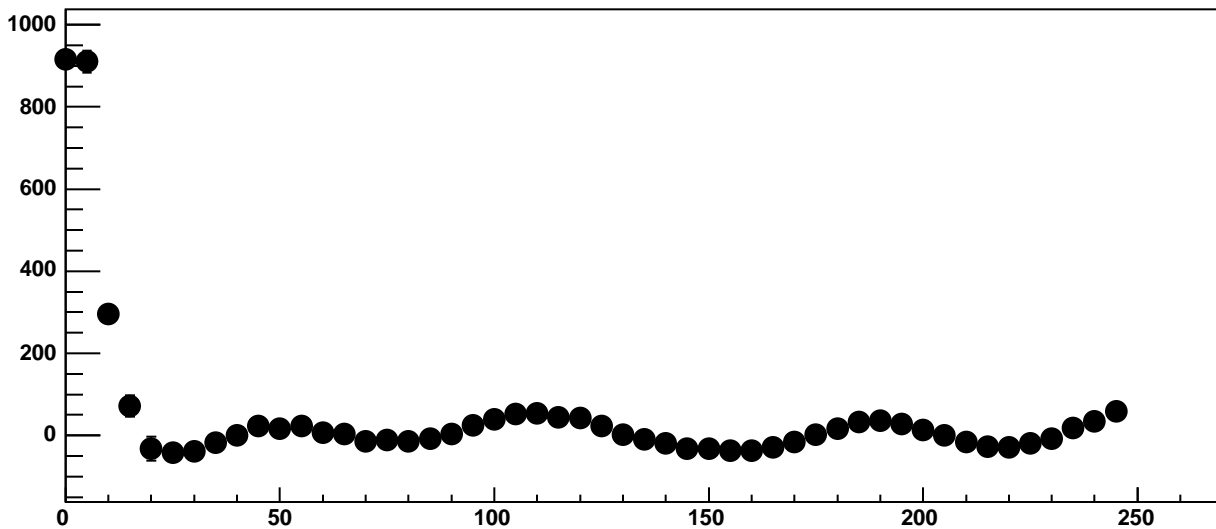


χ^2 / ndf	2747 / 42
p0	-1179 ± 0.862
p1	3.532 ± 0.04444
p2	1.005e+04 ± 4.918
p3	30.89 ± 0.01689

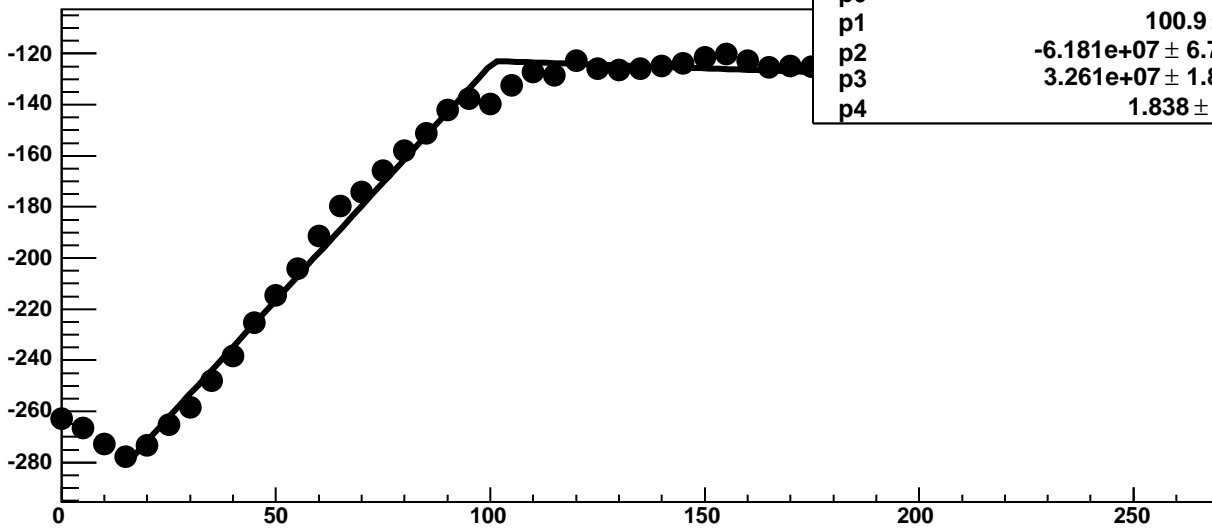
Chip 6, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold

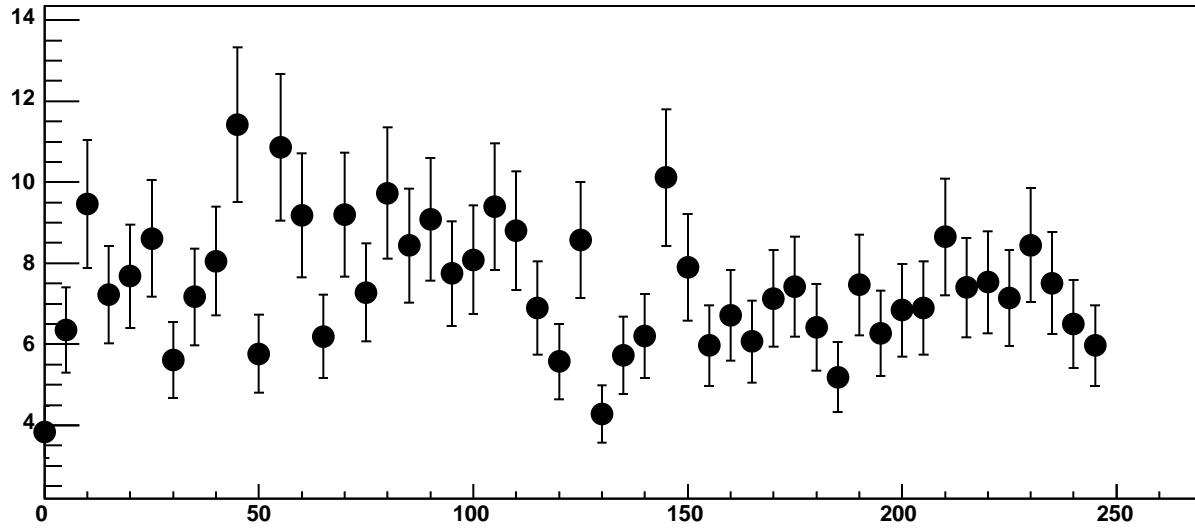


Chip 6, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold

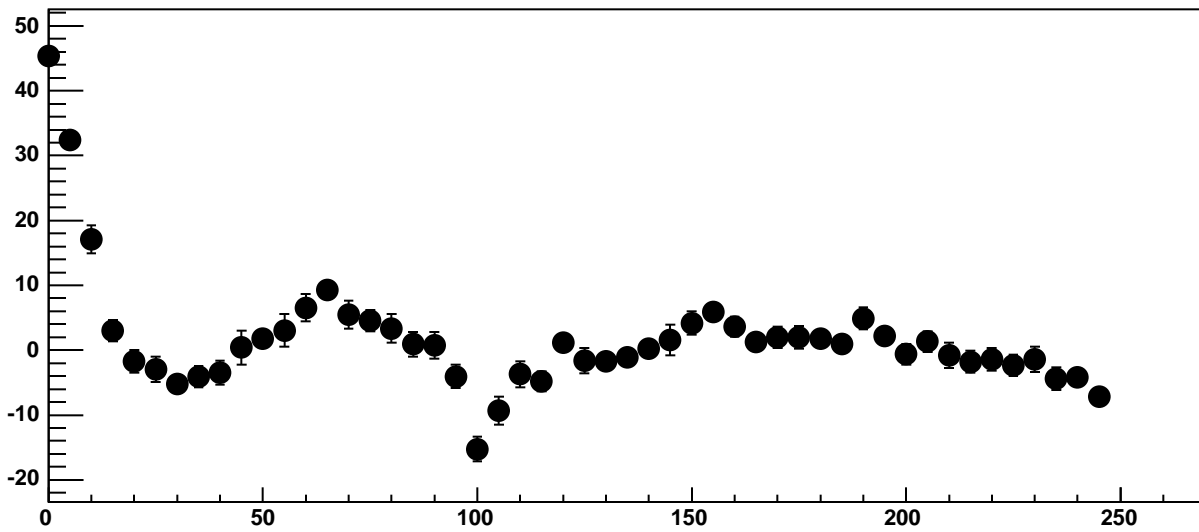


χ^2 / ndf	280.1 / 41
p0	-122.9 ± 0.5816
p1	100.9 ± 0.5511
p2	$-6.181\text{e}+07 \pm 6.798\text{e}+05$
p3	$3.261\text{e}+07 \pm 1.885\text{e}+05$
p4	1.838 ± 0.01618

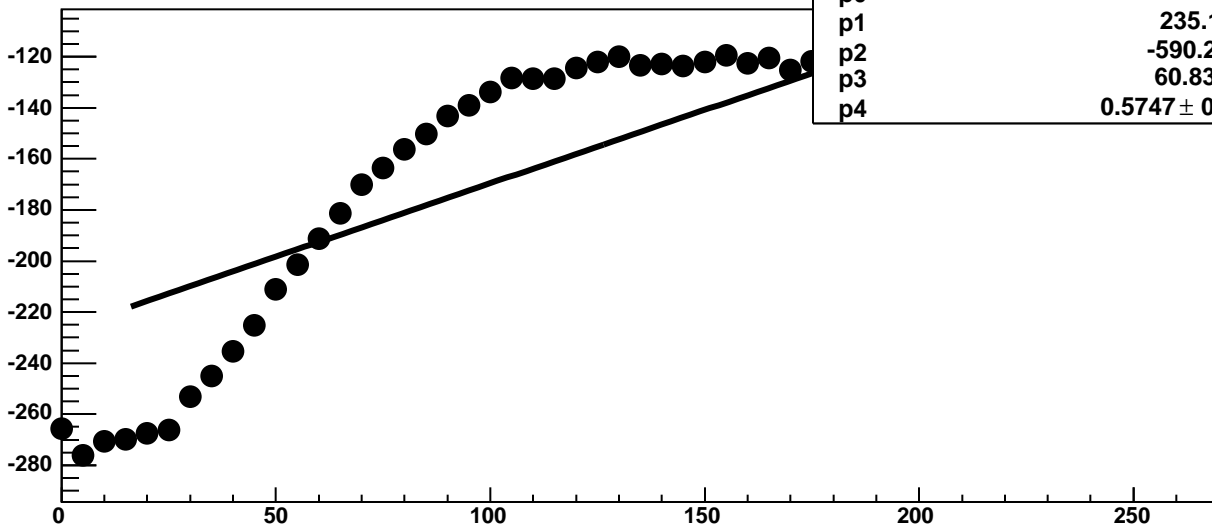
Chip 6, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold

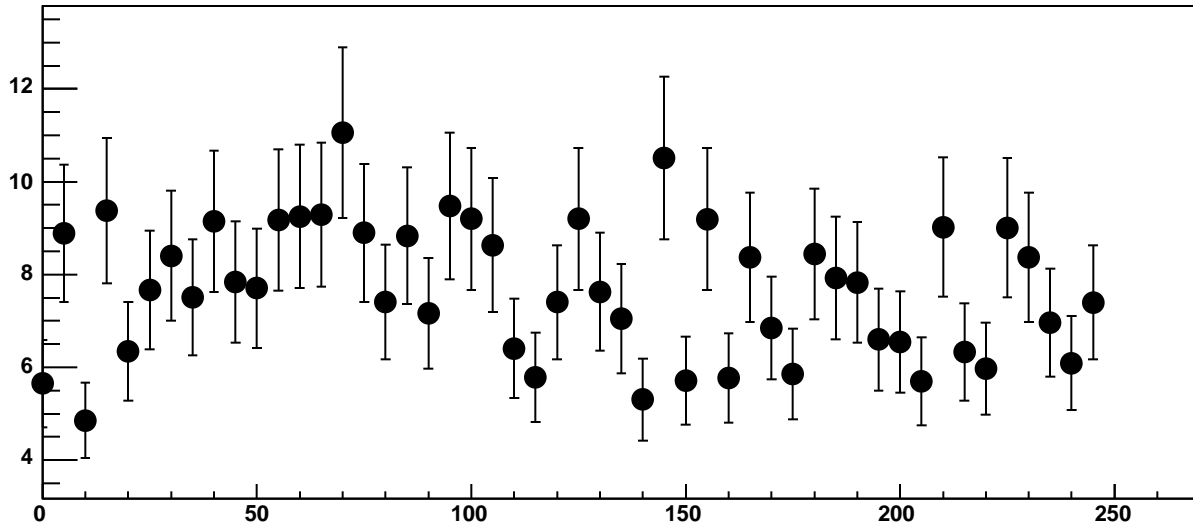


Chip 6, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold

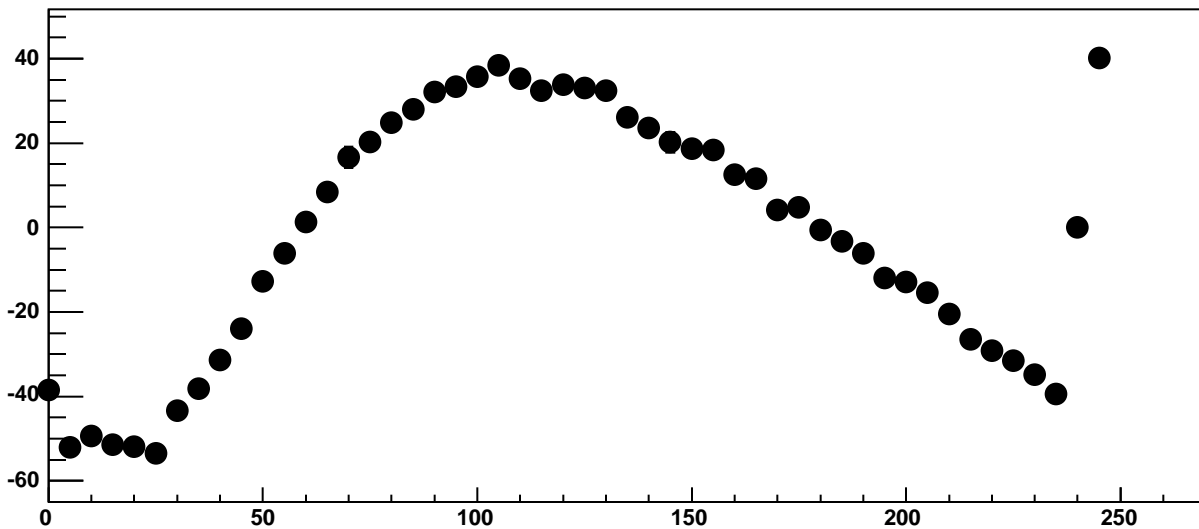


χ^2 / ndf	1.17e+04 / 41
p0	-91.95 ± 1.358
p1	235.1 ± 2.221
p2	-590.2 ± 247.9
p3	60.83 ± 21.88
p4	0.5747 ± 0.003988

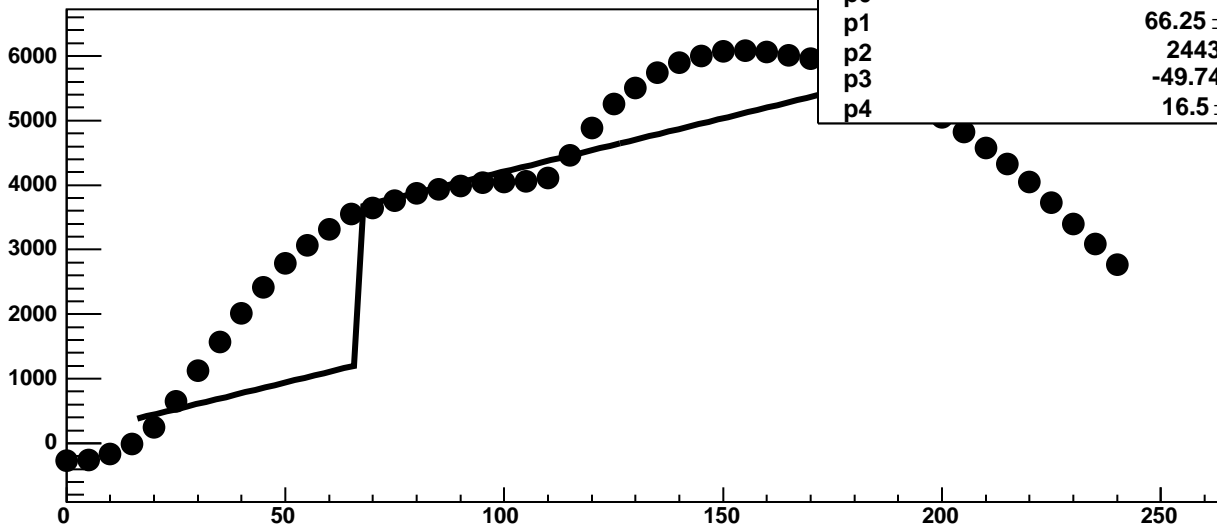
Chip 6, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



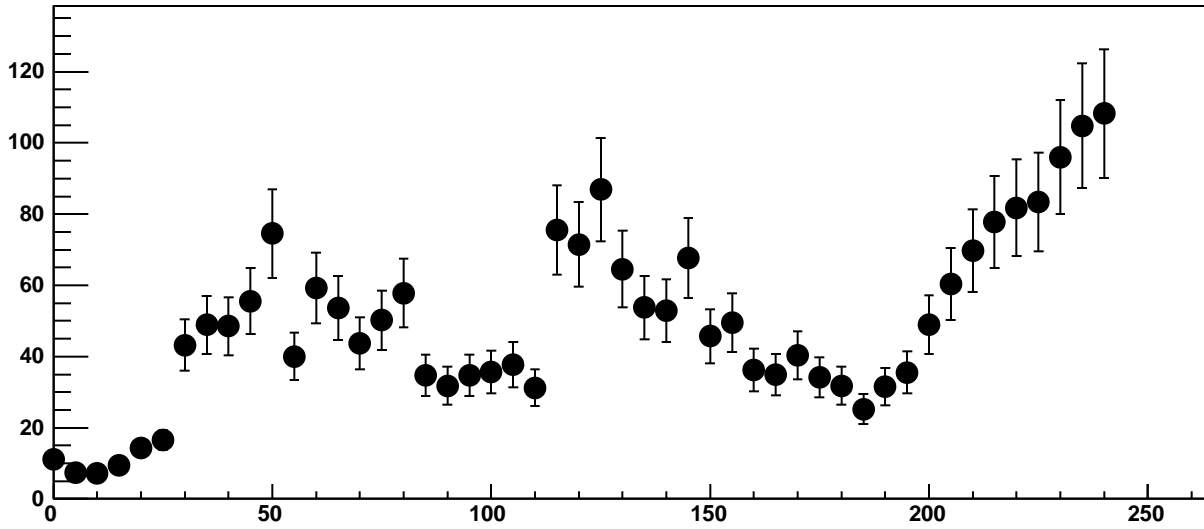
Chip 6, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold



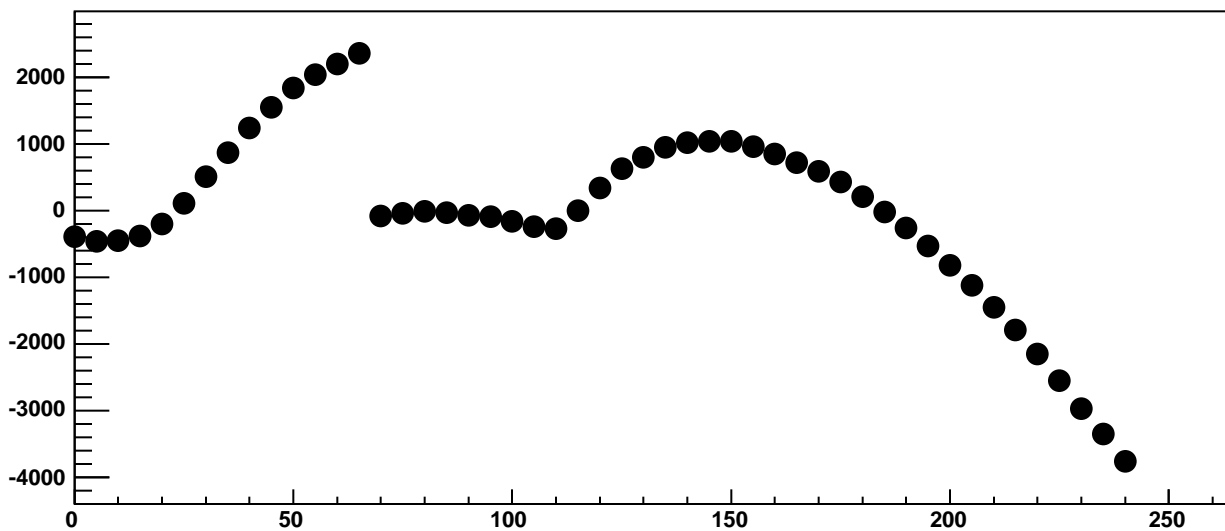
Chip 6, Channel 17, Enable 5, DAC=4000, ADC Mean vs Hold



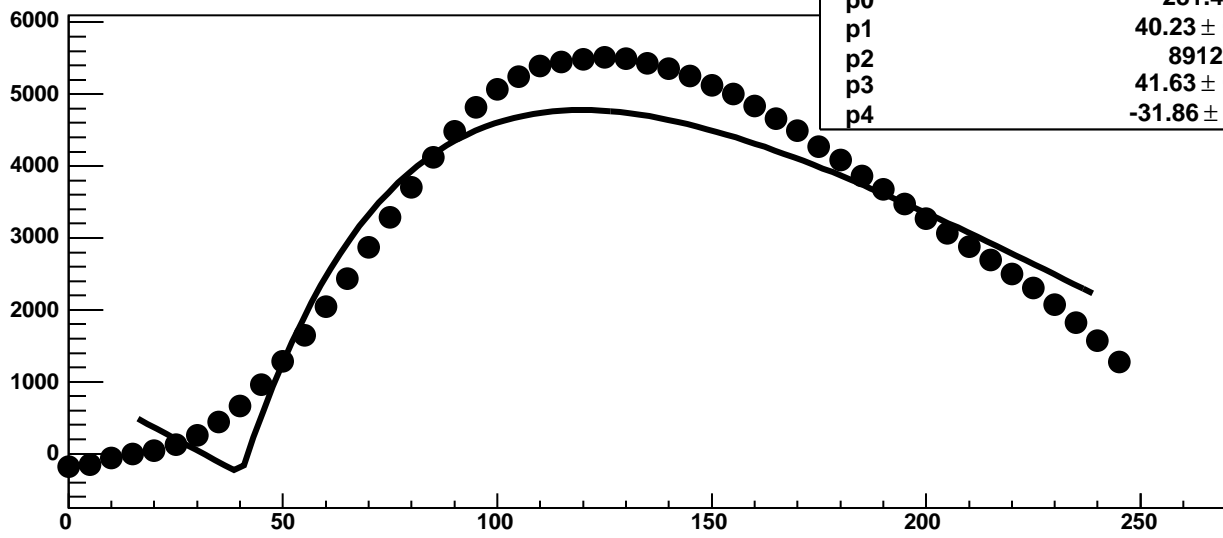
Chip 6, Channel 17, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 6, Channel 17, Enable 5, DAC=4000, ADC Residuals vs Hold

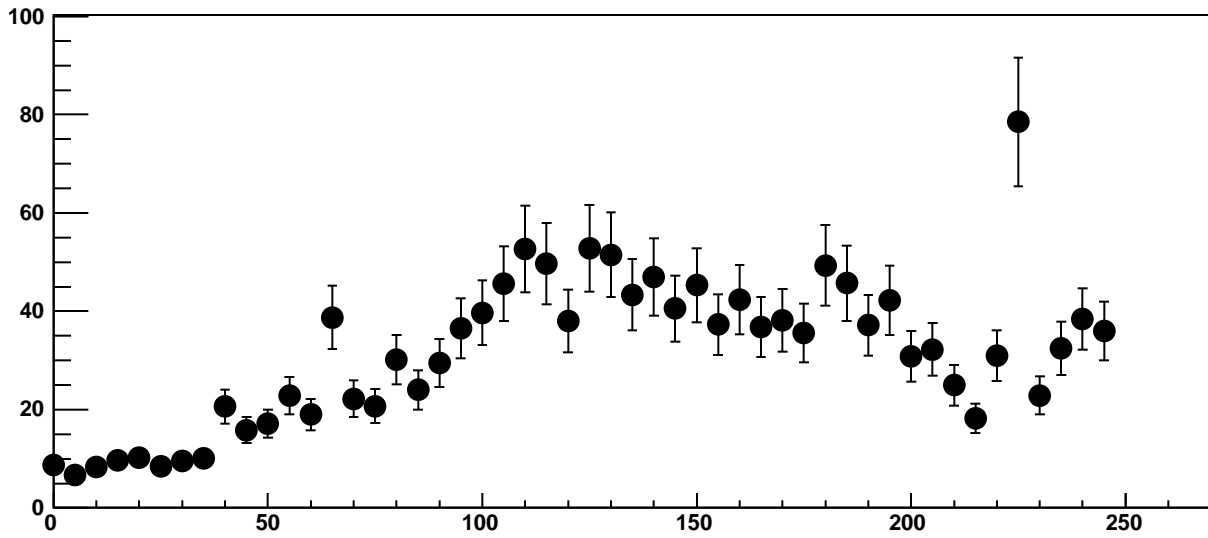


Chip 7, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold

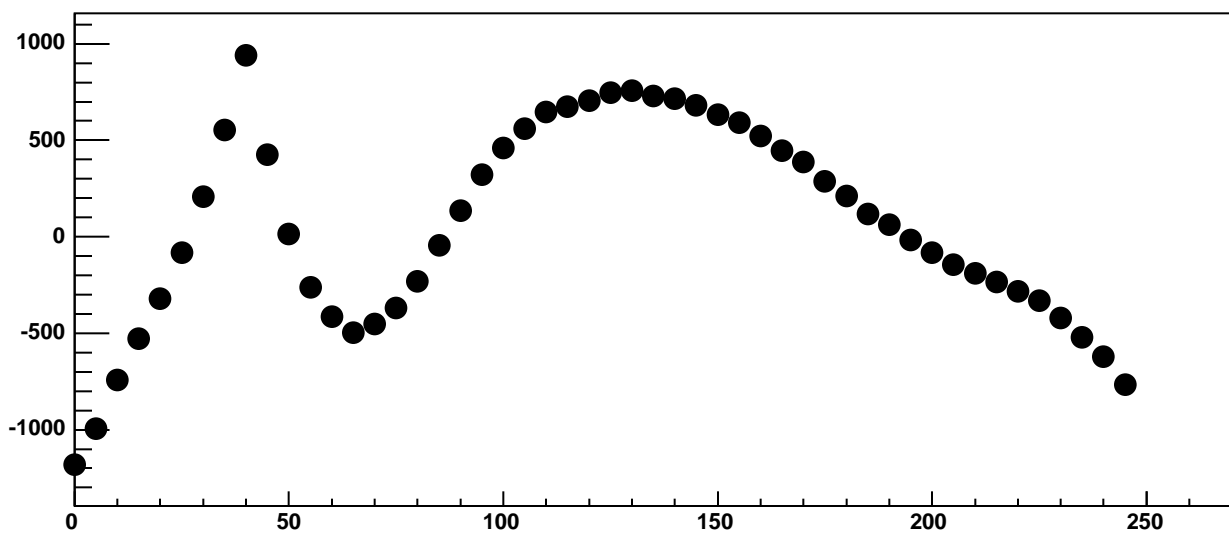


χ^2 / ndf	3.099e+05 / 41
p0	-281.4 ± 1.197
p1	40.23 ± 0.01836
p2	8912 ± 12.04
p3	41.63 ± 0.06917
p4	-31.86 ± 0.06113

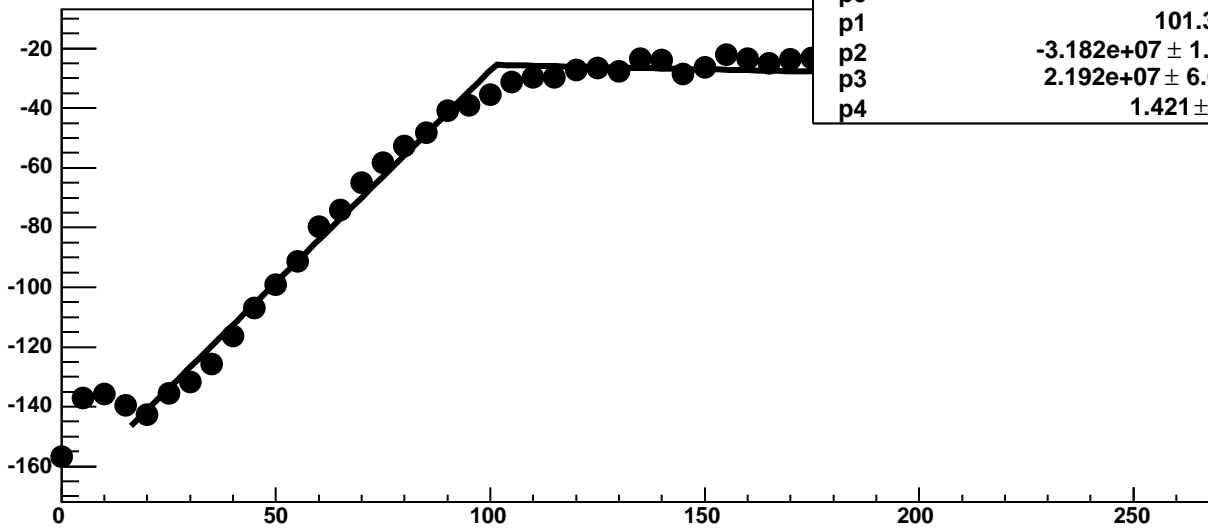
Chip 7, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold

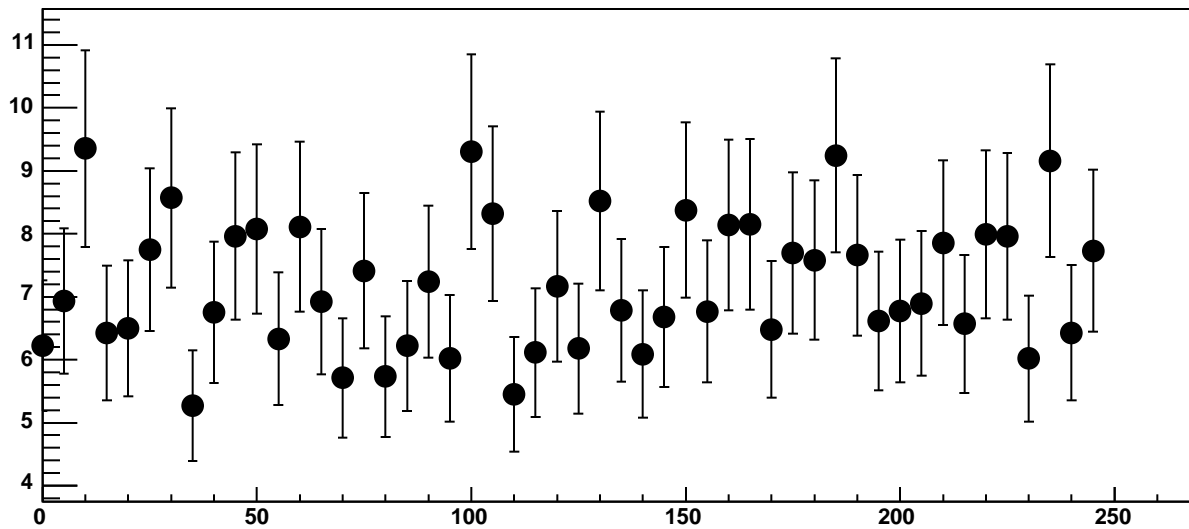


Chip 7, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

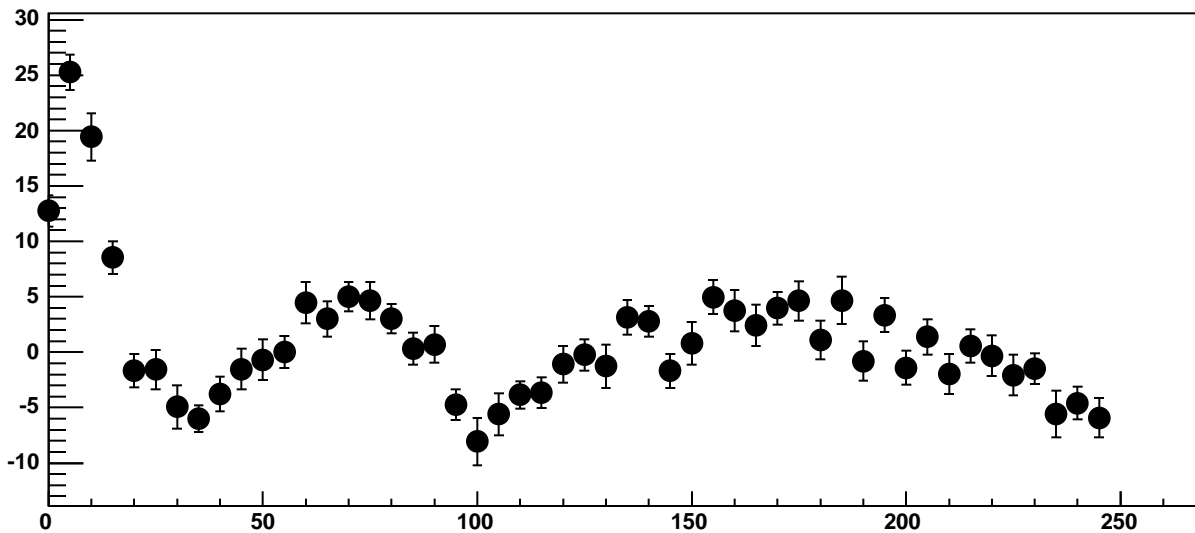


χ^2 / ndf	233.9 / 41
p0	-25.51 ± 0.5819
p1	101.3 ± 0.645
p2	$-3.182\text{e}+07 \pm 1.021\text{e}+06$
p3	$2.192\text{e}+07 \pm 6.692\text{e}+05$
p4	1.421 ± 0.01422

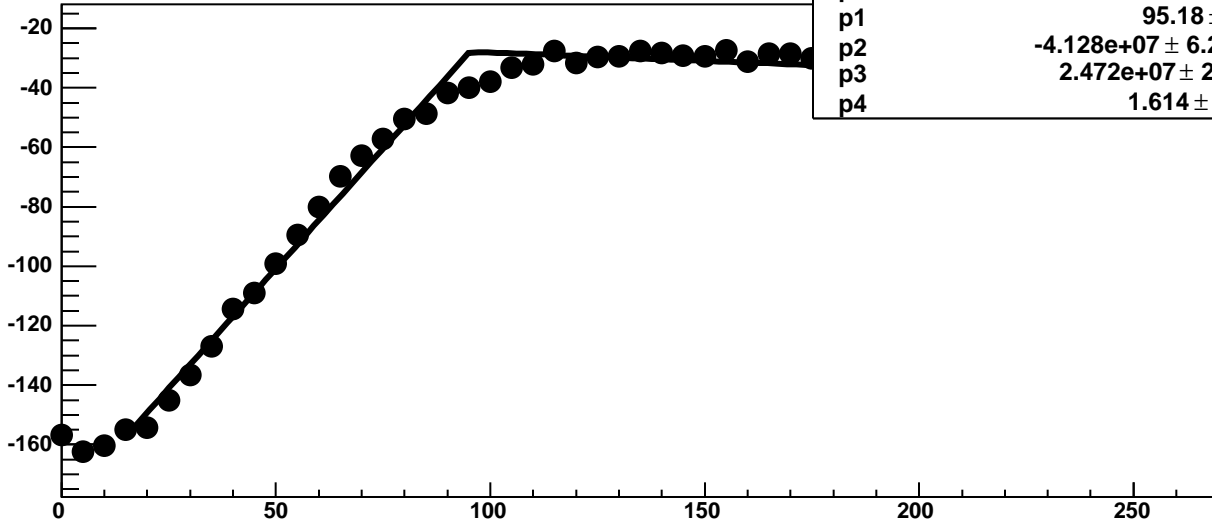
Chip 7, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



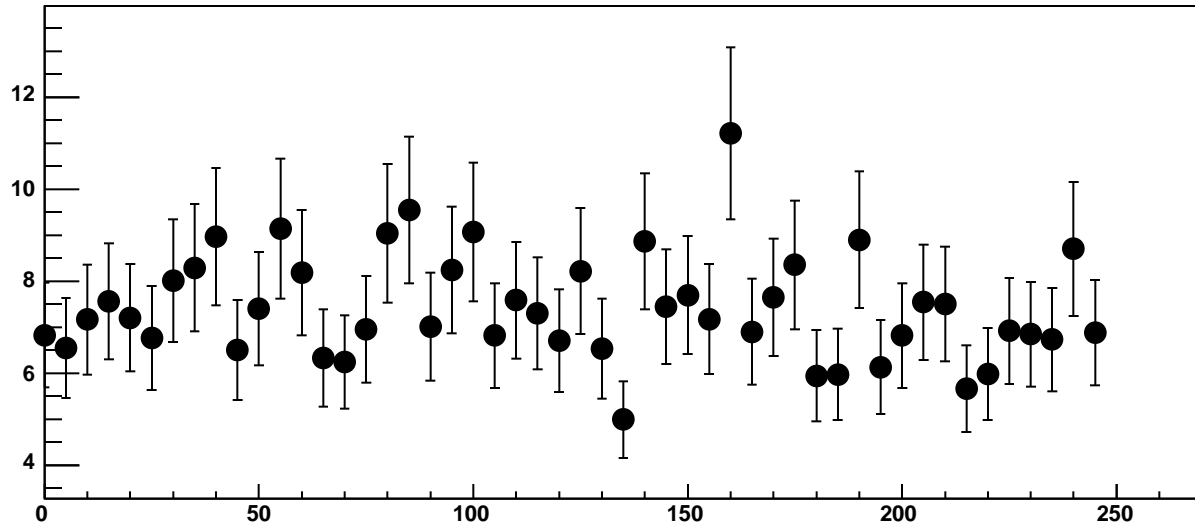
Chip 7, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold



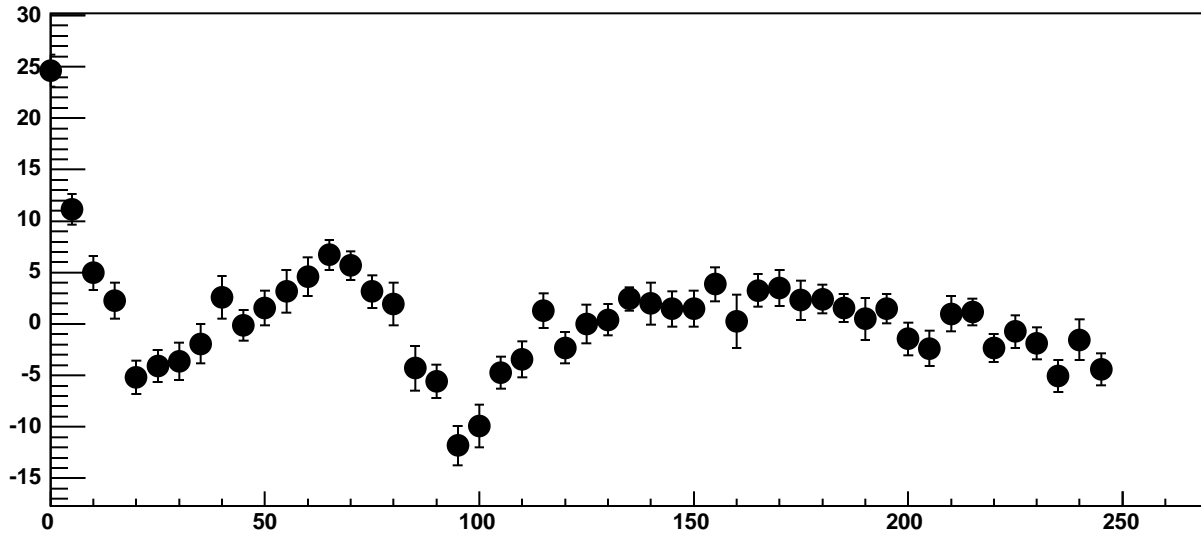
Chip 7, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold



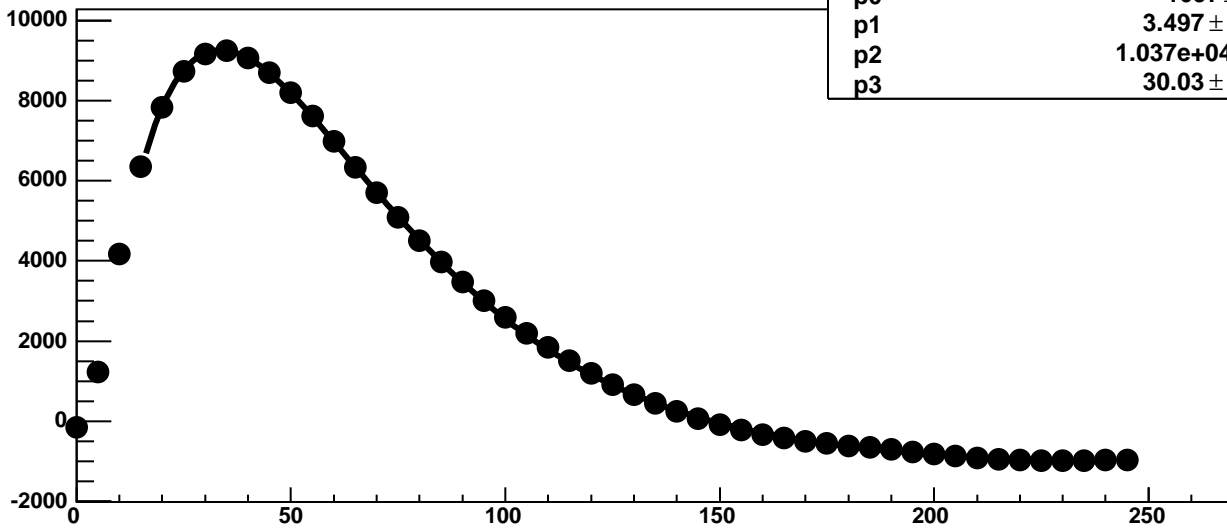
Chip 7, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

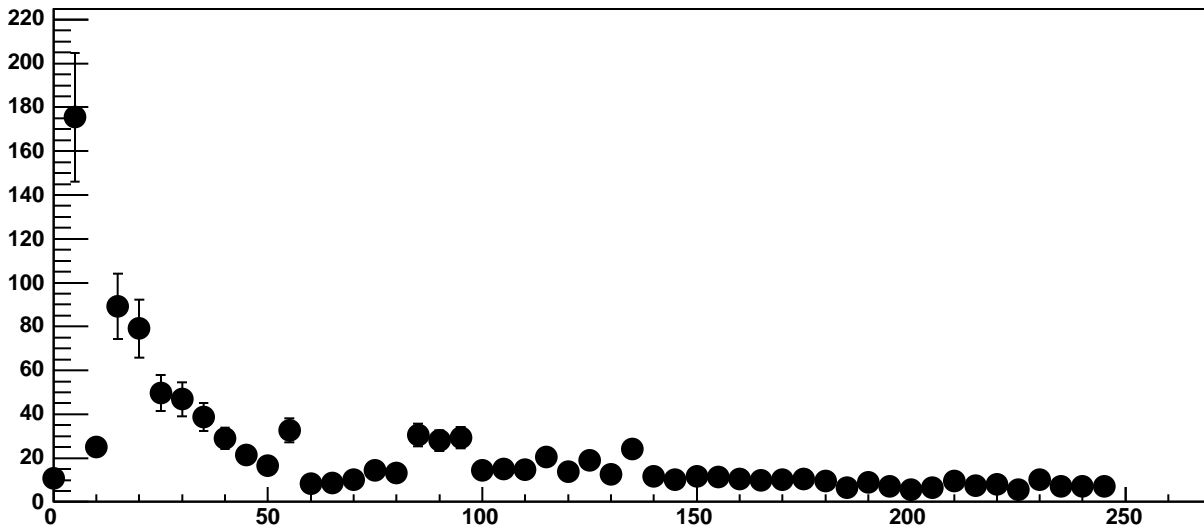


Chip 7, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold

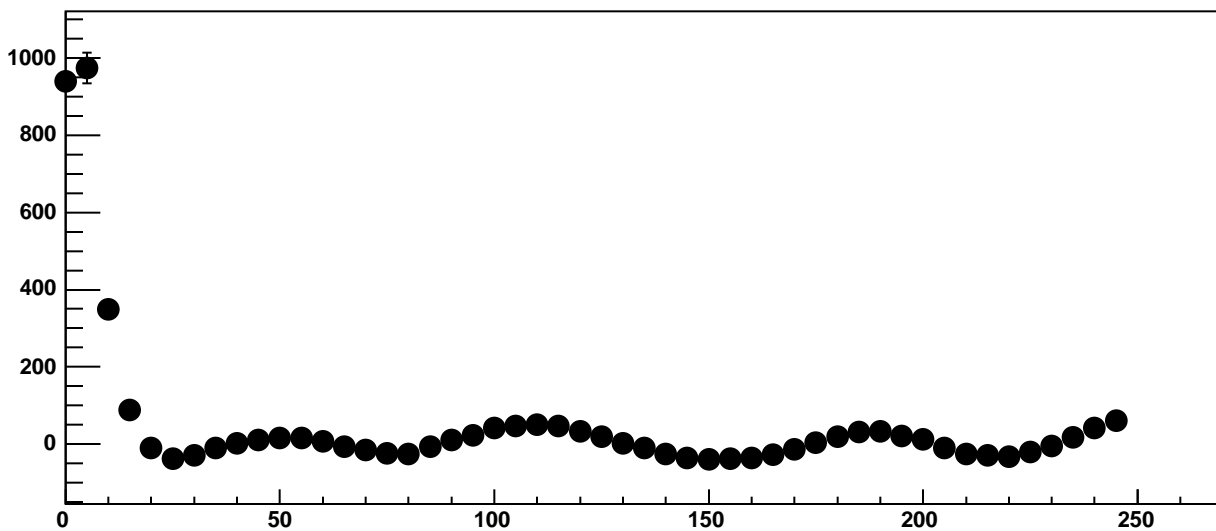


χ^2 / ndf	5086 / 42
p0	-1097 ± 0.6387
p1	3.497 ± 0.02906
p2	1.037e+04 ± 2.991
p3	30.03 ± 0.01088

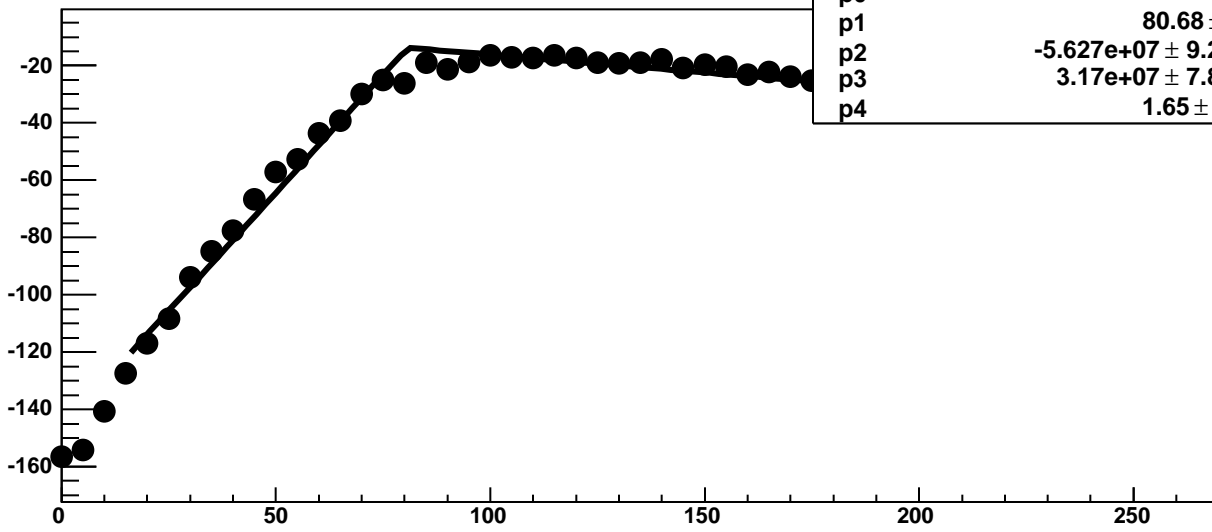
Chip 7, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold



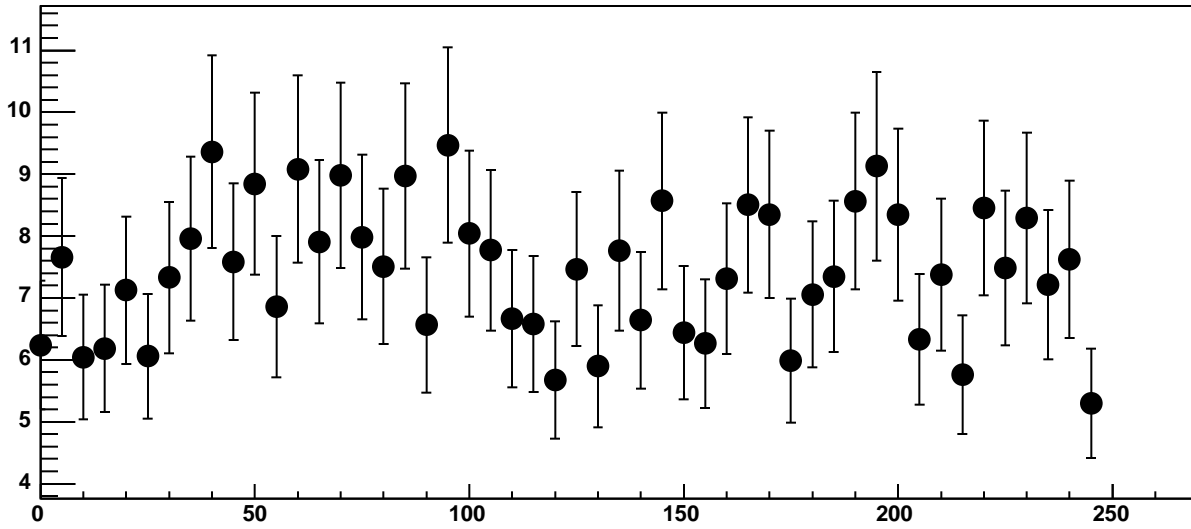
Chip 7, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold



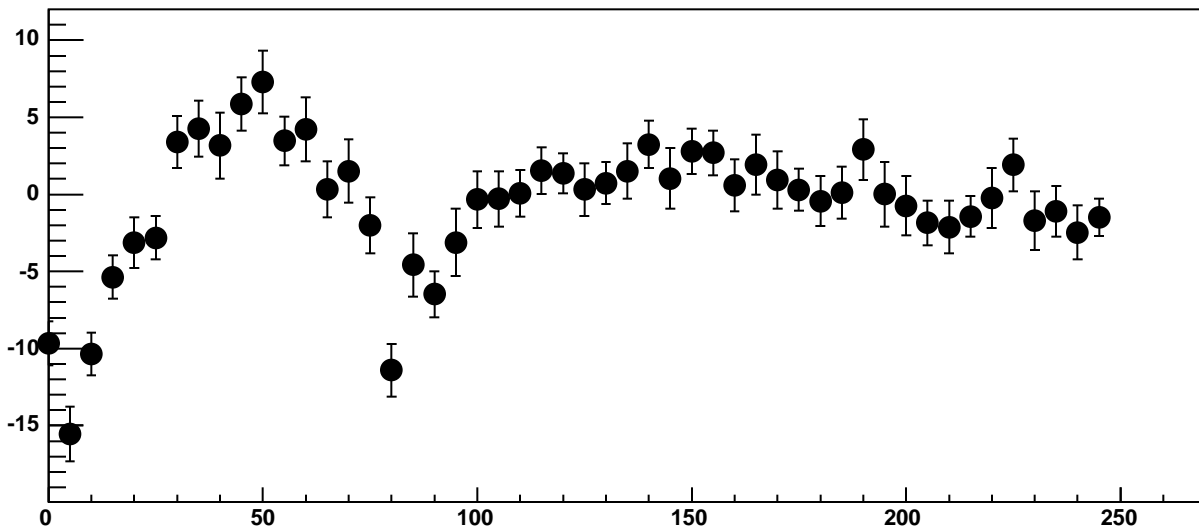
Chip 7, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold



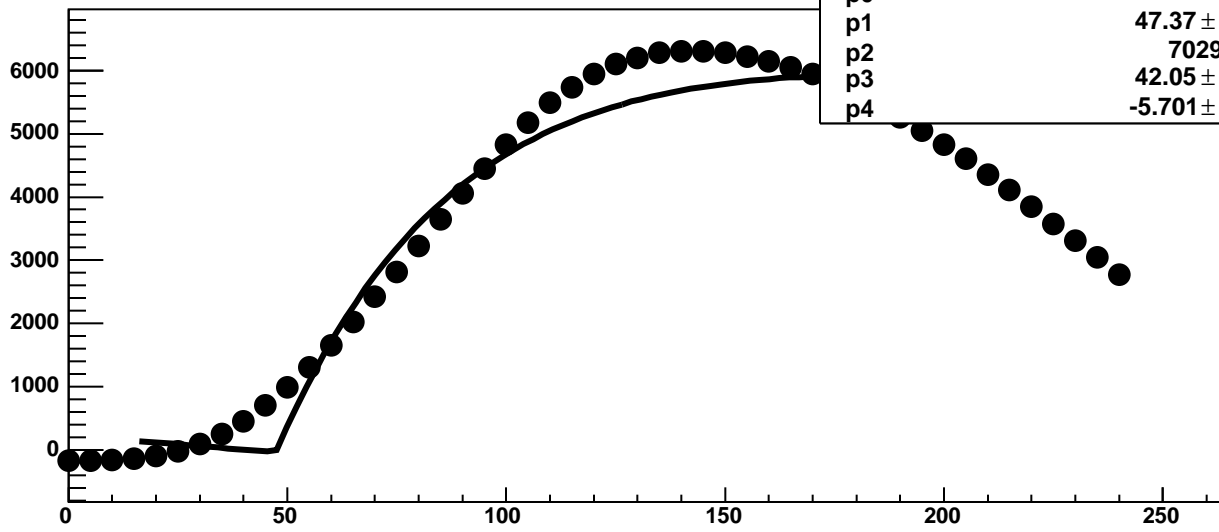
Chip 7, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

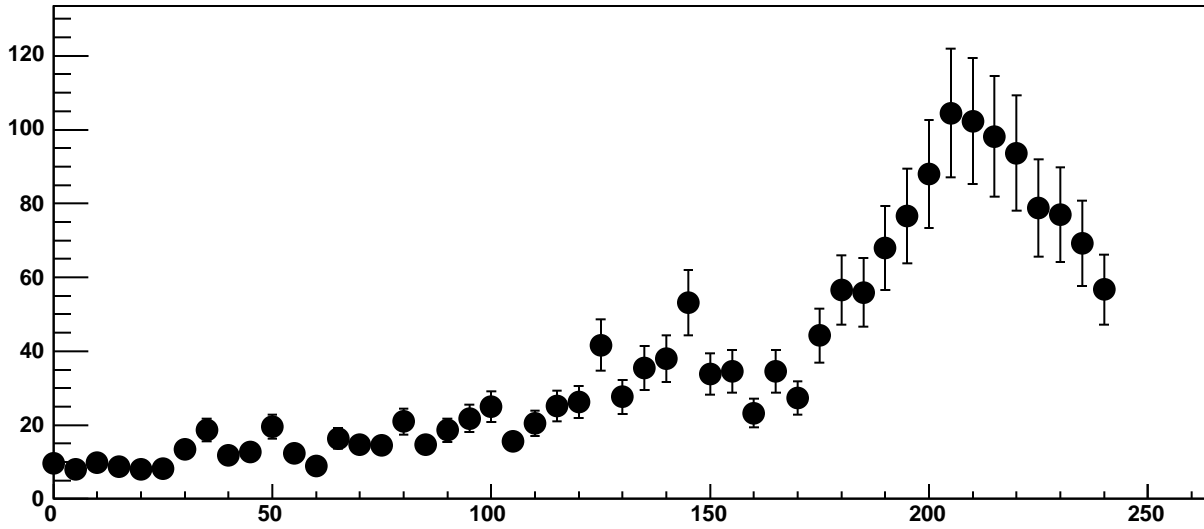


Chip 7, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold

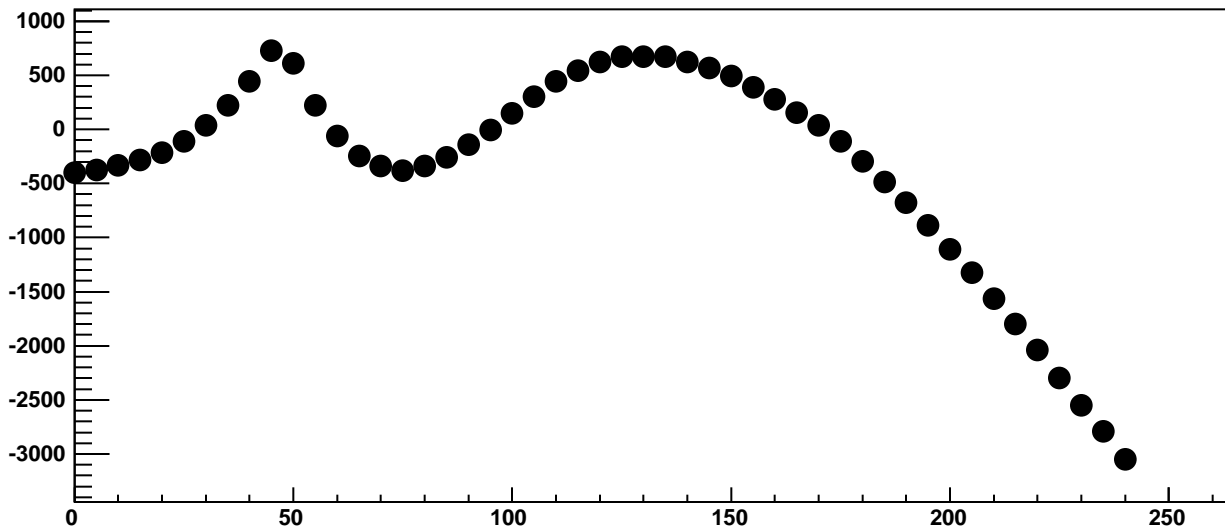


χ^2 / ndf	4.26e+05 / 41
p0	-38.85 ± 1.531
p1	47.37 ± 0.01845
p2	7029 ± 10.69
p3	42.05 ± 0.06773
p4	-5.701 ± 0.06197

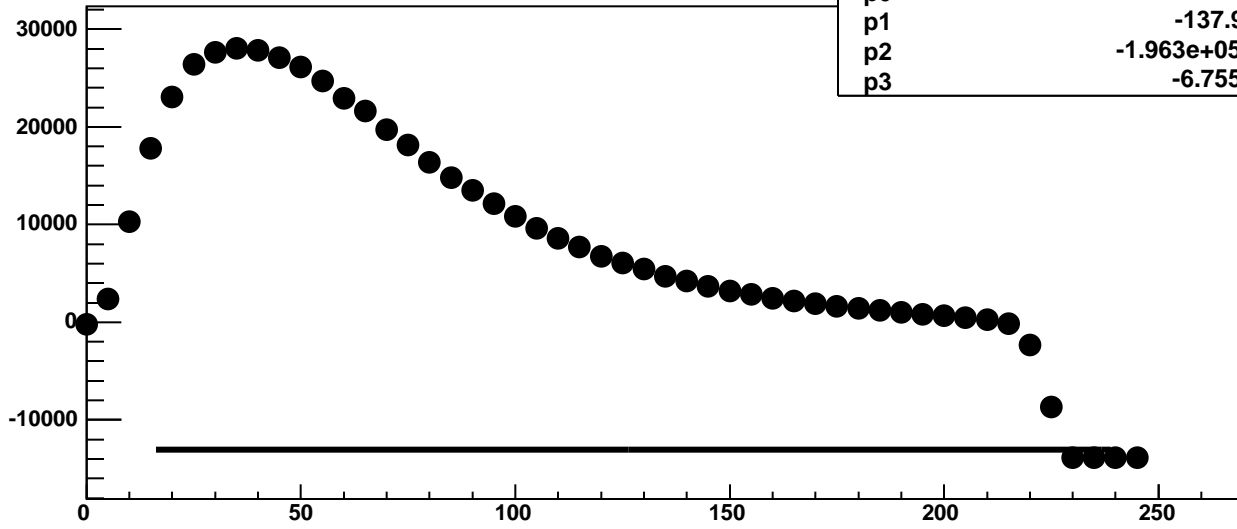
Chip 7, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold

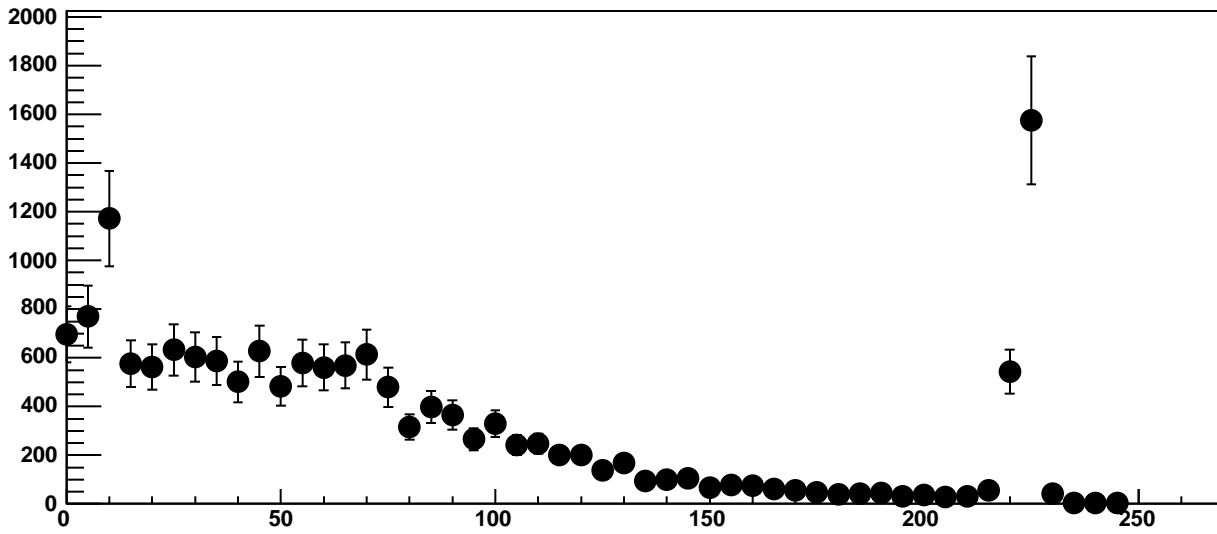


Chip 7, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold

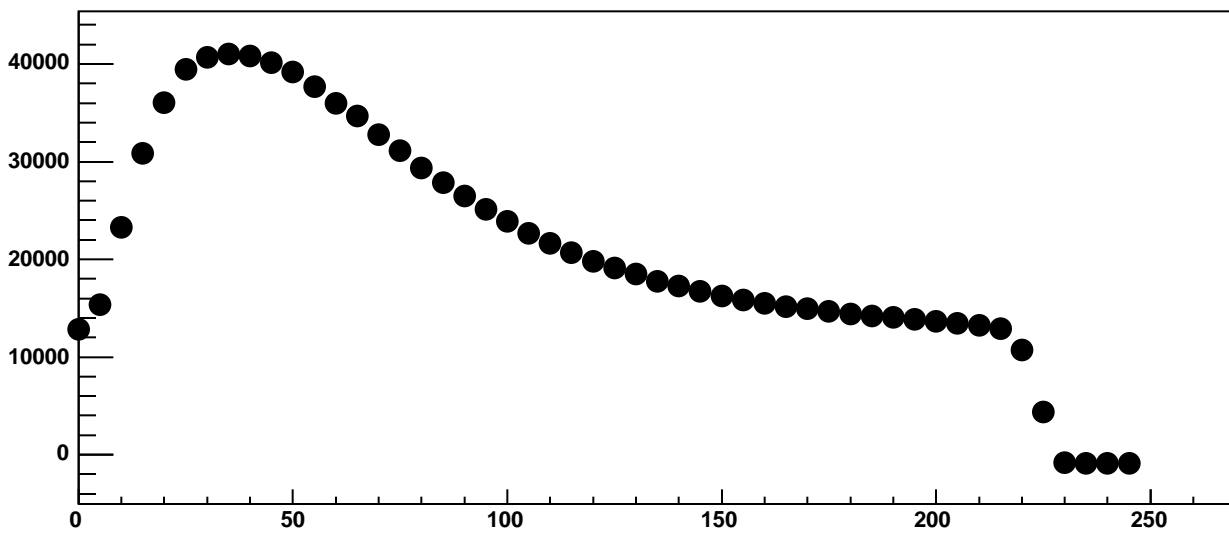


χ^2 / ndf	3.568e+07 / 42
p0	-1.302e+04 ± 0.5948
p1	-137.9 ± 3441
p2	-1.963e+05 ± 1.414
p3	-6.755 ± 1.414

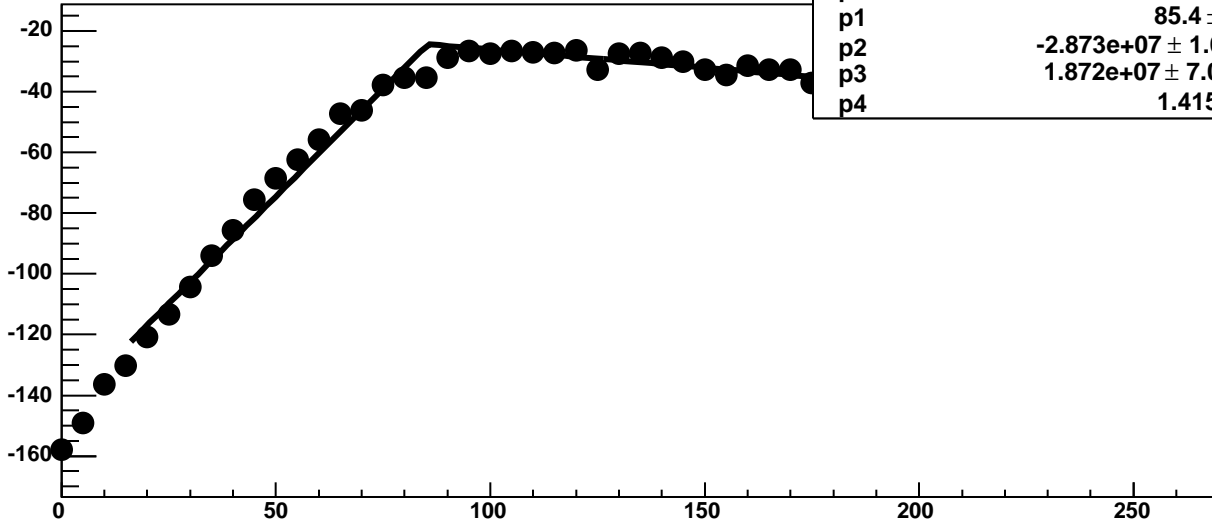
Chip 7, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold

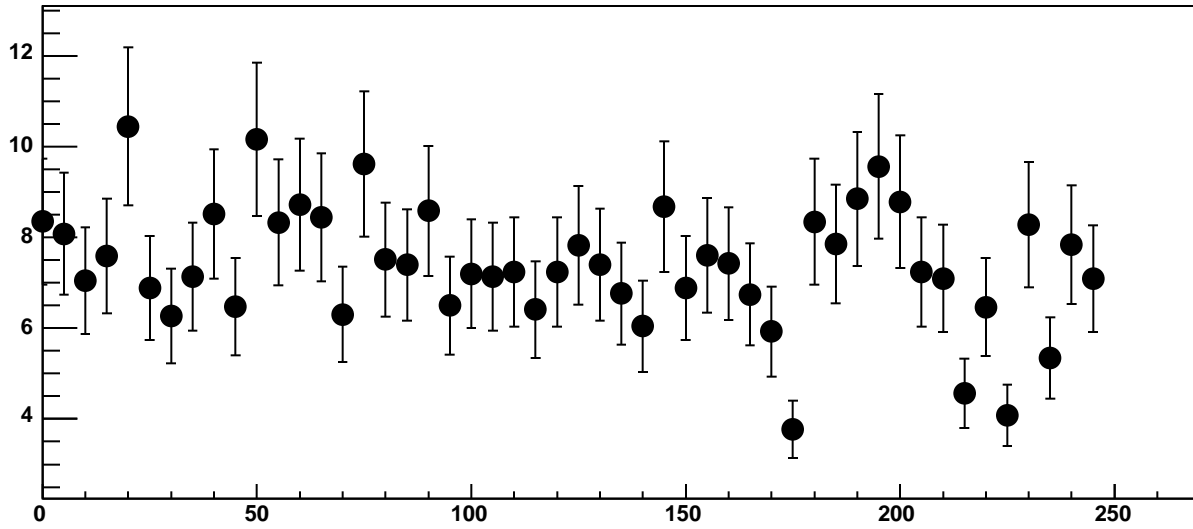


Chip 7, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

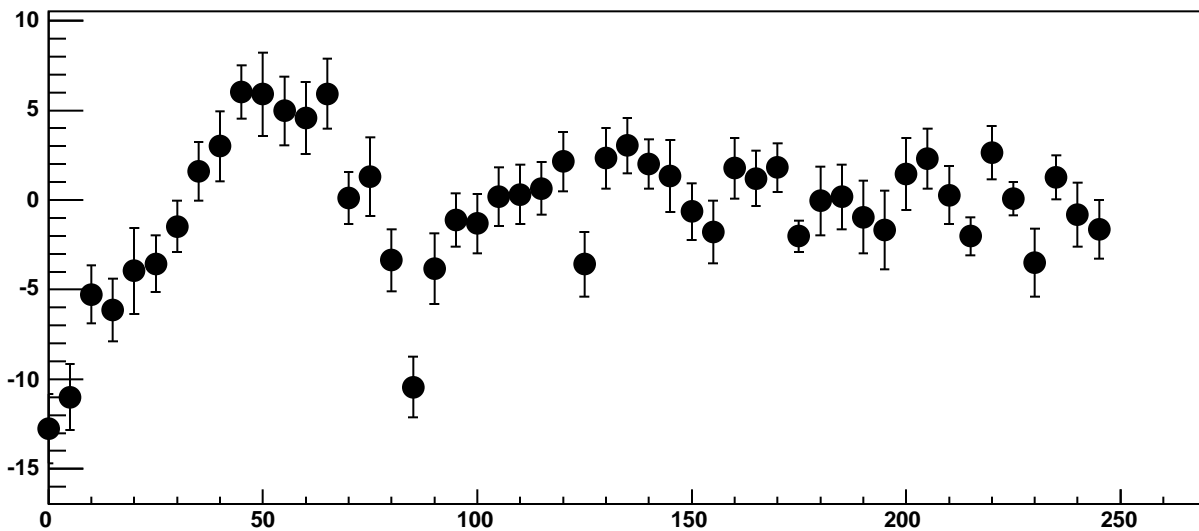


χ^2 / ndf	154.8 / 41
p0	-24.37 ± 0.5479
p1	85.4 ± 0.6916
p2	$-2.873\text{e}+07 \pm 1.091\text{e}+06$
p3	$1.872\text{e}+07 \pm 7.016\text{e}+05$
p4	1.415 ± 0.021

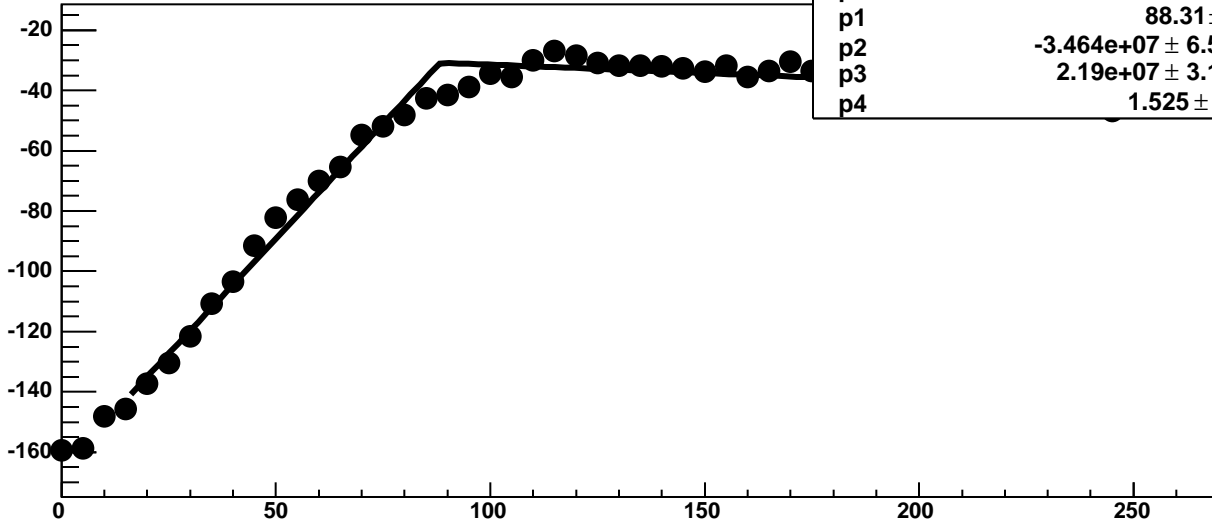
Chip 7, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

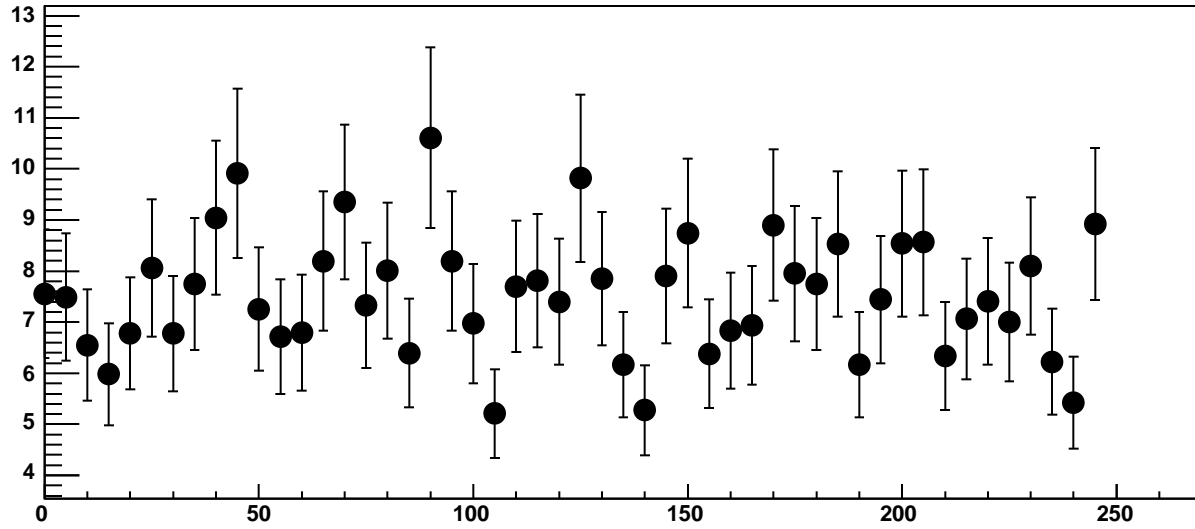


Chip 7, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

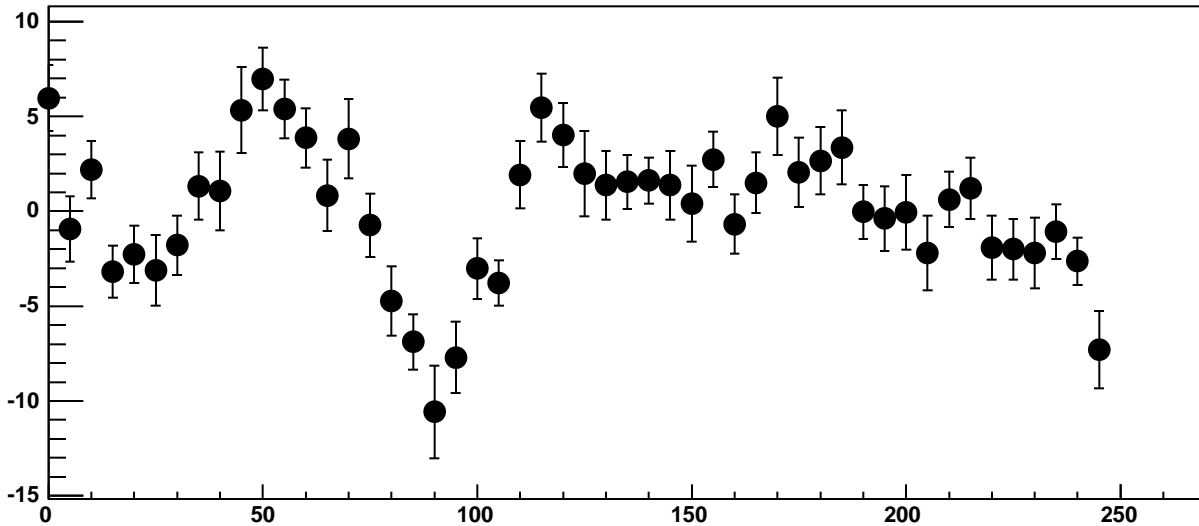


χ^2 / ndf	185 / 41
p0	-30.75 ± 0.5682
p1	88.31 ± 0.6639
p2	$-3.464\text{e}+07 \pm 6.577\text{e}+05$
p3	$2.19\text{e}+07 \pm 3.136\text{e}+05$
p4	1.525 ± 0.01906

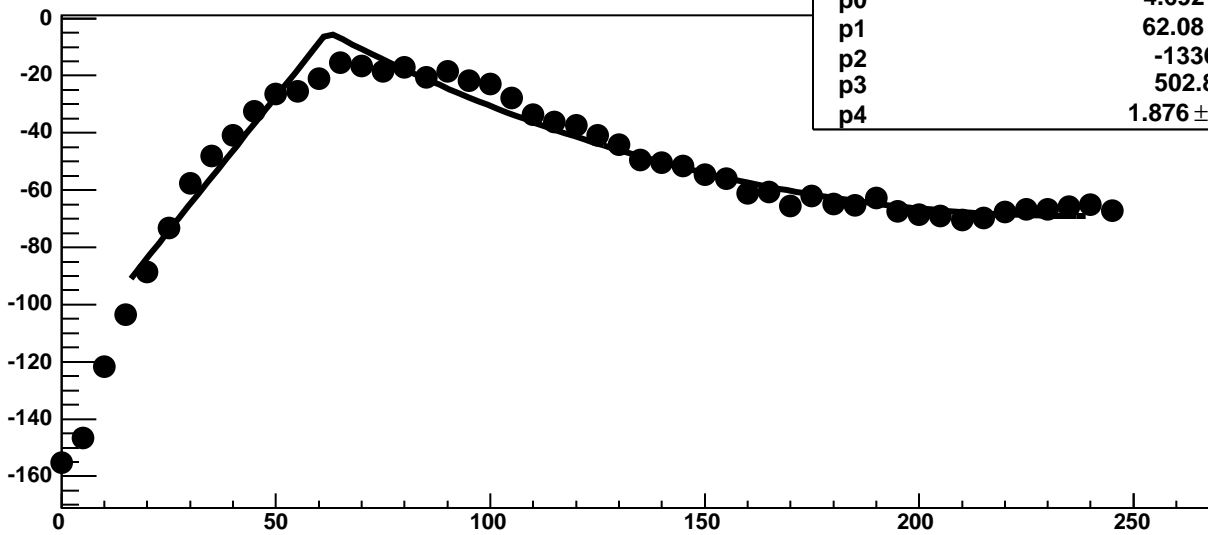
Chip 7, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

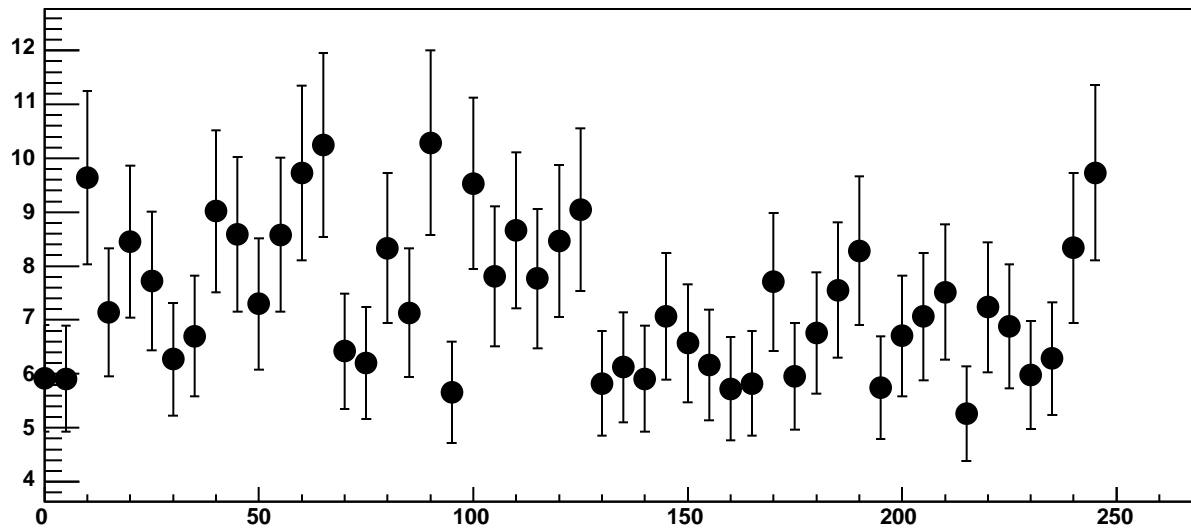


Chip 7, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold

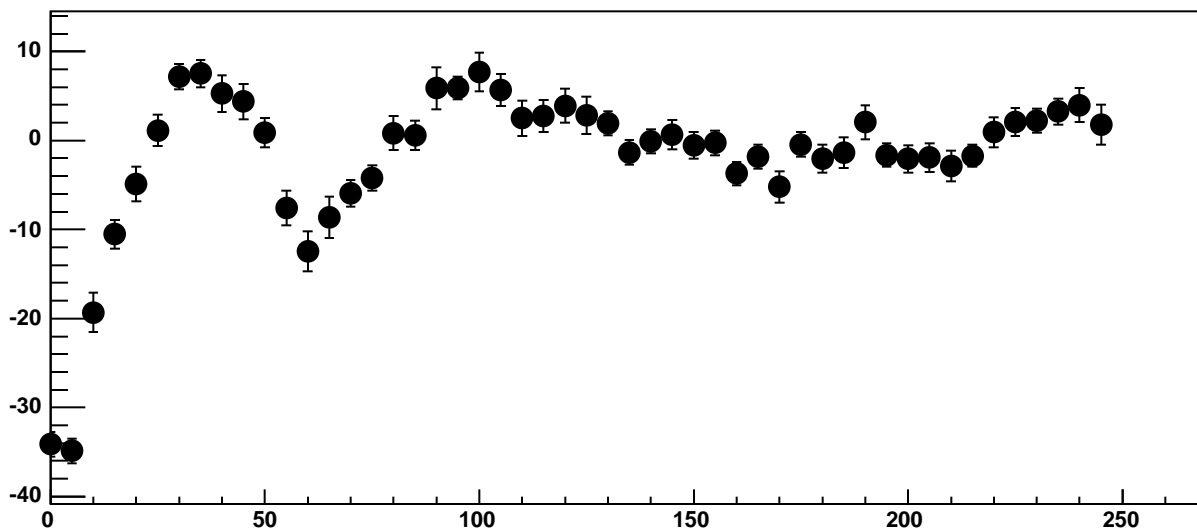


χ^2 / ndf	301.7 / 41
p0	-4.692 ± 0.7142
p1	62.08 ± 0.5529
p2	-1336 ± 67.91
p3	502.8 ± 24.97
p4	1.876 ± 0.04064

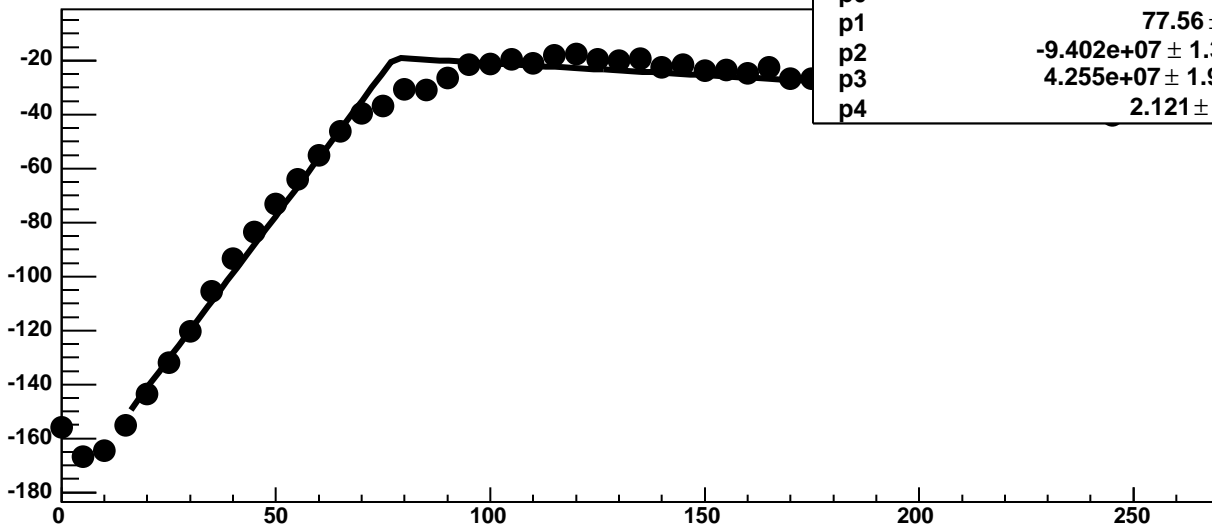
Chip 7, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold

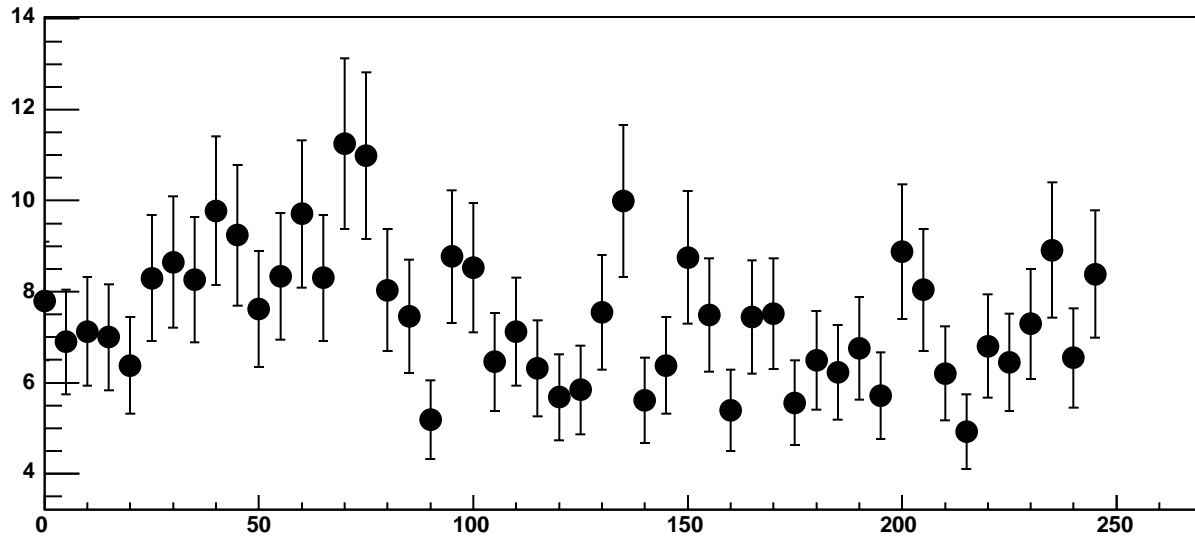


Chip 7, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

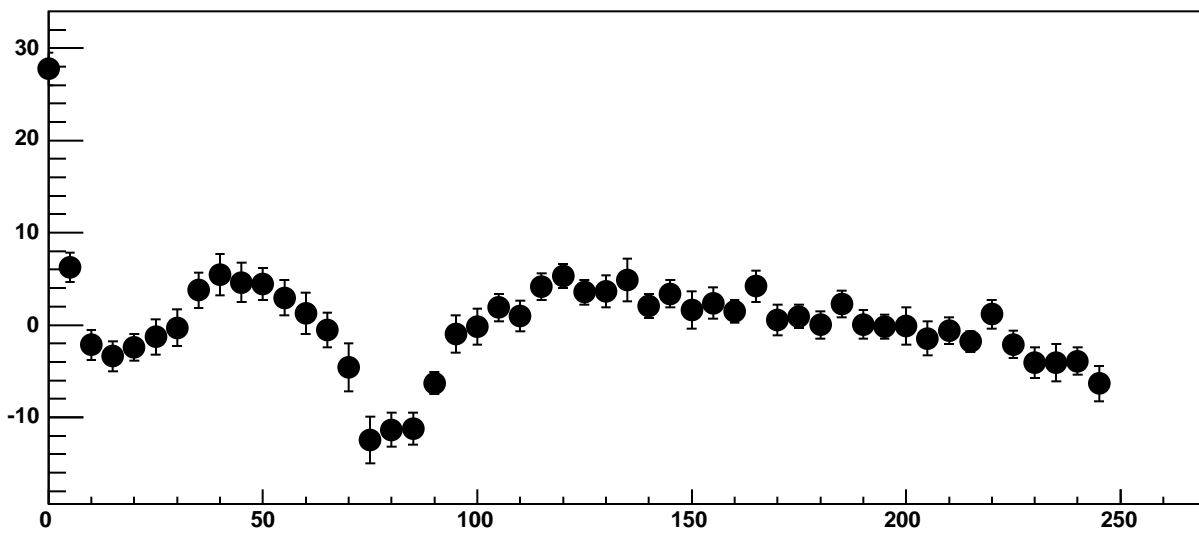


χ^2 / ndf	254.9 / 41
p0	-19.01 ± 0.5268
p1	77.56 ± 0.5919
p2	$-9.402\text{e}+07 \pm 1.304\text{e}+06$
p3	$4.255\text{e}+07 \pm 1.924\text{e}+05$
p4	2.121 ± 0.02879

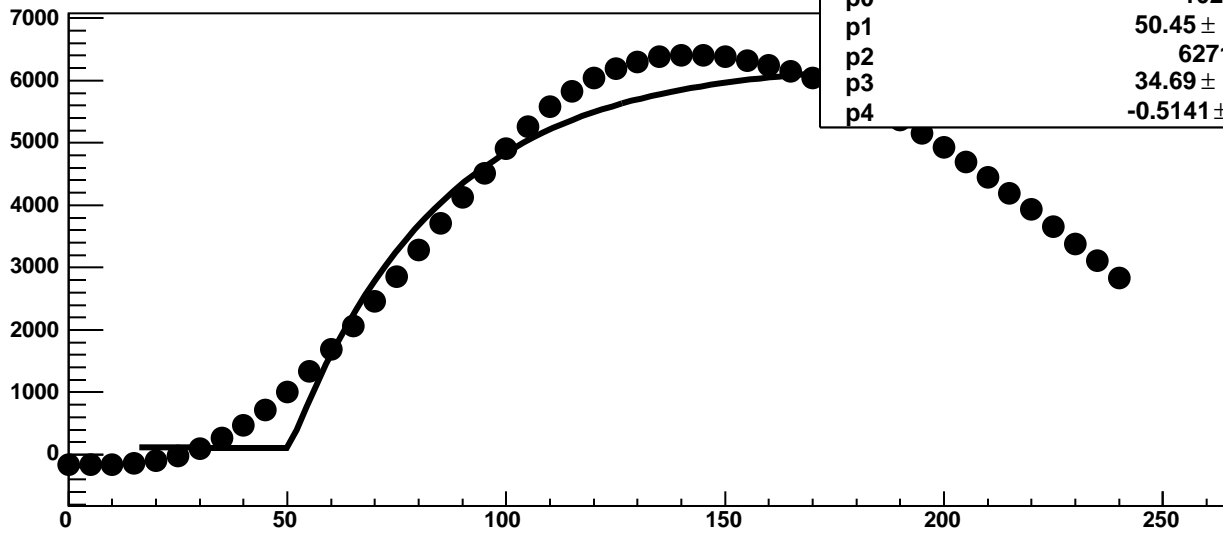
Chip 7, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

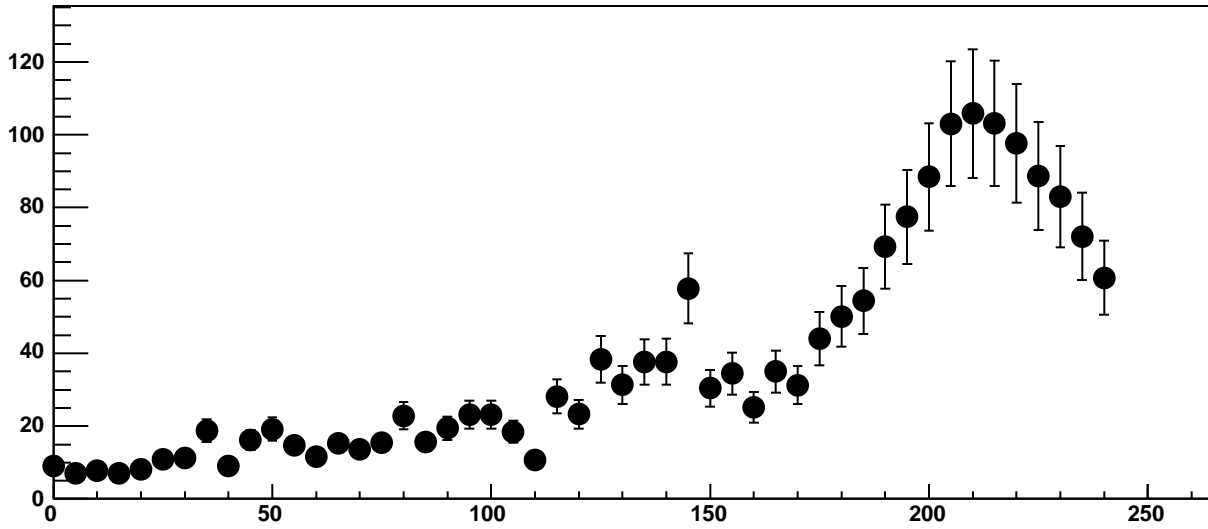


Chip 7, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

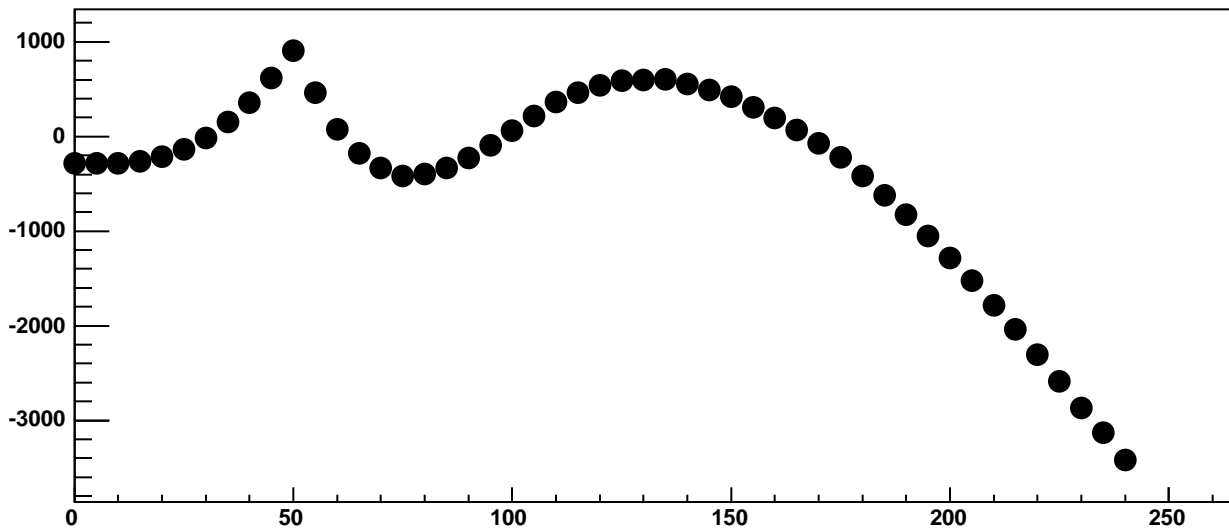


χ^2 / ndf	4.528e+05 / 41
p0	102 ± 1.529
p1	50.45 ± 0.01896
p2	6271 ± 8.47
p3	34.69 ± 0.05609
p4	-0.5141 ± 0.0536

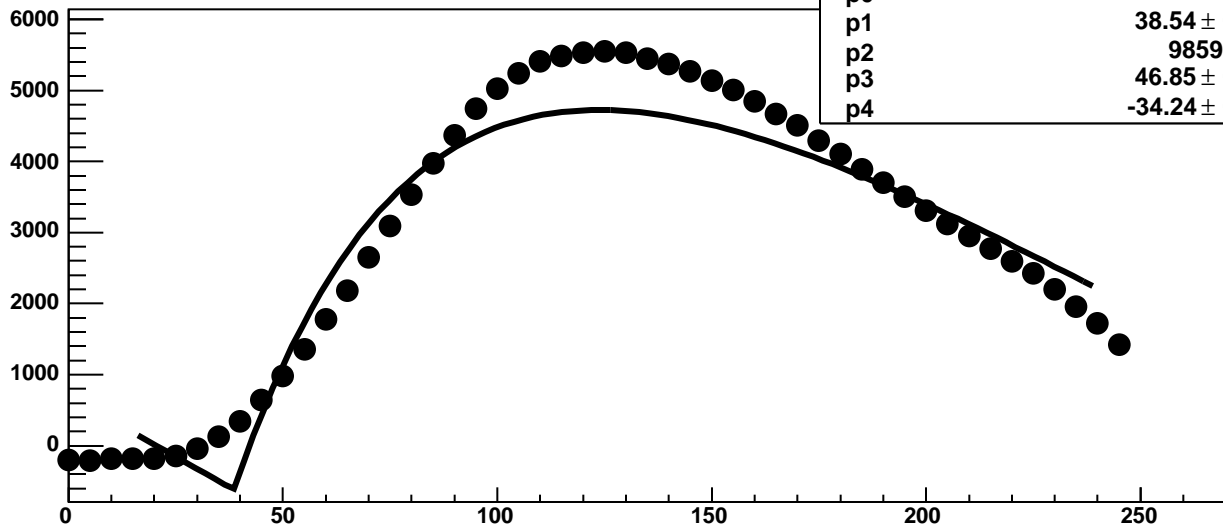
Chip 7, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



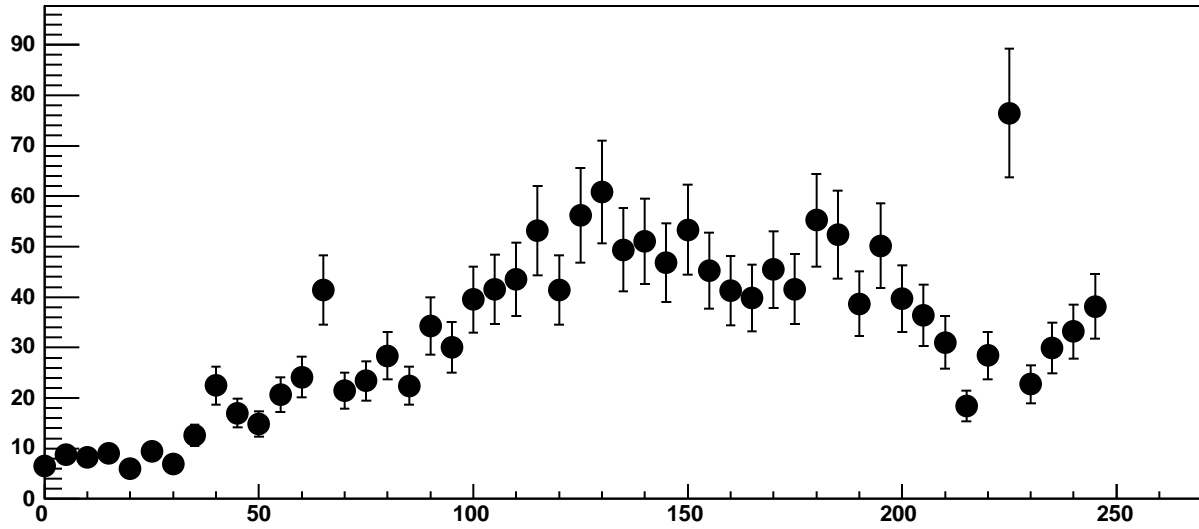
Chip 7, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold



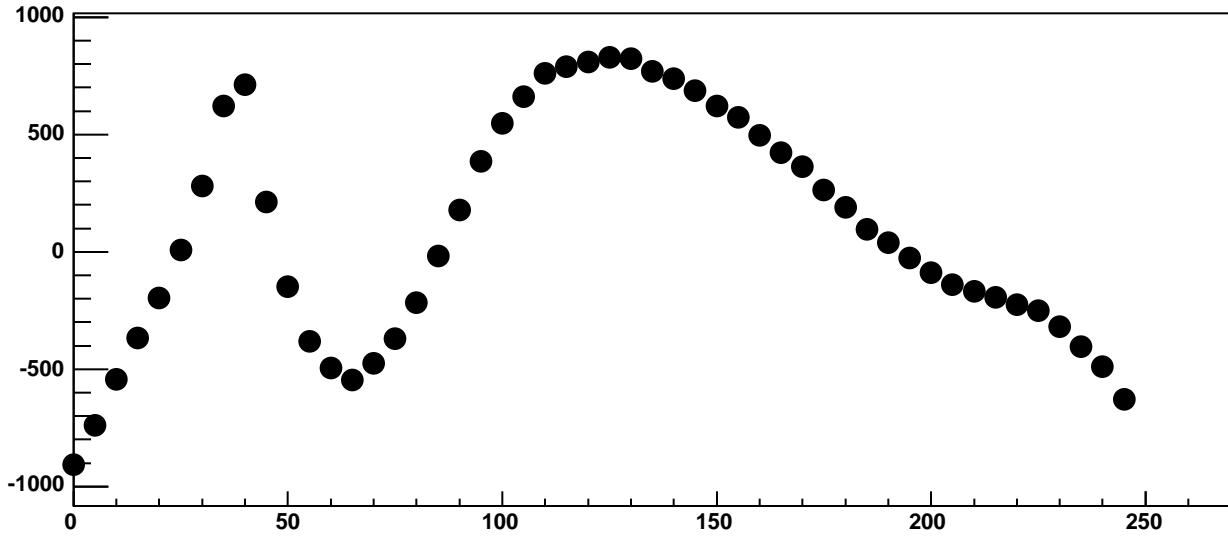
Chip 7, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold



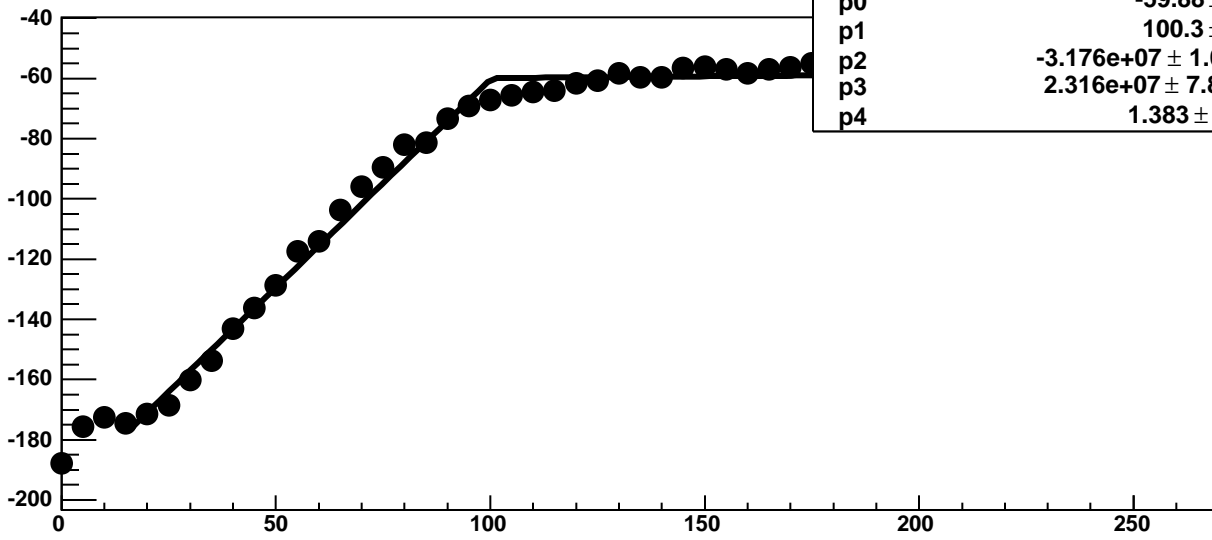
Chip 7, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold

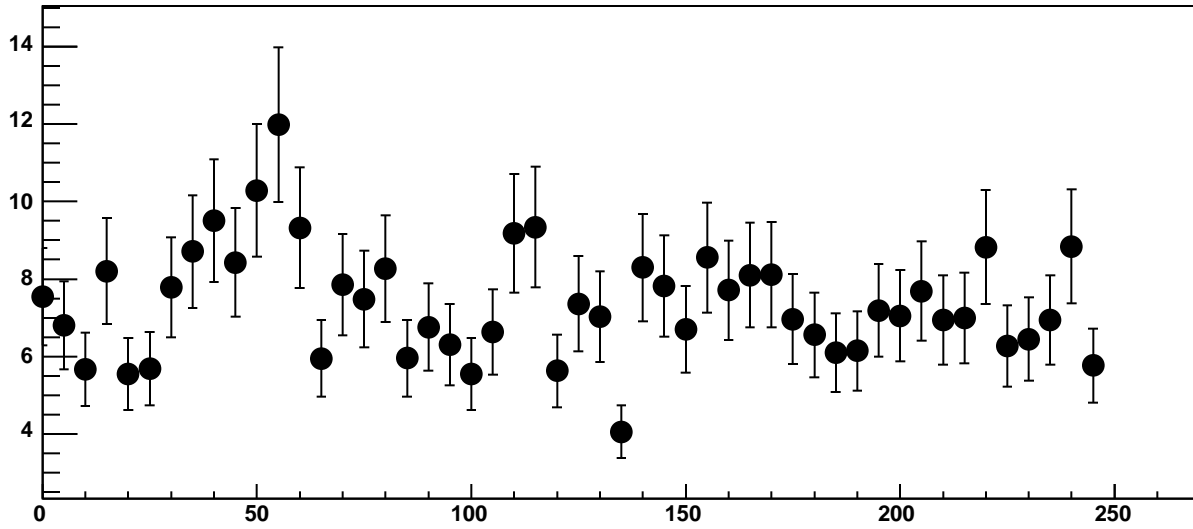


Chip 7, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

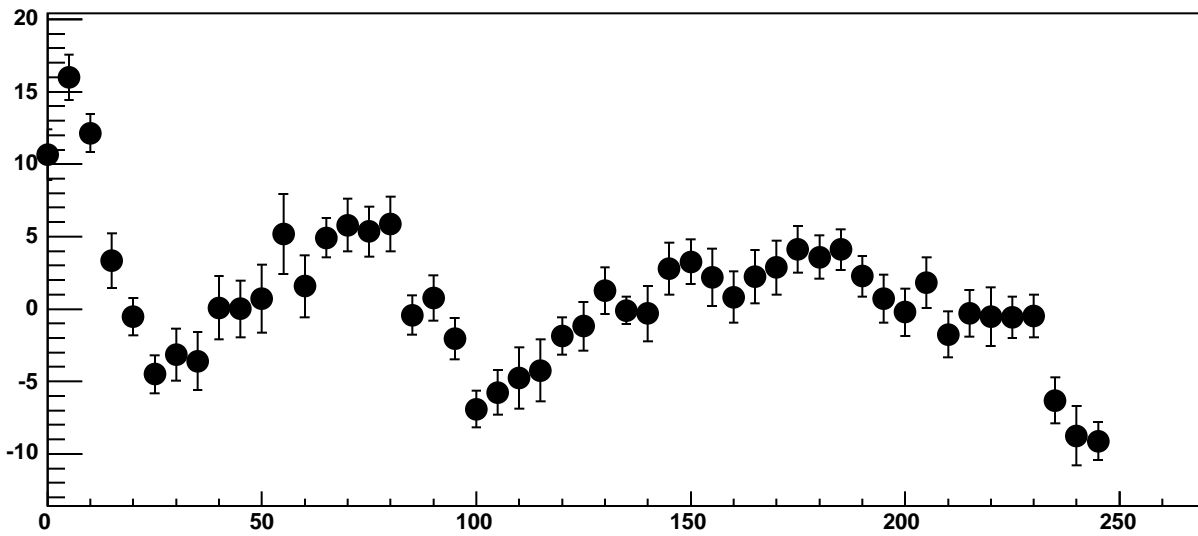


χ^2 / ndf	199.4 / 41
p0	-59.88 ± 0.6023
p1	100.3 ± 0.6487
p2	-3.176e+07 ± 1.054e+06
p3	2.316e+07 ± 7.837e+05
p4	1.383 ± 0.01312

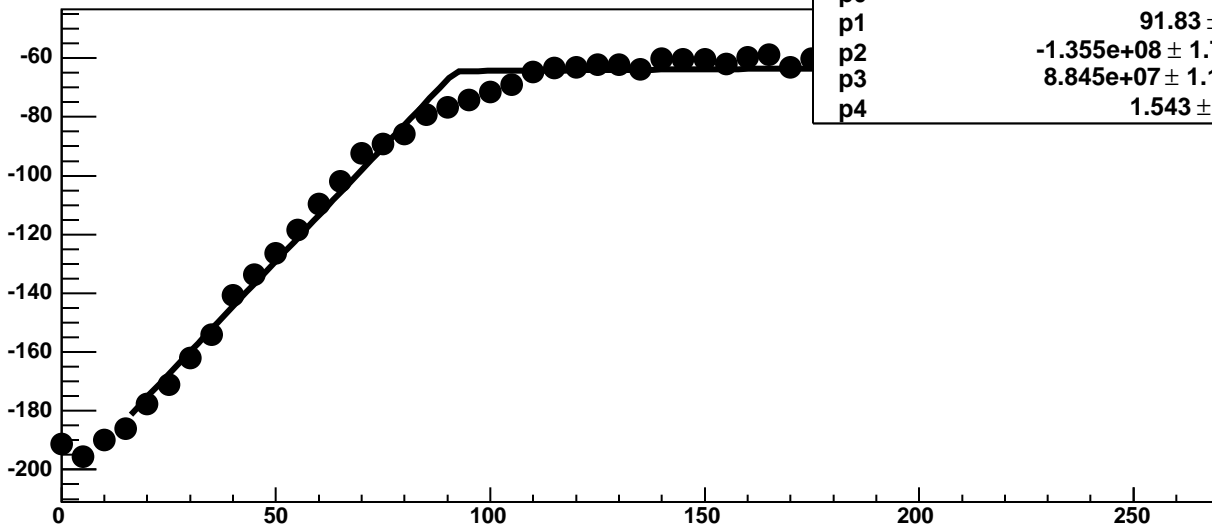
Chip 7, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold

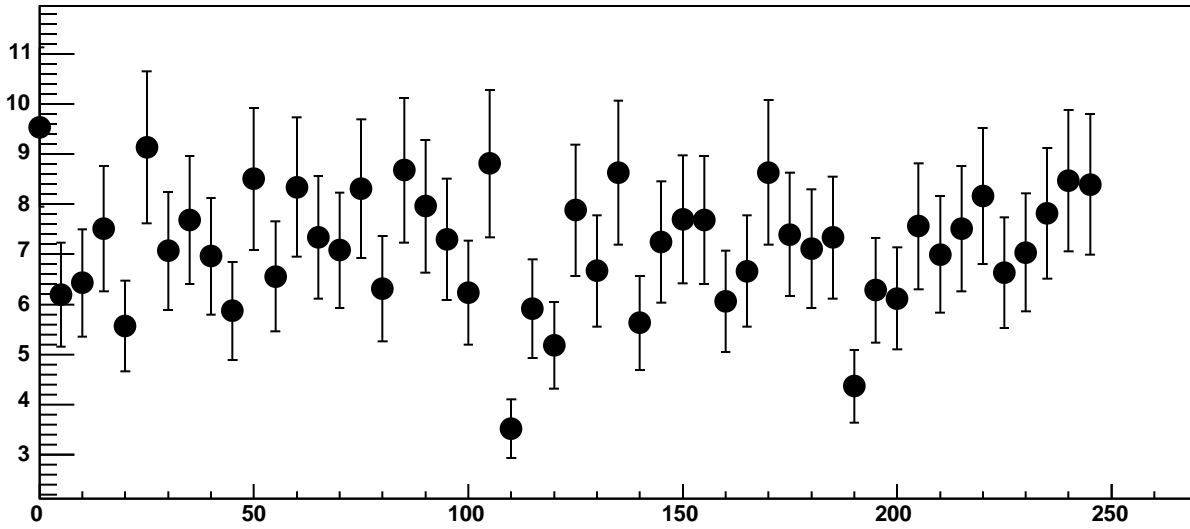


Chip 7, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

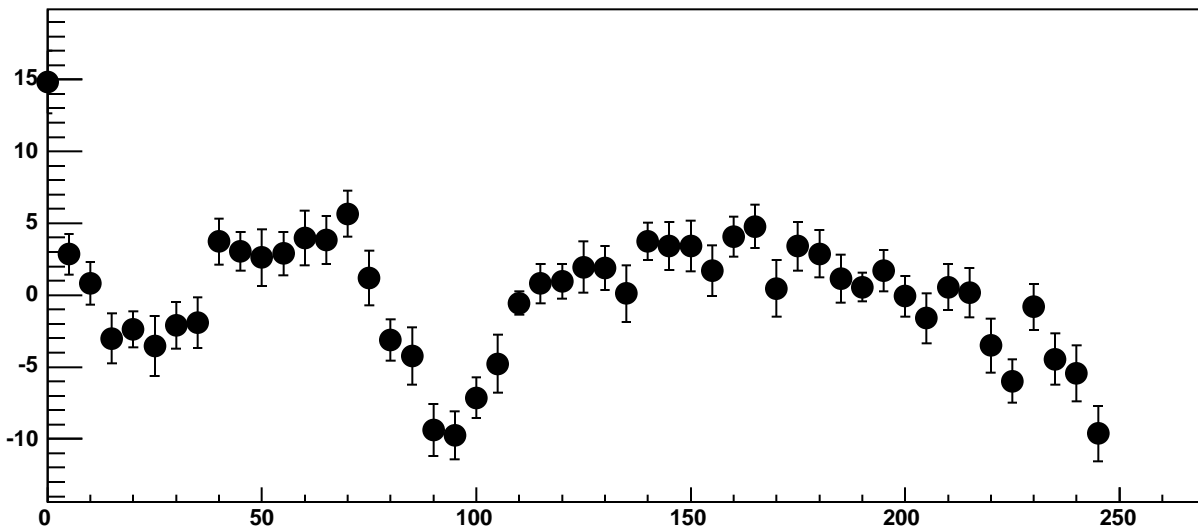


χ^2 / ndf	234.8 / 41
p0	-64.43 ± 0.5272
p1	91.83 ± 0.6594
p2	$-1.355\text{e}+08 \pm 1.711\text{e}+06$
p3	$8.845\text{e}+07 \pm 1.151\text{e}+05$
p4	1.543 ± 0.01841

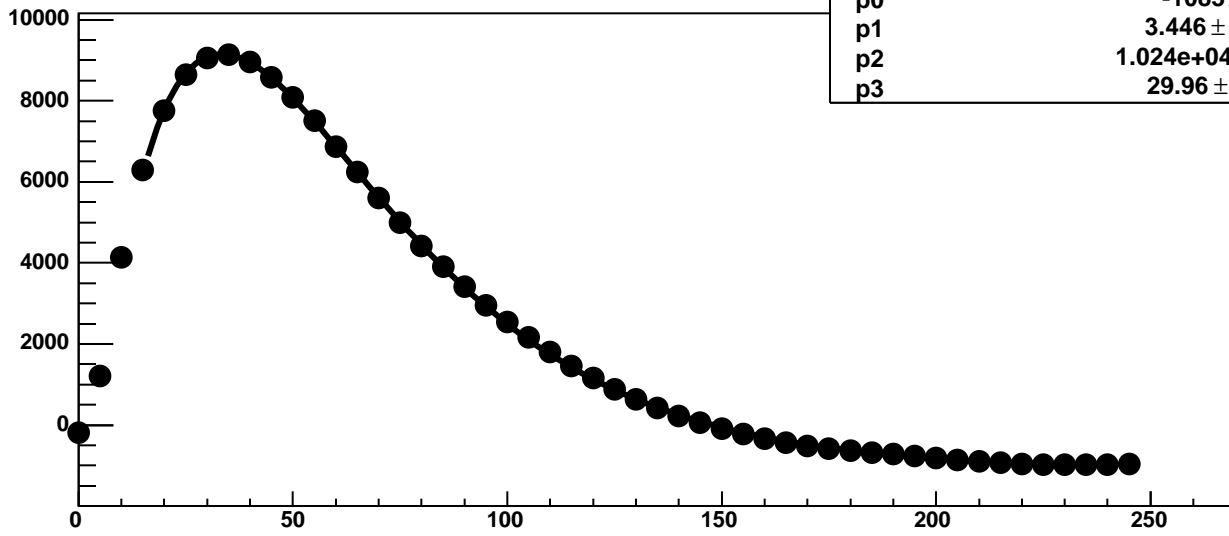
Chip 7, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

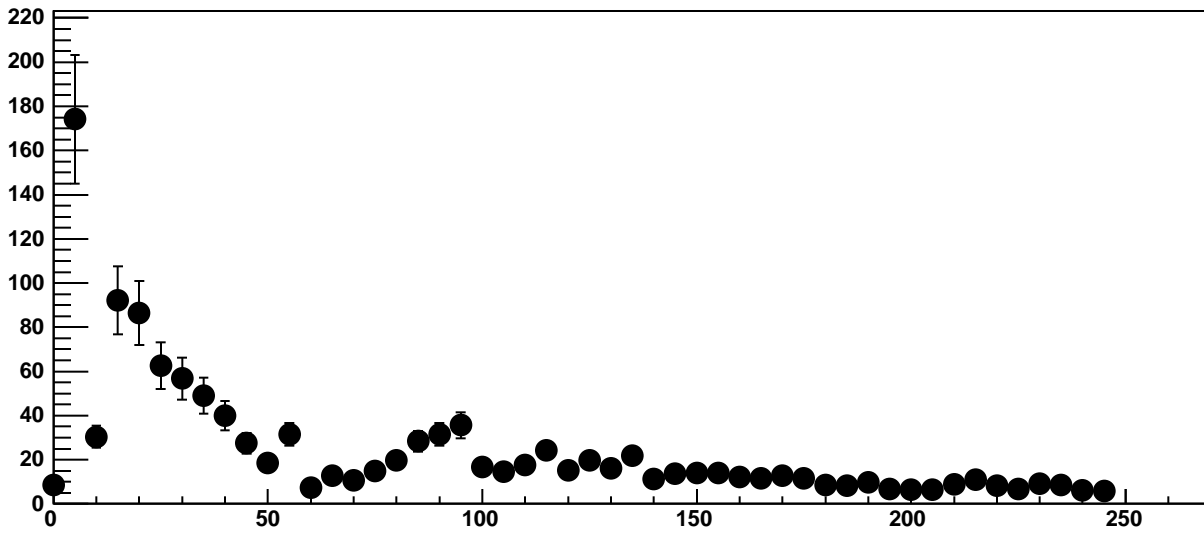


Chip 7, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold

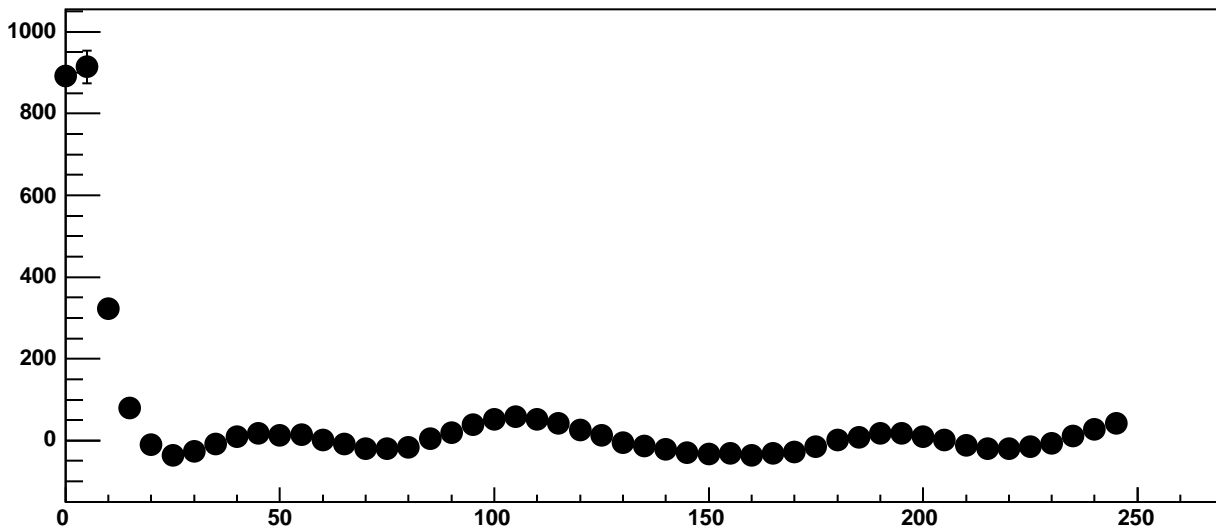


χ^2 / ndf	2740 / 42
p0	-1083 ± 0.6821
p1	3.446 ± 0.03255
p2	1.024e+04 ± 3.473
p3	29.96 ± 0.01201

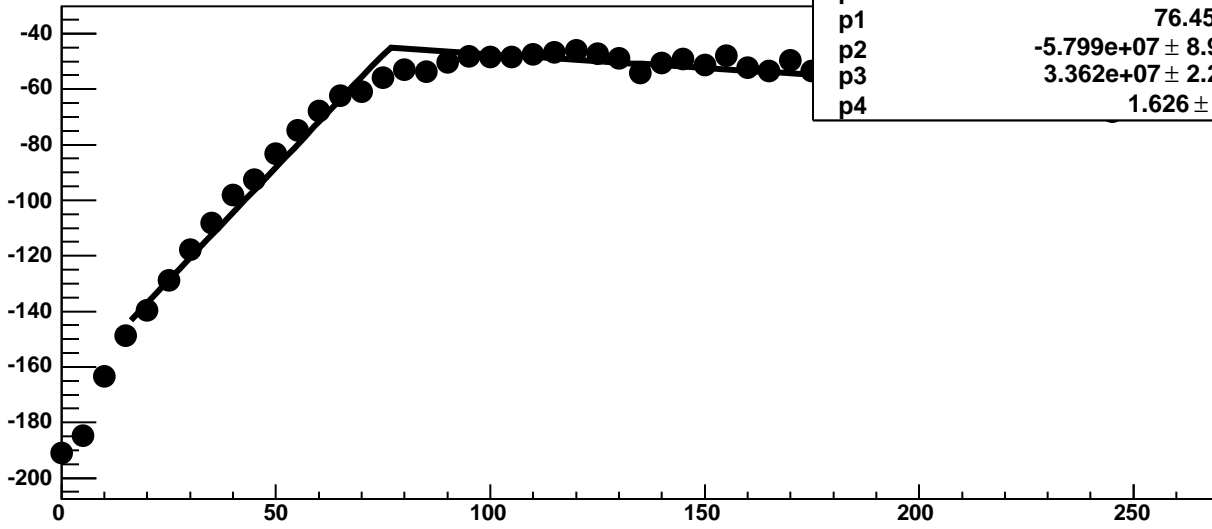
Chip 7, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold

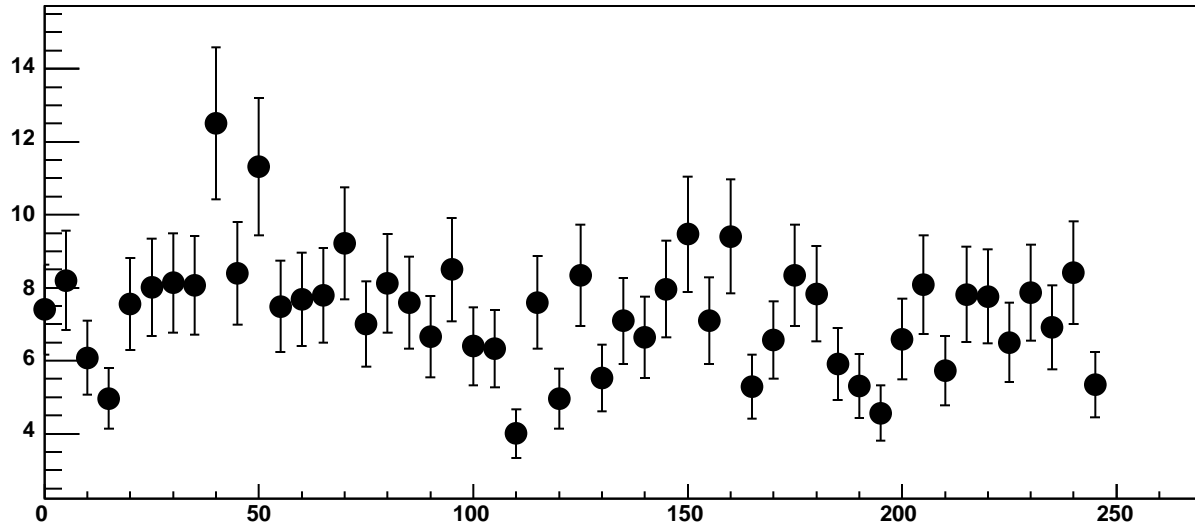


Chip 7, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold

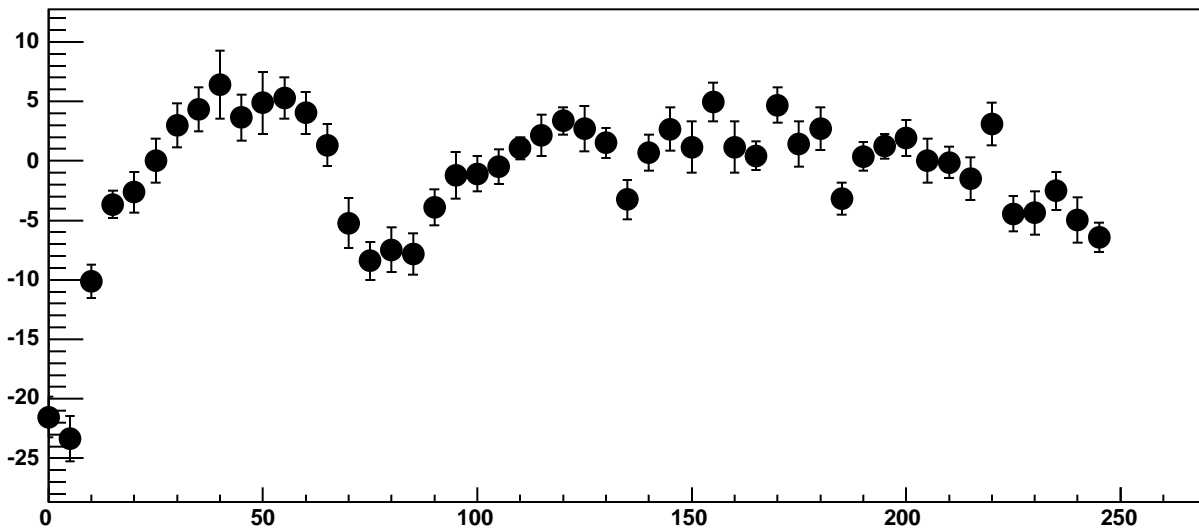


χ^2 / ndf	206 / 41
p0	-45.13 ± 0.5074
p1	76.45 ± 0.634
p2	$-5.799\text{e}+07 \pm 8.977\text{e}+05$
p3	$3.362\text{e}+07 \pm 2.268\text{e}+05$
p4	1.626 ± 0.02356

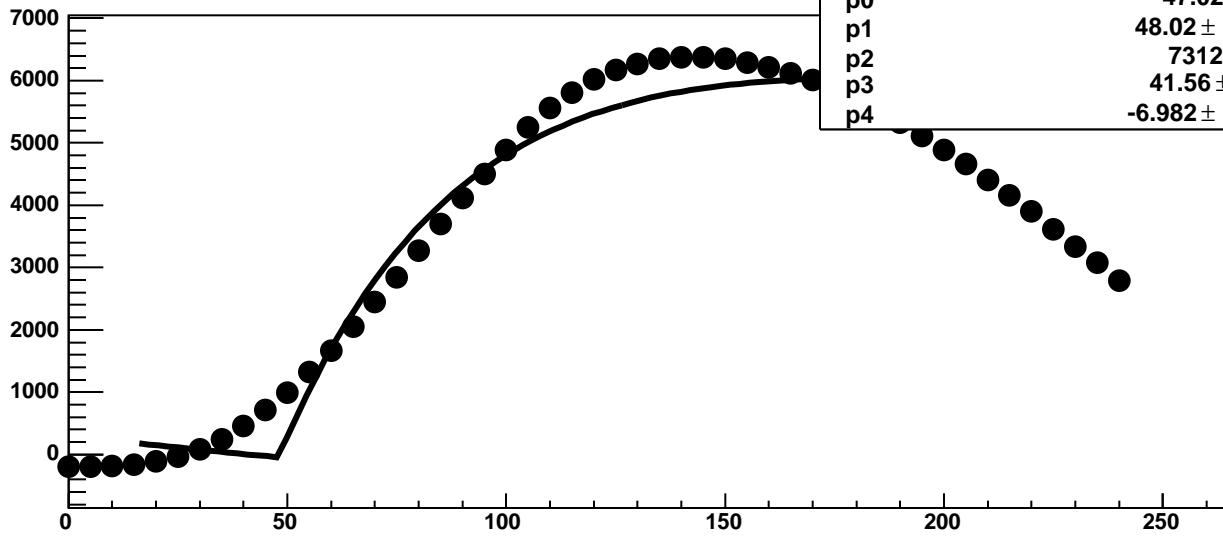
Chip 7, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

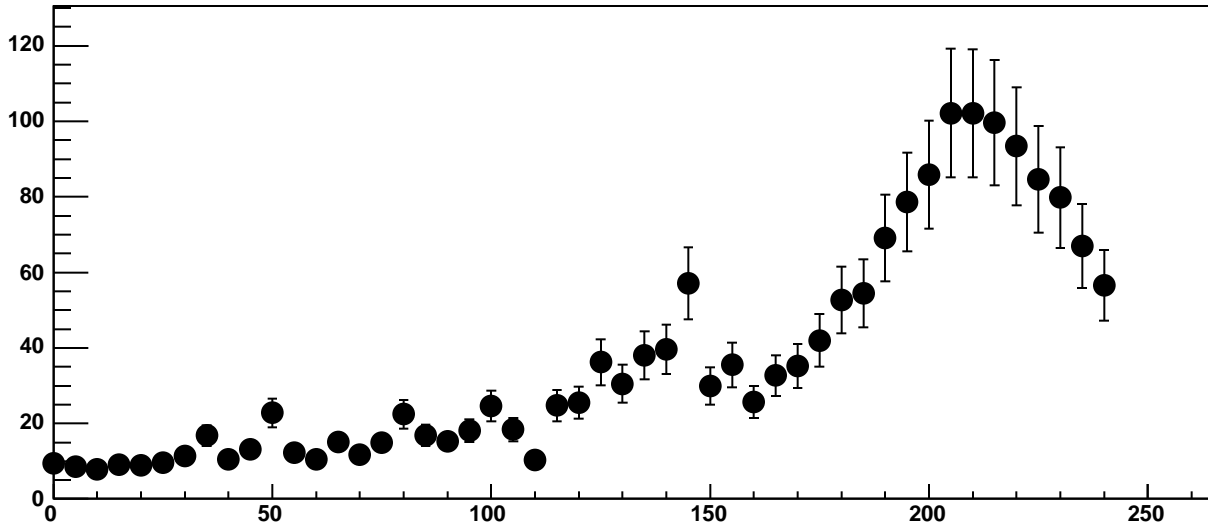


Chip 7, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

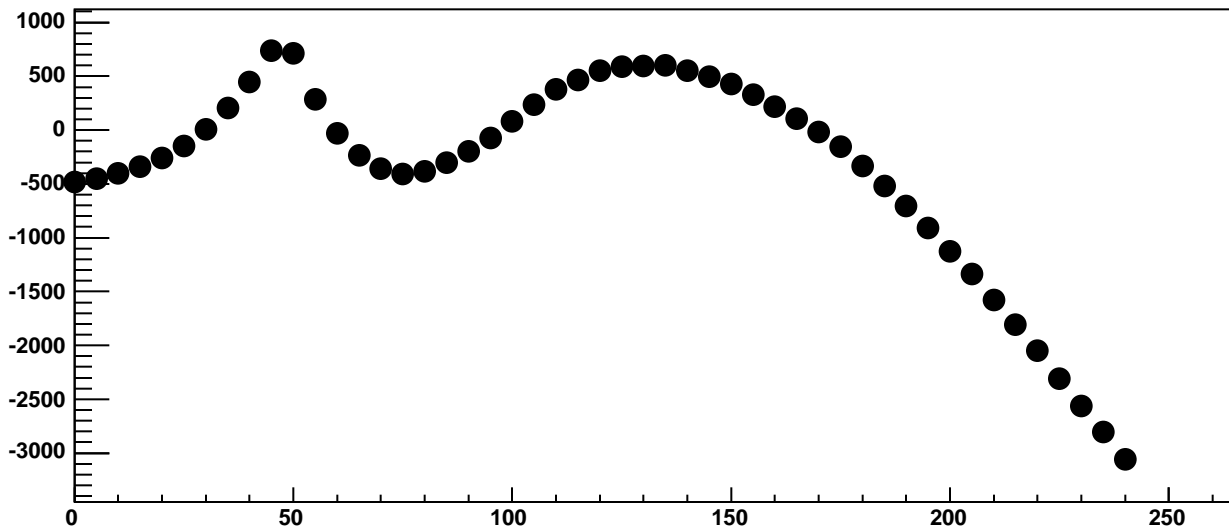


χ^2 / ndf	4.544e+05 / 41
p0	-47.02 ± 1.504
p1	48.02 ± 0.01757
p2	7312 ± 10.36
p3	41.56 ± 0.0633
p4	-6.982 ± 0.06099

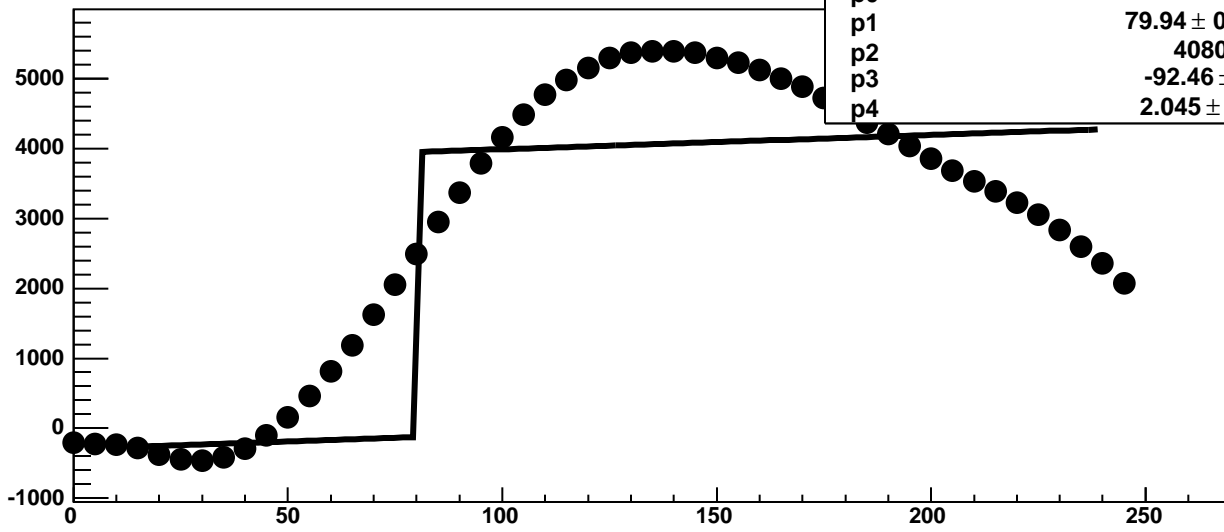
Chip 7, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

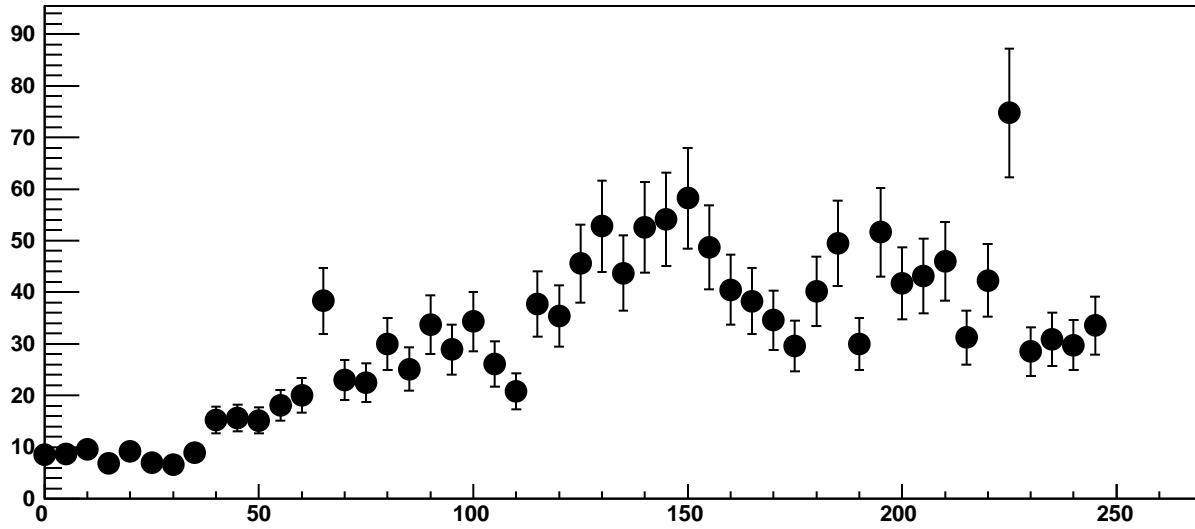


Chip 7, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold

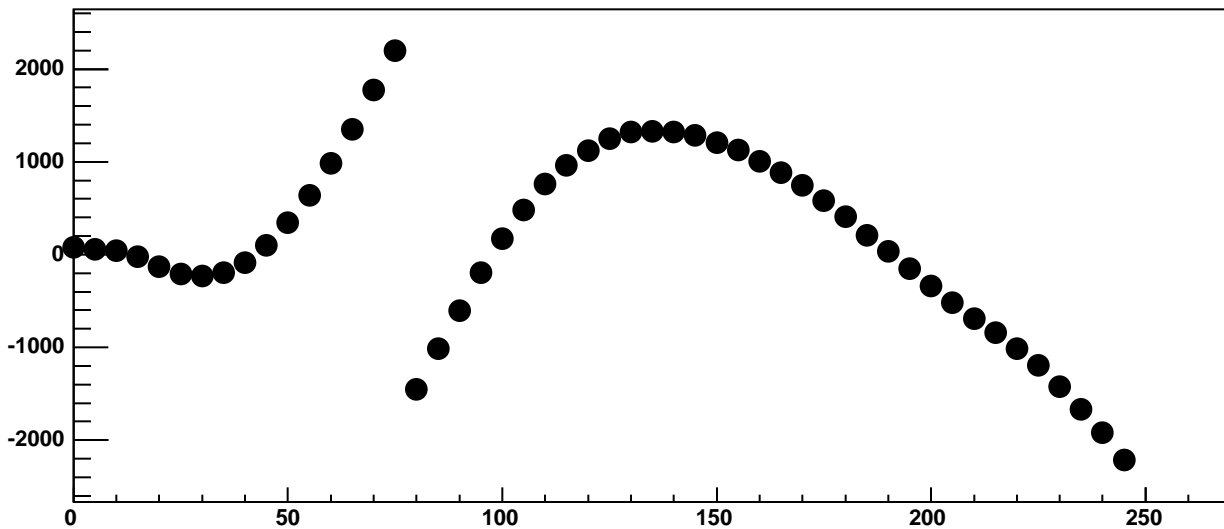


χ^2 / ndf	9.452e+05 / 41
p0	-128.4 ± 1.582
p1	79.94 ± 0.000874
p2	4080 ± 3.506
p3	-92.46 ± 0.0765
p4	2.045 ± 0.02755

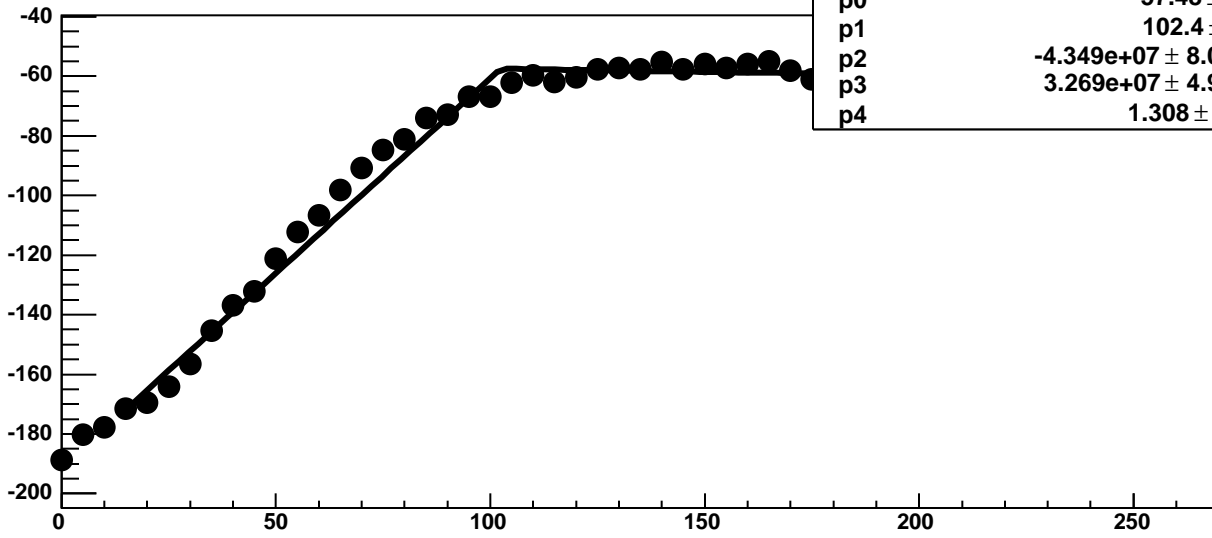
Chip 7, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

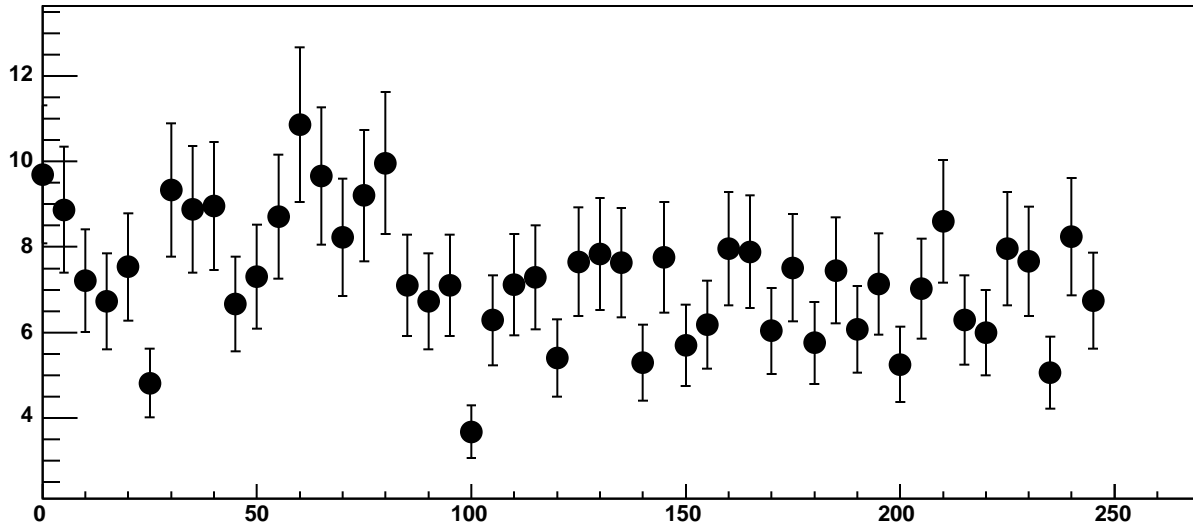


Chip 7, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold

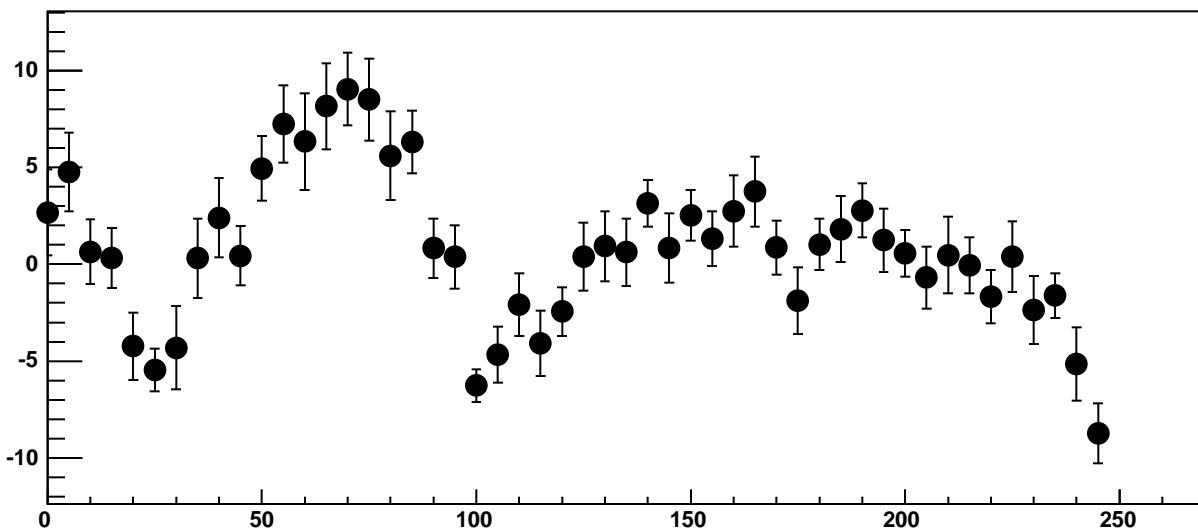


χ^2 / ndf	254.8 / 41
p0	-57.48 ± 0.5674
p1	102.4 ± 0.6442
p2	$-4.349\text{e}+07 \pm 8.021\text{e}+05$
p3	$3.269\text{e}+07 \pm 4.904\text{e}+05$
p4	1.308 ± 0.01248

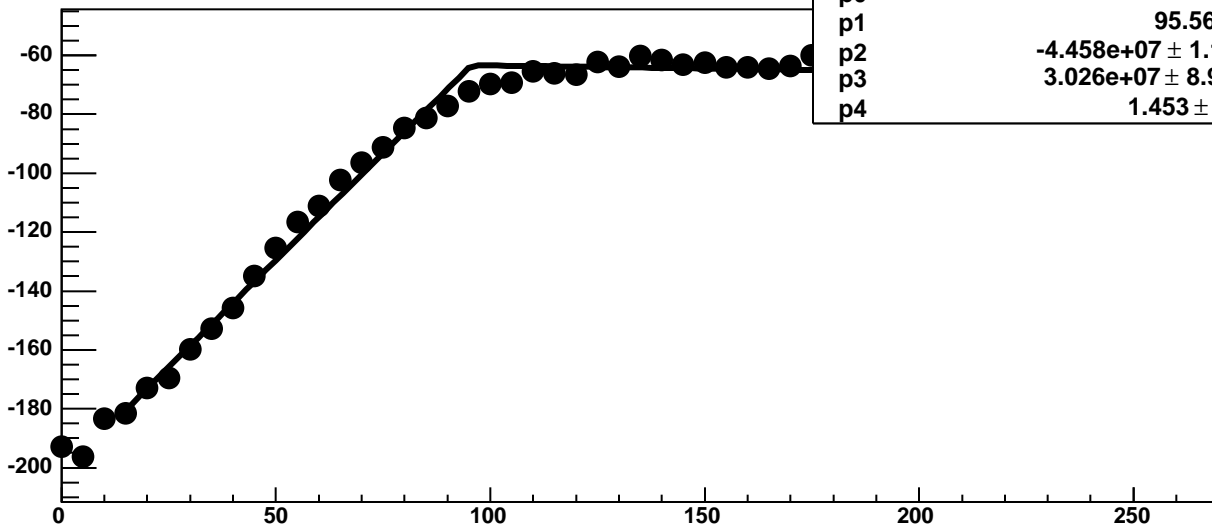
Chip 7, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold

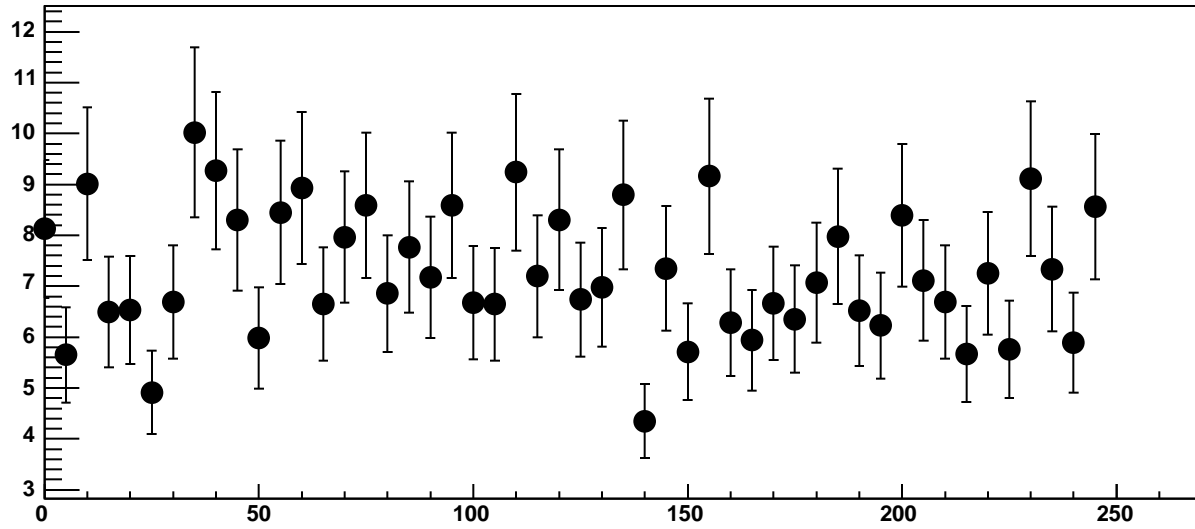


Chip 7, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

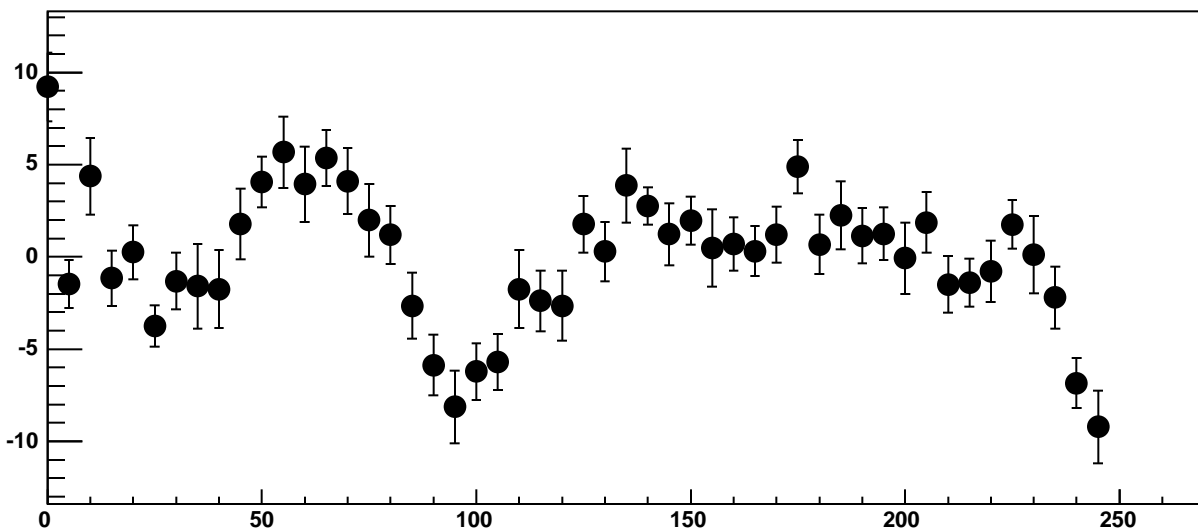


χ^2 / ndf	185 / 41
p0	-63.33 ± 0.5671
p1	95.56 ± 0.633
p2	-4.458e+07 ± 1.162e+06
p3	3.026e+07 ± 8.951e+05
p4	1.453 ± 0.01454

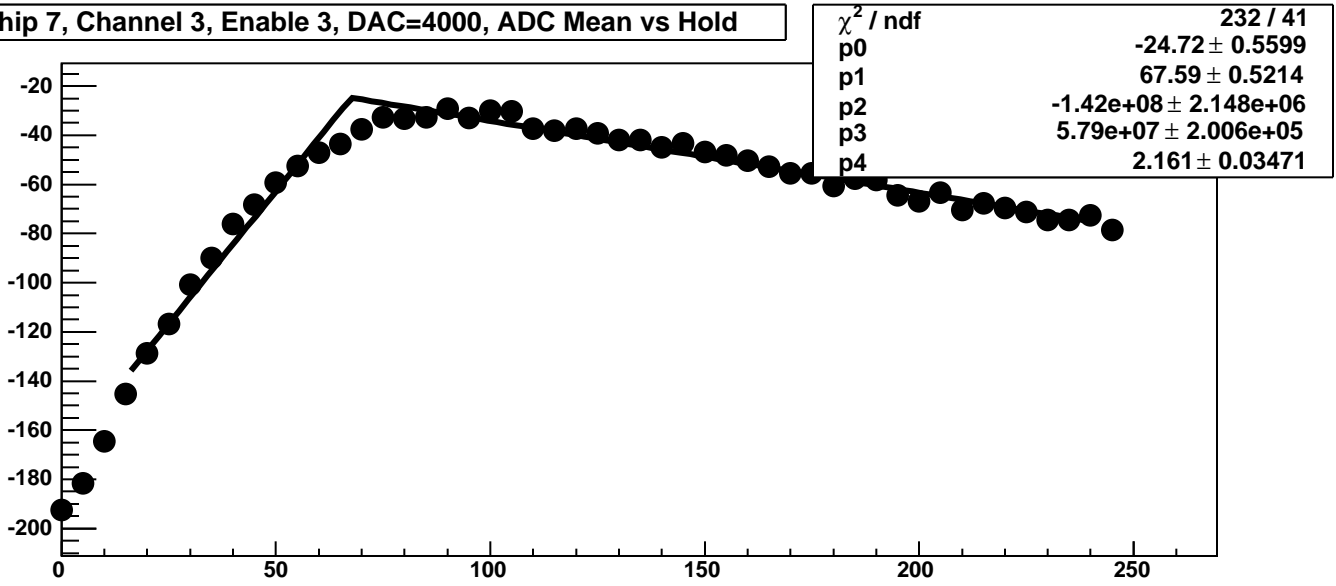
Chip 7, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



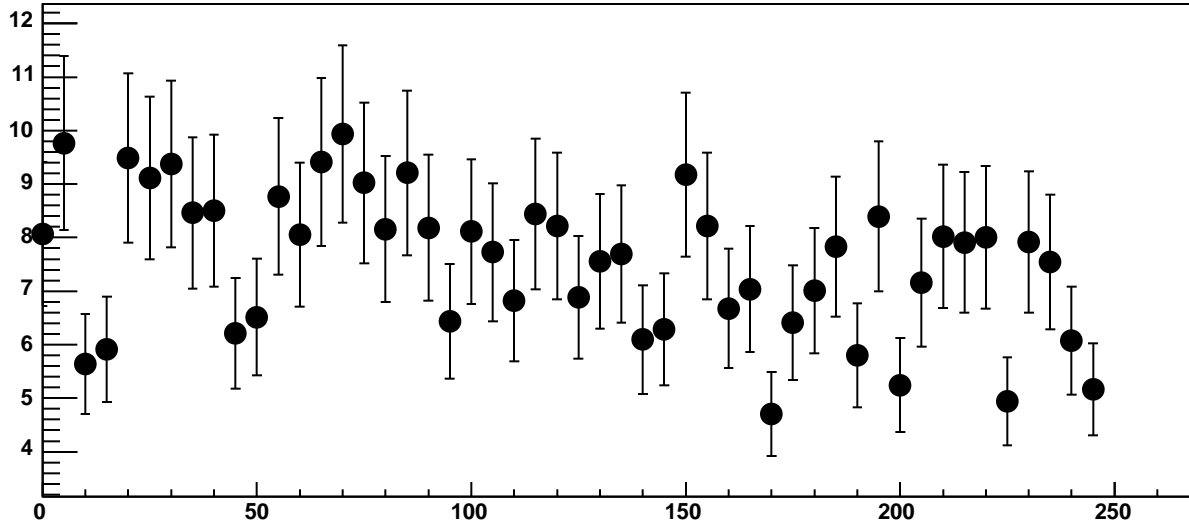
Chip 7, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold



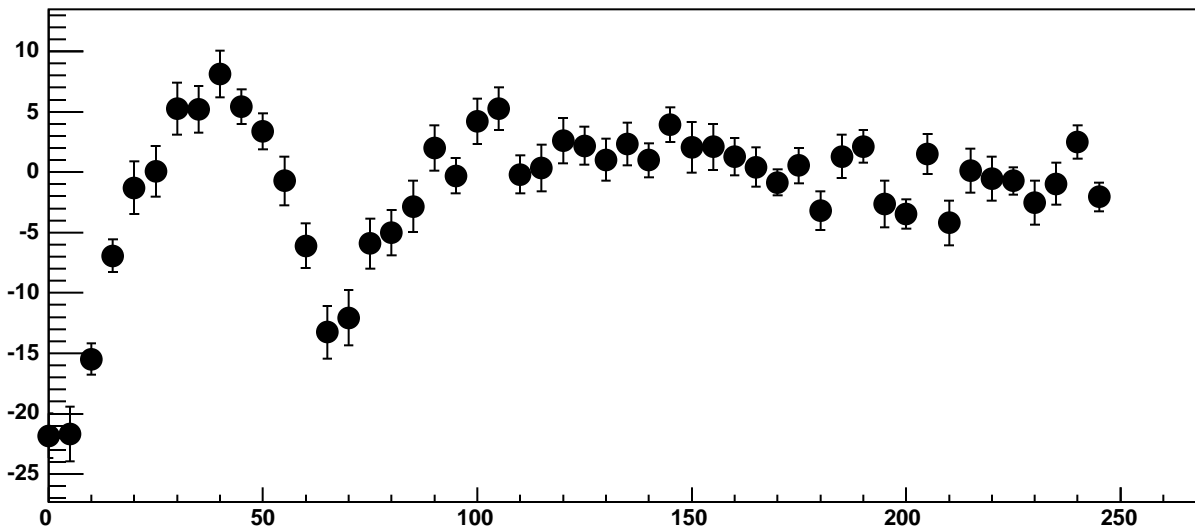
Chip 7, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold



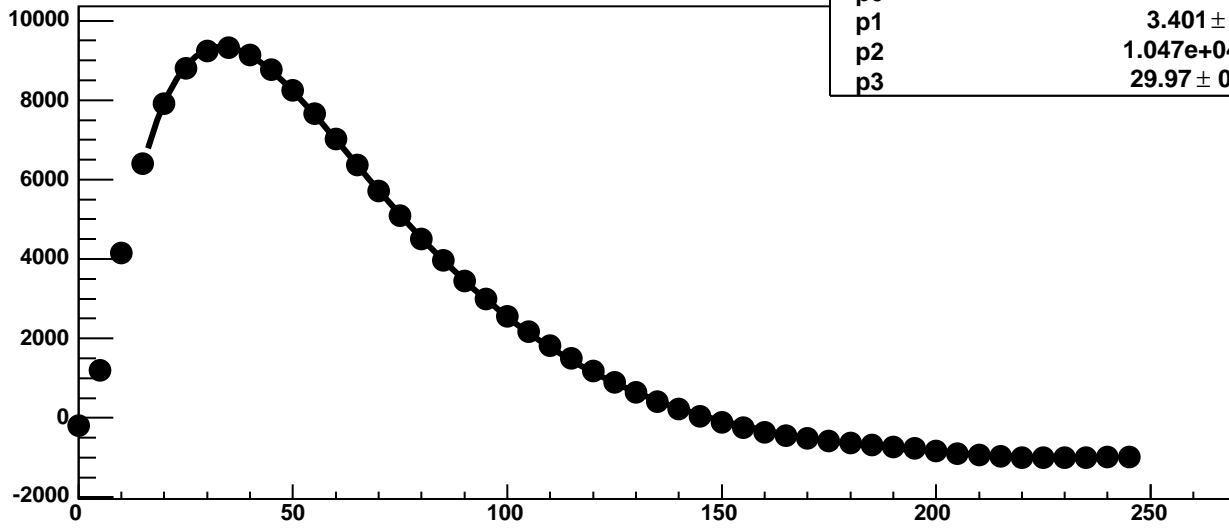
Chip 7, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

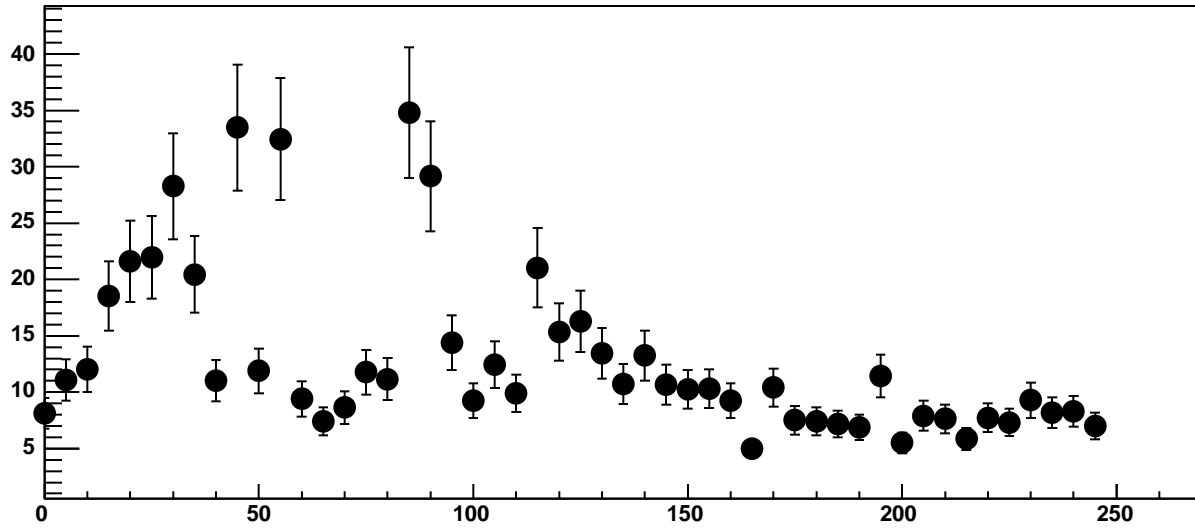


Chip 7, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold

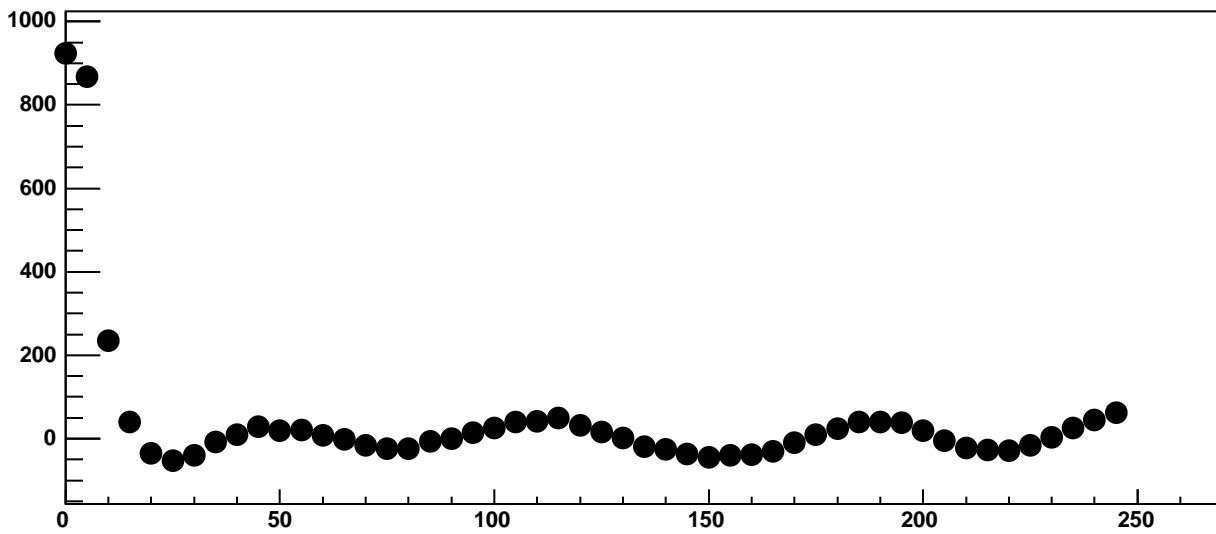


χ^2 / ndf	7065 / 42
p0	-1118 ± 0.5723
p1	3.401 ± 0.01074
p2	1.047e+04 ± 1.39
p3	29.97 ± 0.006993

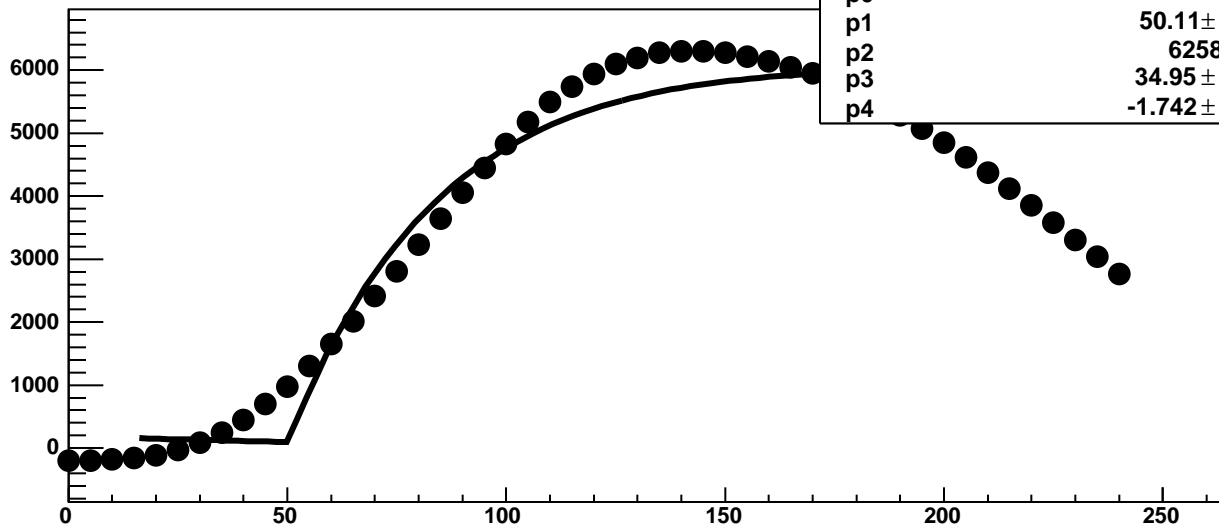
Chip 7, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold

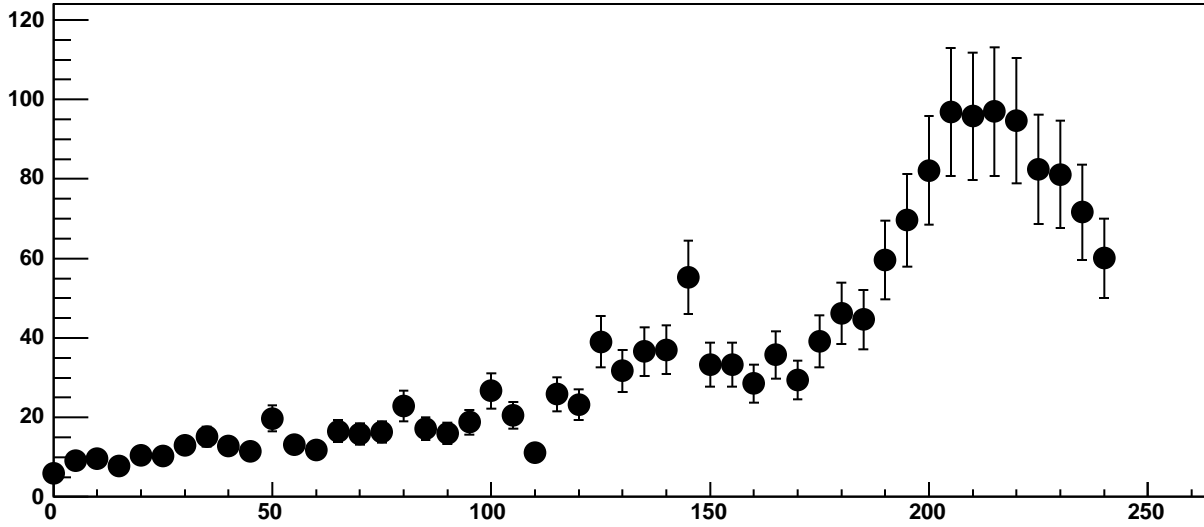


Chip 7, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold

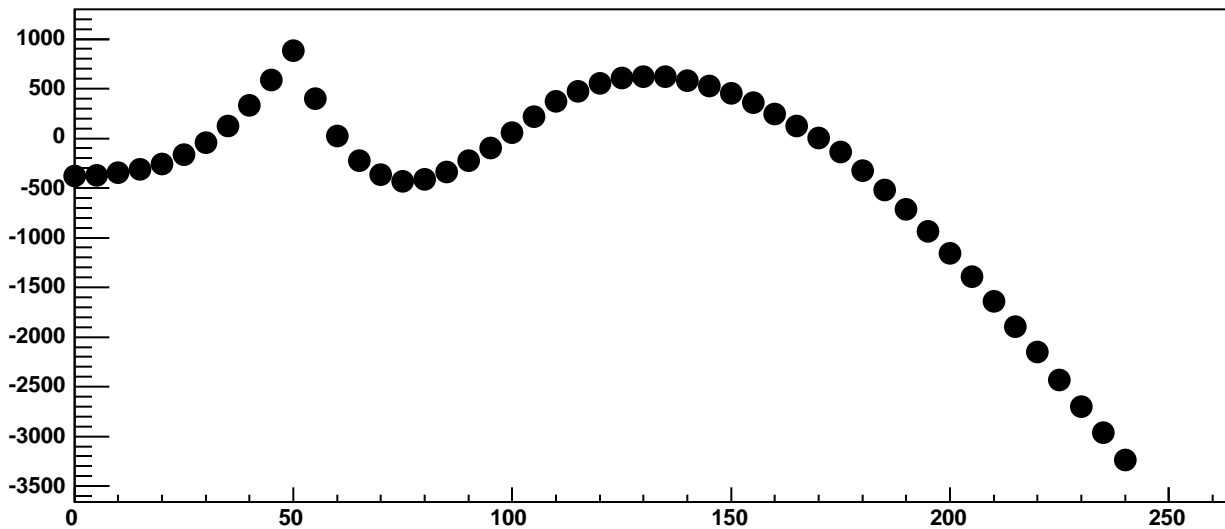


χ^2 / ndf	4.469e+05 / 41
p0	96.63 ± 1.503
p1	50.11 ± 0.01894
p2	6258 ± 8.597
p3	34.95 ± 0.05748
p4	-1.742 ± 0.05412

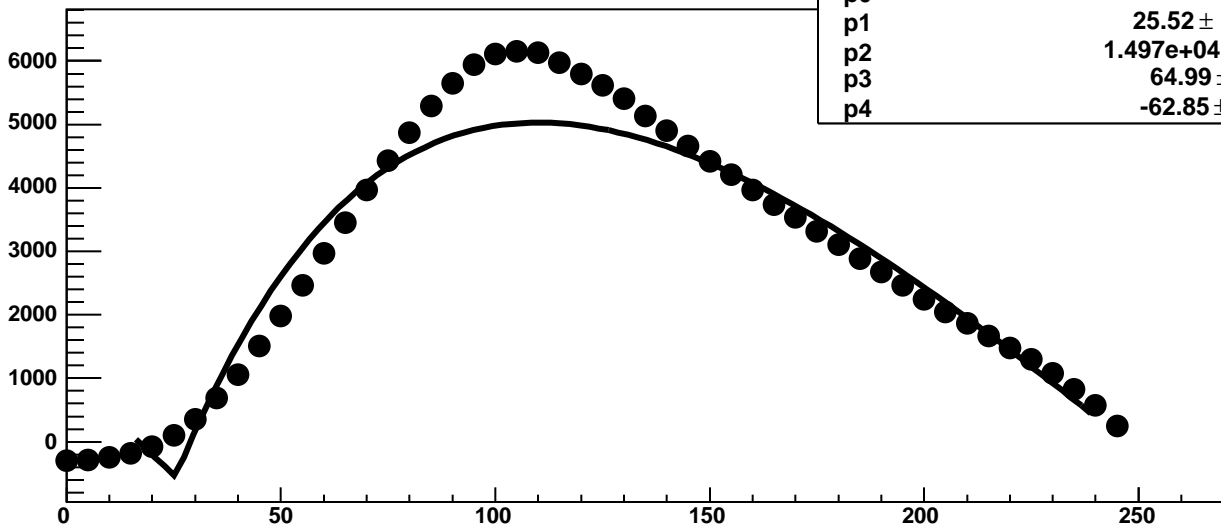
Chip 7, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold

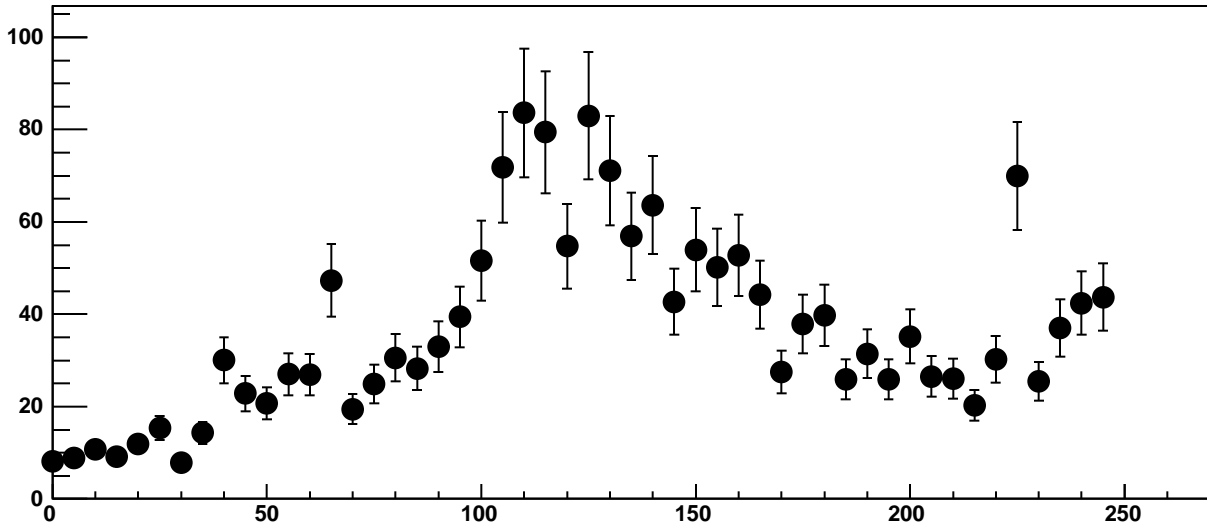


Chip 7, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

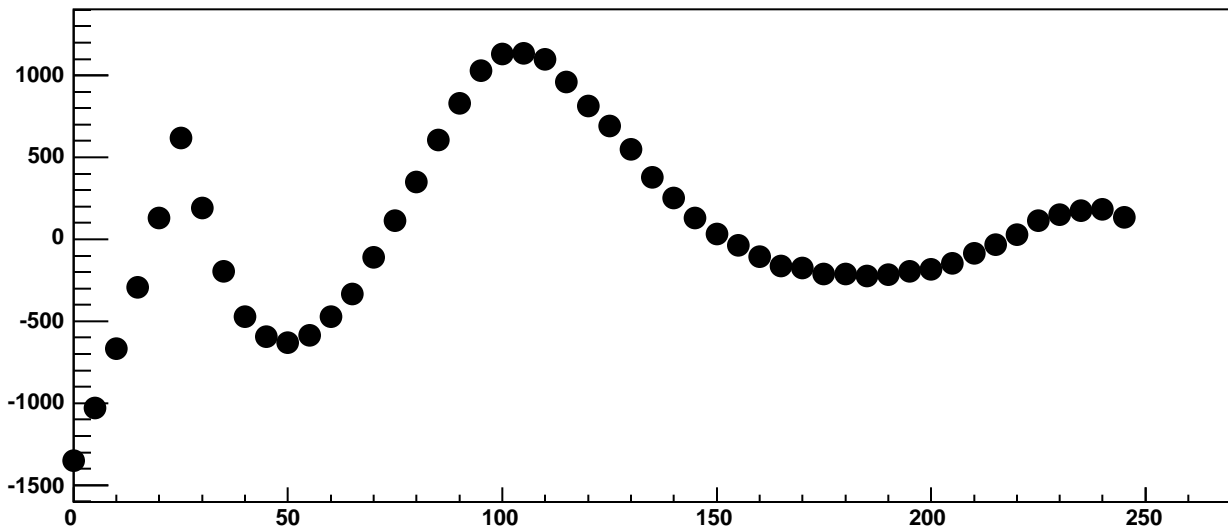


χ^2 / ndf	1.917e+05 / 41
p0	-551.3 ± 1.321
p1	25.52 ± 0.01265
p2	1.497e+04 ± 30.07
p3	64.99 ± 0.1251
p4	-62.85 ± 0.1258

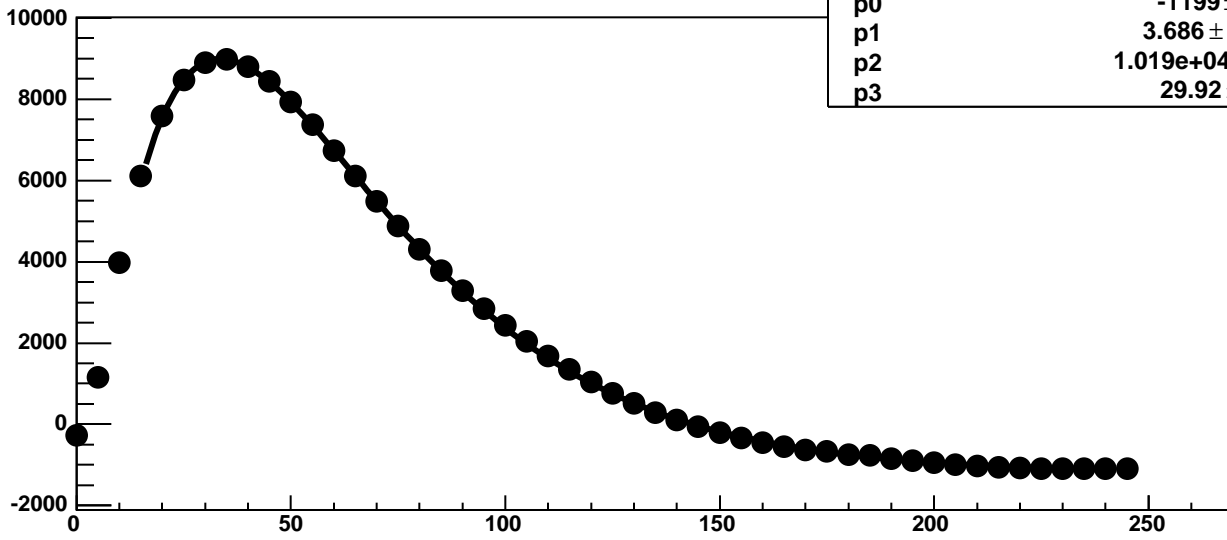
Chip 7, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

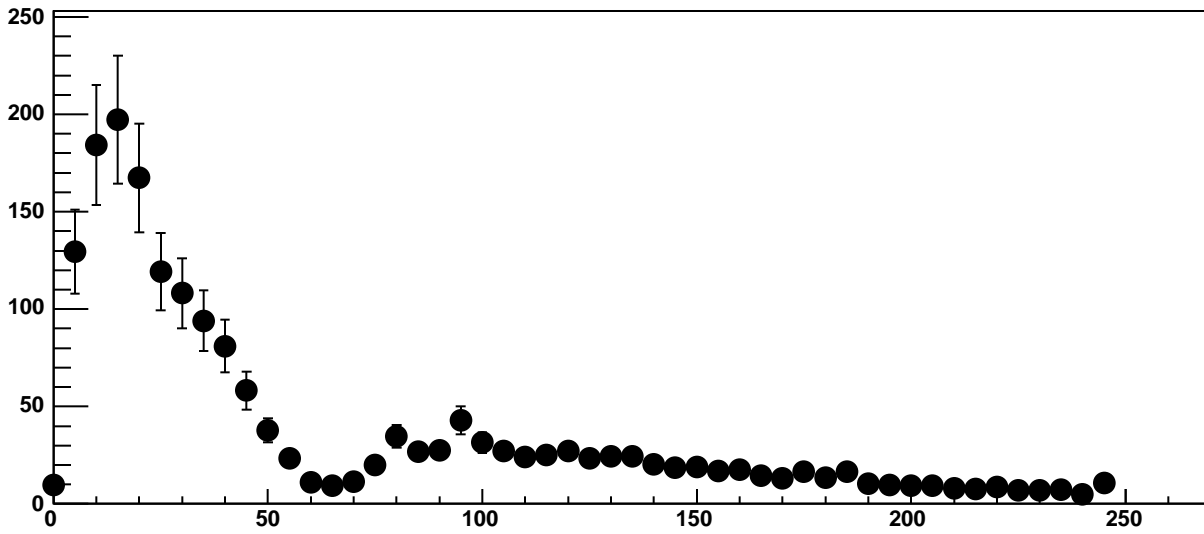


Chip 7, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold

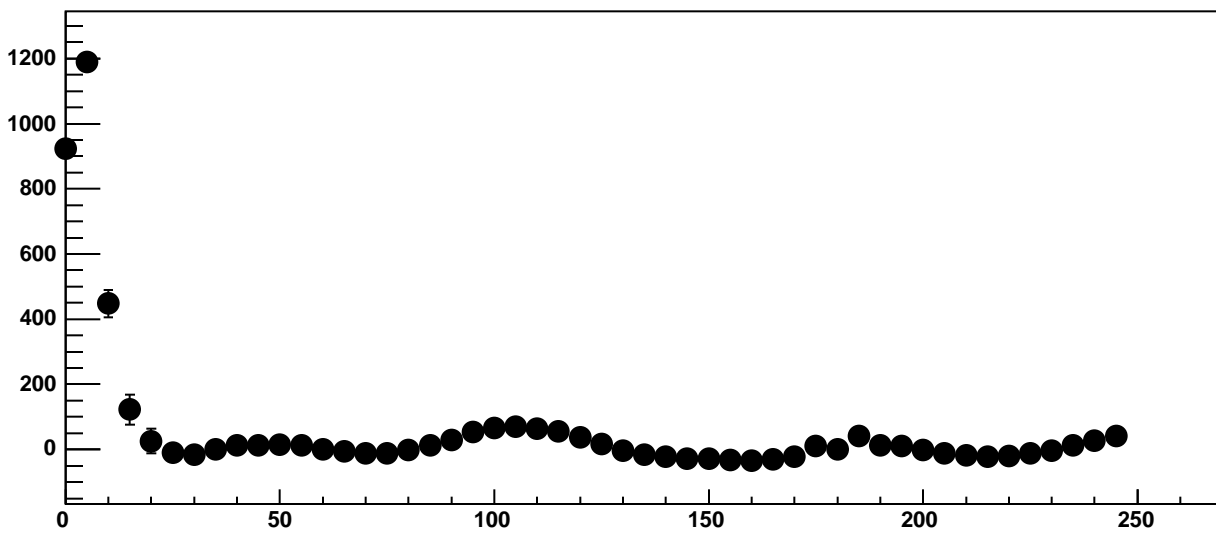


χ^2 / ndf	2186 / 42
p0	-1199 ± 0.7323
p1	3.686 ± 0.05767
p2	1.019e+04 ± 6.595
p3	29.92 ± 0.0171

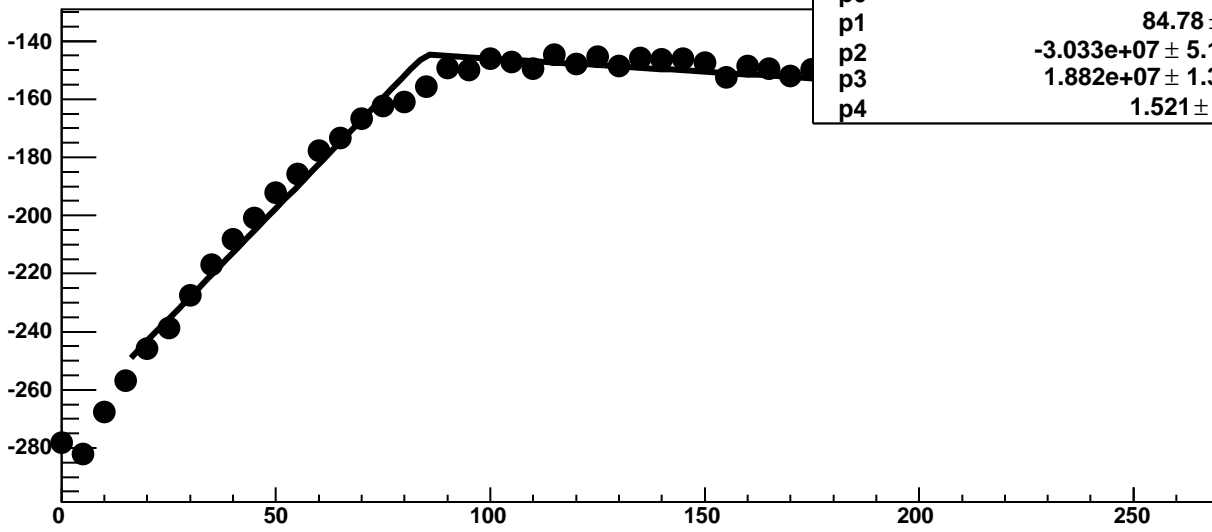
Chip 7, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold

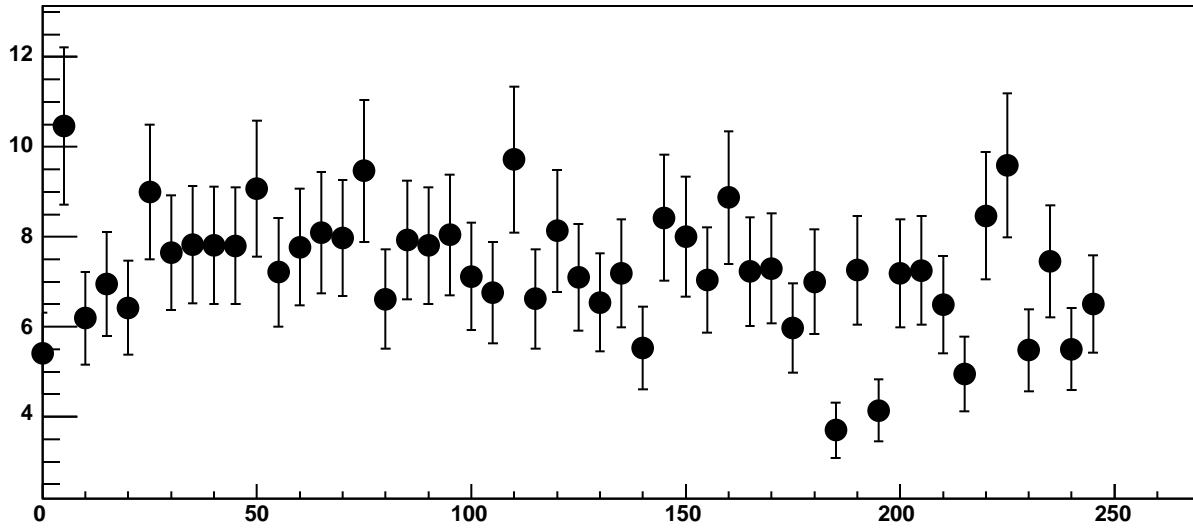


Chip 7, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

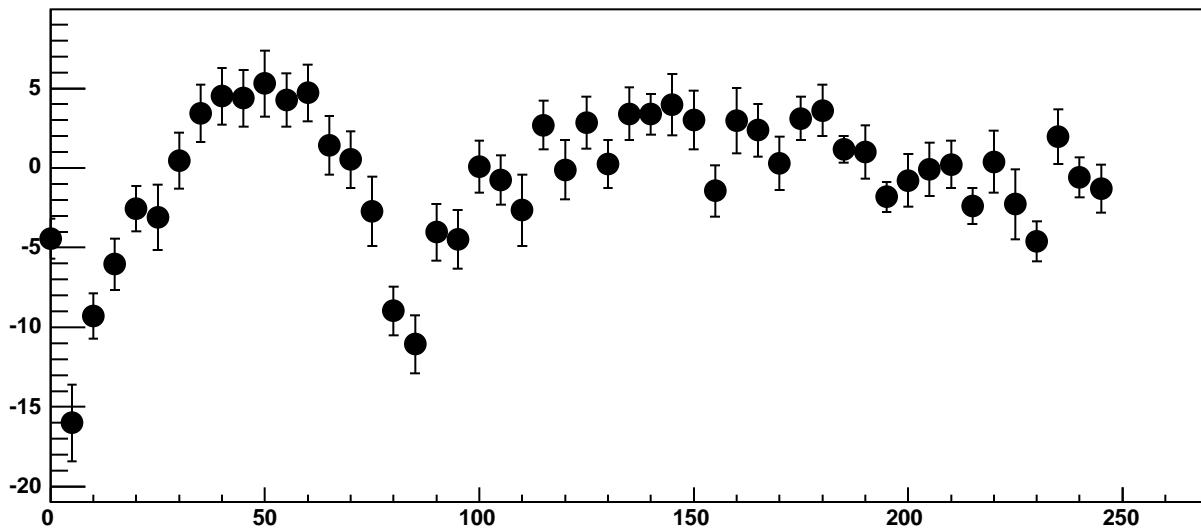


χ^2 / ndf	209.1 / 41
p0	-144.6 ± 0.5514
p1	84.78 ± 0.7077
p2	$-3.033\text{e}+07 \pm 5.128\text{e}+05$
p3	$1.882\text{e}+07 \pm 1.376\text{e}+05$
p4	1.521 ± 0.02267

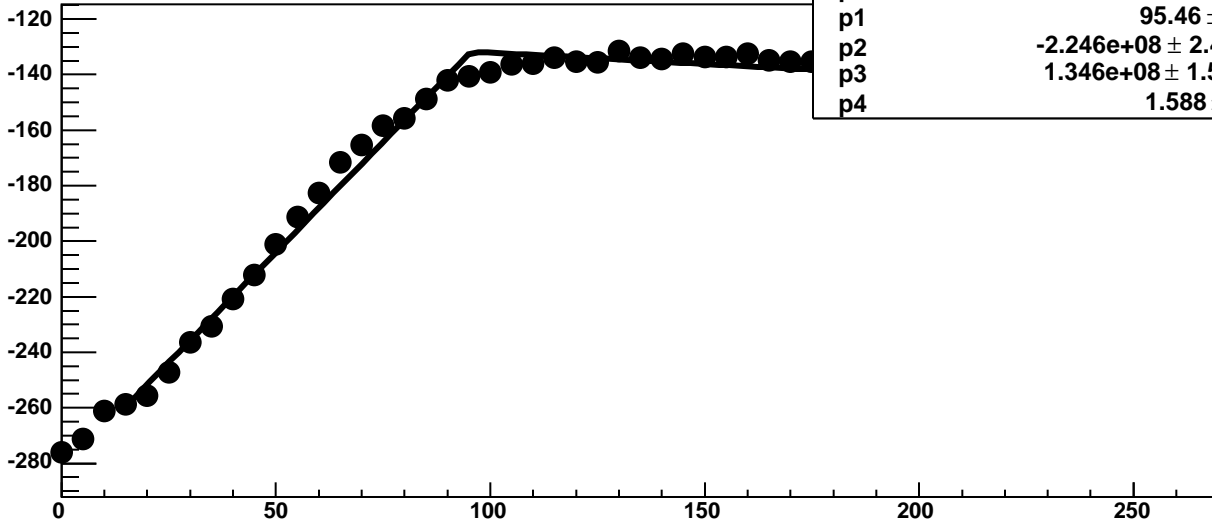
Chip 7, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

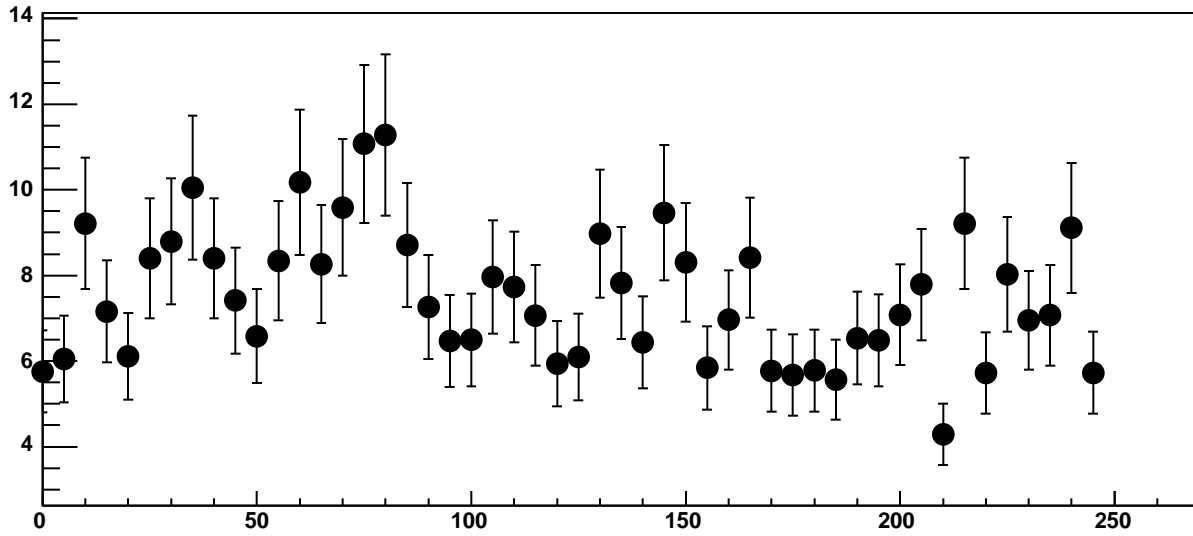


Chip 7, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

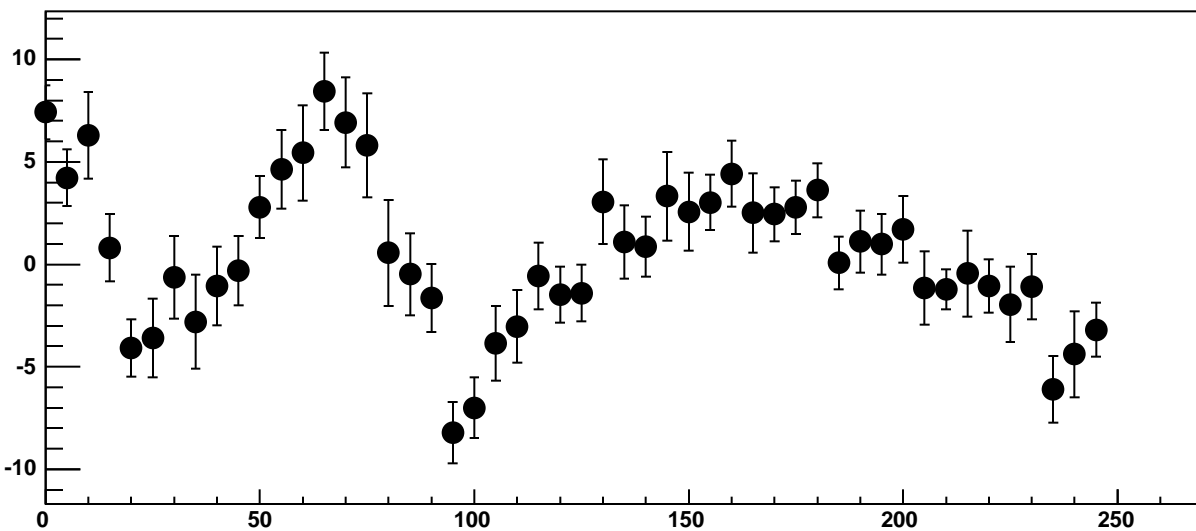


χ^2 / ndf	189 / 41
p0	-131.7 ± 0.5877
p1	95.46 ± 0.6316
p2	$-2.246\text{e}+08 \pm 2.496\text{e}+06$
p3	$1.346\text{e}+08 \pm 1.572\text{e}+05$
p4	1.588 ± 0.0171

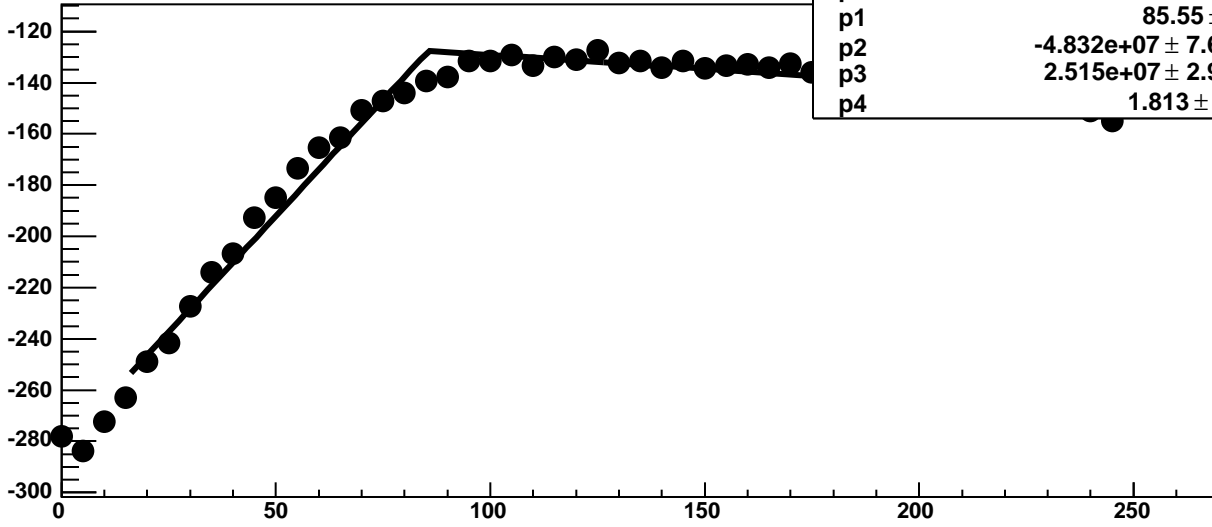
Chip 7, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

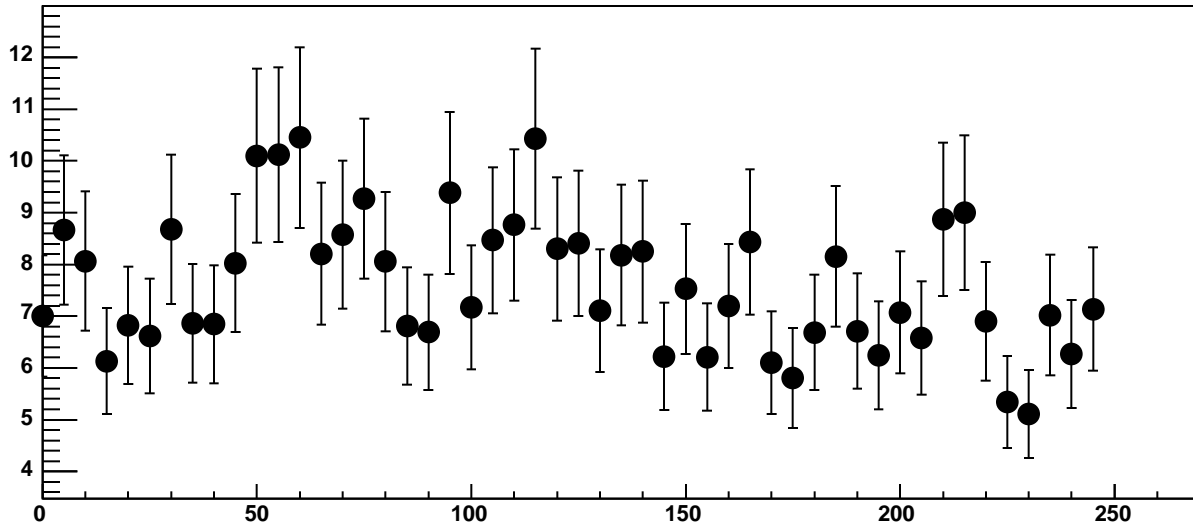


Chip 7, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold

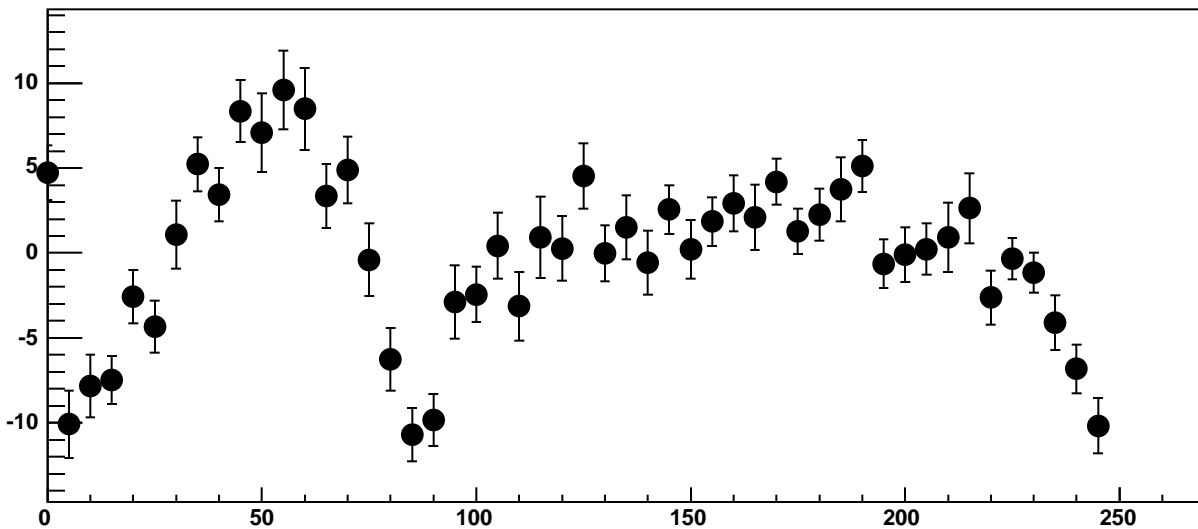


χ^2 / ndf	307.9 / 41
p0	-127.6 ± 0.6068
p1	85.55 ± 0.5762
p2	$-4.832\text{e}+07 \pm 7.655\text{e}+05$
p3	$2.515\text{e}+07 \pm 2.934\text{e}+05$
p4	1.813 ± 0.01987

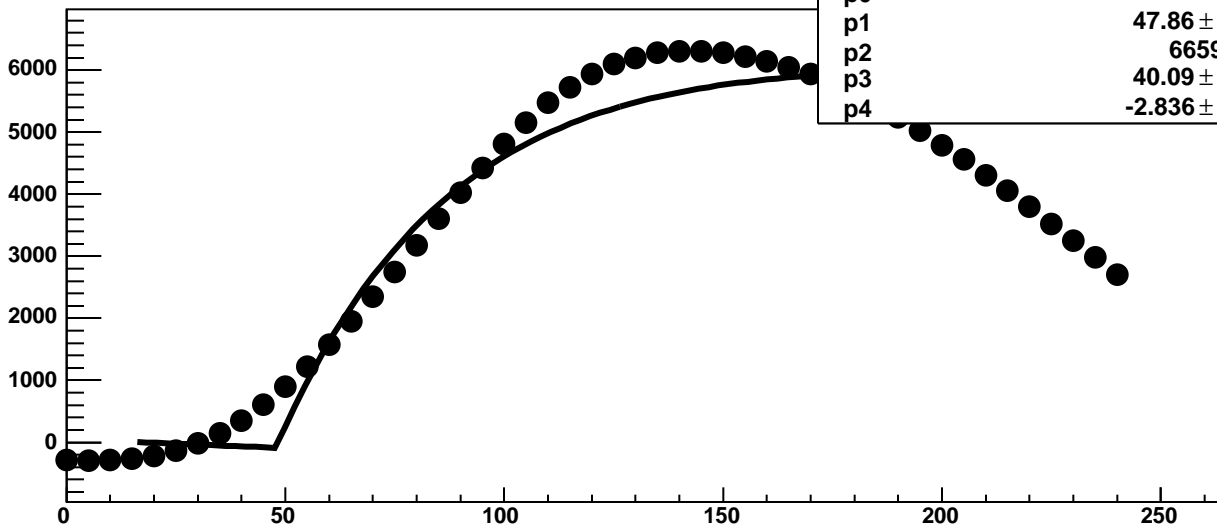
Chip 7, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold



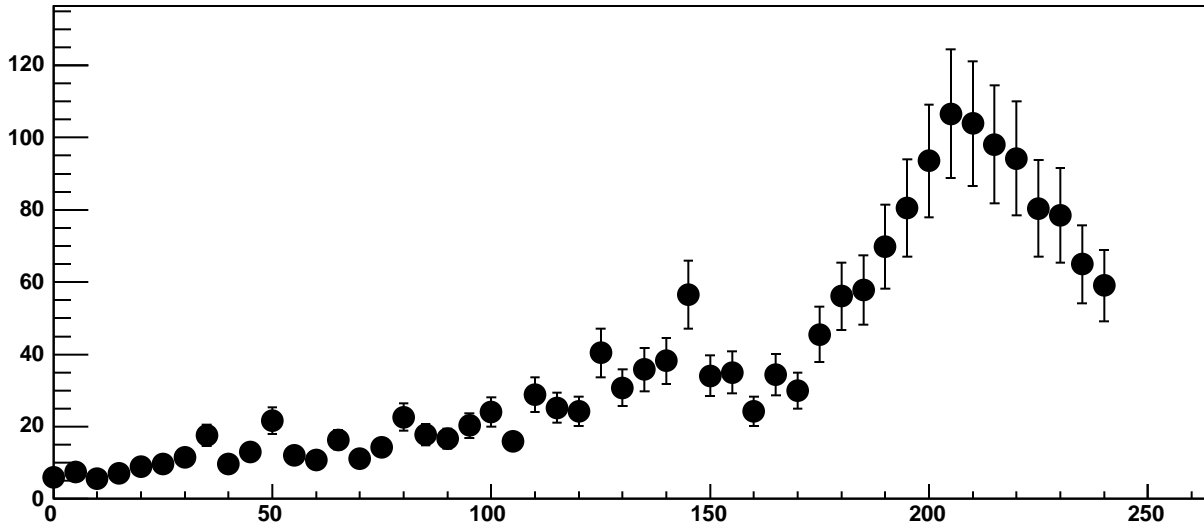
Chip 7, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold



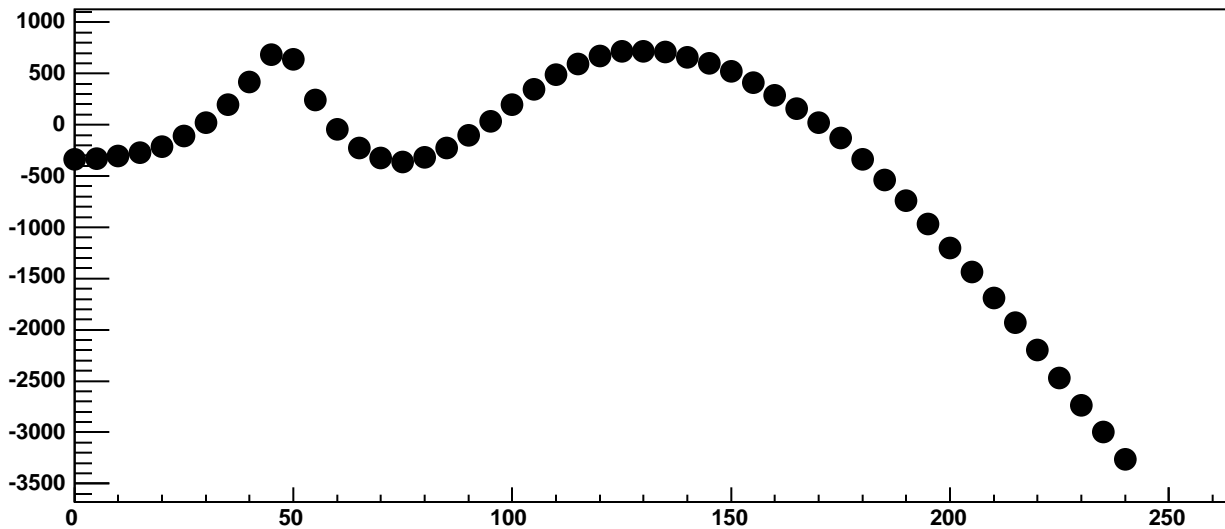
Chip 7, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold



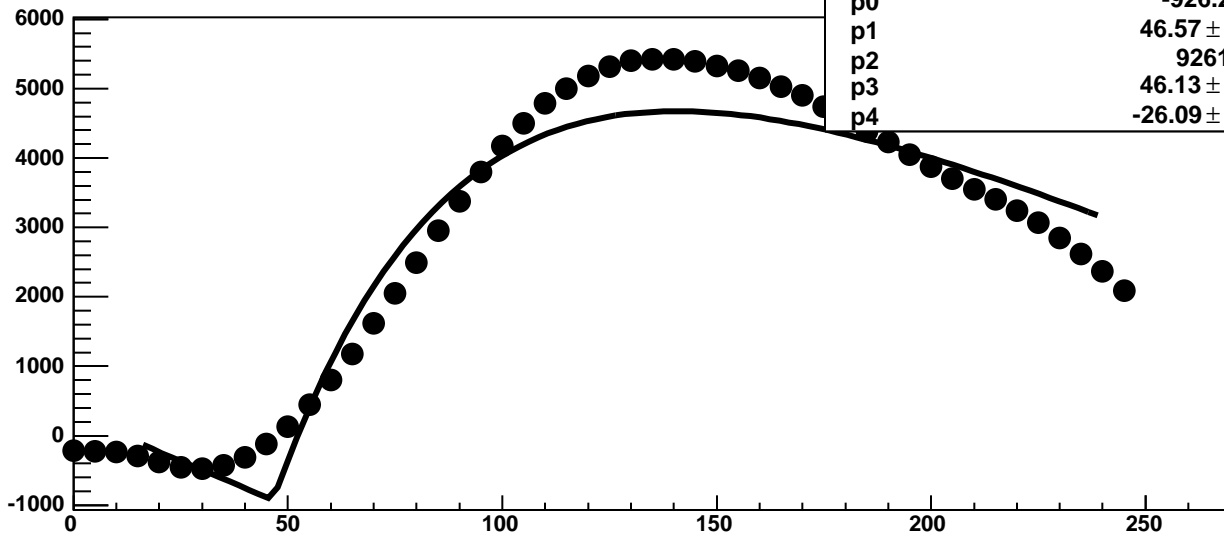
Chip 7, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold

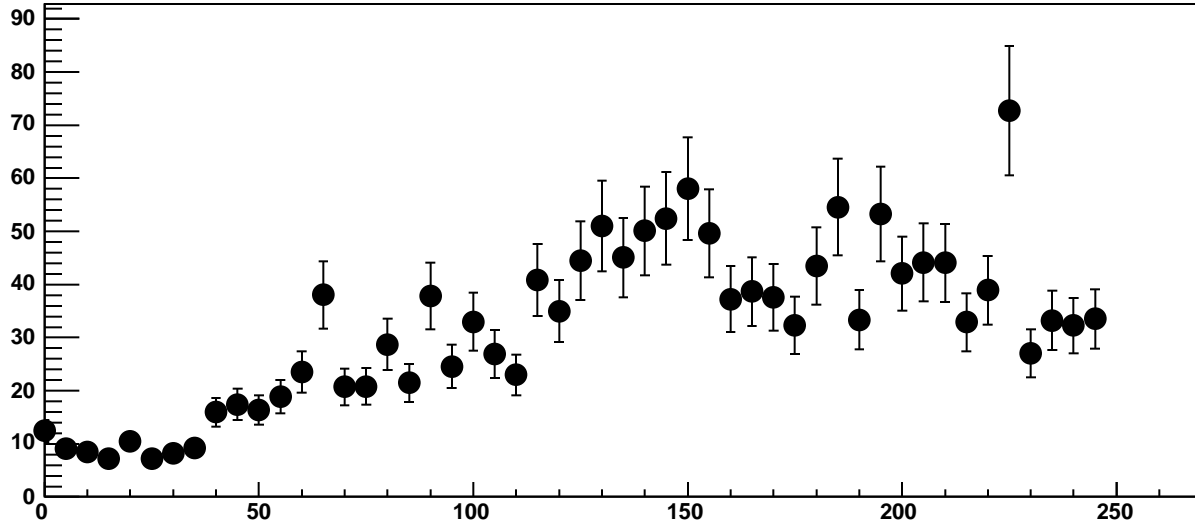


Chip 7, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold

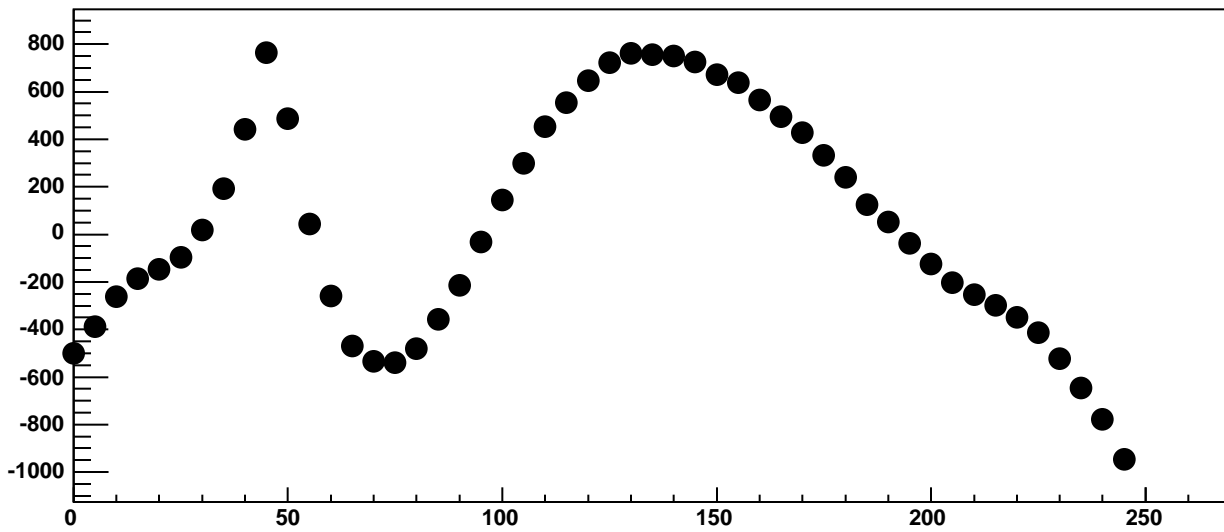


χ^2 / ndf	2.301e+05 / 41
p0	-926.2 ± 1.361
p1	46.57 ± 0.01862
p2	9261 ± 12.74
p3	46.13 ± 0.07146
p4	-26.09 ± 0.06212

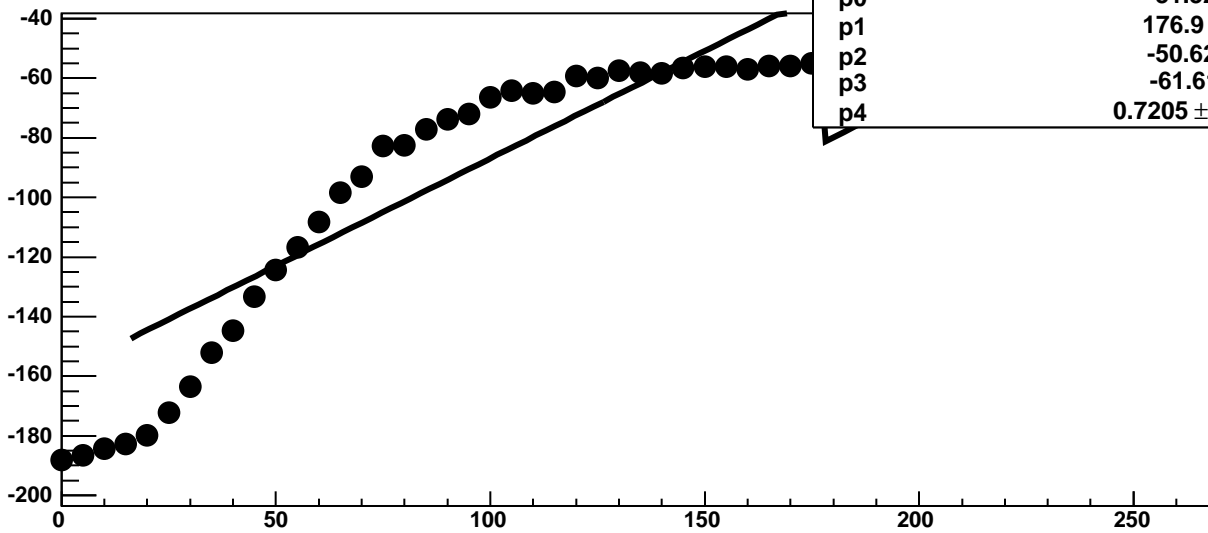
Chip 7, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

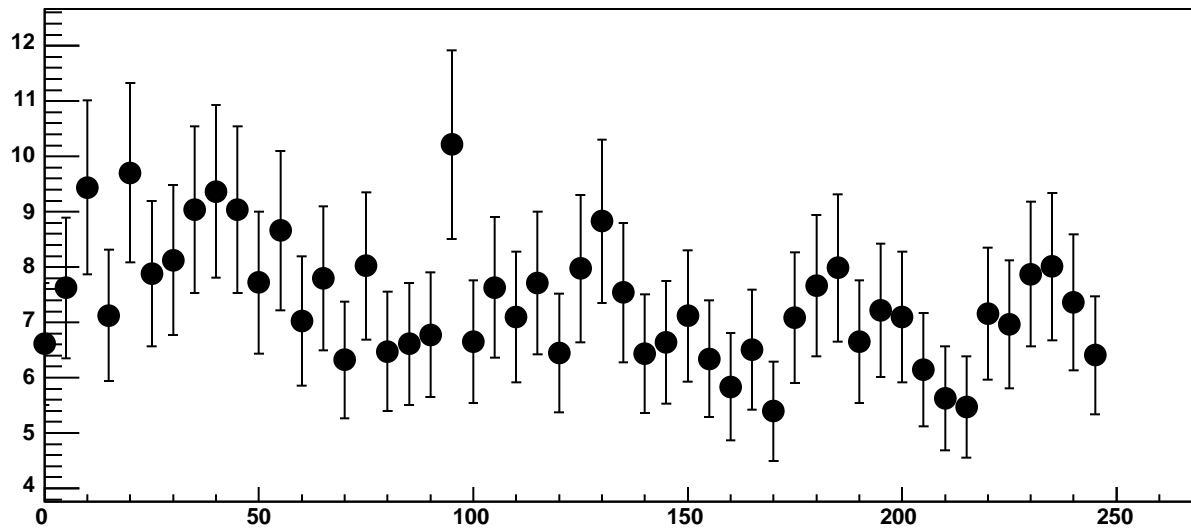


Chip 7, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold

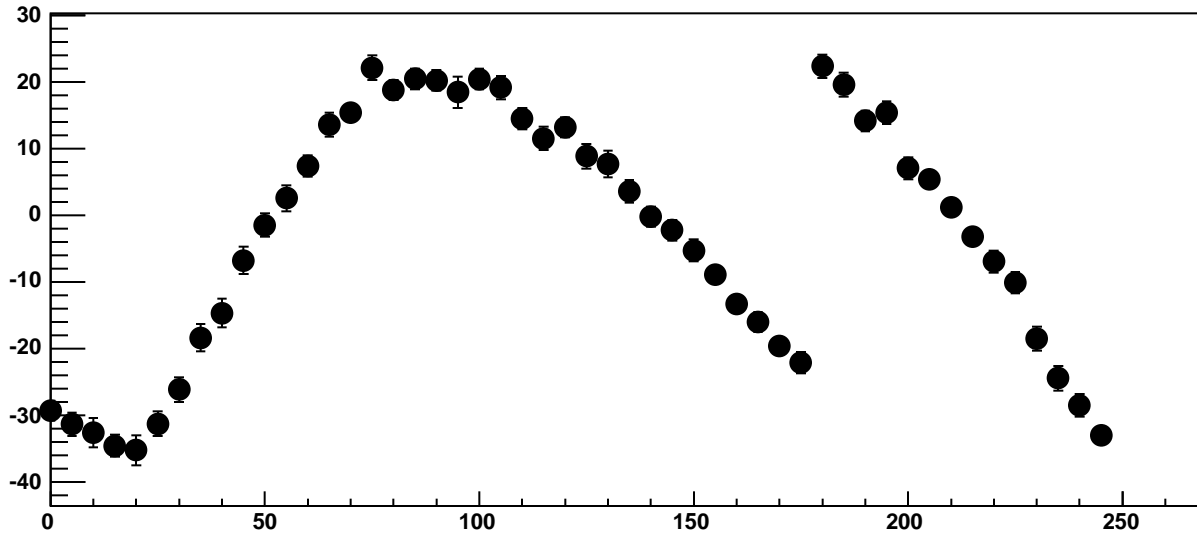


χ^2 / ndf	4595 / 41
p0	-31.52 ± 3.078
p1	176.9 ± 0.6902
p2	-50.62 ± 1.913
p3	-61.61 ± 28.16
p4	0.7205 ± 0.02678

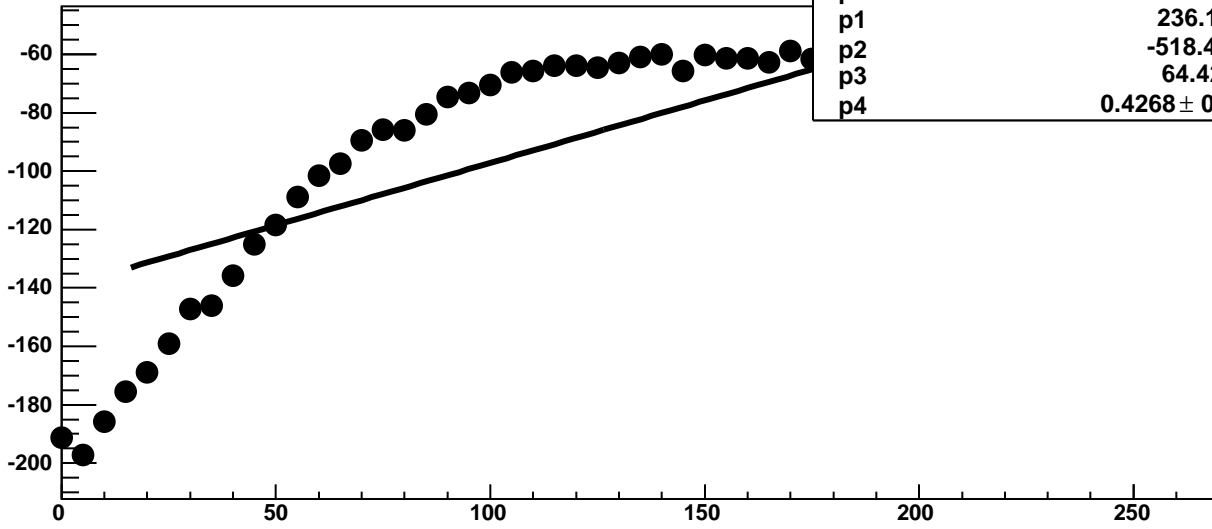
Chip 7, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold



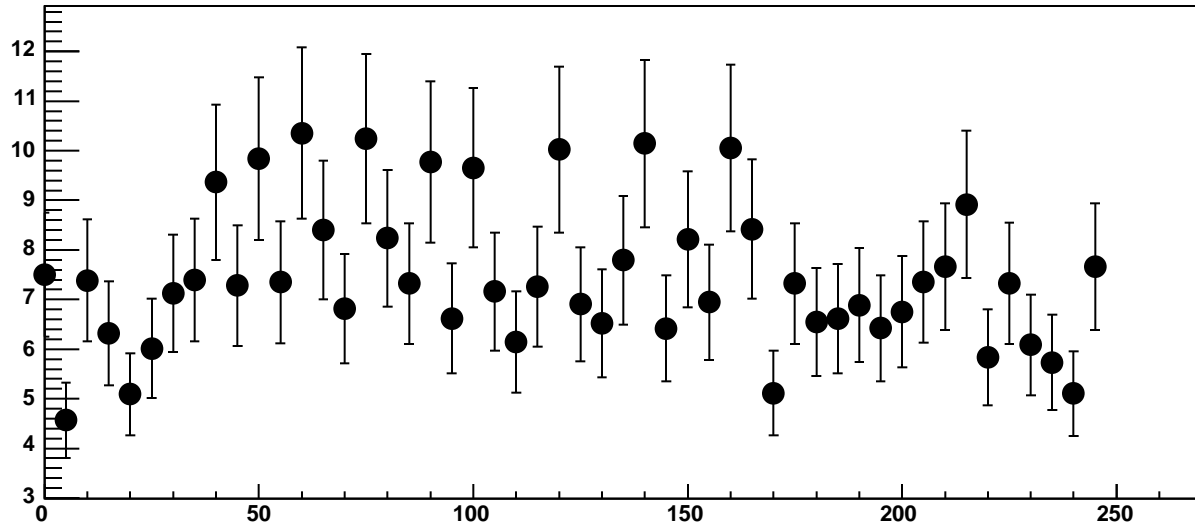
Chip 7, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold



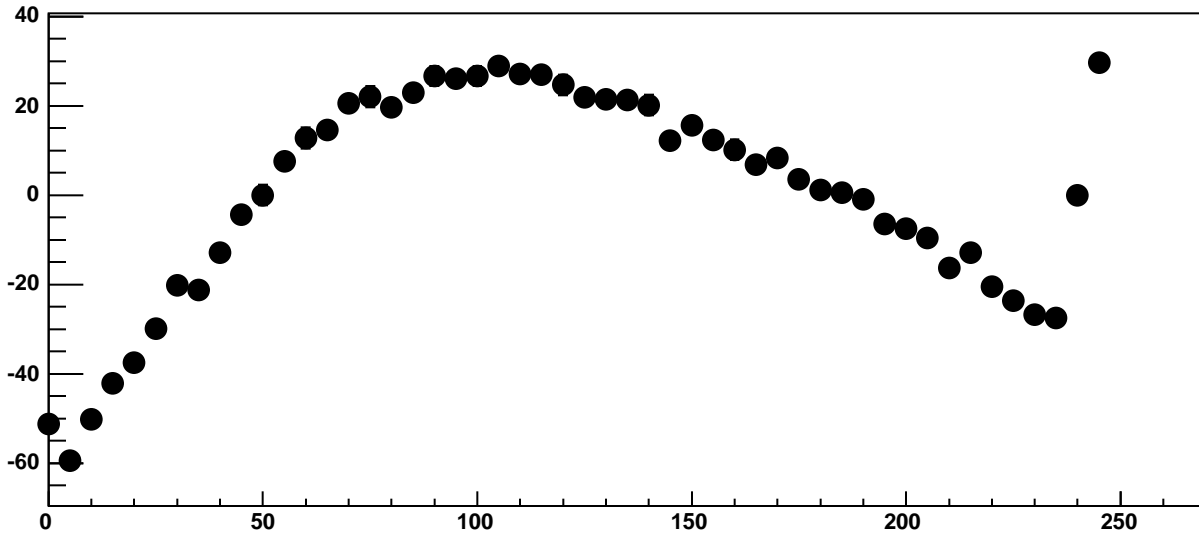
Chip 7, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold



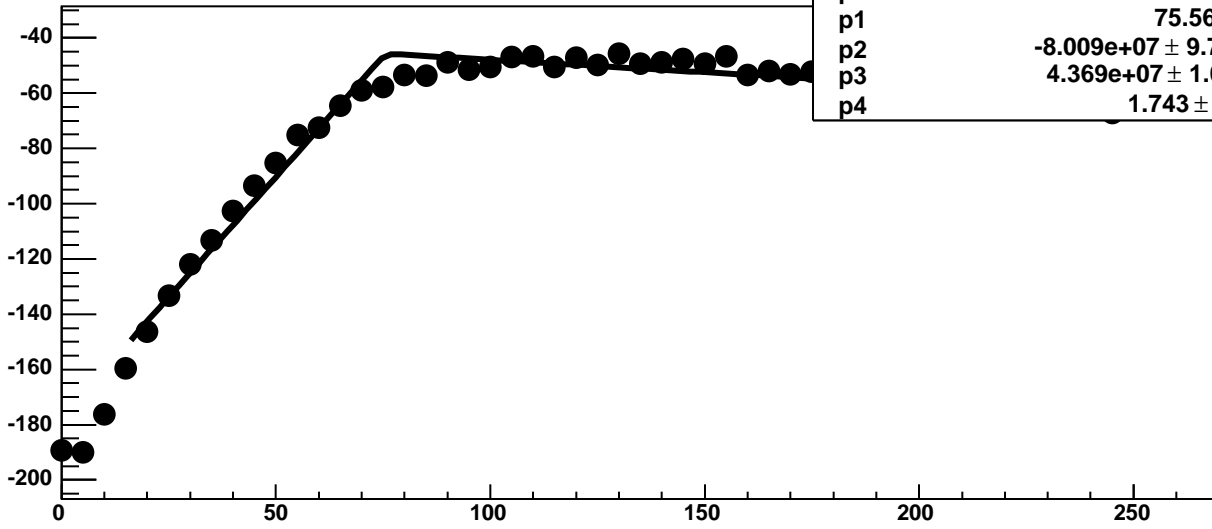
Chip 7, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

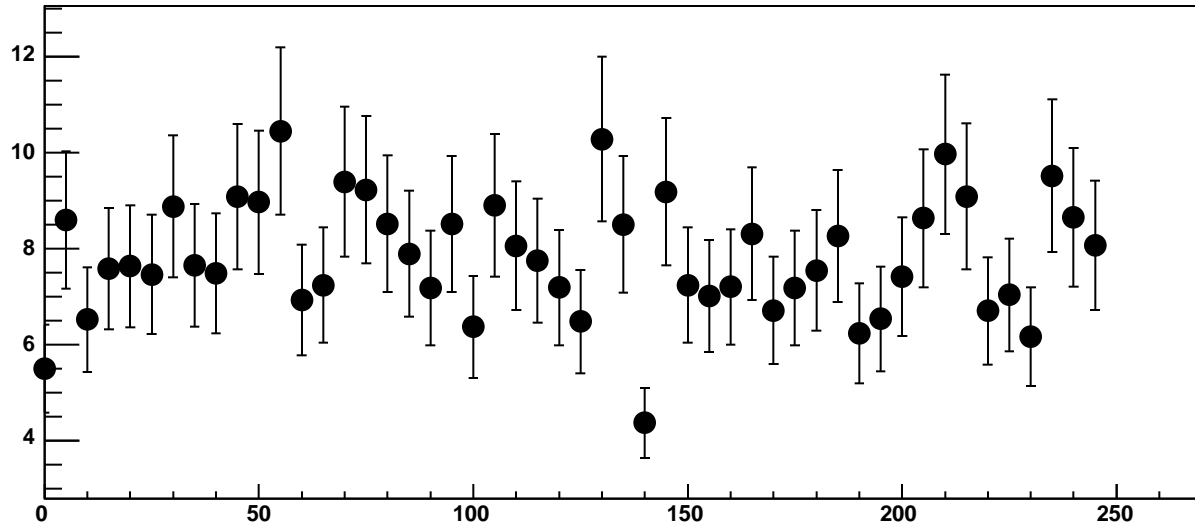


Chip 7, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

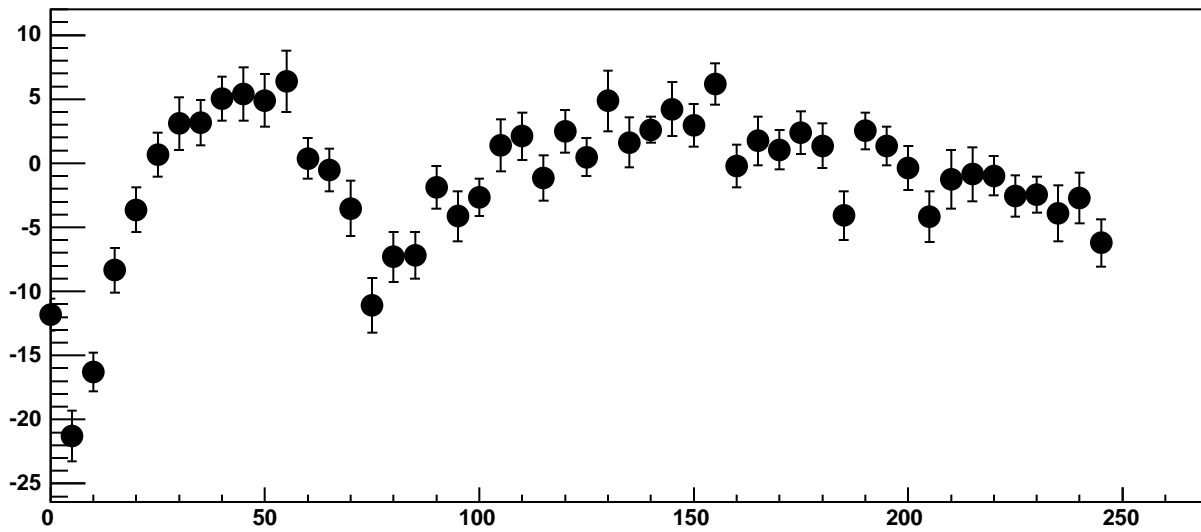


χ^2 / ndf	197 / 41
p0	-45.71 ± 0.6649
p1	75.56 ± 0.392
p2	$-8.009\text{e}+07 \pm 9.707\text{e}+05$
p3	$4.369\text{e}+07 \pm 1.073\text{e}+06$
p4	1.743 ± 0.01884

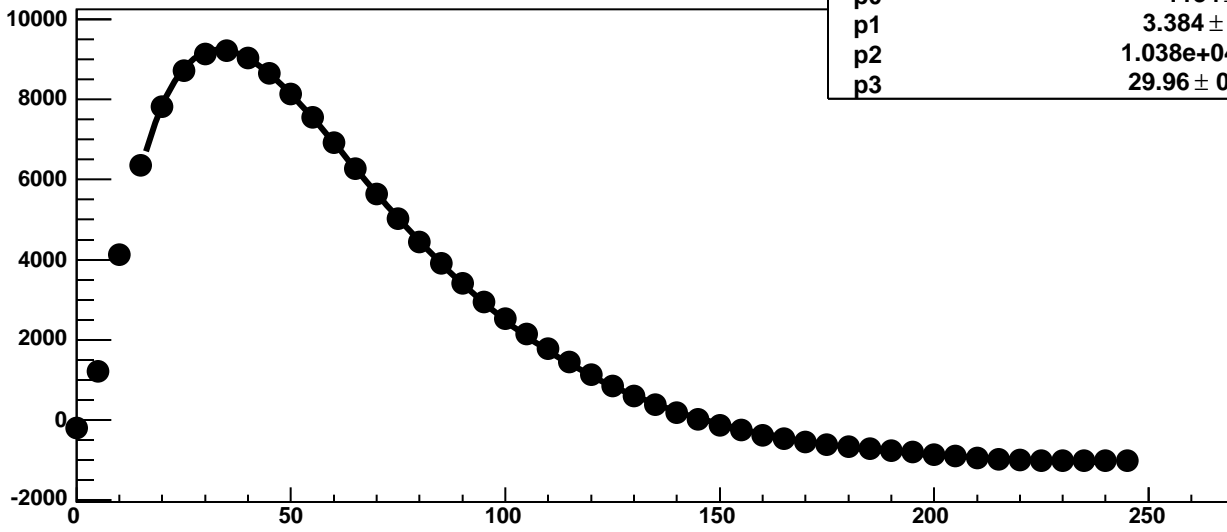
Chip 7, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

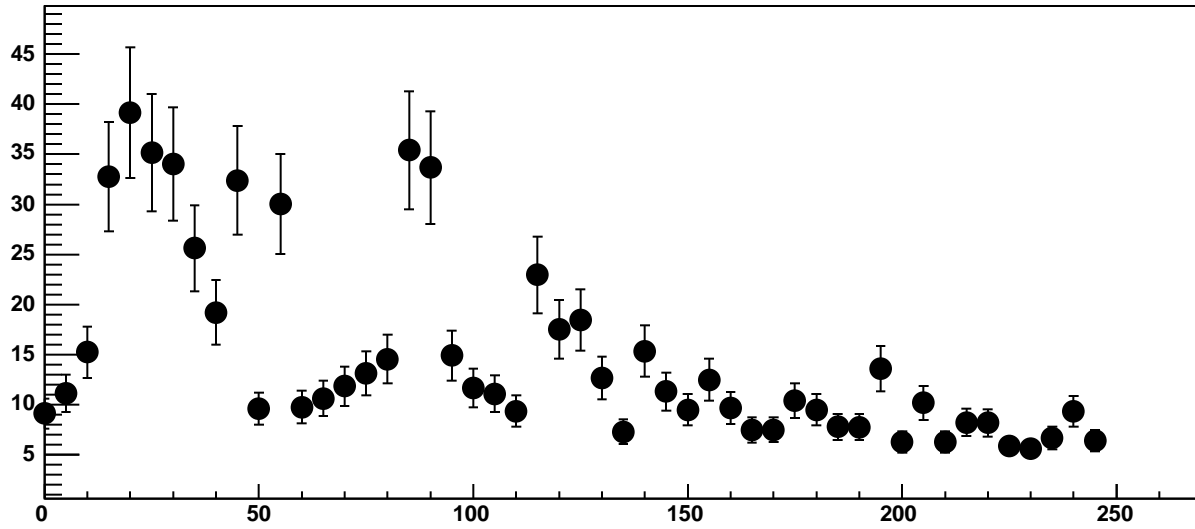


Chip 7, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold

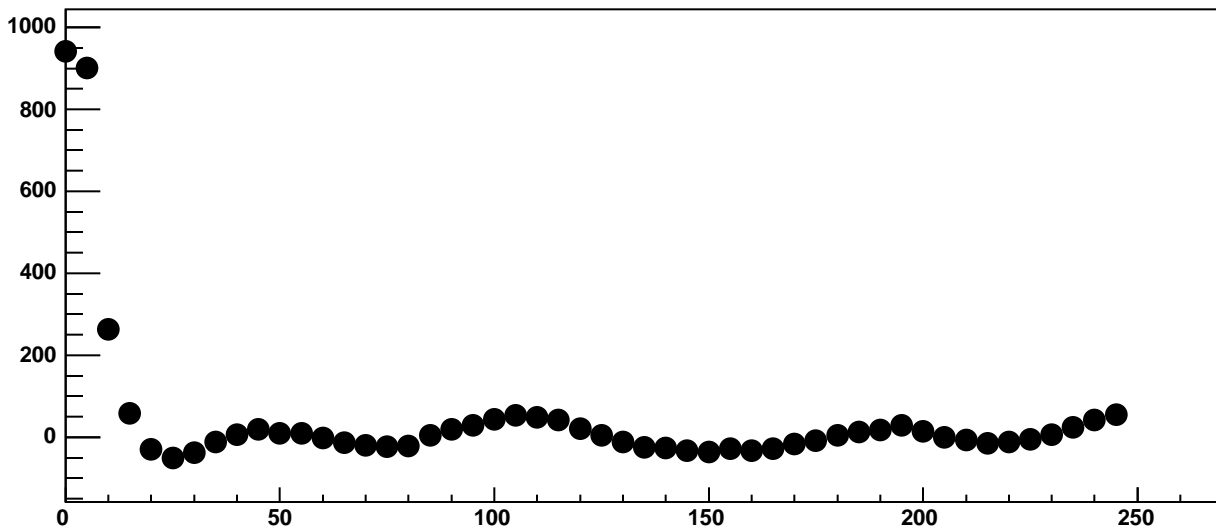


χ^2 / ndf	4304 / 42
p0	-1134 ± 0.5882
p1	3.384 ± 0.01592
p2	1.038e+04 ± 1.85
p3	29.96 ± 0.007924

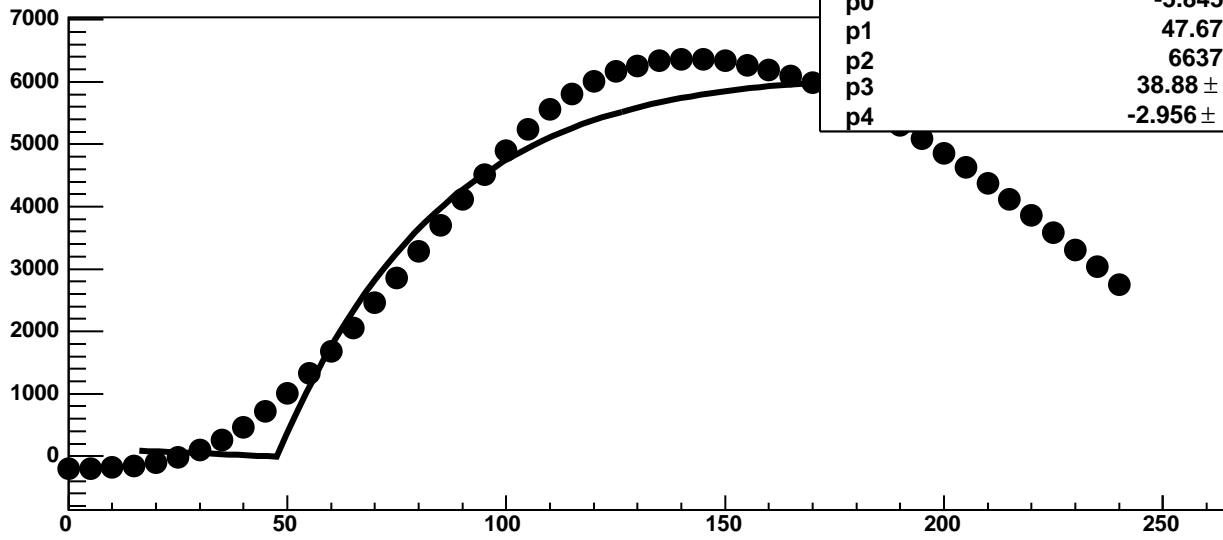
Chip 7, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold

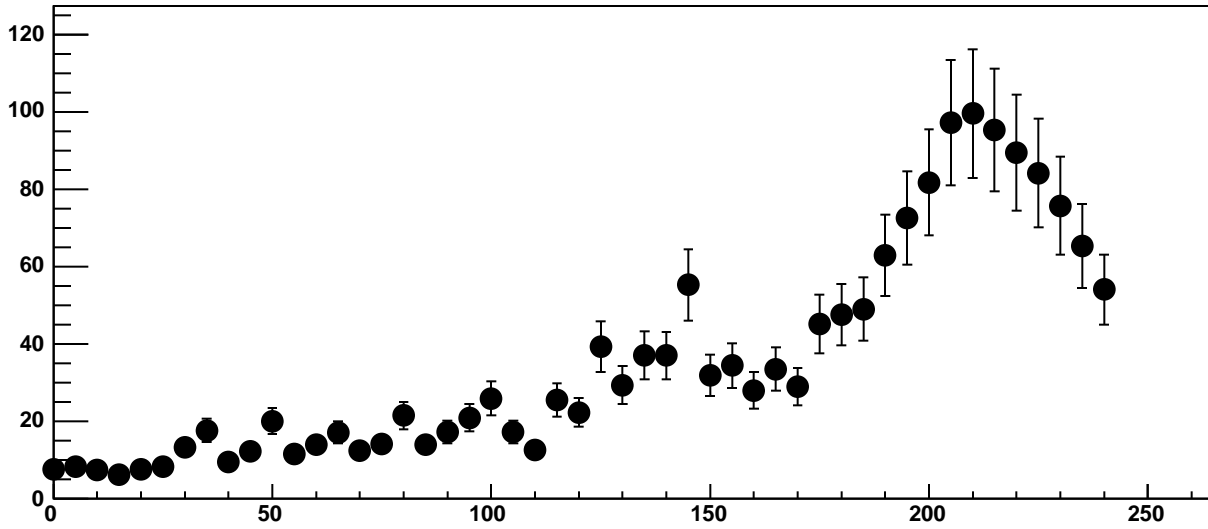


Chip 7, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

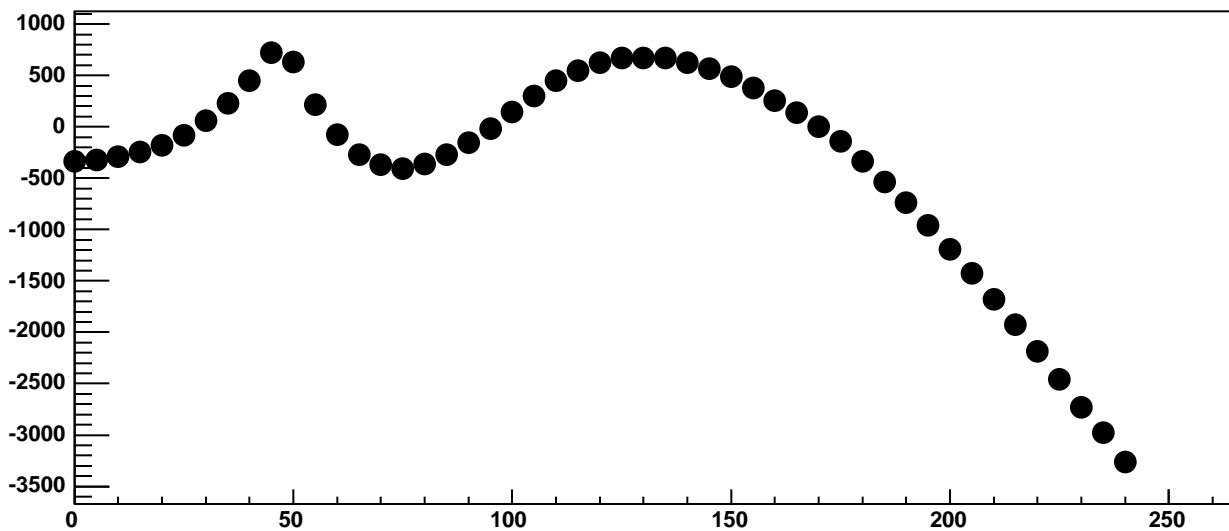


χ^2 / ndf	5.121e+05 / 41
p0	-5.845 ± 1.413
p1	47.67 ± 0.018
p2	6637 ± 8.905
p3	38.88 ± 0.05821
p4	-2.956 ± 0.05343

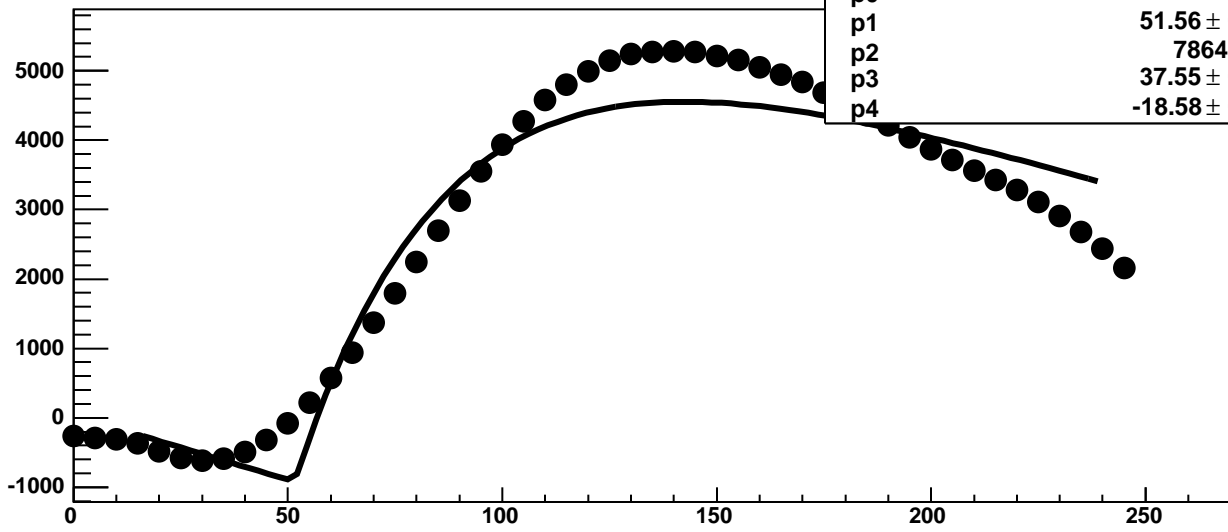
Chip 7, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

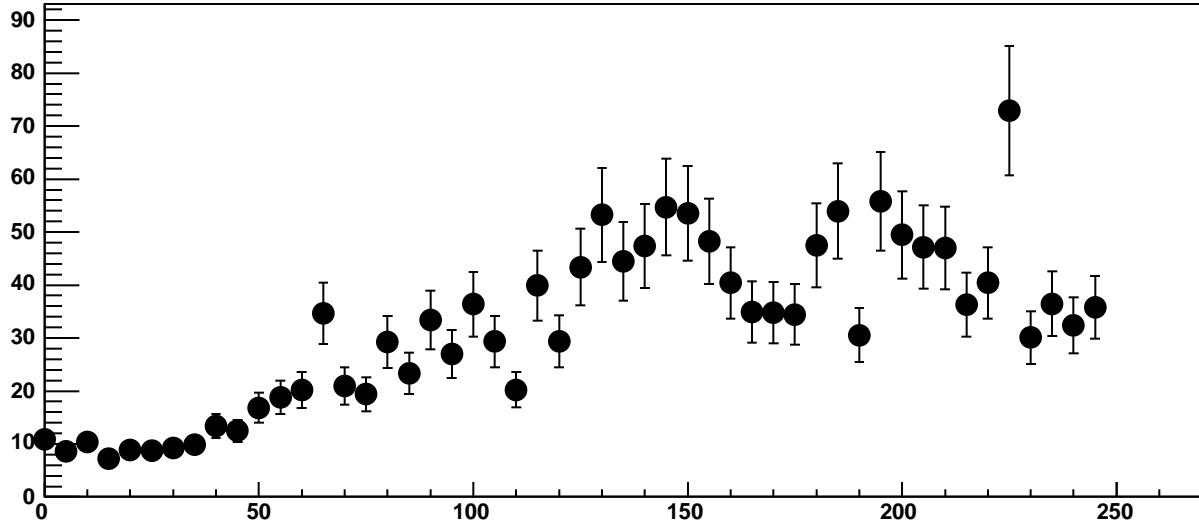


Chip 7, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

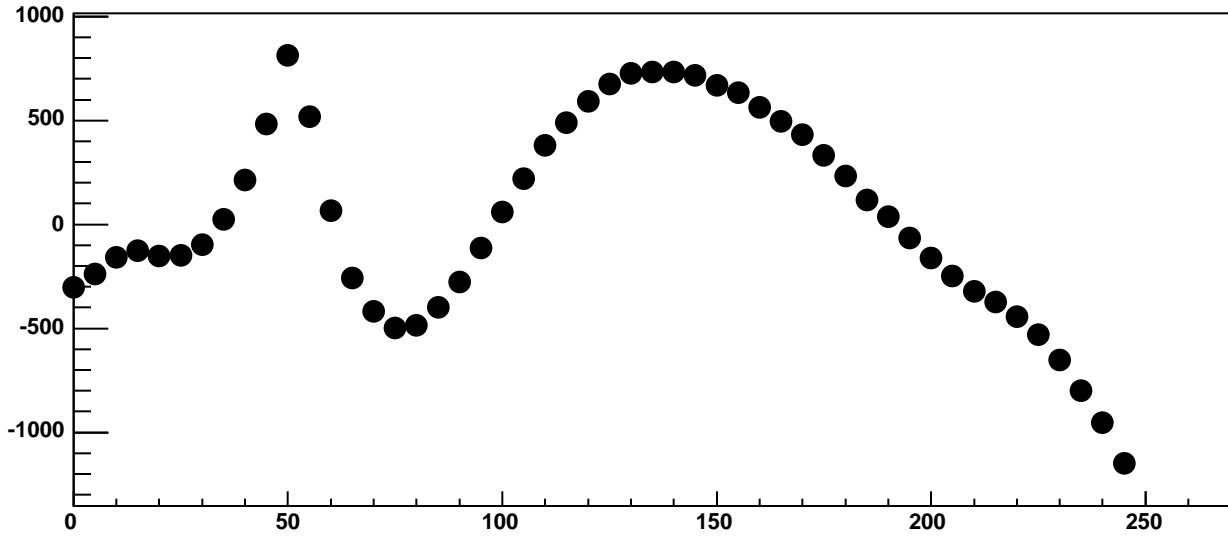


χ^2 / ndf	2.46e+05 / 41
p0	-921.1 ± 1.372
p1	51.56 ± 0.02022
p2	7864 ± 9.764
p3	37.55 ± 0.05978
p4	-18.58 ± 0.05157

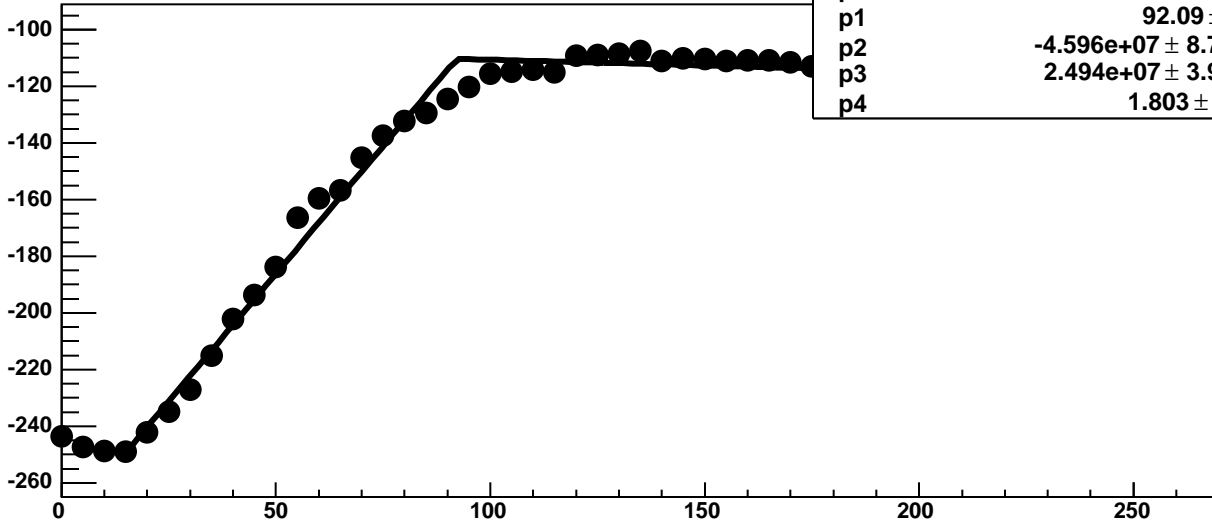
Chip 7, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold

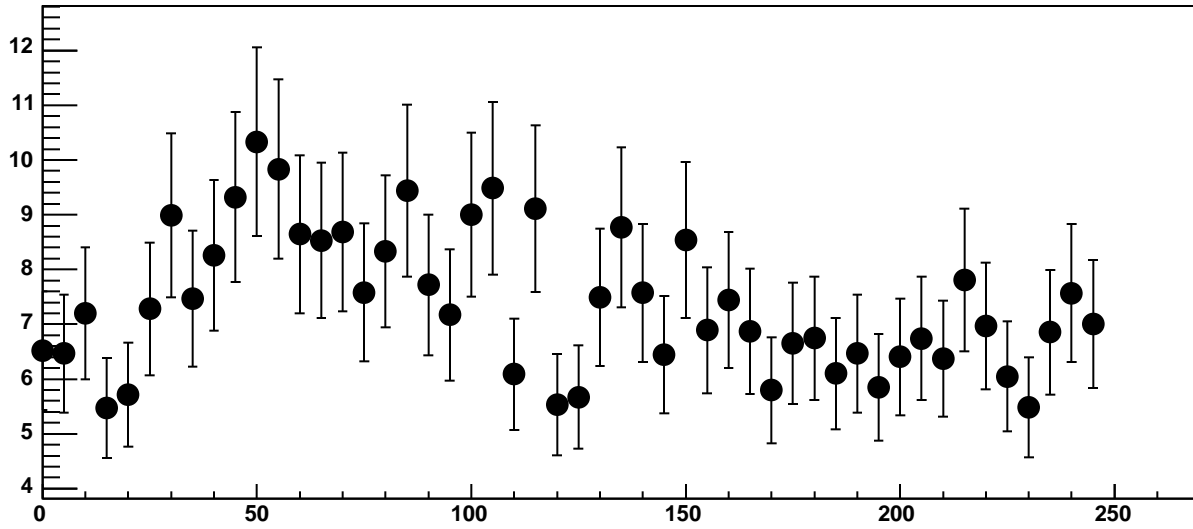


Chip 7, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold

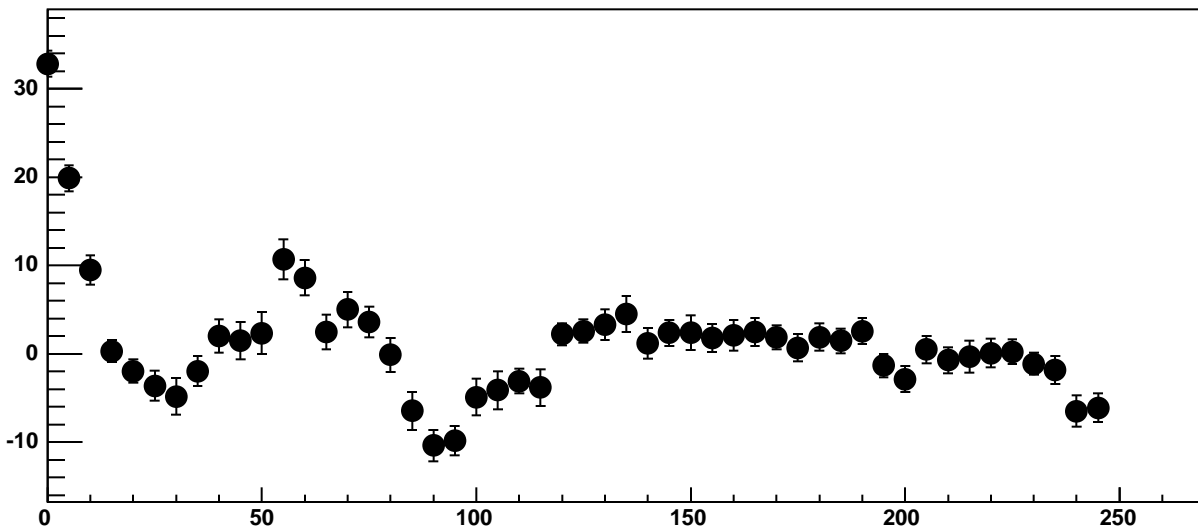


χ^2 / ndf	220.6 / 41
p0	-110.3 ± 0.581
p1	92.09 ± 0.5958
p2	$-4.596e+07 \pm 8.716e+05$
p3	$2.494e+07 \pm 3.966e+05$
p4	1.803 ± 0.01792

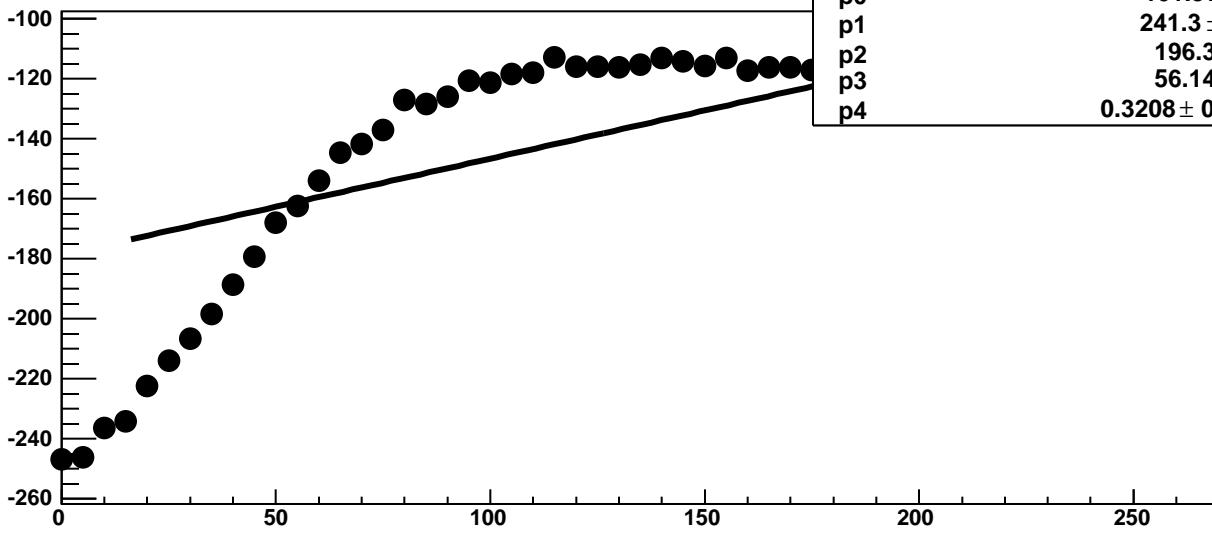
Chip 7, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold

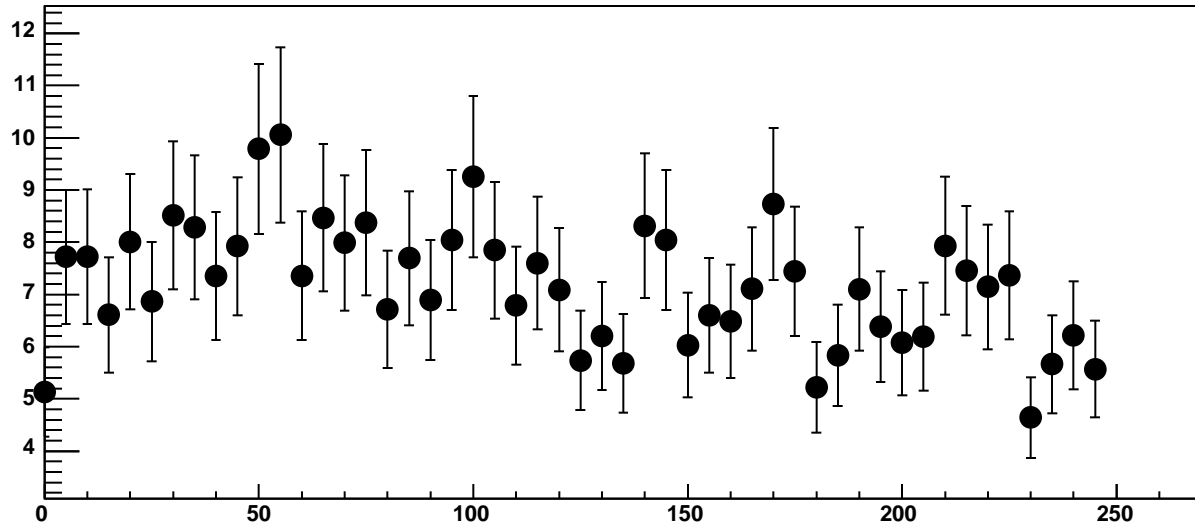


Chip 7, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold

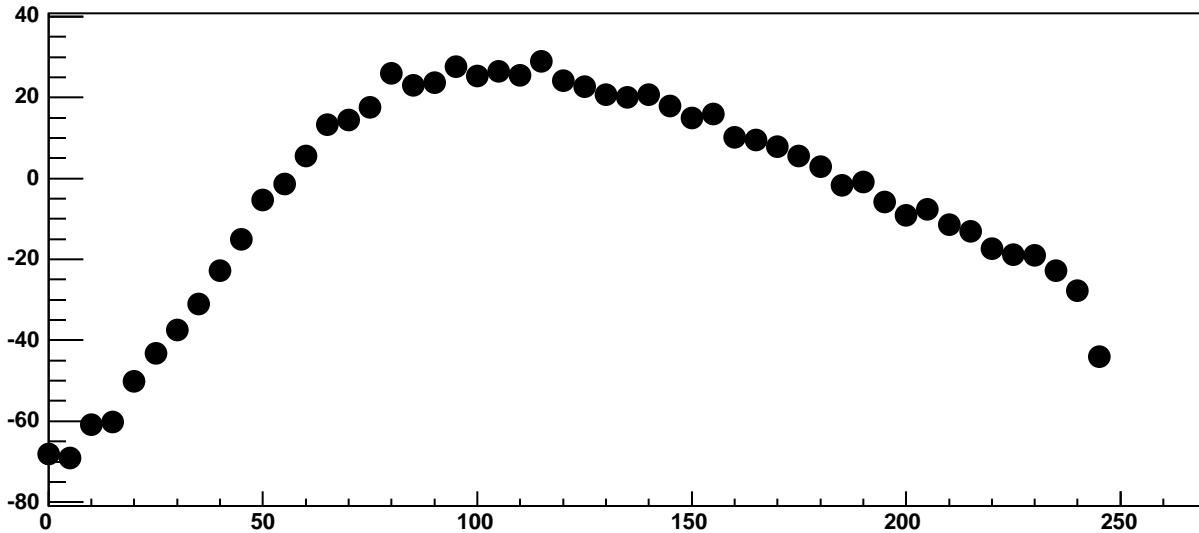


χ^2 / ndf	8971 / 41
p0	-101.3 ± 0.2353
p1	241.3 ± 0.7334
p2	196.3 ± 84.85
p3	56.14 ± 1.414
p4	0.3208 ± 0.001929

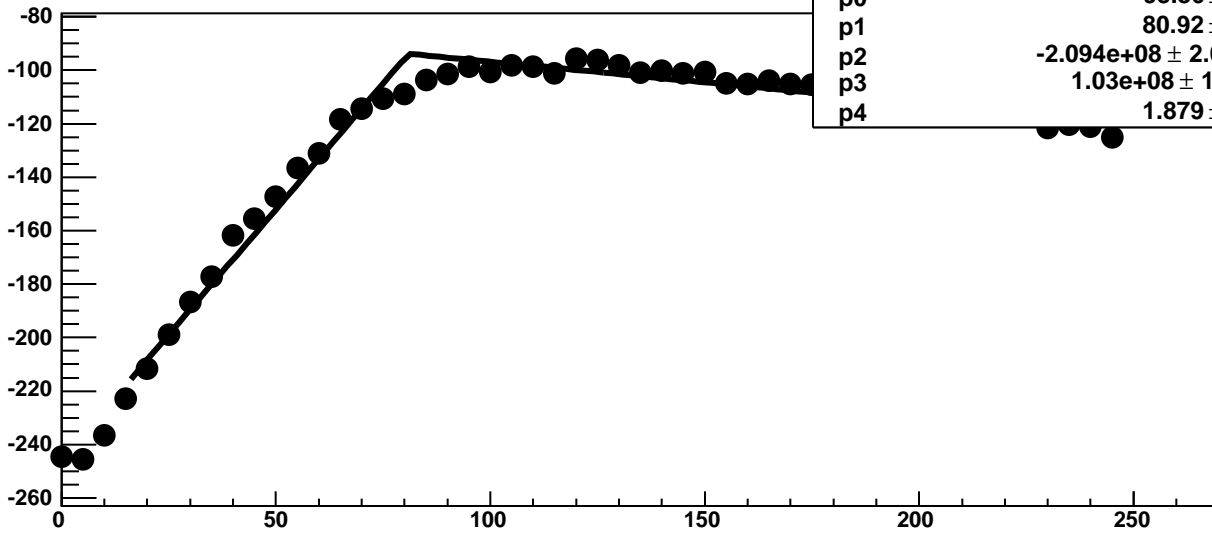
Chip 7, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold

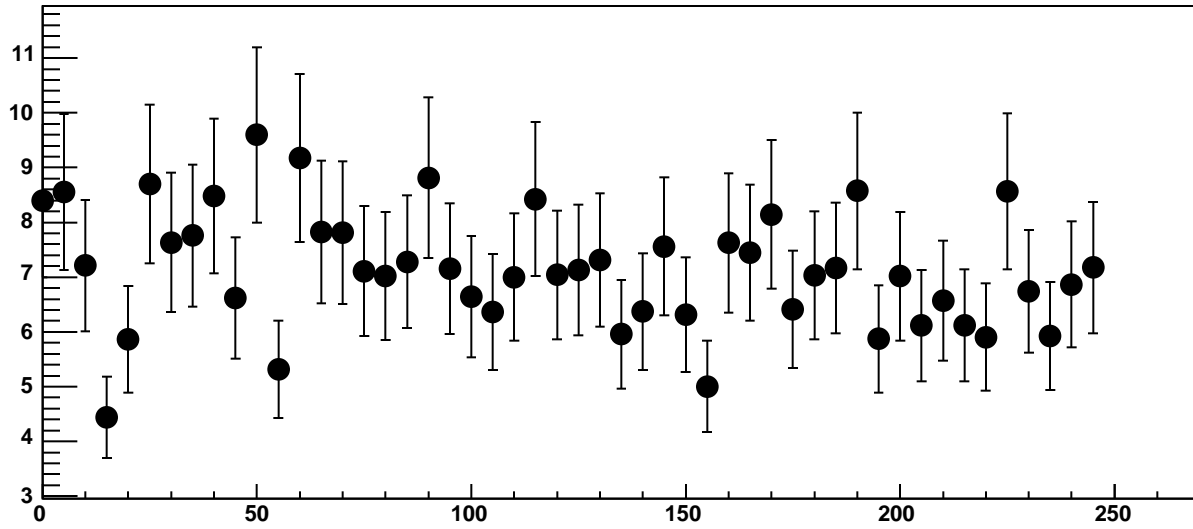


Chip 7, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

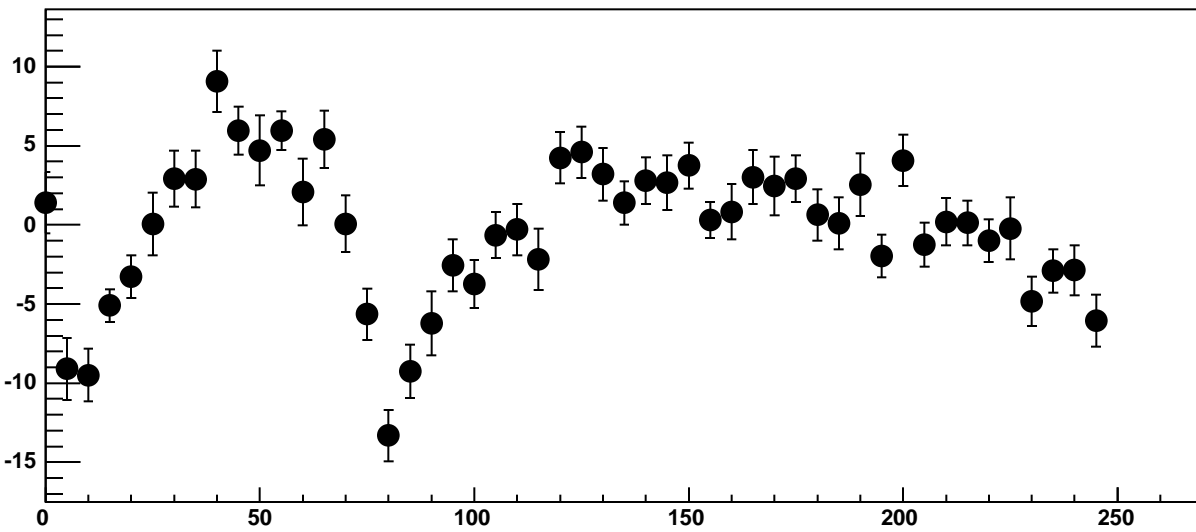


χ^2 / ndf	312.7 / 41
p0	-93.86 ± 0.5312
p1	80.92 ± 0.4702
p2	$-2.094\text{e}+08 \pm 2.092\text{e}+06$
p3	$1.03\text{e}+08 \pm 1.37\text{e}+05$
p4	1.879 ± 0.0188

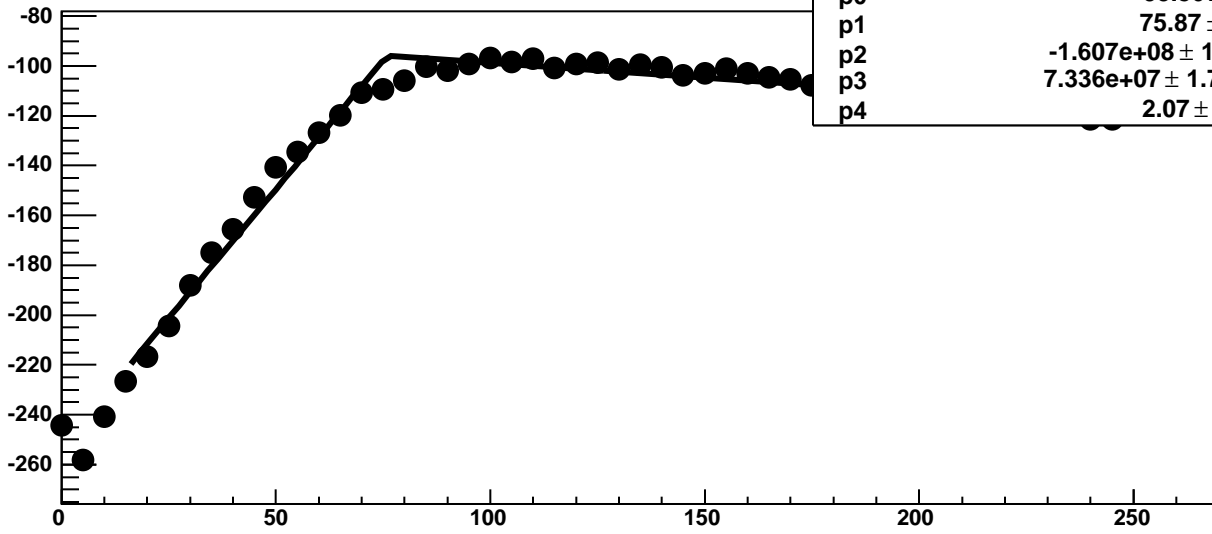
Chip 7, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

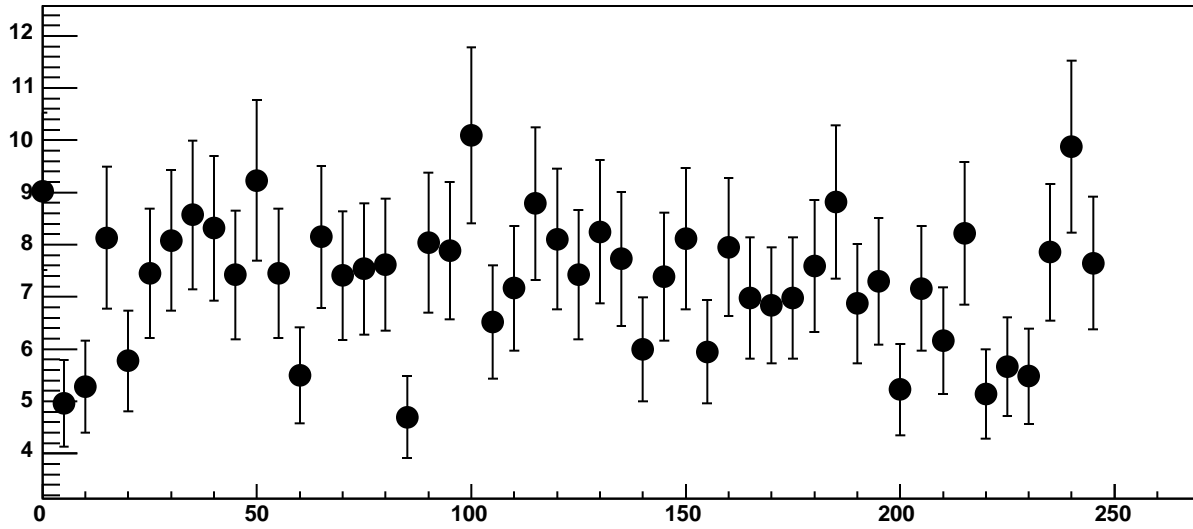


Chip 7, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

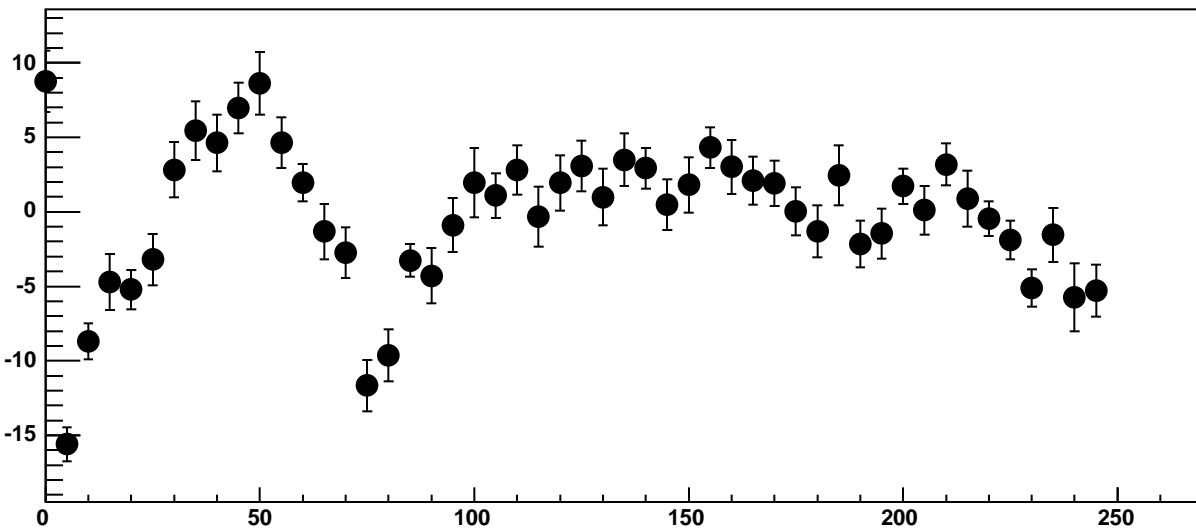


χ^2 / ndf	250.6 / 41
p0	-95.86 ± 0.5433
p1	75.87 ± 0.4846
p2	$-1.607\text{e}+08 \pm 1.86\text{e}+06$
p3	$7.336\text{e}+07 \pm 1.705\text{e}+05$
p4	2.07 ± 0.02464

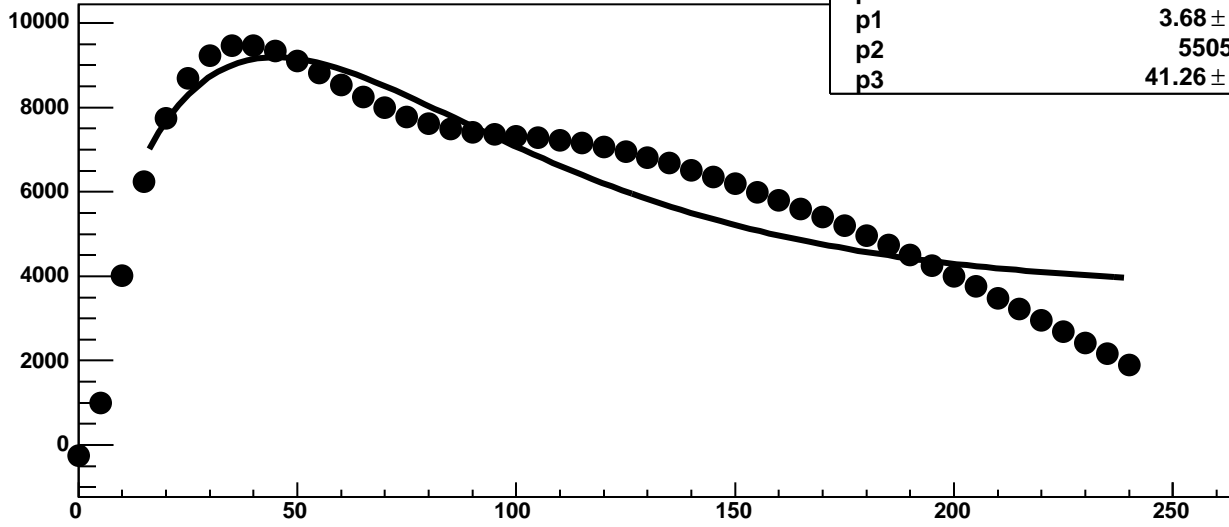
Chip 7, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

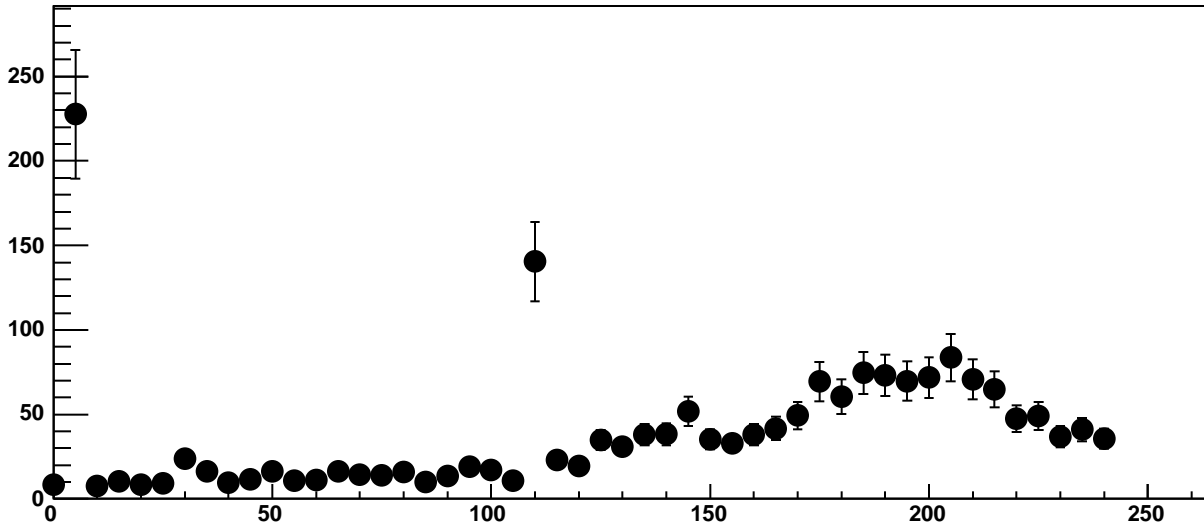


Chip 7, Channel 6, Enable 5!, DAC=4000, ADC Mean vs Hold

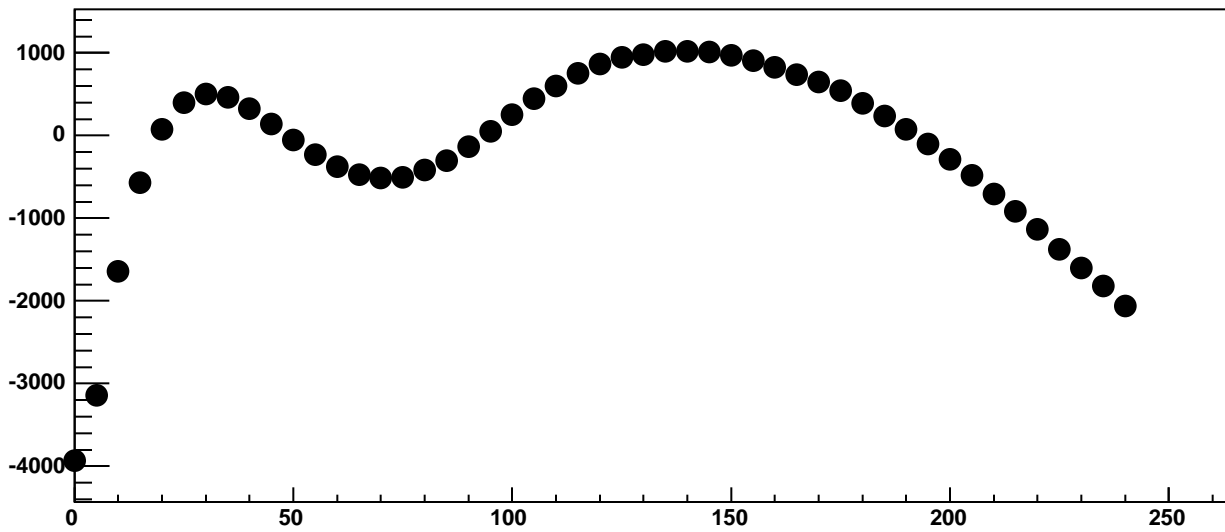


χ^2 / ndf	6.461e+05 / 42
p0	3683 \pm 5.285
p1	3.68 \pm 0.03115
p2	5505 \pm 4.894
p3	41.26 \pm 0.04205

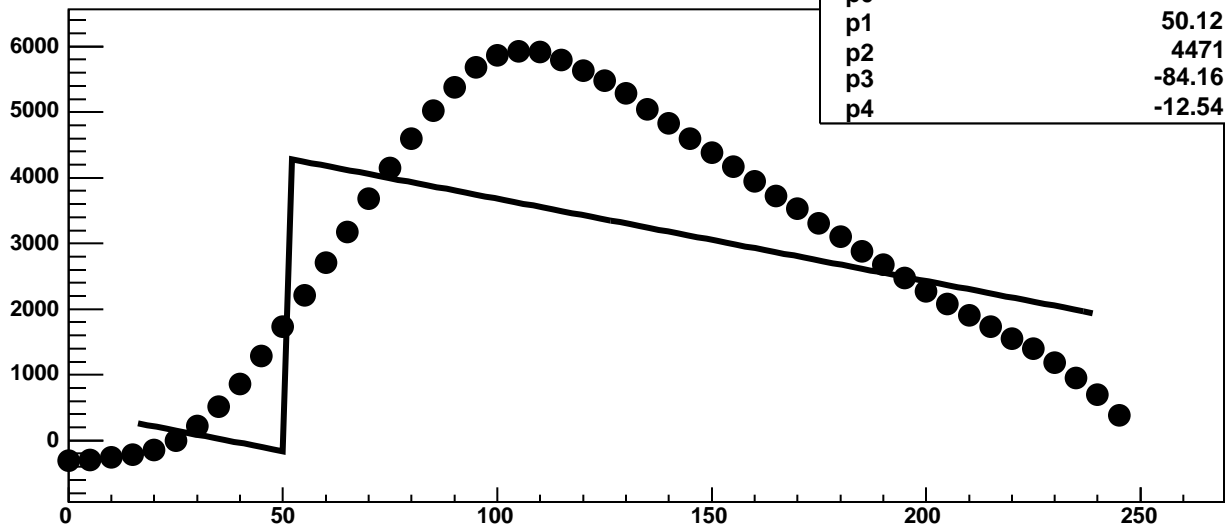
Chip 7, Channel 6, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 6, Enable 5!, DAC=4000, ADC Residuals vs Hold

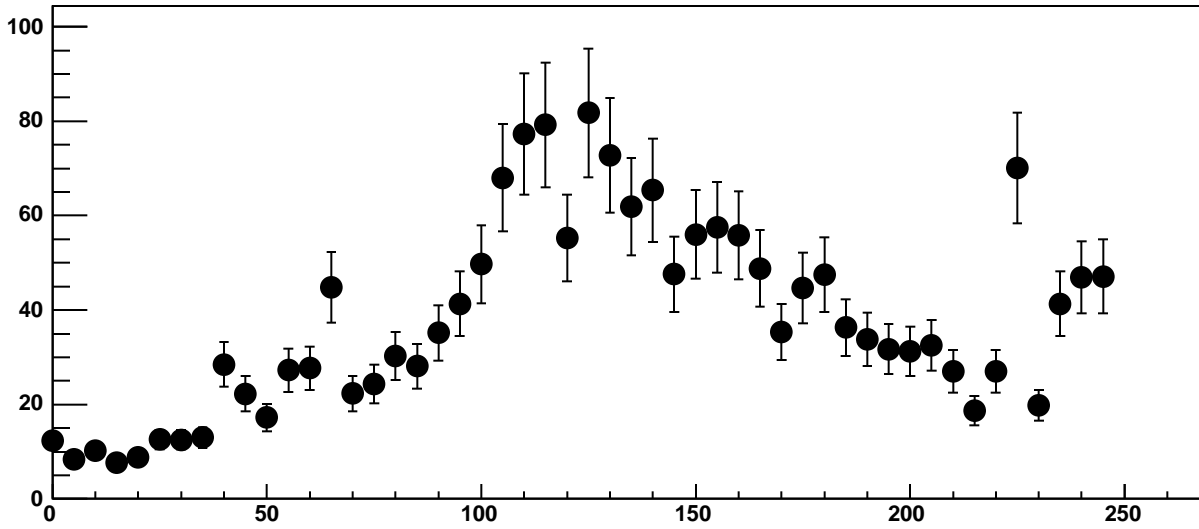


Chip 7, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

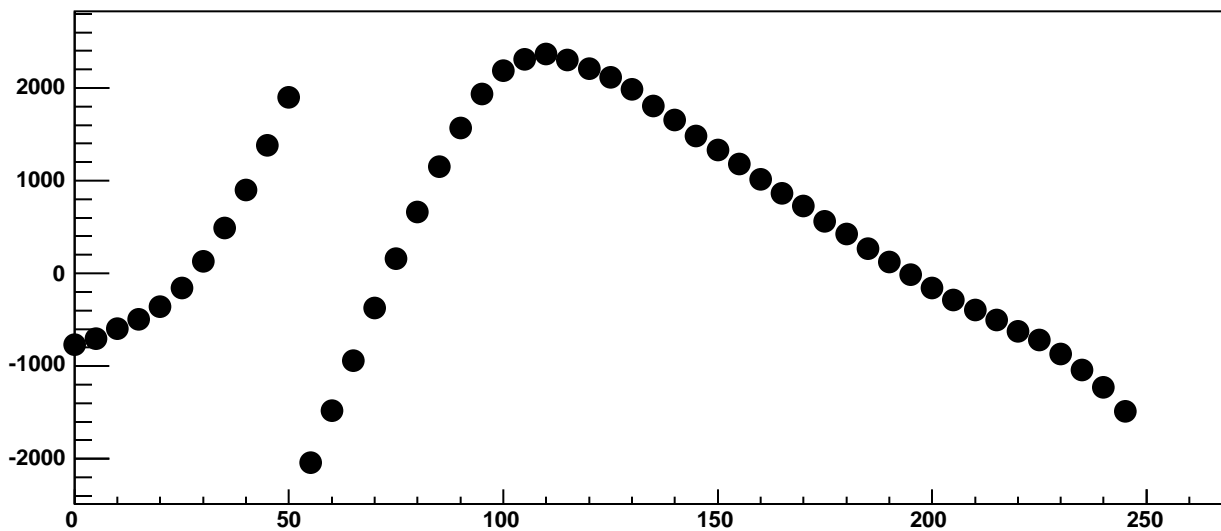


χ^2 / ndf	1.094e+06 / 41
p0	-164.2 ± 1.49
p1	50.12 ± 3.103
p2	4471 ± 2.006
p3	-84.16 ± 3.103
p4	-12.54 ± 3.103

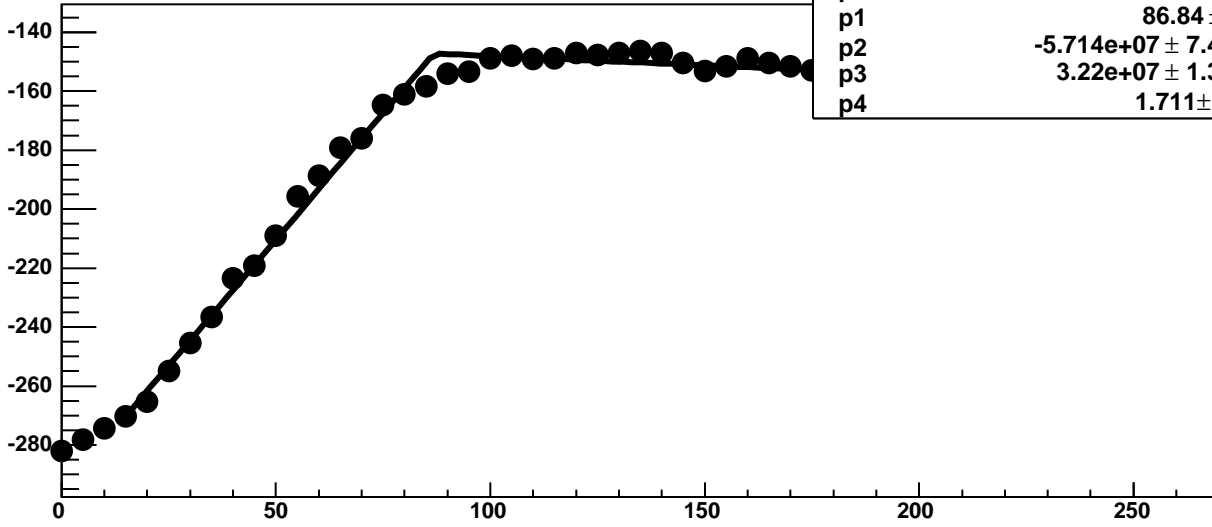
Chip 7, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

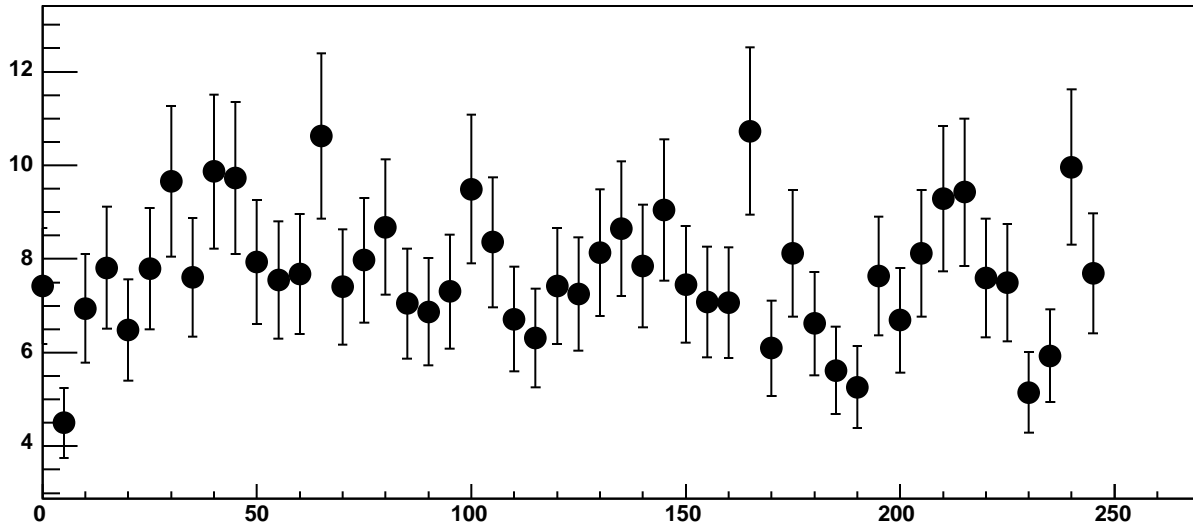


Chip 7, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold

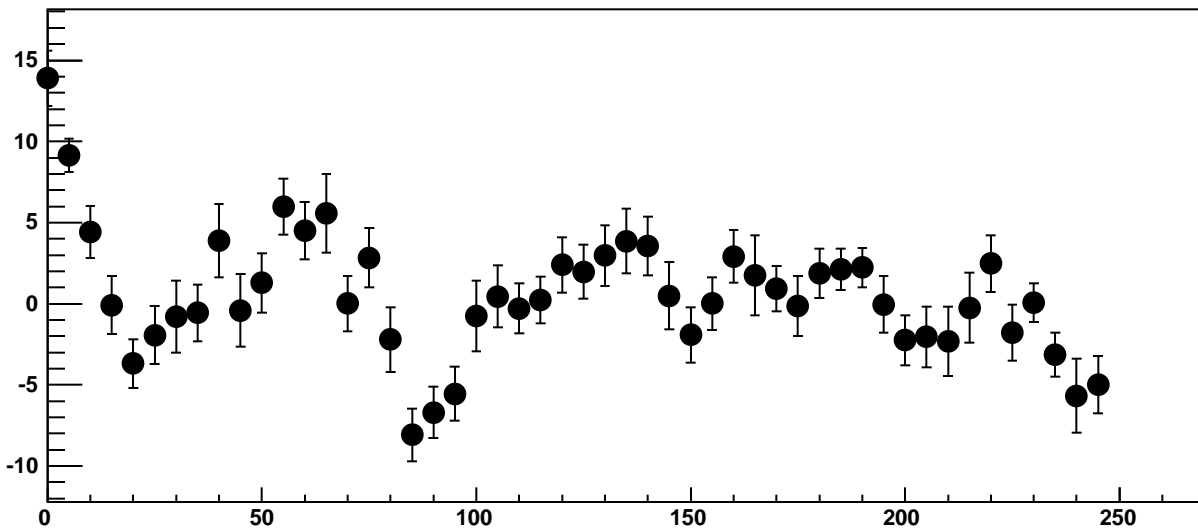


χ^2 / ndf	138.4 / 41
p0	-147.3 ± 0.5948
p1	86.84 ± 0.6208
p2	$-5.714\text{e}+07 \pm 7.436\text{e}+05$
p3	$3.22\text{e}+07 \pm 1.398\text{e}+05$
p4	1.711 ± 0.02101

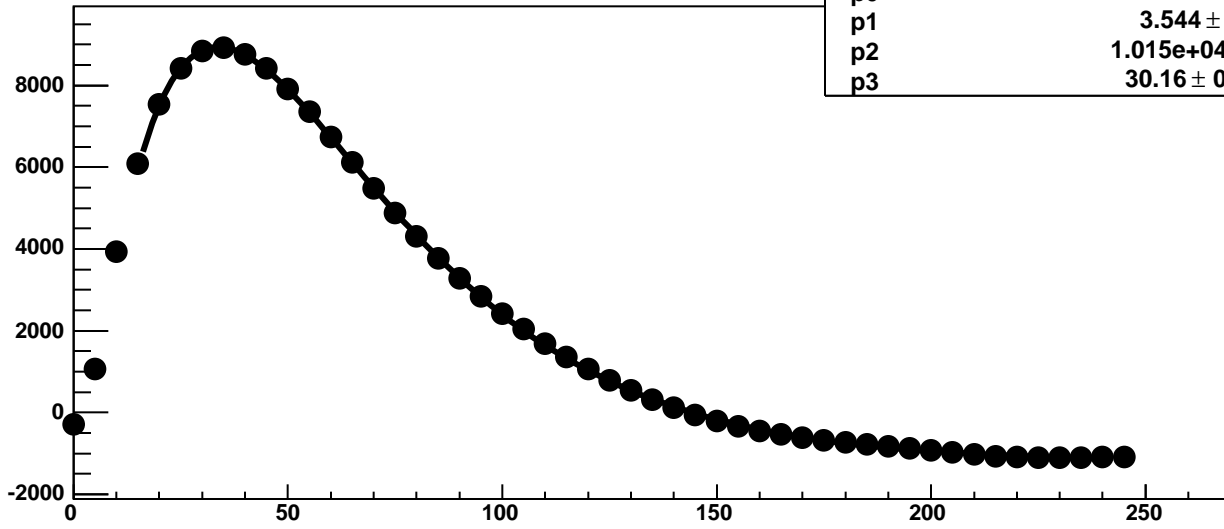
Chip 7, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

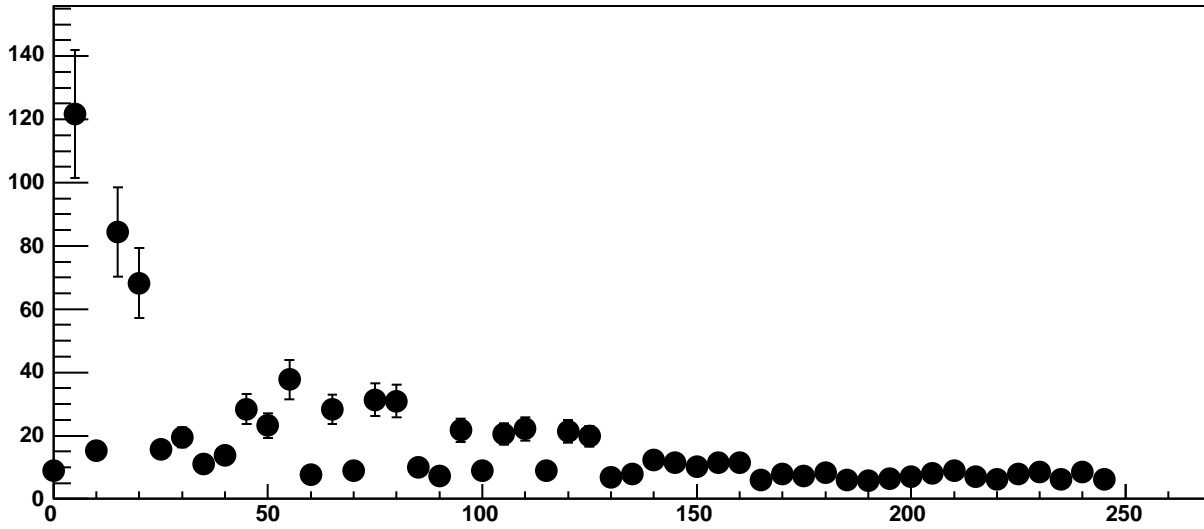


Chip 7, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold

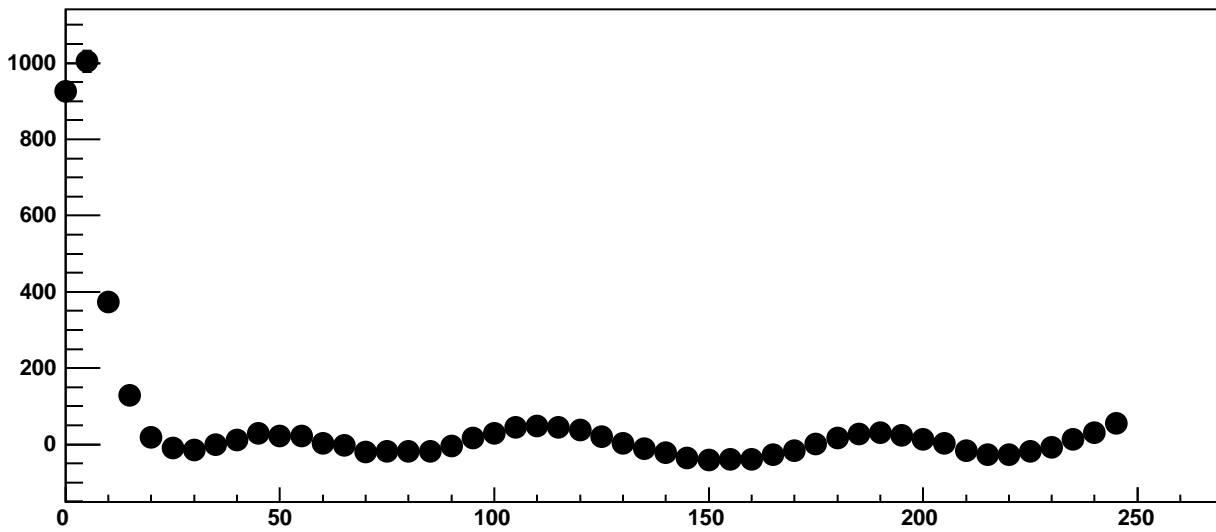


χ^2 / ndf	5312 / 42
p0	-1210 ± 0.5893
p1	3.544 ± 0.01999
p2	1.015e+04 ± 1.452
p3	30.16 ± 0.008723

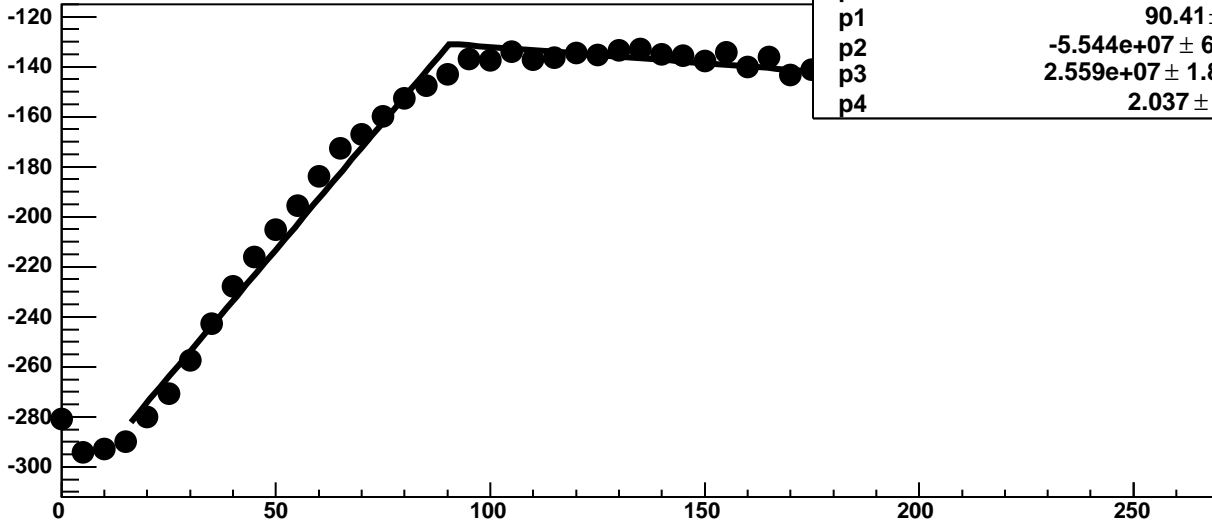
Chip 7, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold

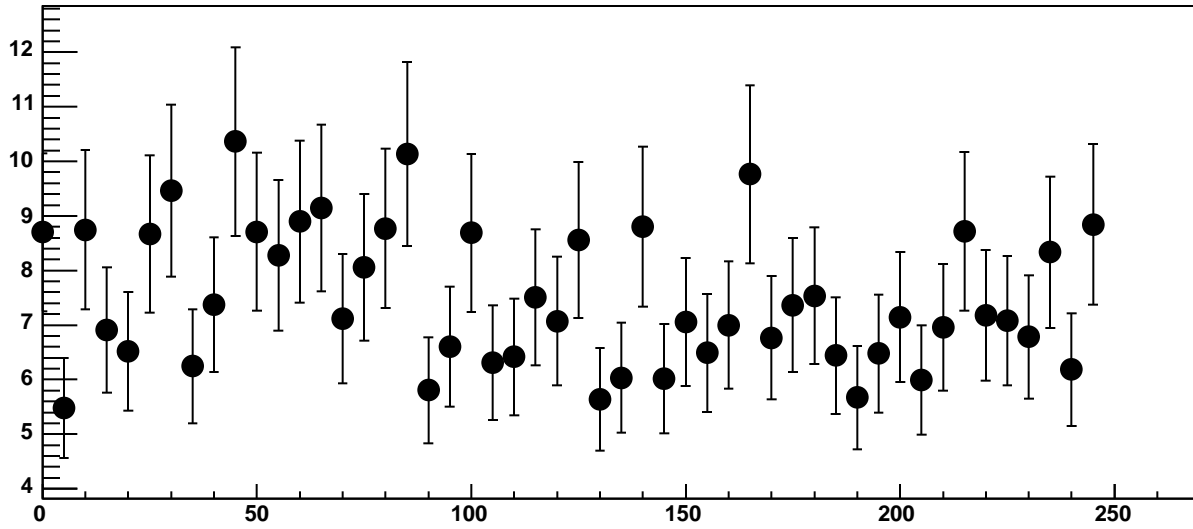


Chip 7, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

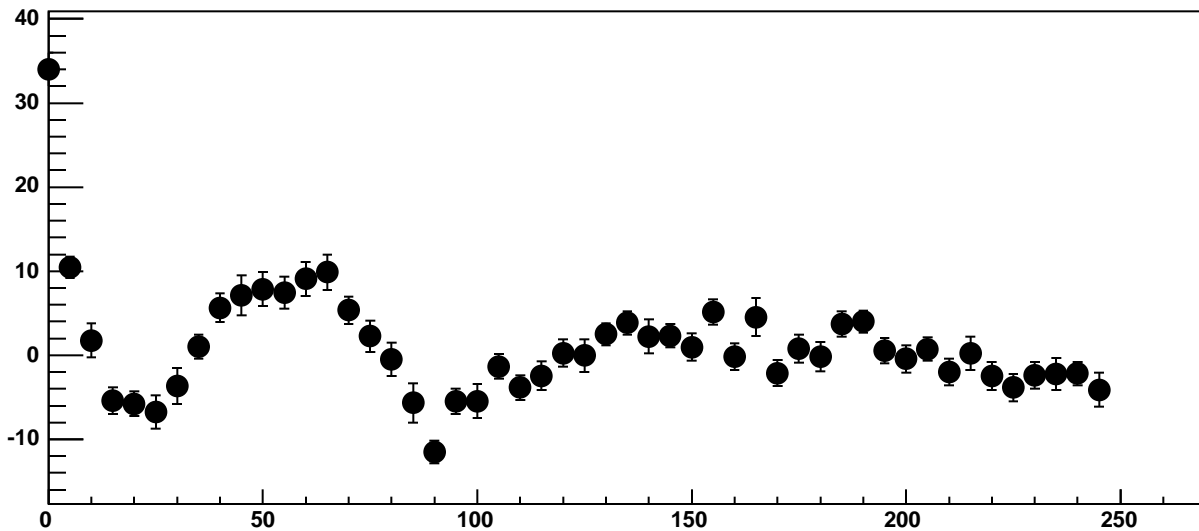


χ^2 / ndf	322.7 / 41
p0	-130.8 ± 0.5583
p1	90.41 ± 0.4733
p2	$-5.544\text{e}+07 \pm 6.17\text{e}+05$
p3	$2.559\text{e}+07 \pm 1.812\text{e}+05$
p4	2.037 ± 0.01819

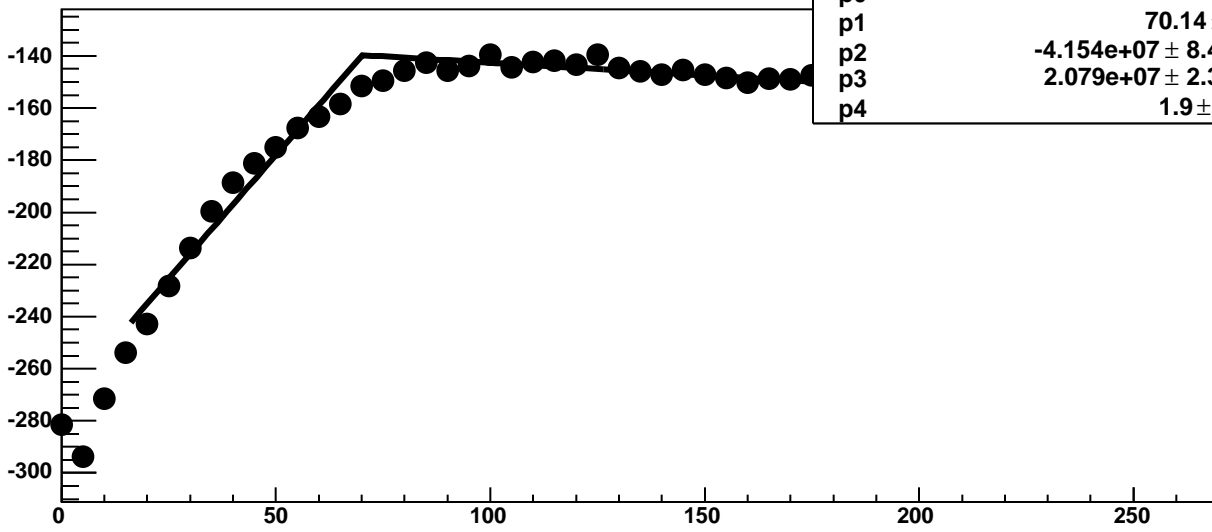
Chip 7, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

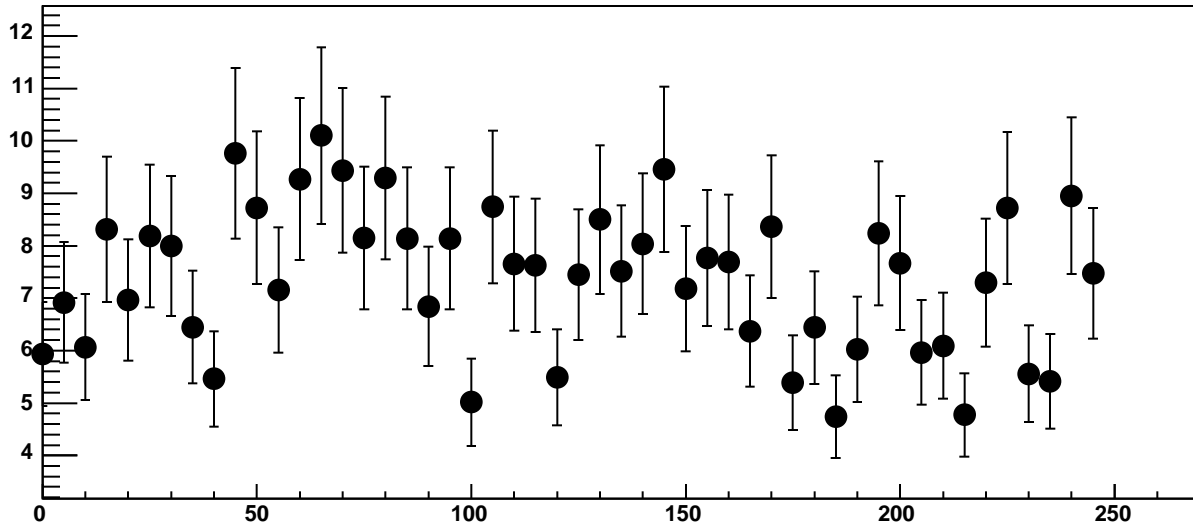


Chip 7, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

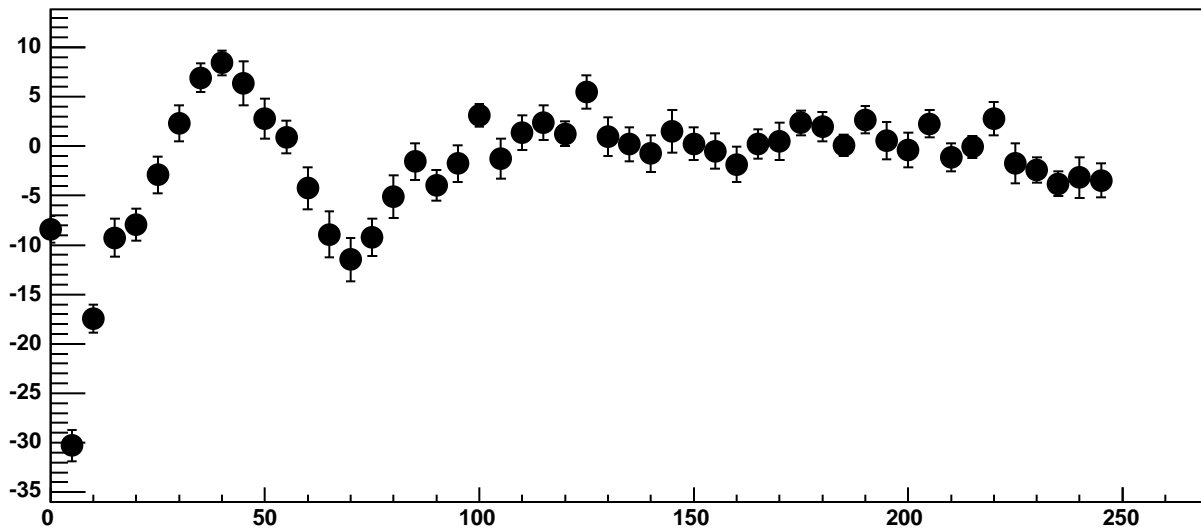


χ^2 / ndf	269.8 / 41
p0	-139.7 ± 0.5559
p1	70.14 ± 0.6191
p2	$-4.154\text{e}+07 \pm 8.498\text{e}+05$
p3	$2.079\text{e}+07 \pm 2.399\text{e}+05$
p4	1.9 ± 0.03191

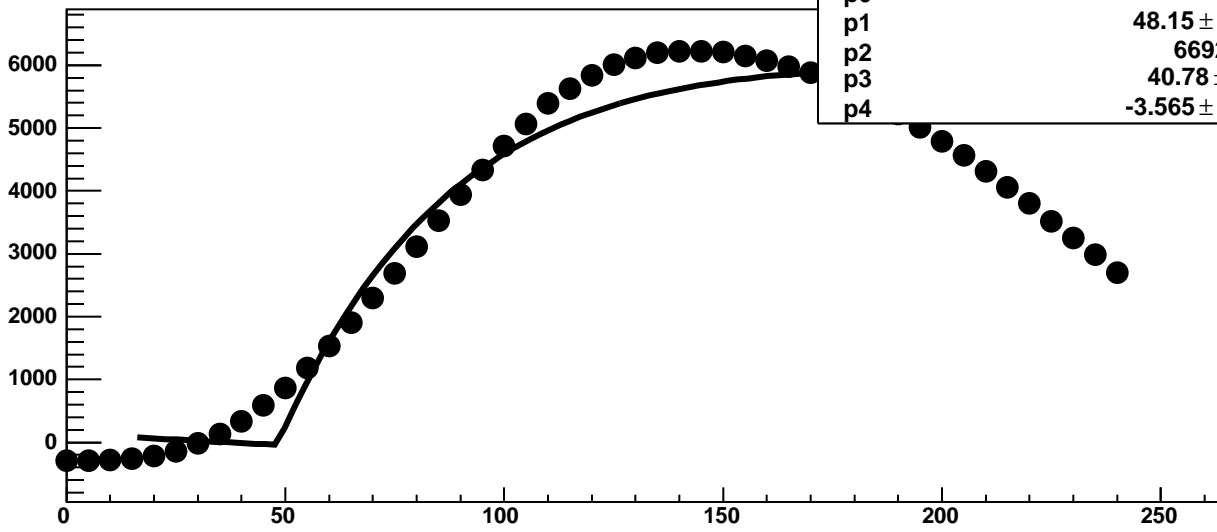
Chip 7, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

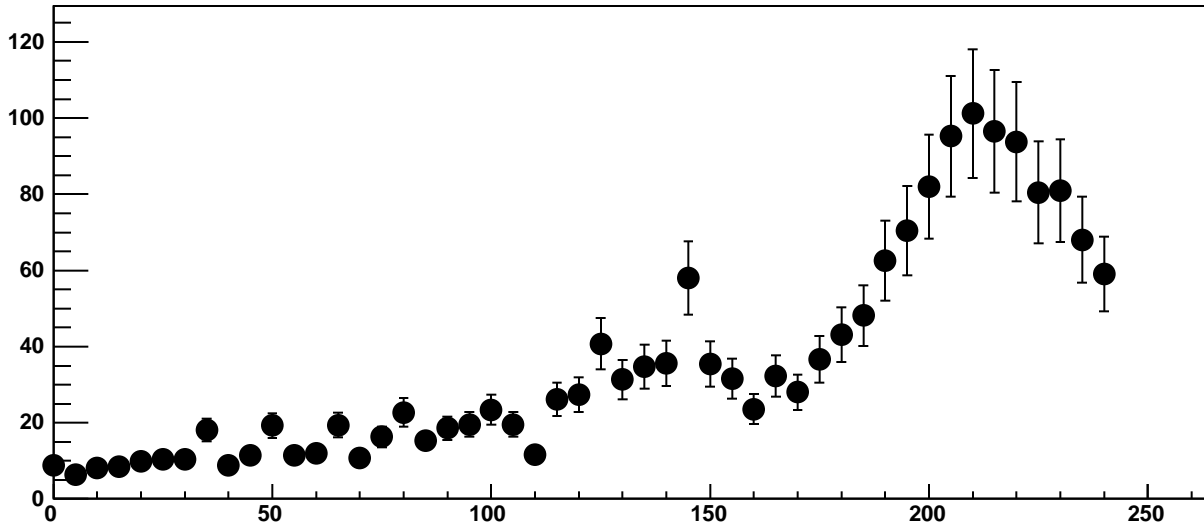


Chip 7, Channel 7, Enable 5, DAC=4000, ADC Mean vs Hold

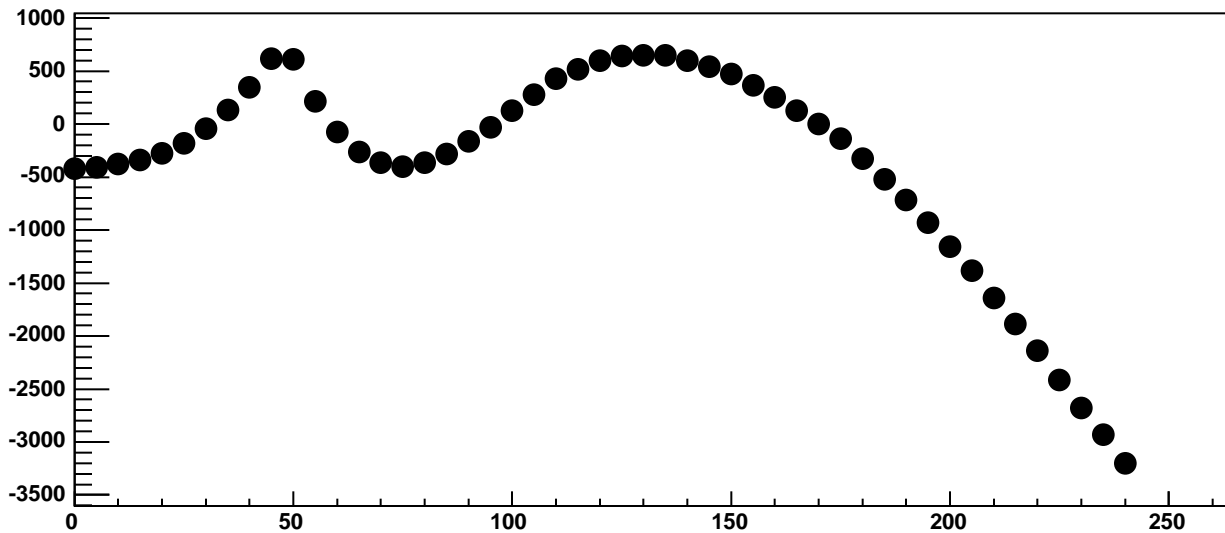


χ^2 / ndf	4.623e+05 / 41
p0	-38.42 ± 1.404
p1	48.15 ± 0.01736
p2	6692 ± 9.39
p3	40.78 ± 0.0602
p4	-3.565 ± 0.05646

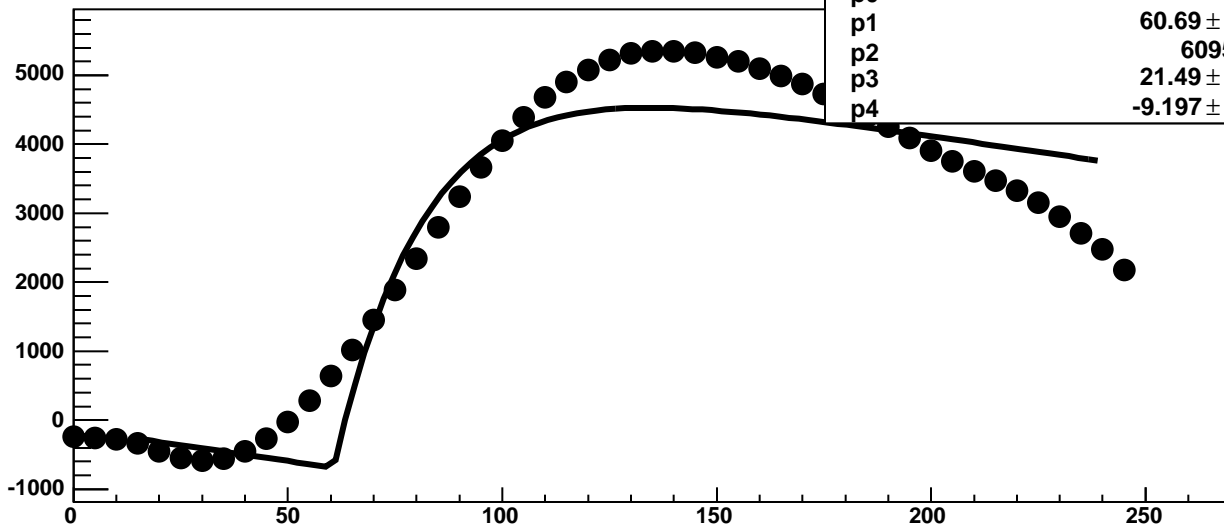
Chip 7, Channel 7, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 7, Enable 5, DAC=4000, ADC Residuals vs Hold

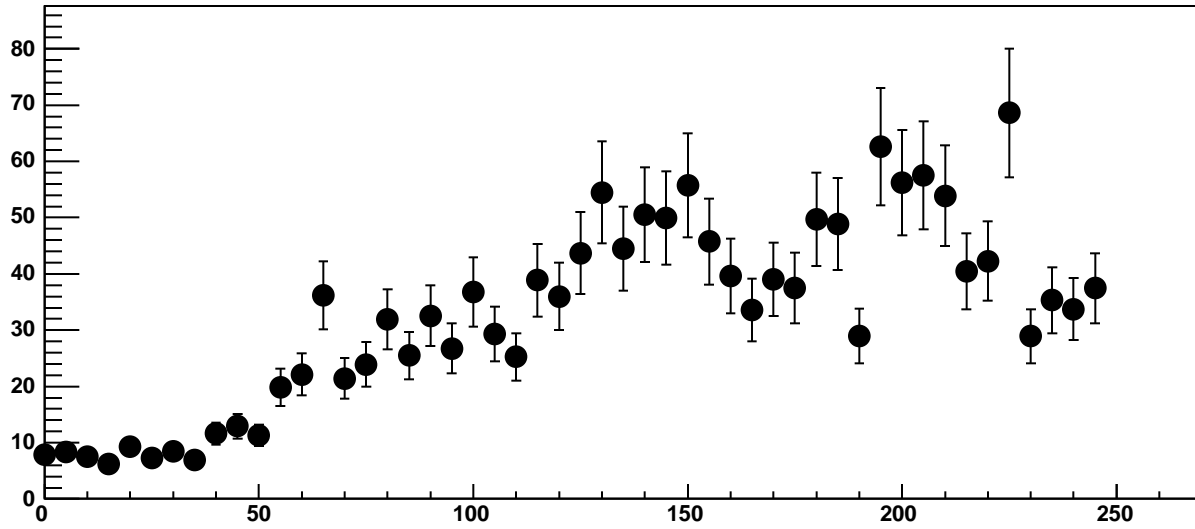


Chip 7, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

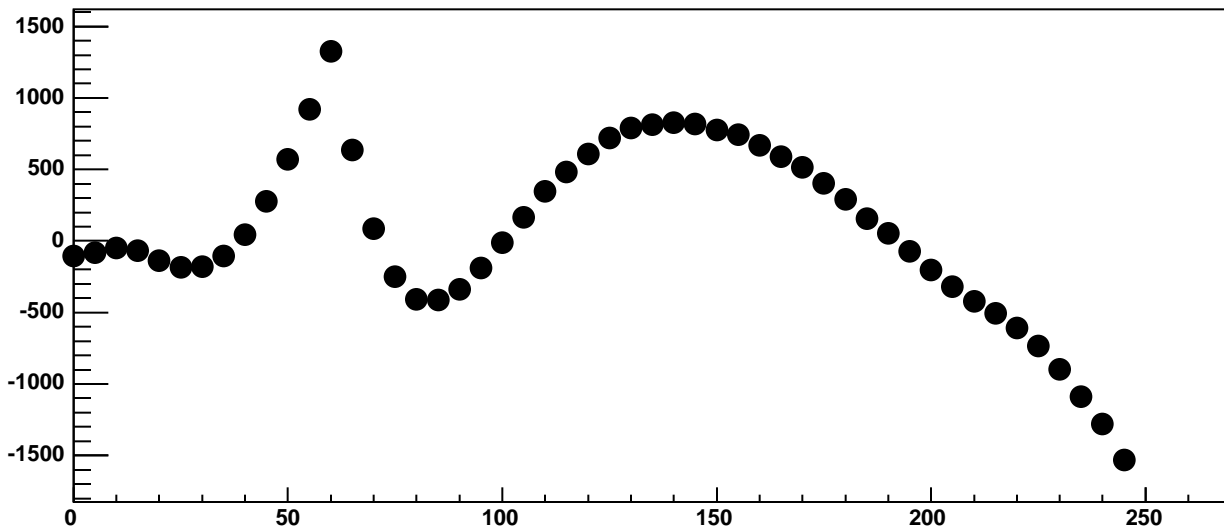


χ^2 / ndf	3.576e+05 / 41
p0	-691 ± 1.318
p1	60.69 ± 0.02707
p2	6095 ± 6.32
p3	21.49 ± 0.04525
p4	-9.197 ± 0.03796

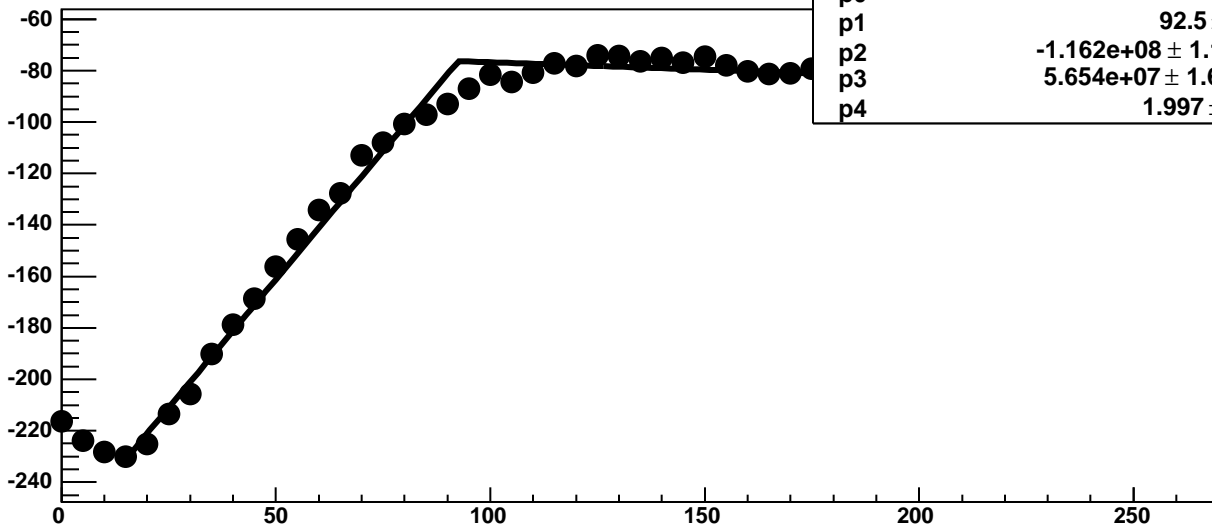
Chip 7, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

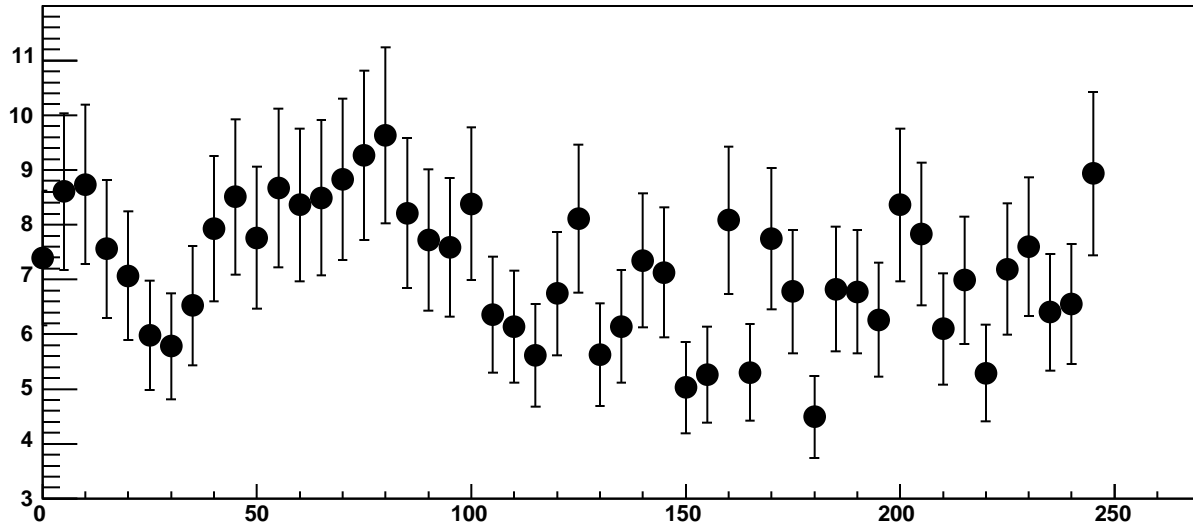


Chip 7, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

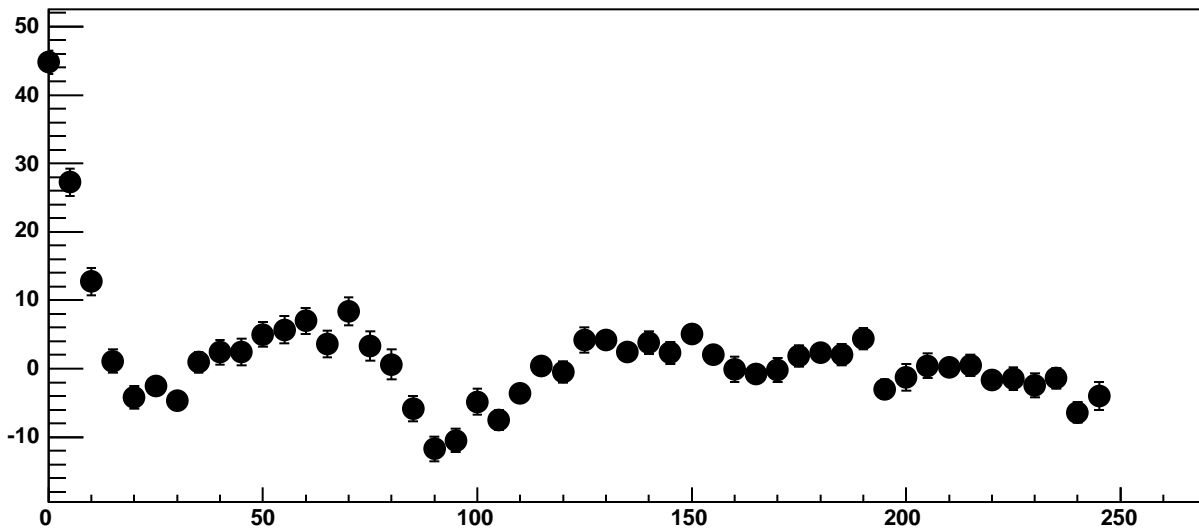


χ^2 / ndf	301.8 / 41
p0	-76.27 ± 0.5384
p1	92.5 ± 0.5351
p2	-1.162e+08 ± 1.179e+06
p3	5.654e+07 ± 1.697e+05
p4	1.997 ± 0.0189

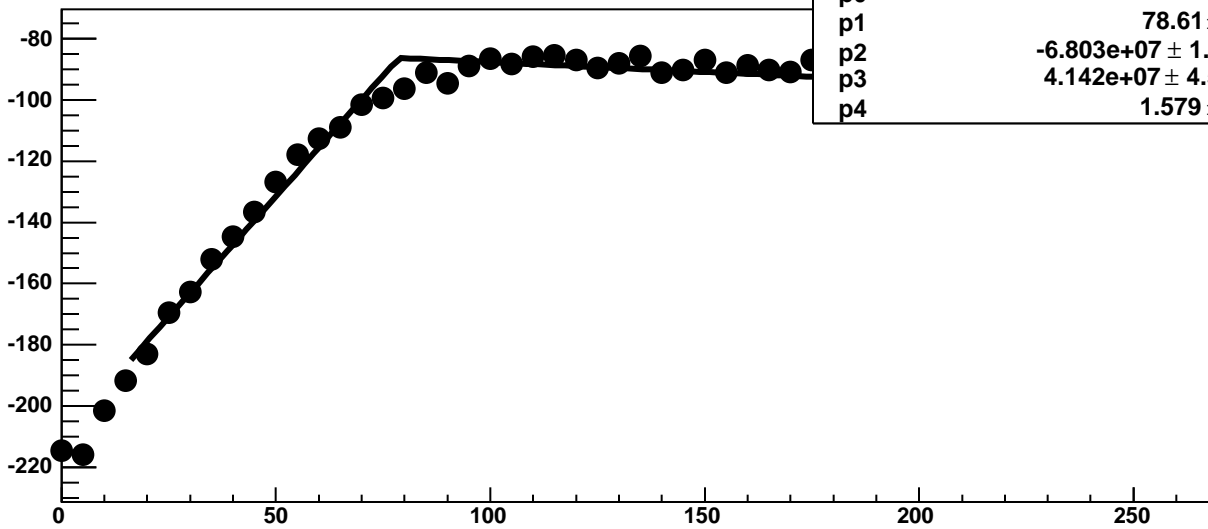
Chip 7, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

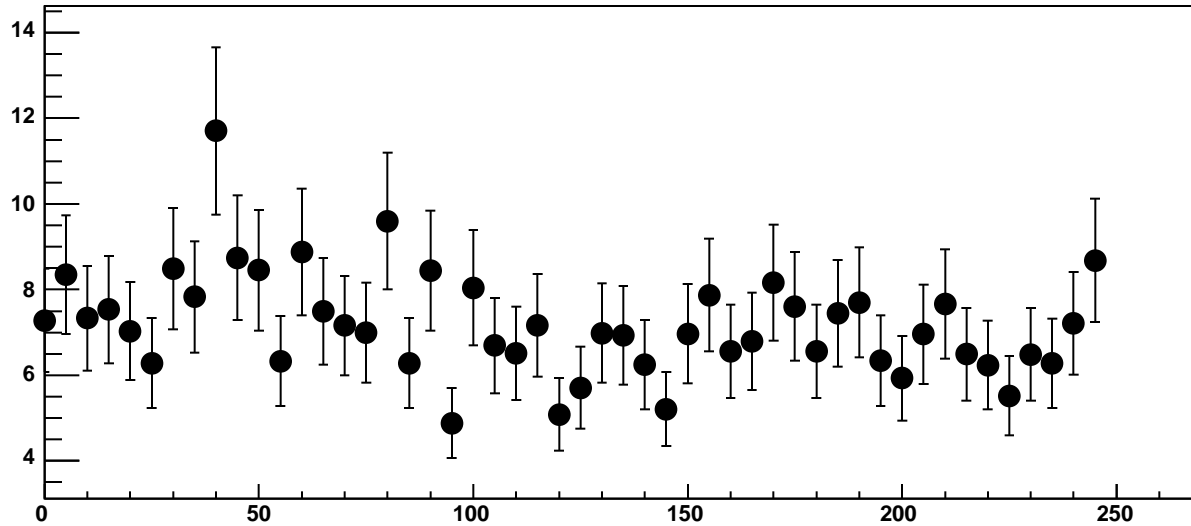


Chip 7, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold

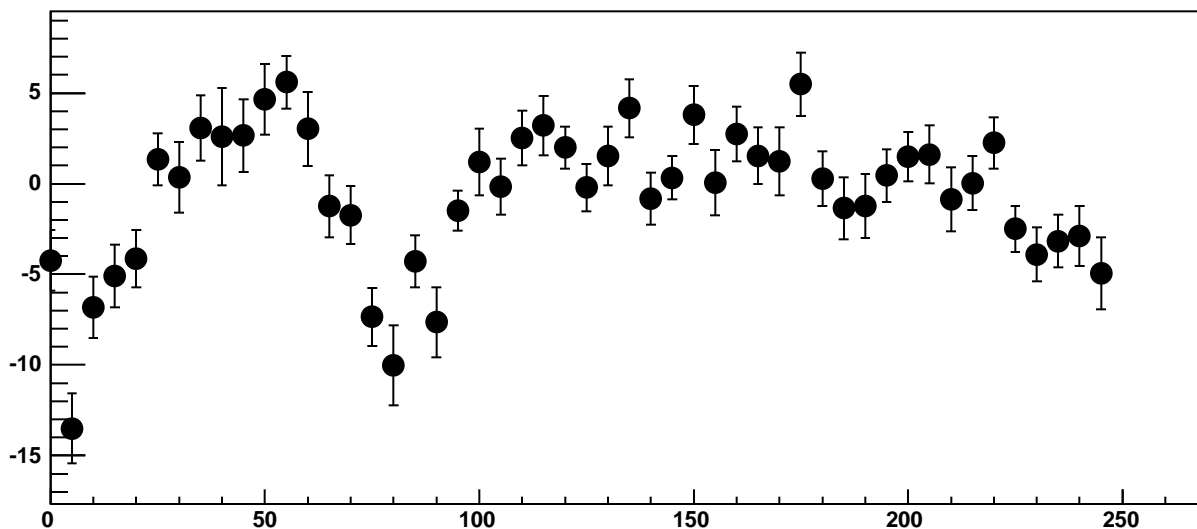


χ^2 / ndf	177.8 / 41
p0	-86.26 ± 0.5104
p1	78.61 ± 0.6685
p2	$-6.803\text{e}+07 \pm 1.275\text{e}+06$
p3	$4.142\text{e}+07 \pm 4.573\text{e}+05$
p4	1.579 ± 0.0246

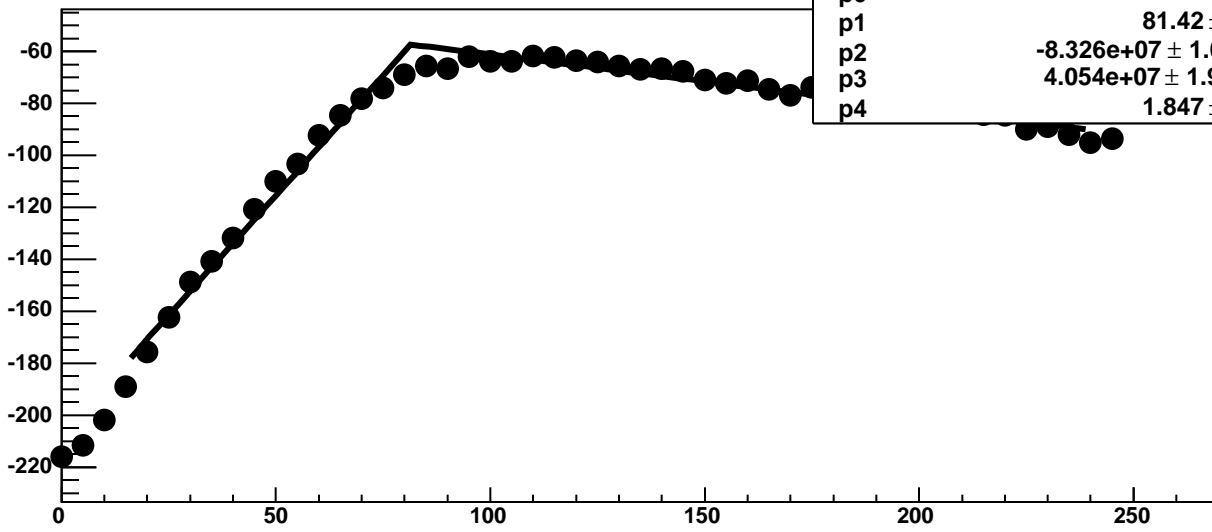
Chip 7, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold

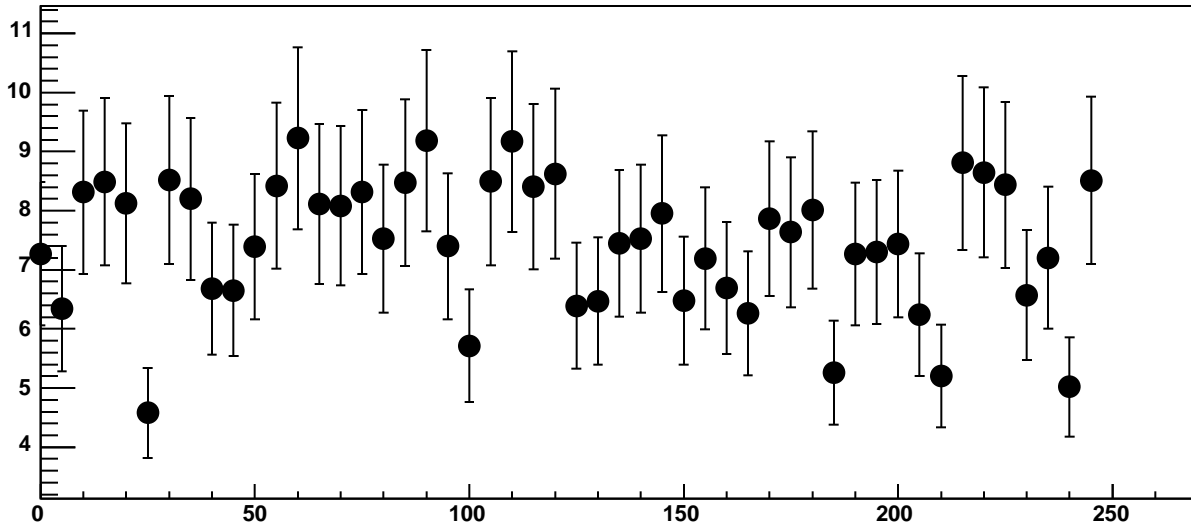


Chip 7, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

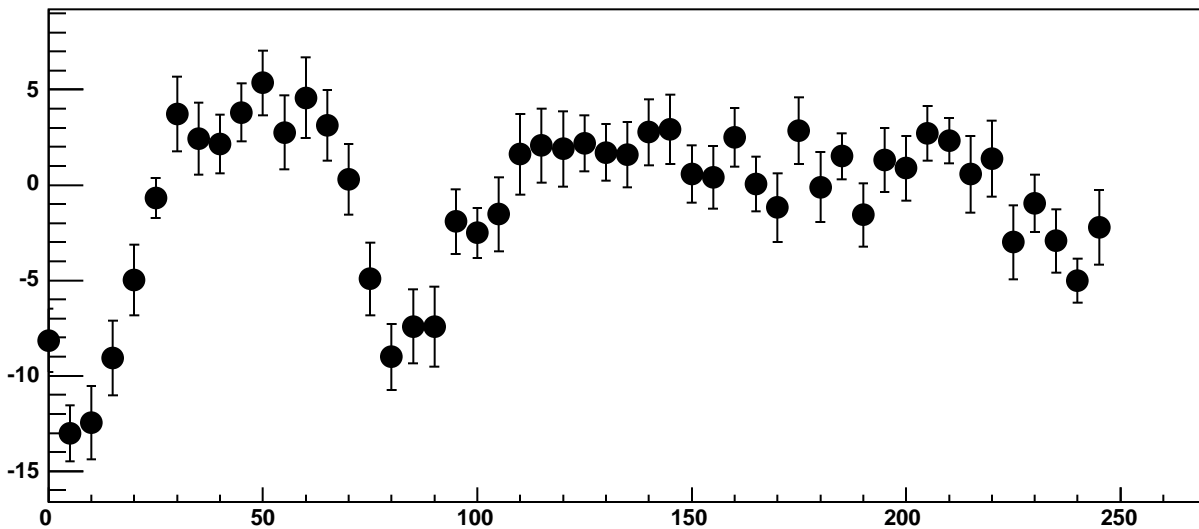


χ^2 / ndf	182.5 / 41
p0	-57.38 ± 0.5566
p1	81.42 ± 0.5537
p2	$-8.326\text{e}+07 \pm 1.038\text{e}+06$
p3	$4.054\text{e}+07 \pm 1.947\text{e}+05$
p4	1.847 ± 0.0229

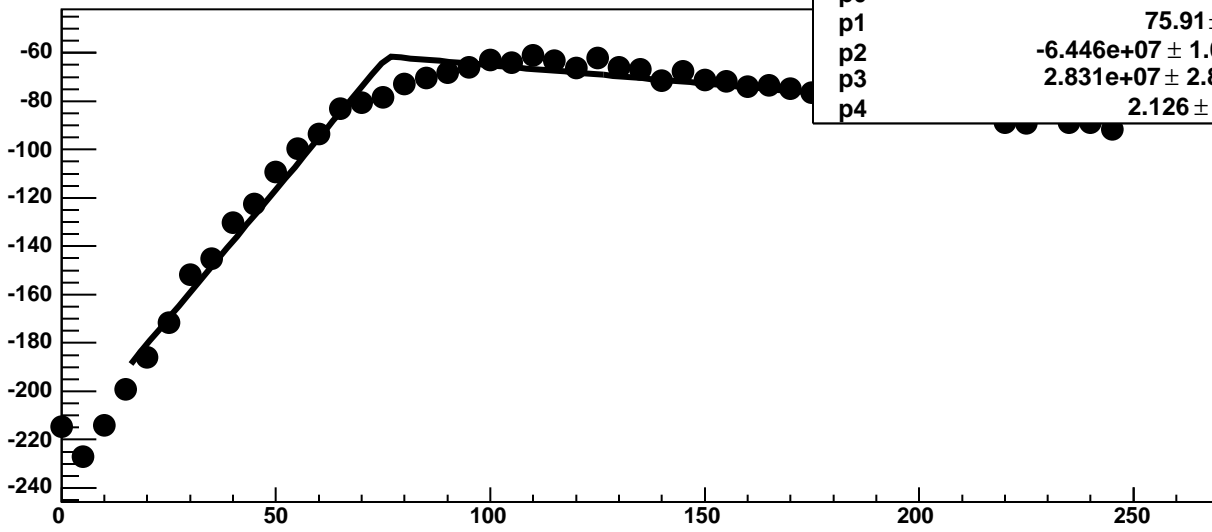
Chip 7, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



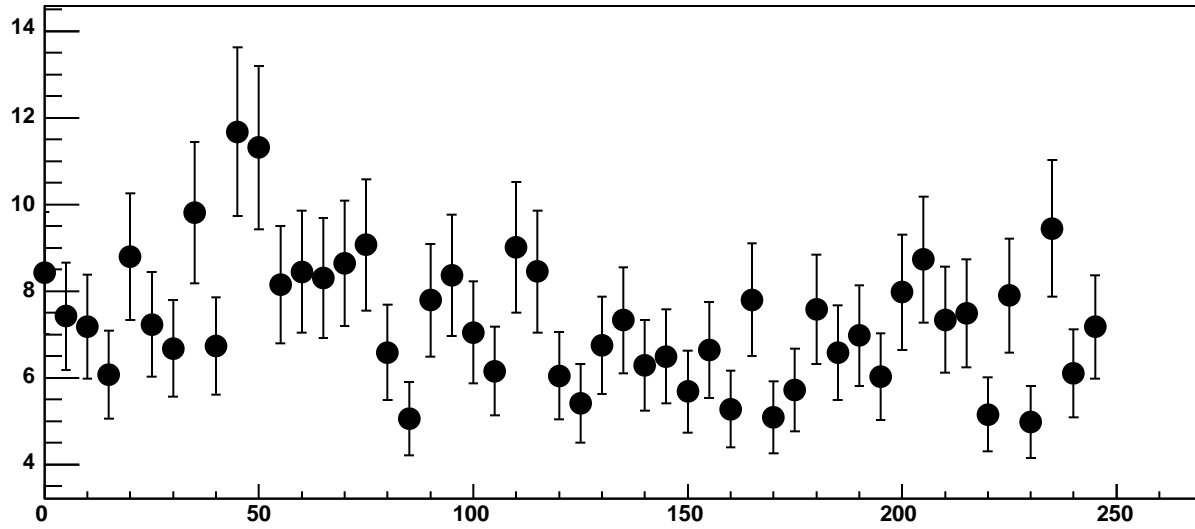
Chip 7, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold



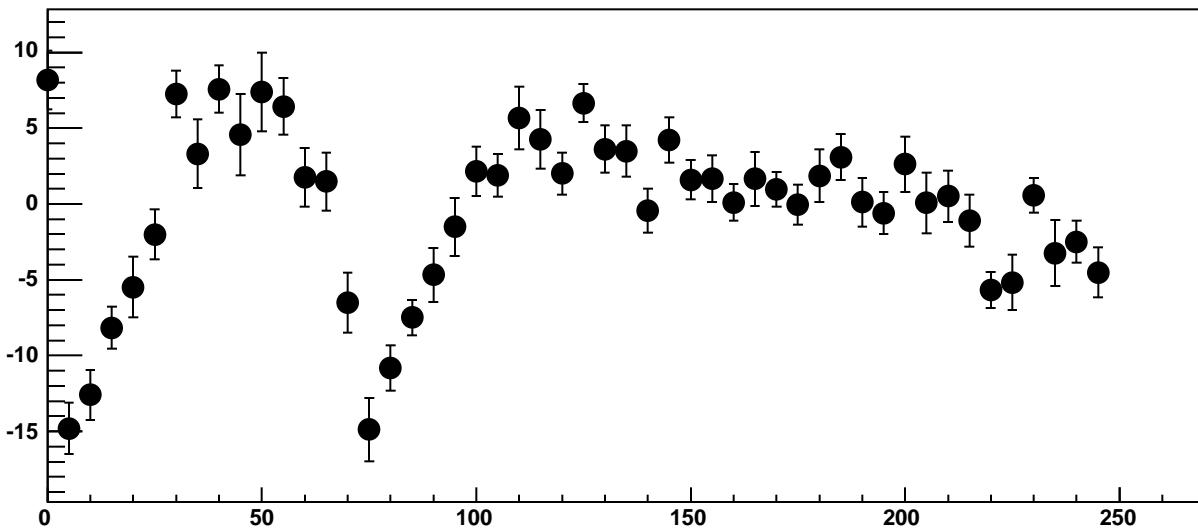
Chip 7, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold



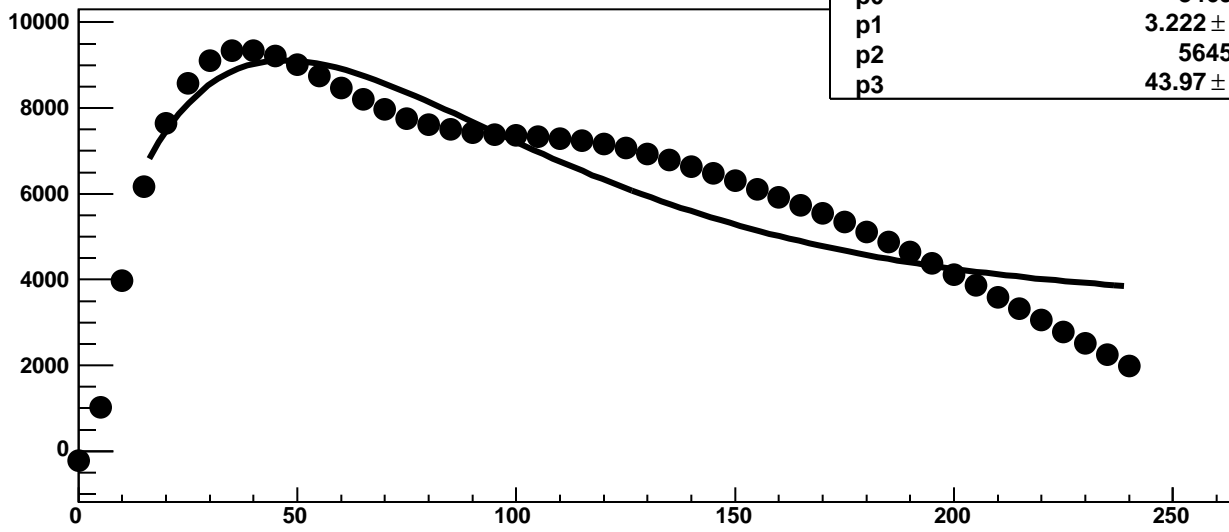
Chip 7, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

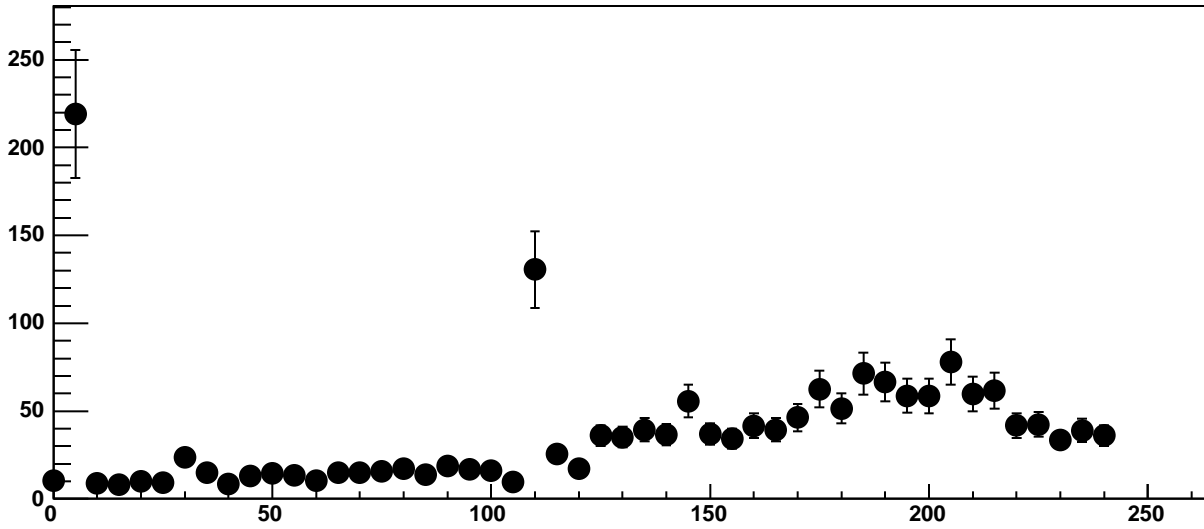


Chip 7, Channel 8, Enable 5!, DAC=4000, ADC Mean vs Hold

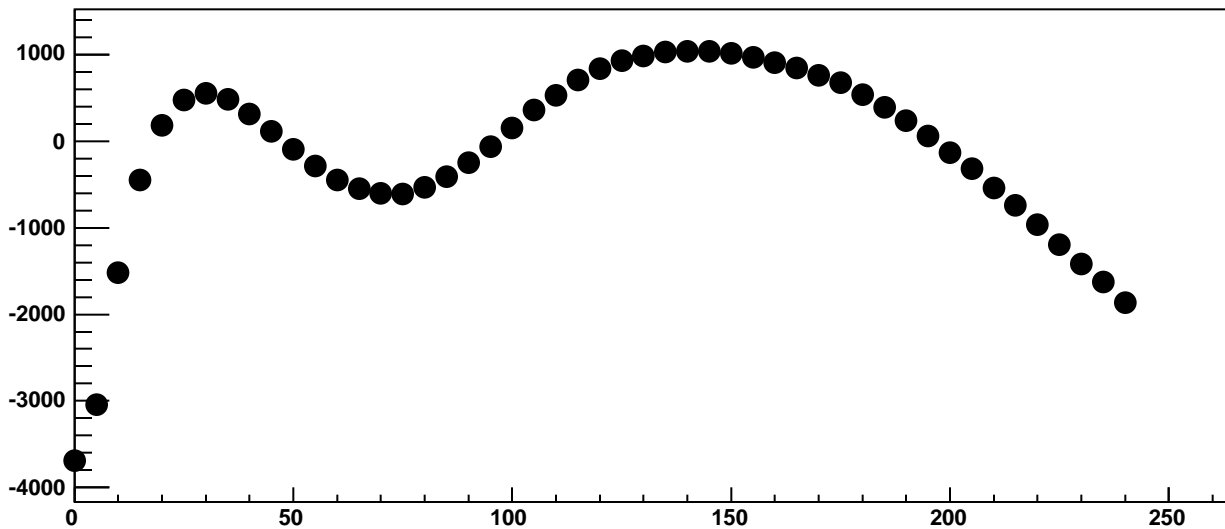


χ^2 / ndf	6.89e+05 / 42
p0	3469 ± 5.297
p1	3.222 ± 0.02949
p2	5645 ± 5.022
p3	43.97 ± 0.04188

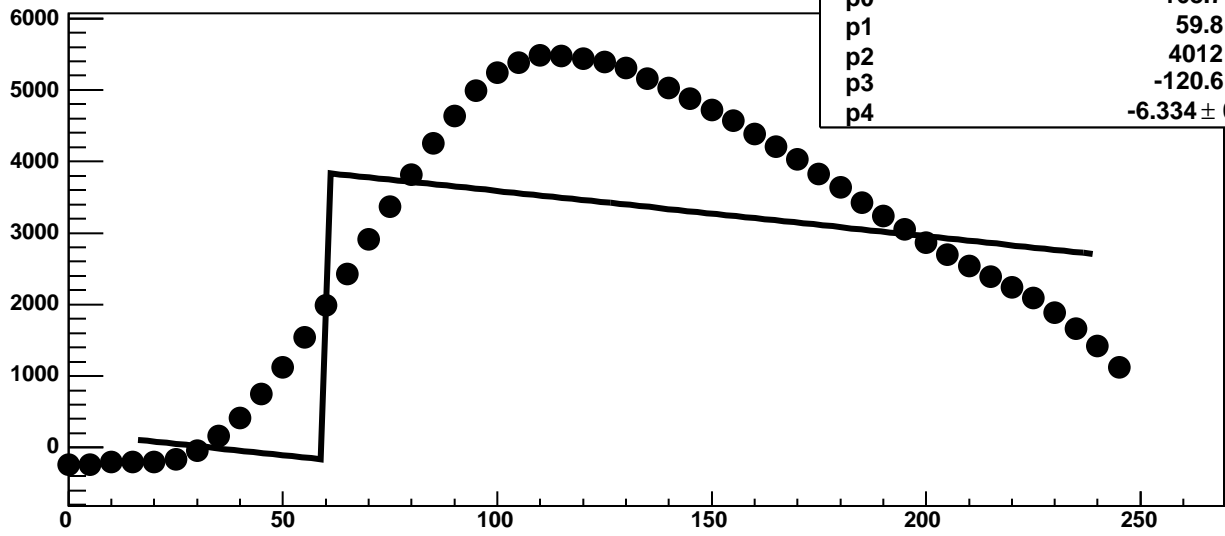
Chip 7, Channel 8, Enable 5!, DAC=4000, ADC Noise vs Hold



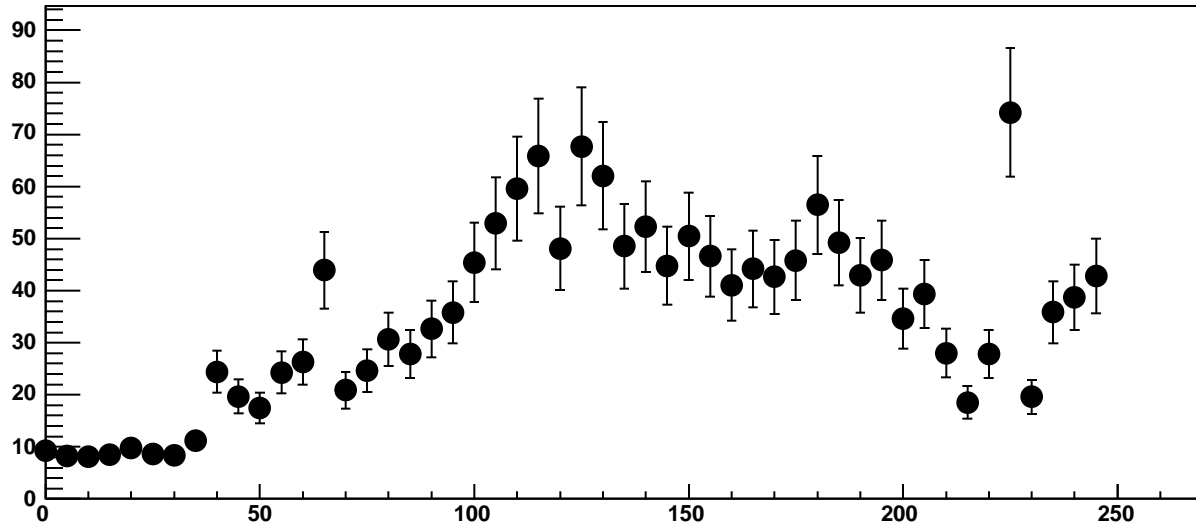
Chip 7, Channel 8, Enable 5!, DAC=4000, ADC Residuals vs Hold



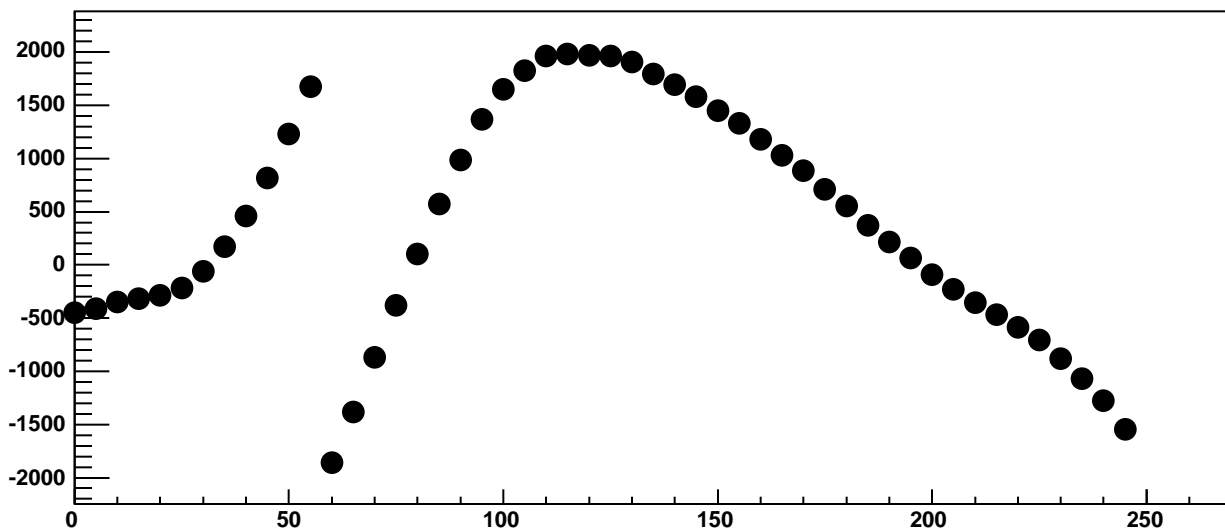
Chip 7, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold



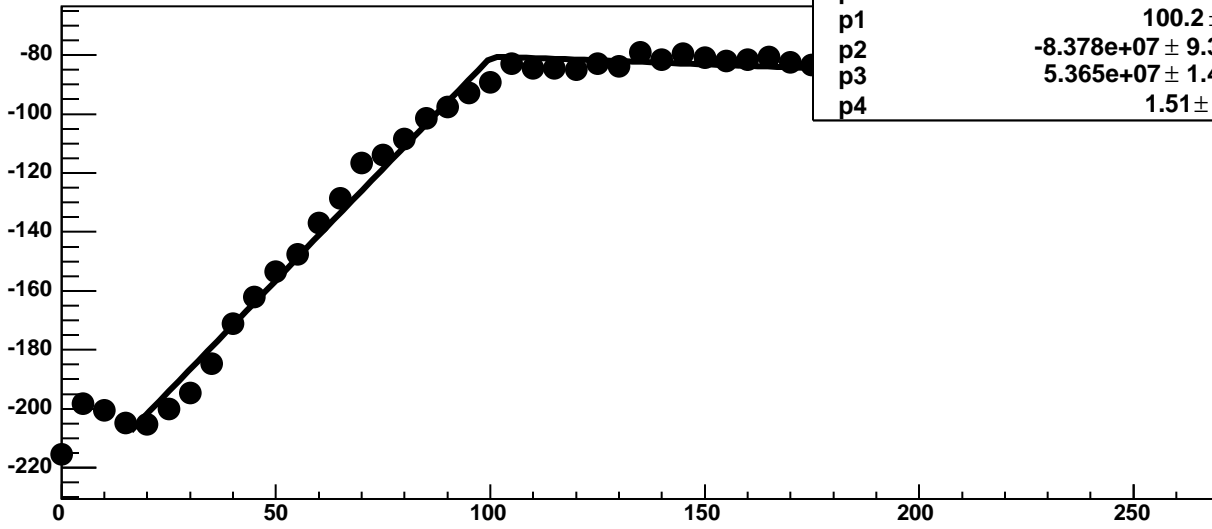
Chip 7, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold

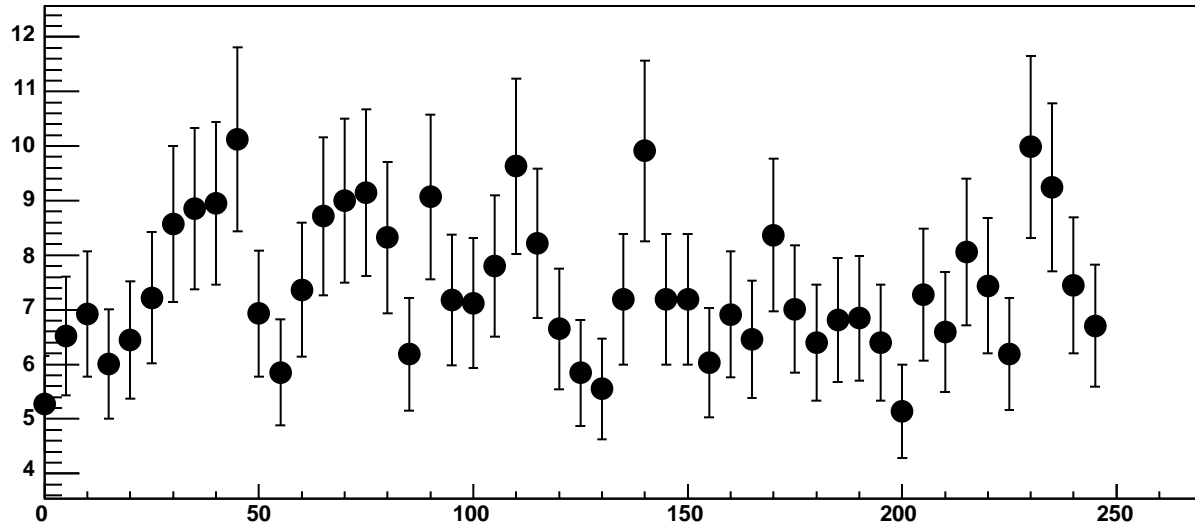


Chip 7, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

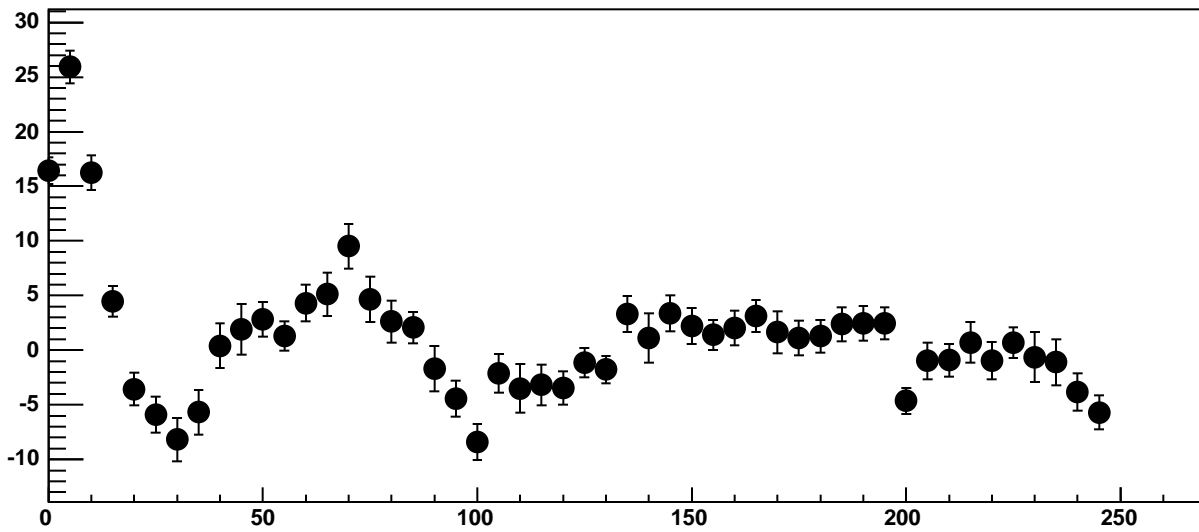


χ^2 / ndf	200.2 / 41
p0	-80.53 ± 0.6249
p1	100.2 ± 0.6463
p2	$-8.378e+07 \pm 9.312e+05$
p3	$5.365e+07 \pm 1.449e+05$
p4	1.51 ± 0.01495

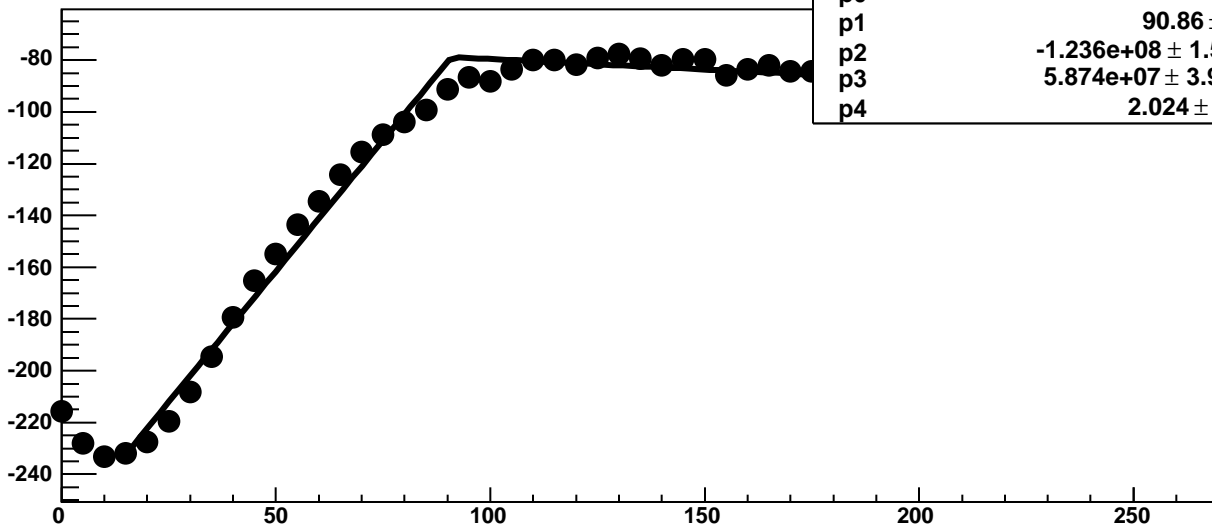
Chip 7, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

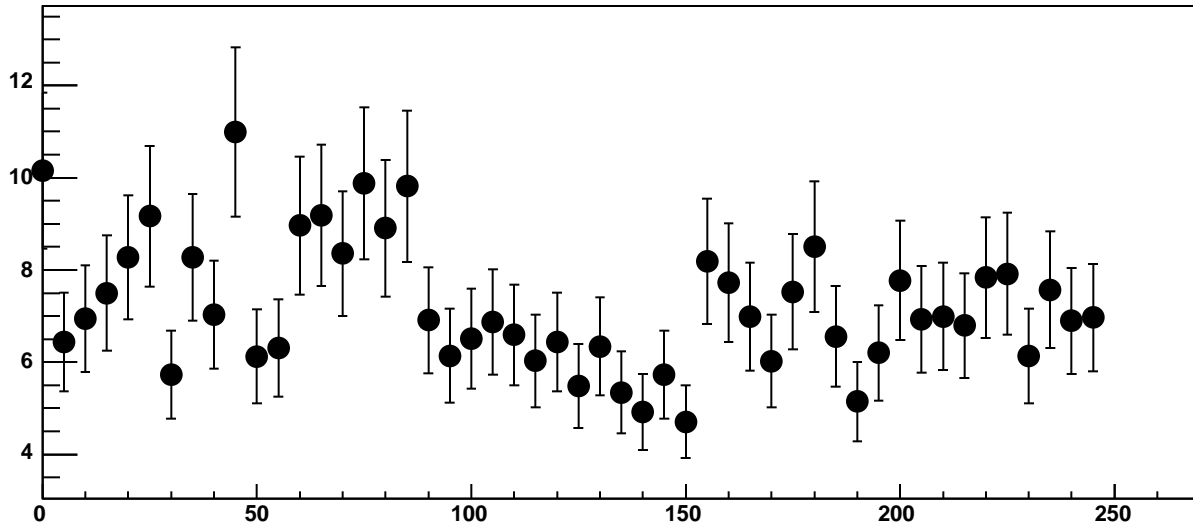


Chip 7, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

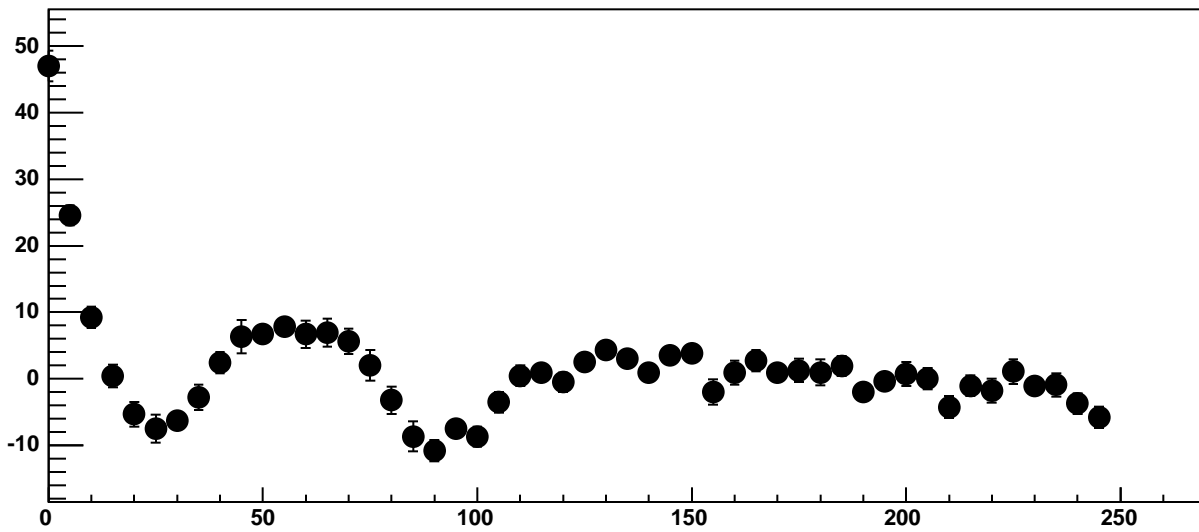


χ^2 / ndf	332.6 / 41
p0	-78.83 ± 0.5131
p1	90.86 ± 0.5048
p2	$-1.236\text{e}+08 \pm 1.518\text{e}+06$
p3	$5.874\text{e}+07 \pm 3.933\text{e}+05$
p4	2.024 ± 0.01977

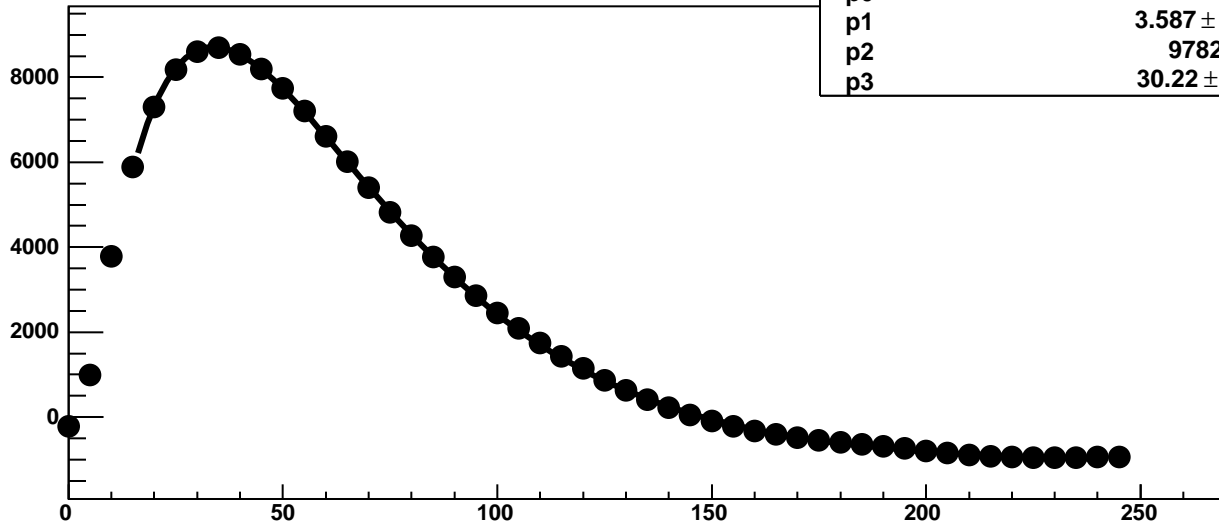
Chip 7, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold

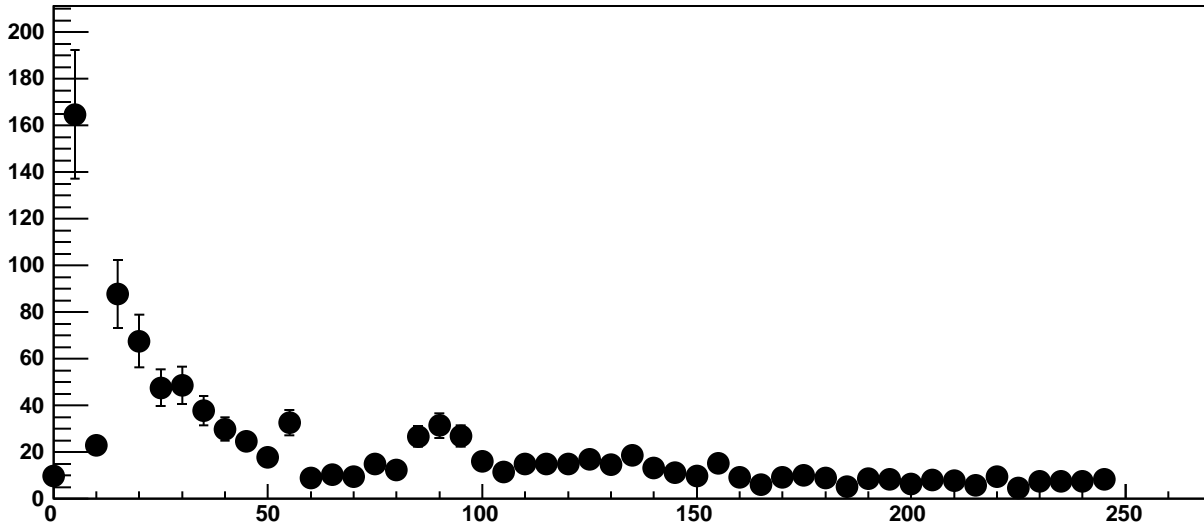


Chip 7, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold

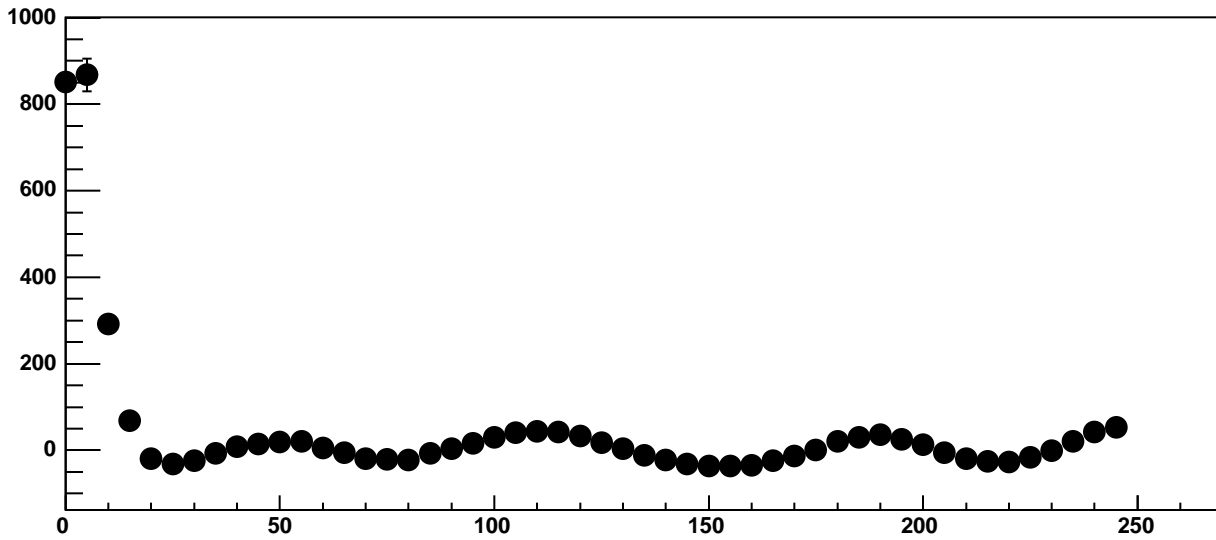


χ^2 / ndf	5010 / 42
p0	-1068 ± 0.6195
p1	3.587 ± 0.02916
p2	9782 ± 2.882
p3	30.22 ± 0.01103

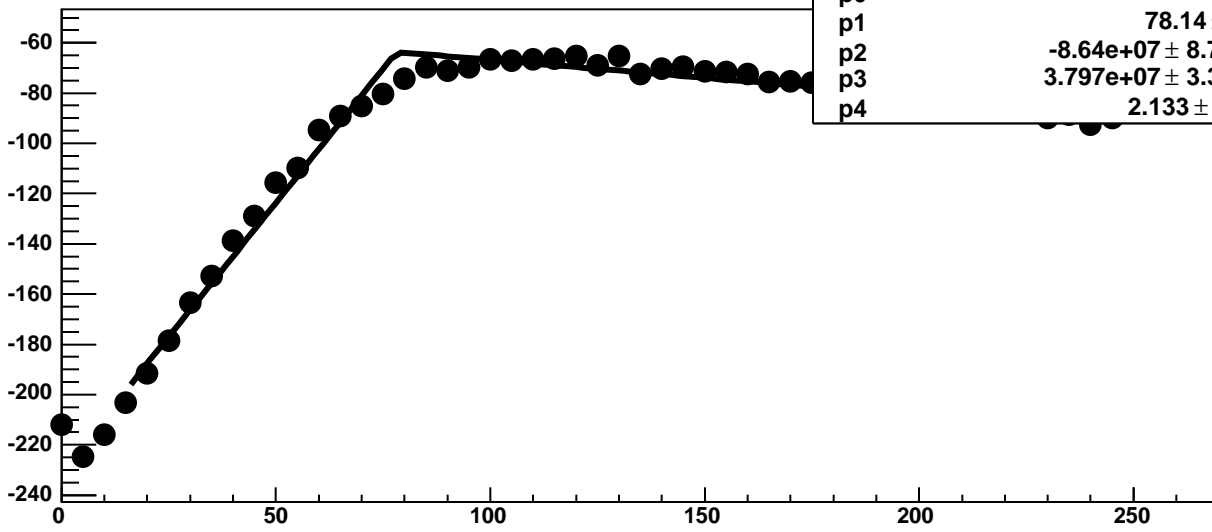
Chip 7, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold

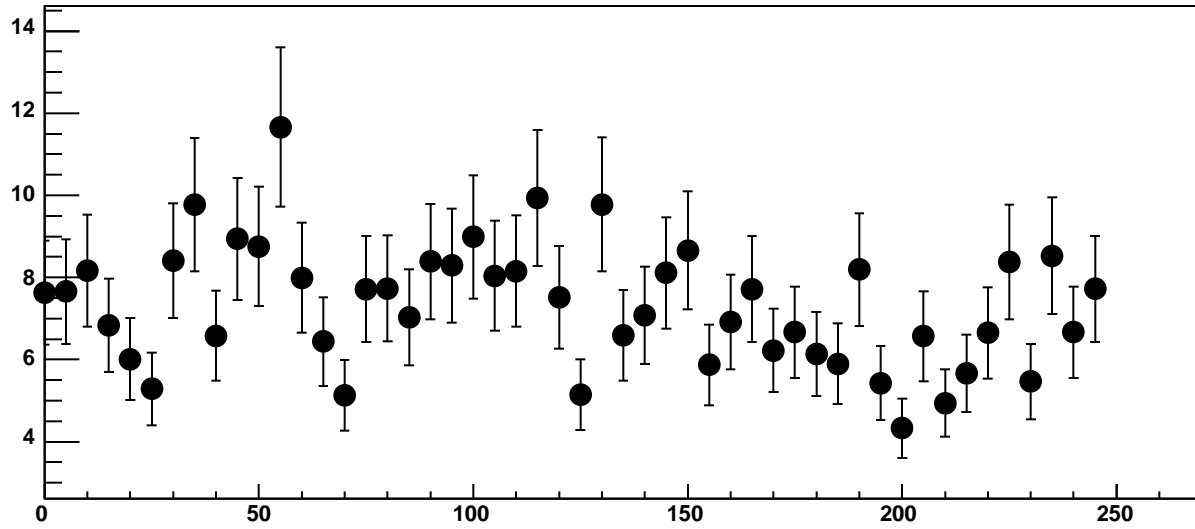


Chip 7, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

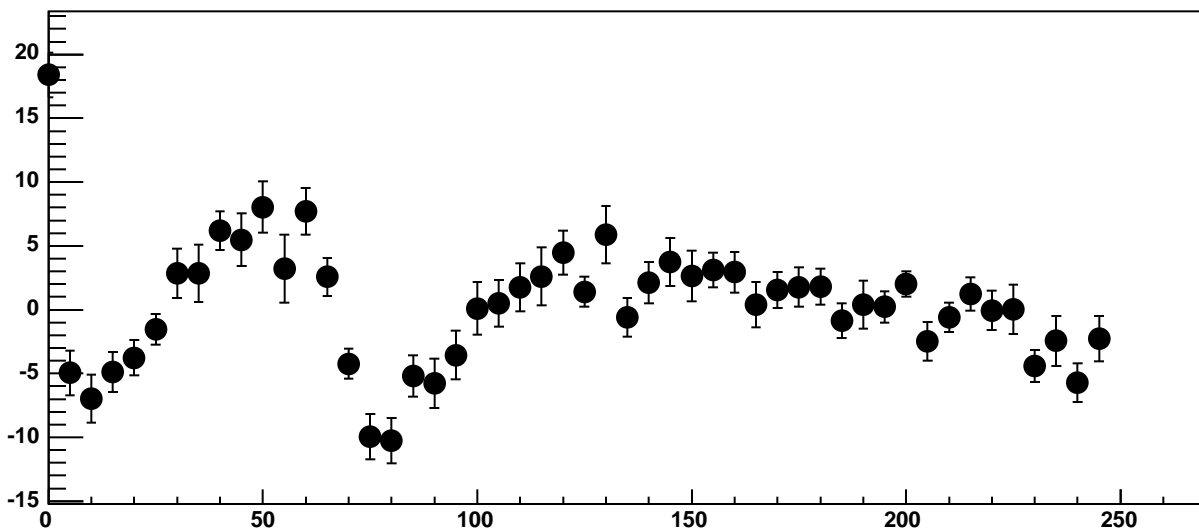


χ^2 / ndf	260.2 / 41
p0	-63.65 ± 0.5687
p1	78.14 ± 0.4681
p2	$-8.64\text{e}+07 \pm 8.703\text{e}+05$
p3	$3.797\text{e}+07 \pm 3.338\text{e}+05$
p4	2.133 ± 0.02292

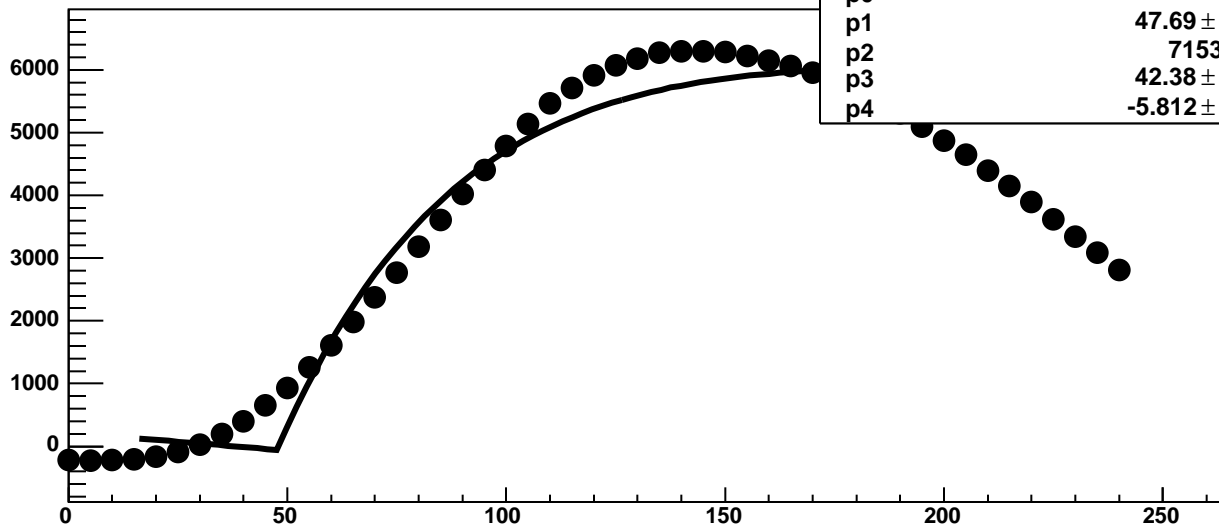
Chip 7, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

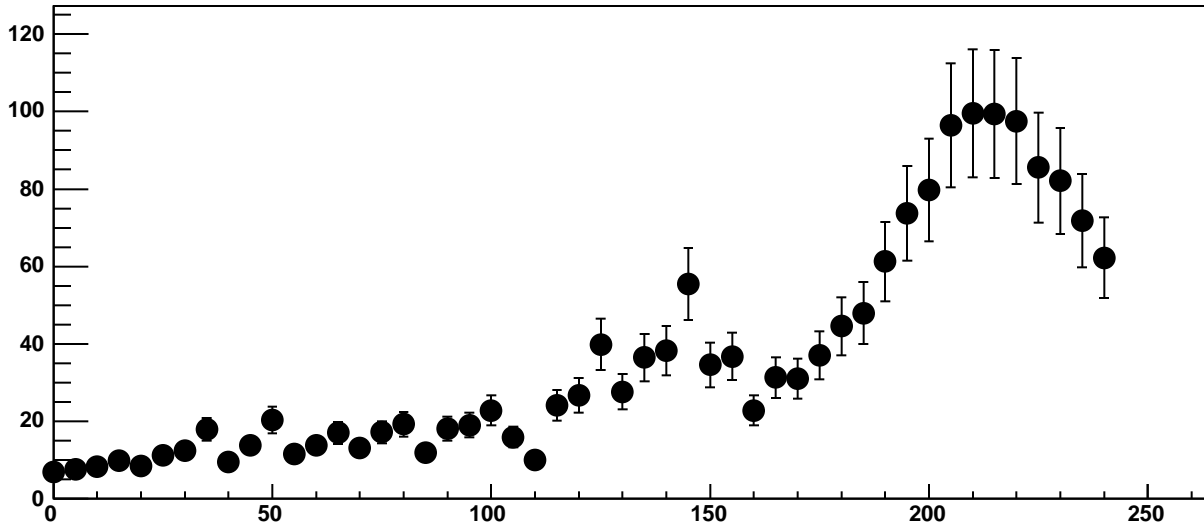


Chip 7, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold

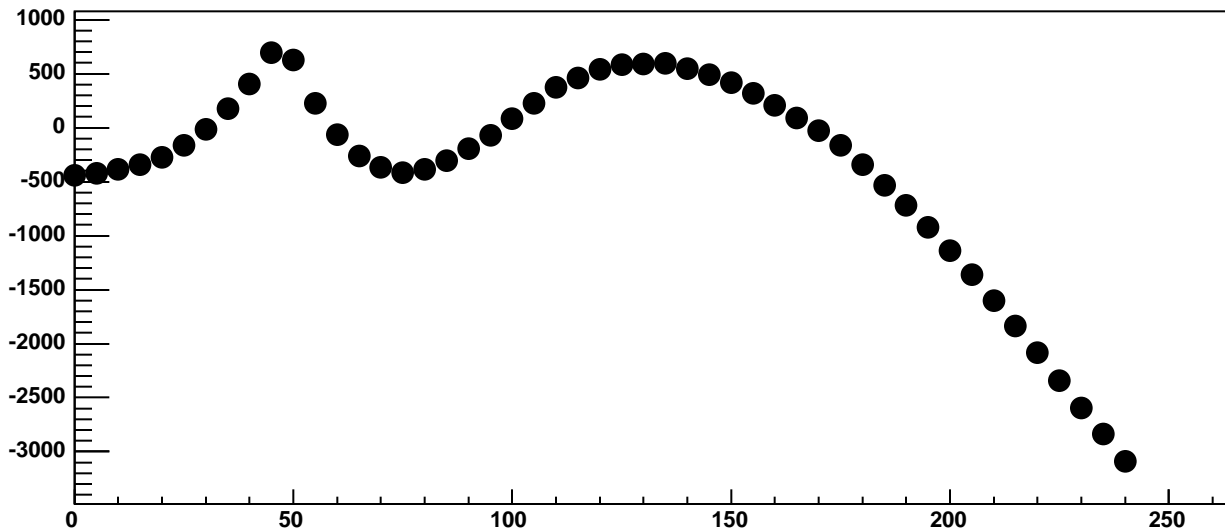


χ^2 / ndf	4.303e+05 / 41
p0	-57.08 ± 1.509
p1	47.69 ± 0.01833
p2	7153 ± 10.57
p3	42.38 ± 0.06627
p4	-5.812 ± 0.06157

Chip 7, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold

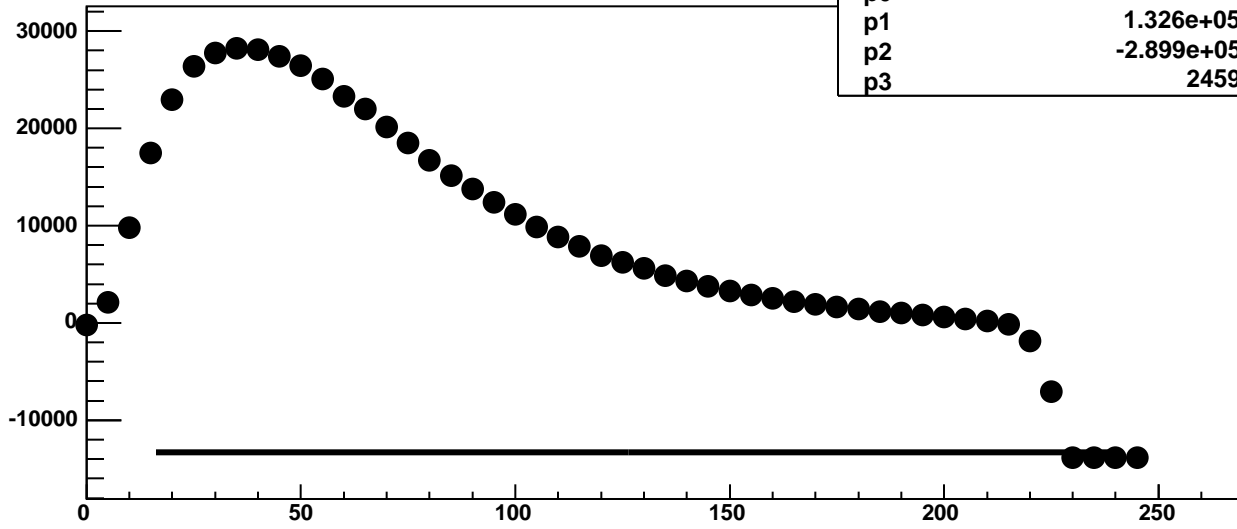


Chip 7, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold

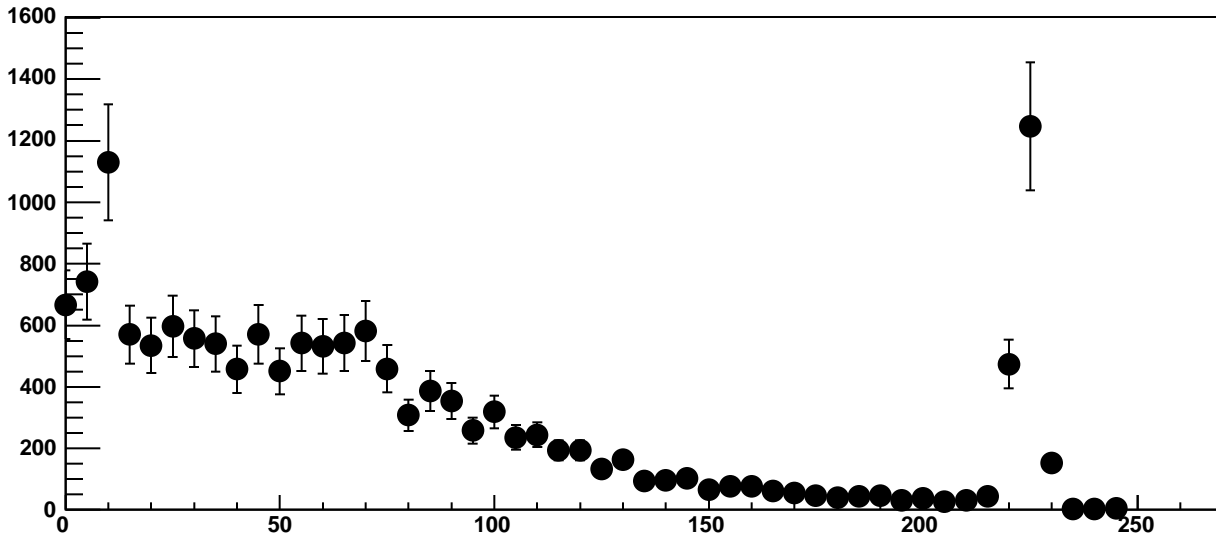


Chip 7, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold

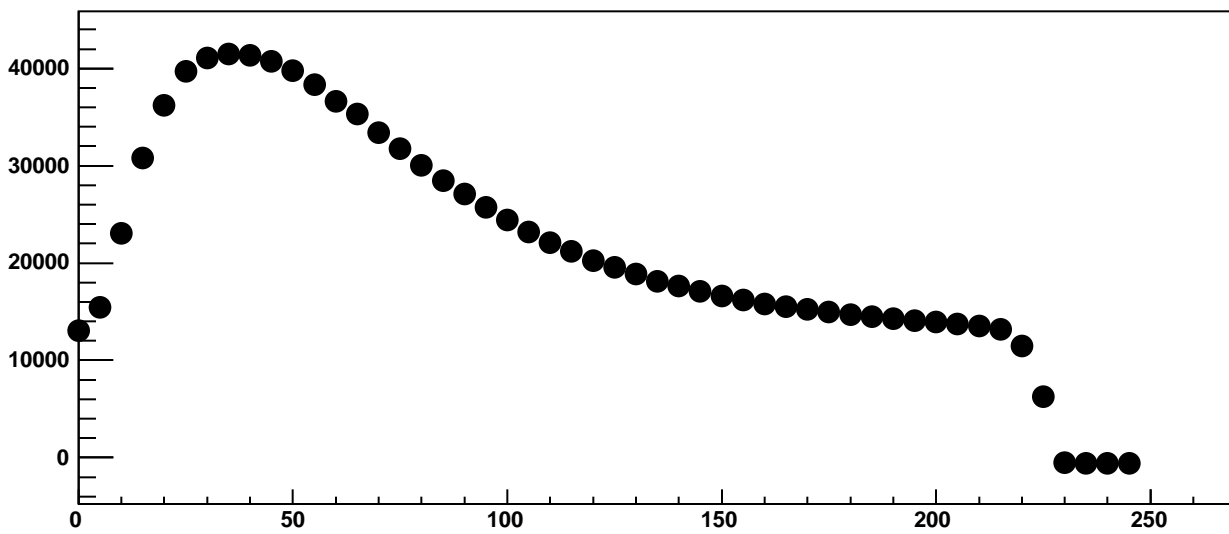
χ^2 / ndf	3.676e+07 / 42
p0	-1.33e+04 ± 0.4792
p1	1.326e+05 ± 1.909
p2	-2.899e+05 ± 1.414
p3	2459 ± 1.414



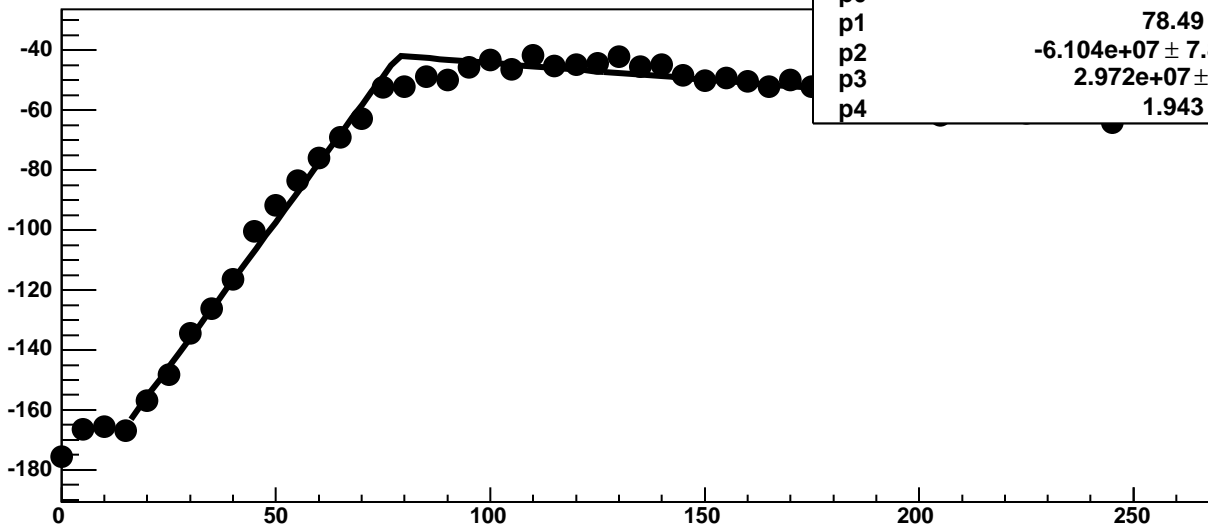
Chip 7, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold

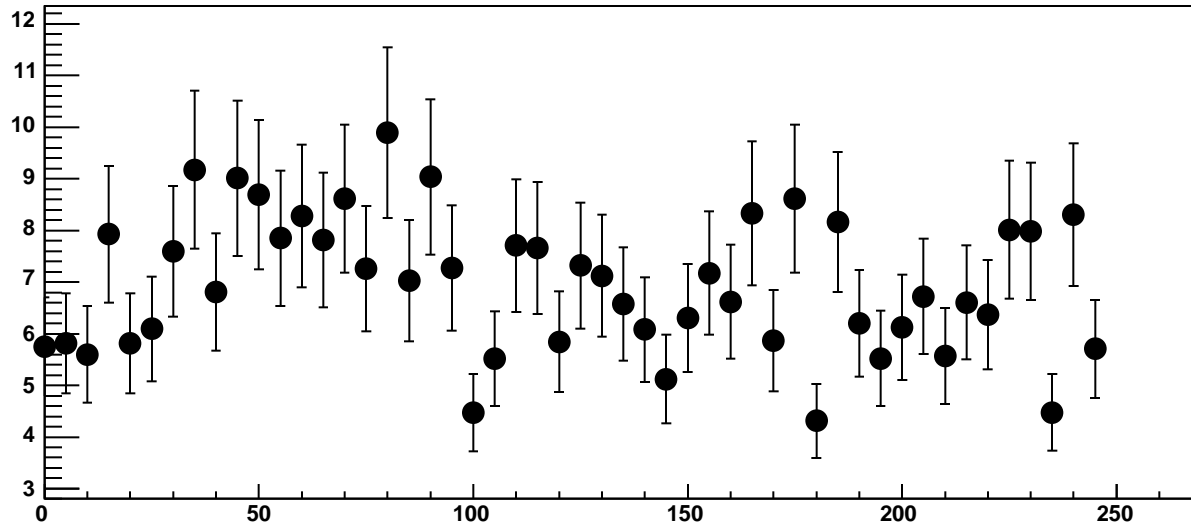


Chip 7, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

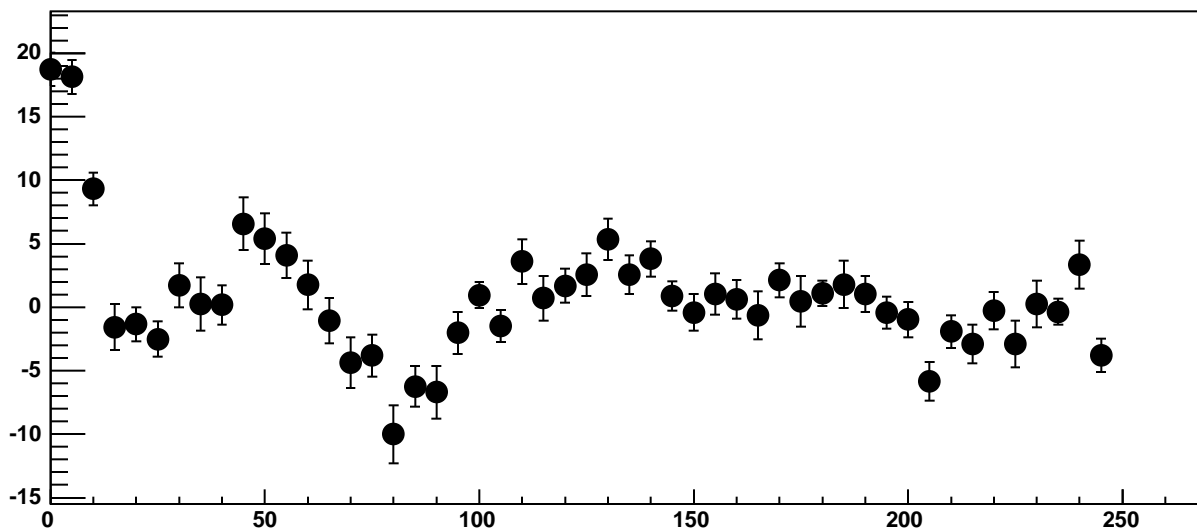


χ^2 / ndf	150.5 / 41
p0	-41.88 ± 0.5025
p1	78.49 ± 0.5547
p2	$-6.104\text{e}+07 \pm 7.877\text{e}+05$
p3	$2.972\text{e}+07 \pm 1.2\text{e}+05$
p4	1.943 ± 0.0248

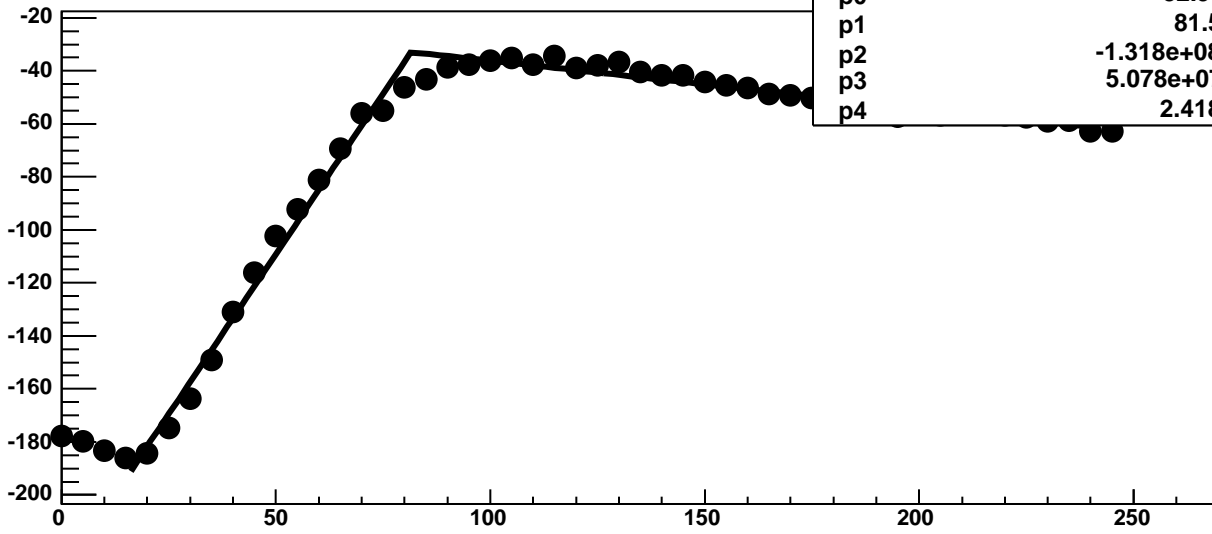
Chip 7, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

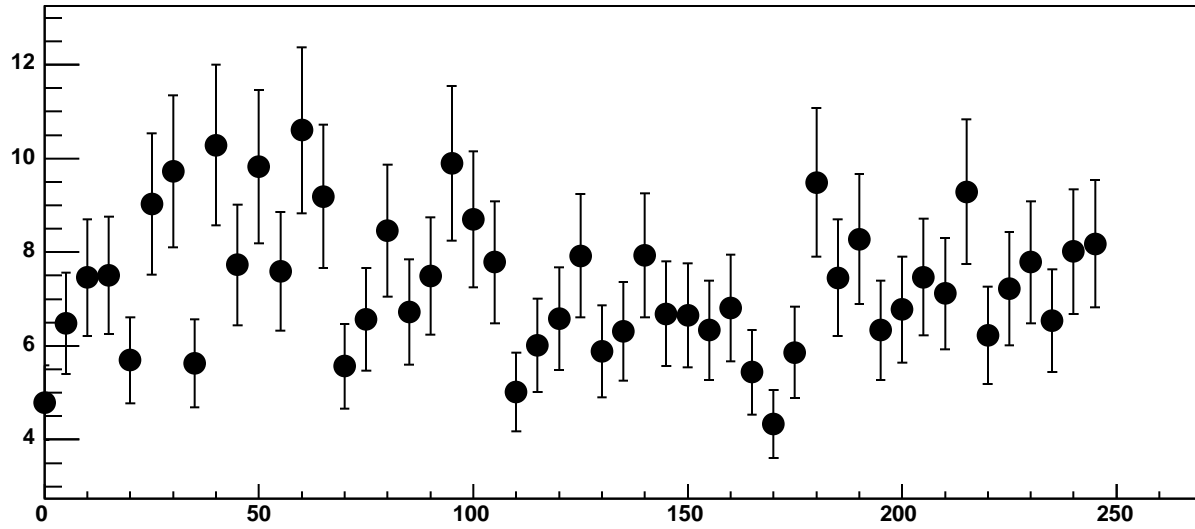


Chip 7, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

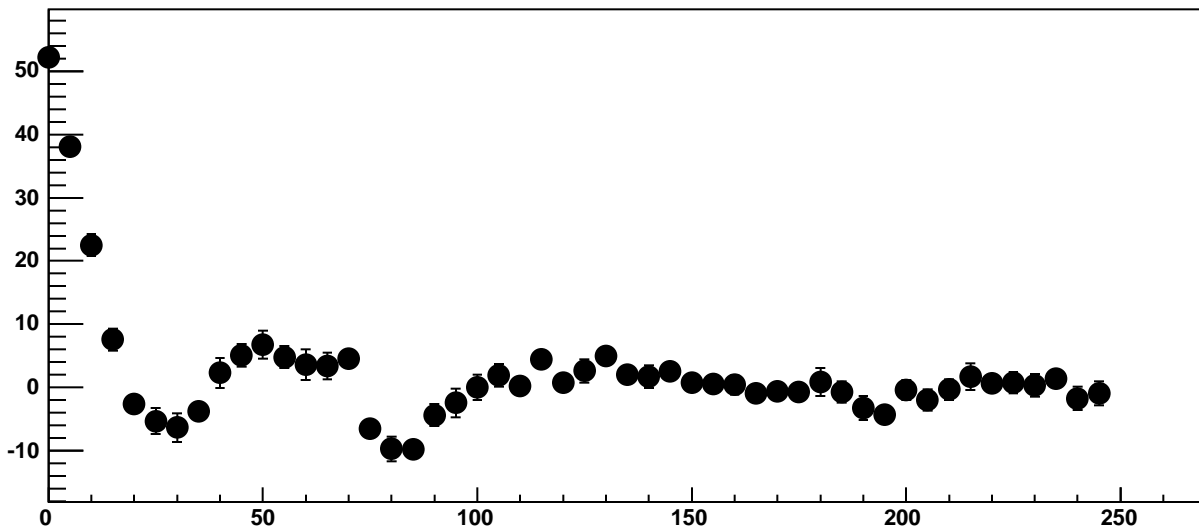


	χ^2 / ndf	232.9 / 41
p0	$-32.91 \pm$	nan
p1	$81.5 \pm$	nan
p2	$-1.318e+08 \pm$	nan
p3	$5.078e+07 \pm$	nan
p4	$2.418 \pm$	nan

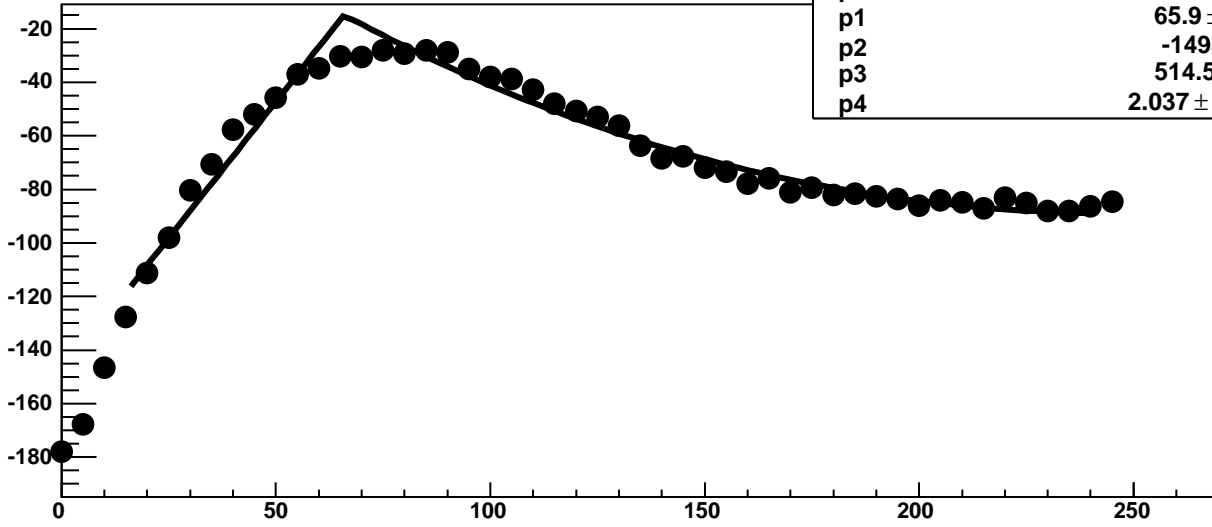
Chip 7, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

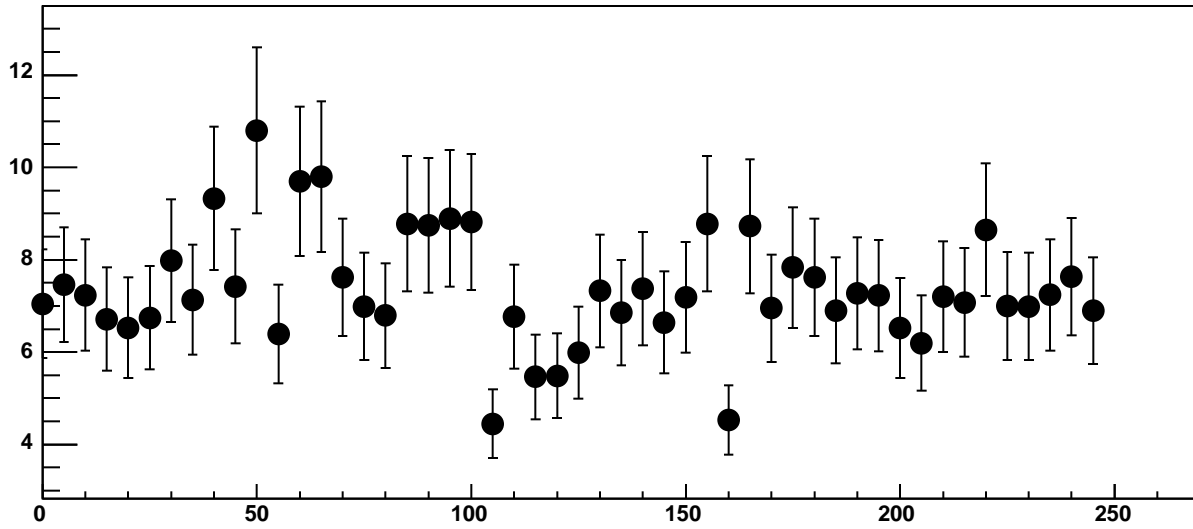


Chip 7, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold

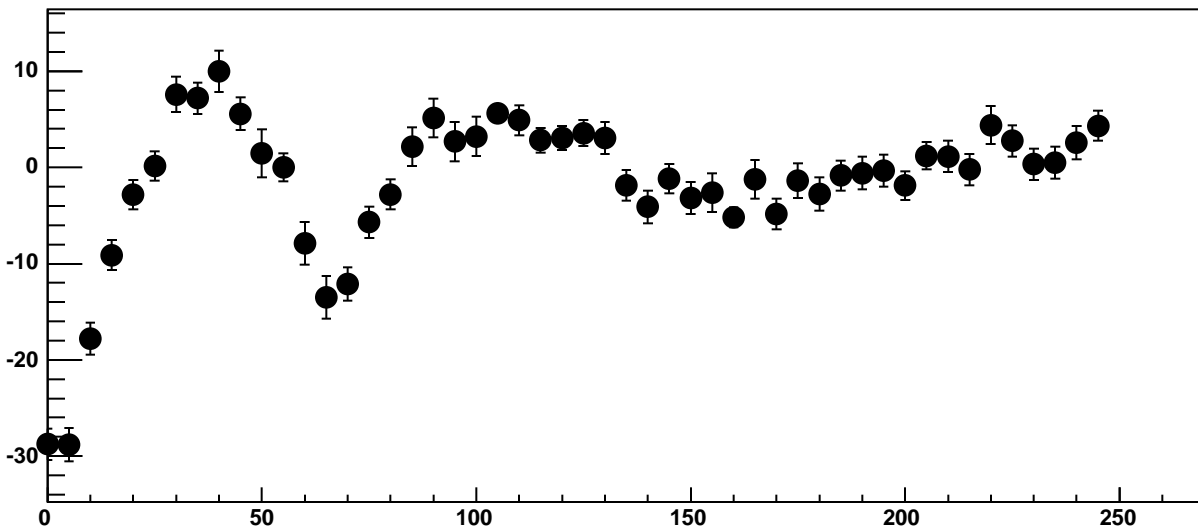


χ^2 / ndf	358.4 / 41
p0	-14.87 ± 0.7293
p1	65.9 ± 0.5014
p2	-1493 ± 74.6
p3	514.5 ± 27.02
p4	2.037 ± 0.03387

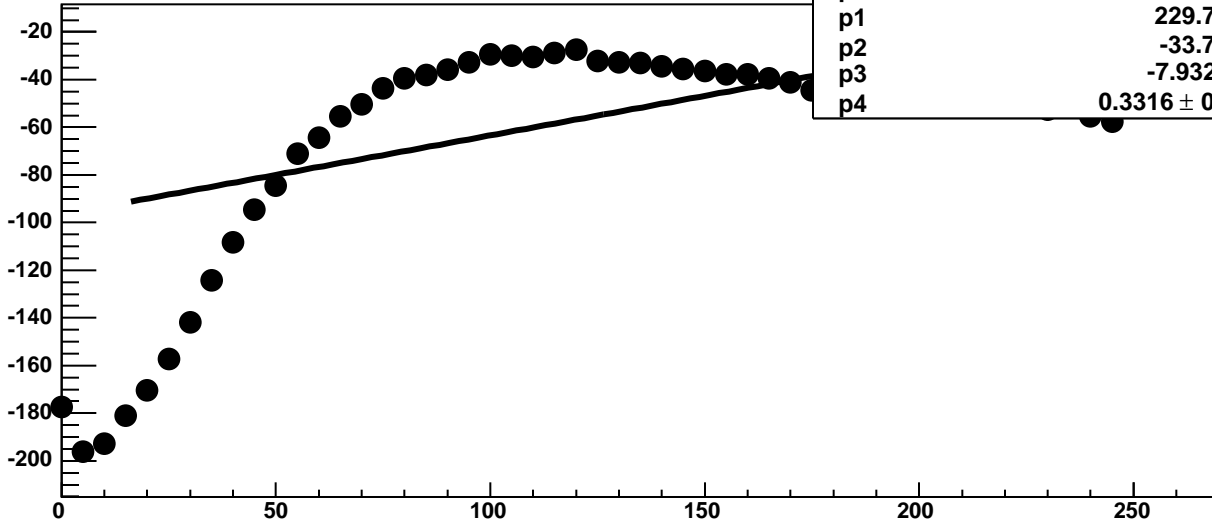
Chip 7, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold

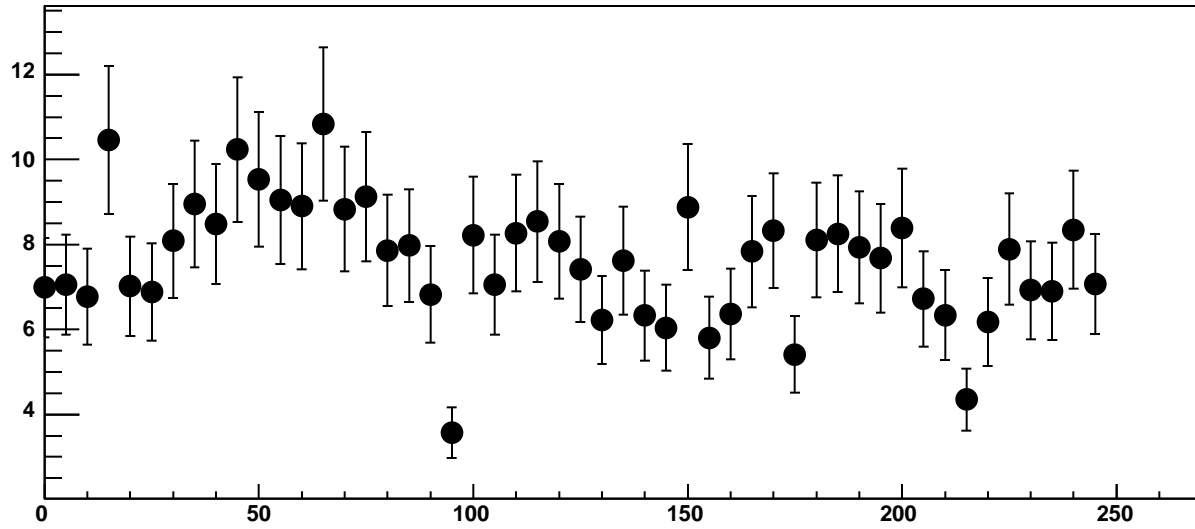


Chip 7, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

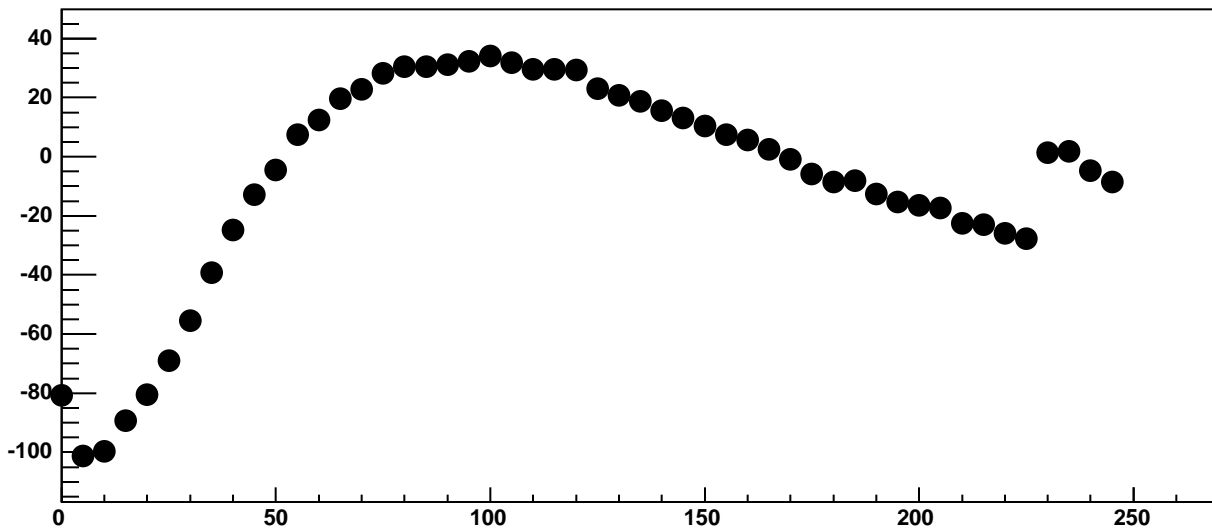


χ^2 / ndf	1.414e+04 / 41
p0	-20.39 ± 0.4391
p1	229.7 ± 6.303
p2	-33.7 ± 0.848
p3	-7.932 ± 7.174
p4	0.3316 ± 0.002317

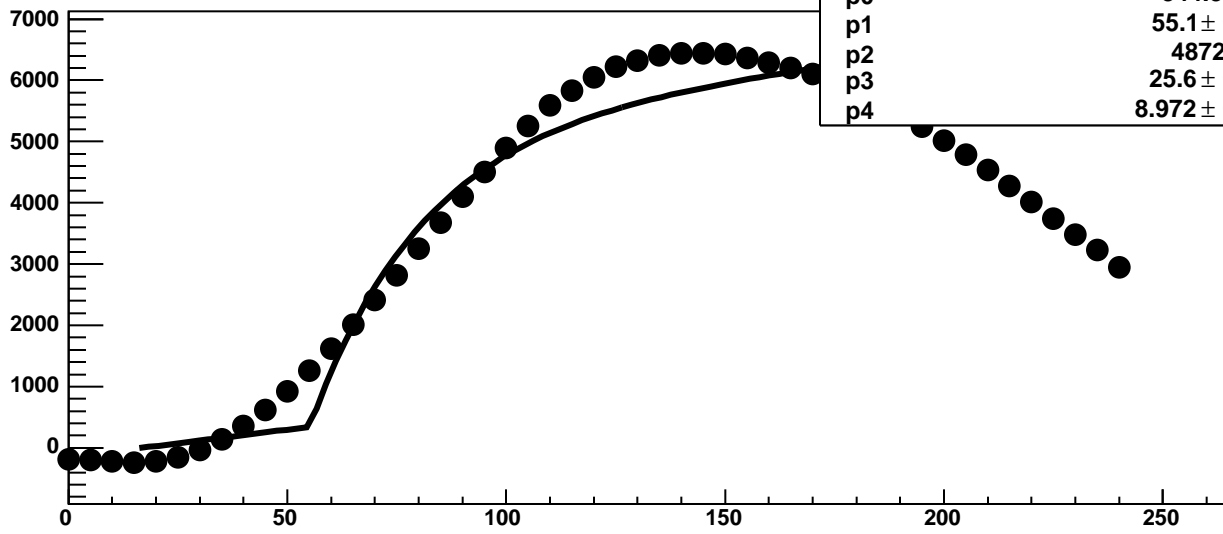
Chip 7, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

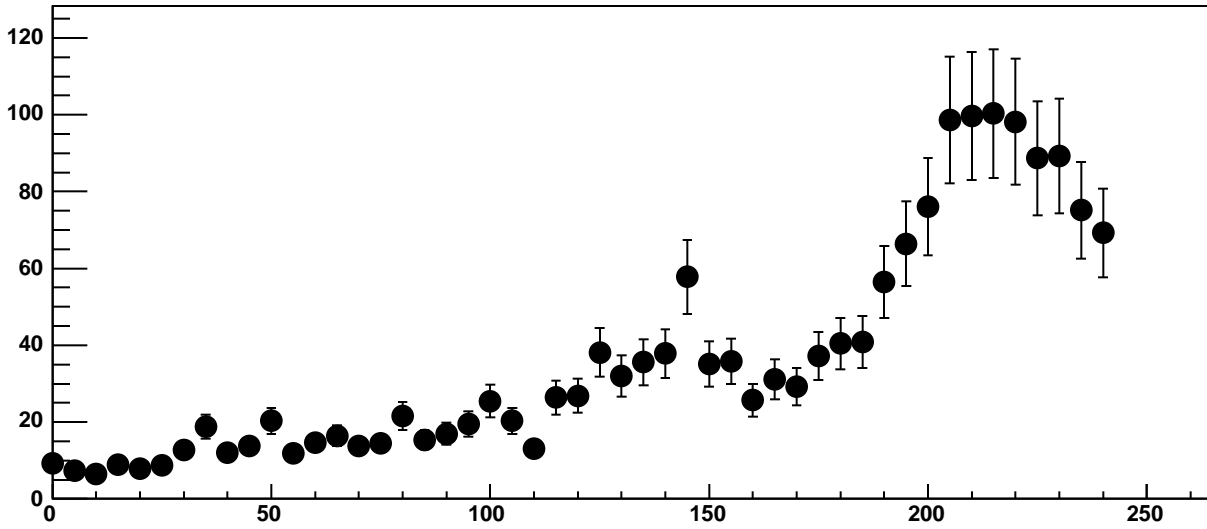


Chip 7, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold

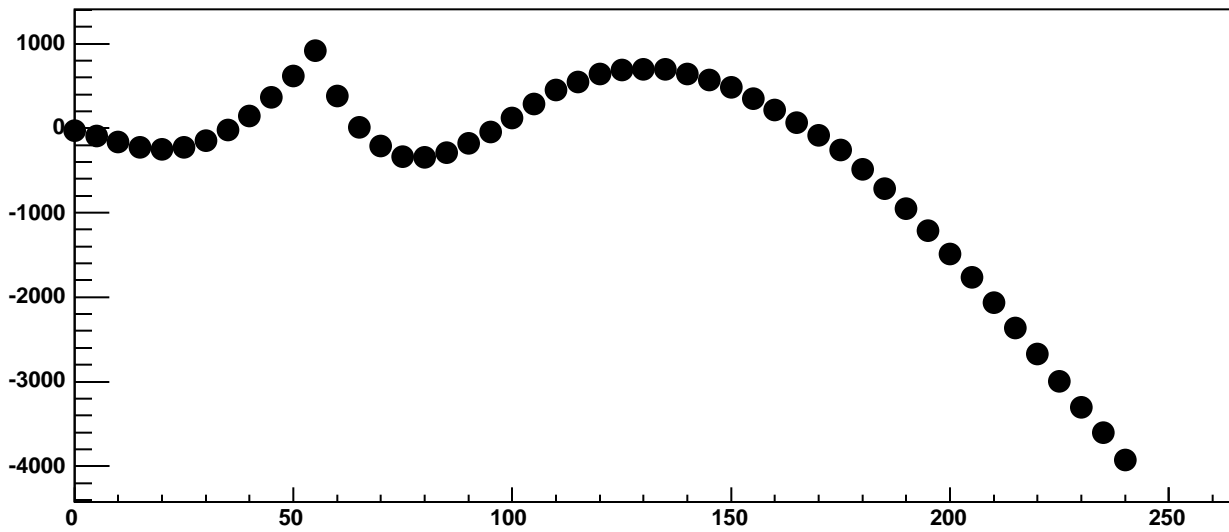


χ^2 / ndf	5.366e+05 / 41
p0	344.3 ± 1.487
p1	55.1 ± 0.02124
p2	4872 ± 6.241
p3	25.6 ± 0.04552
p4	8.972 ± 0.04442

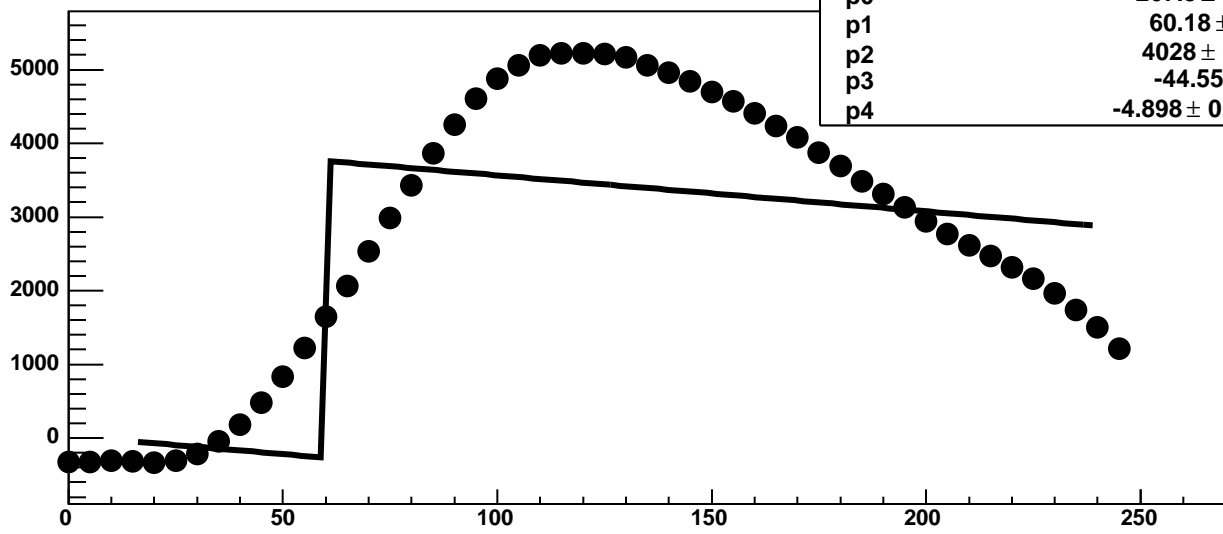
Chip 7, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

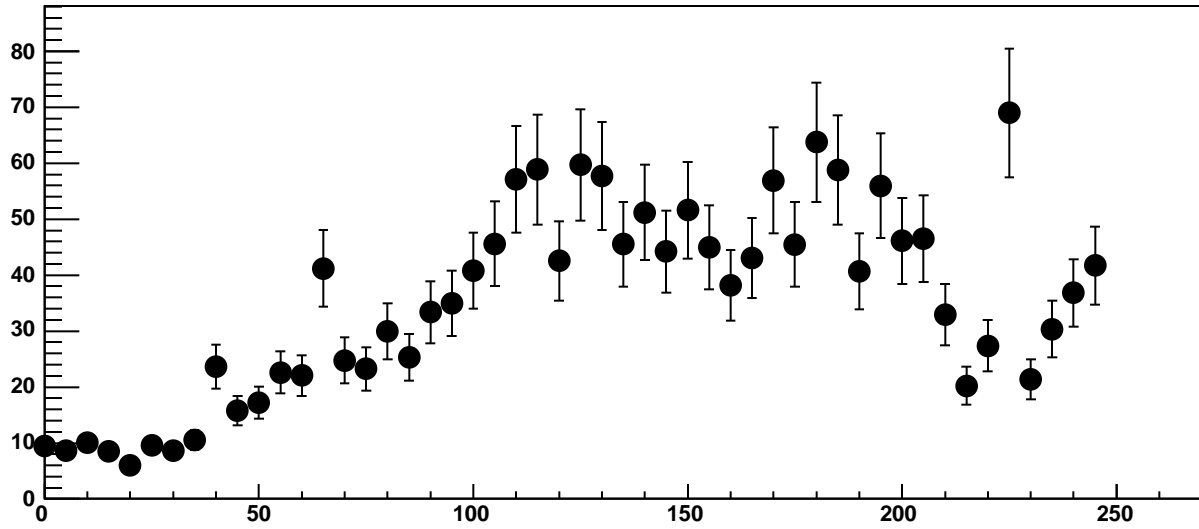


Chip 7, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold

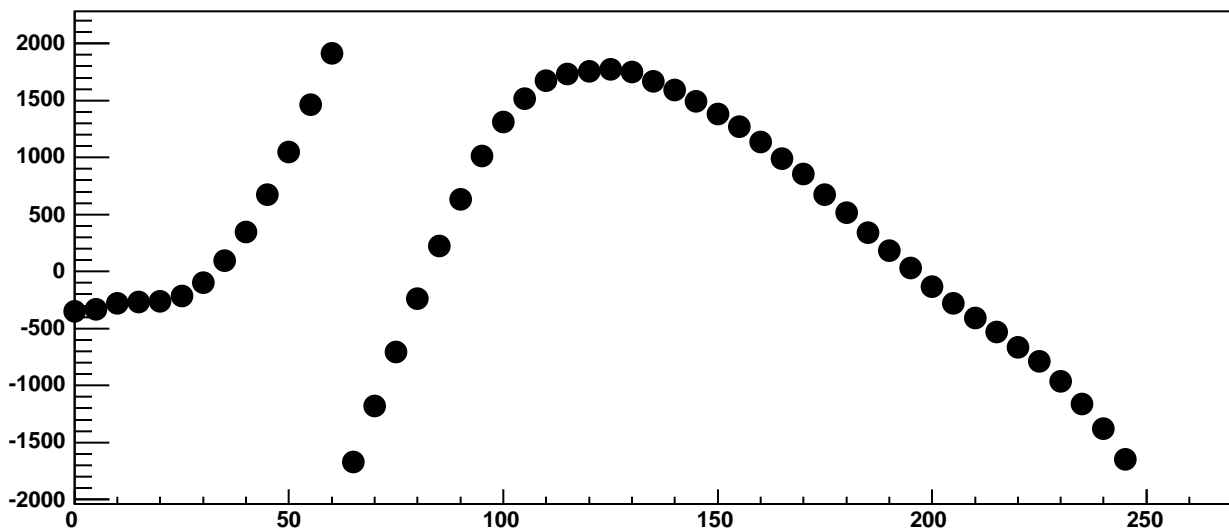


χ^2 / ndf	9.153e+05 / 41
p0	-267.5 ± 0.08878
p1	60.18 ± 0.6882
p2	4028 ± 0.08985
p3	-44.55 ± 7.484
p4	-4.898 ± 0.002674

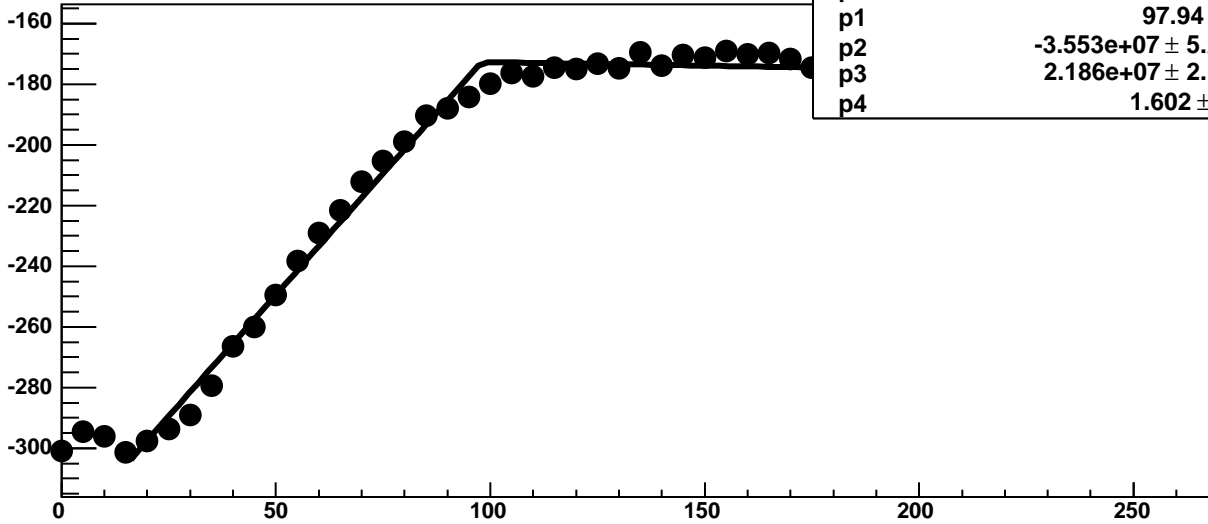
Chip 7, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold

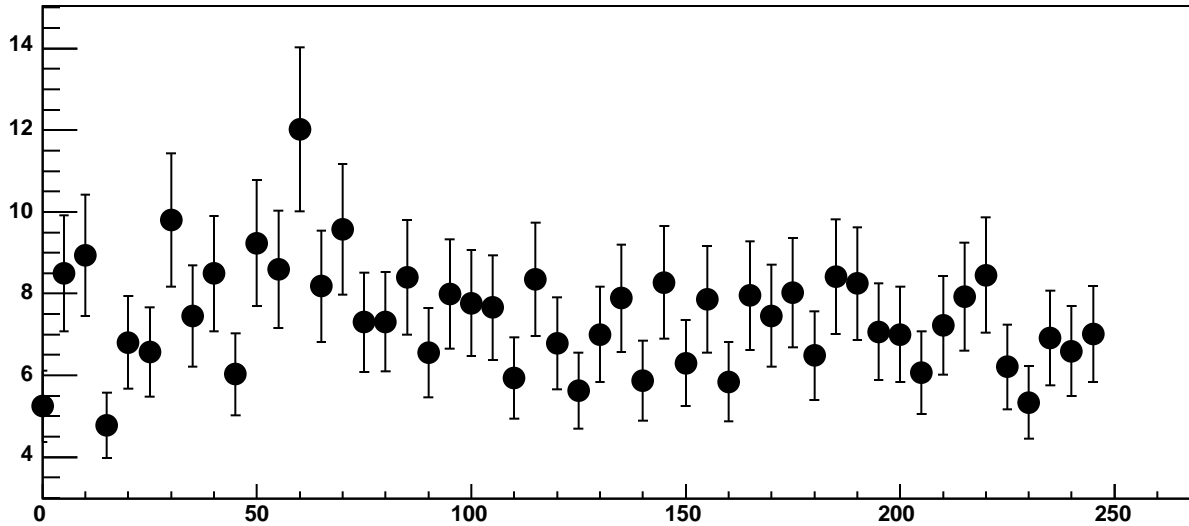


Chip 7, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

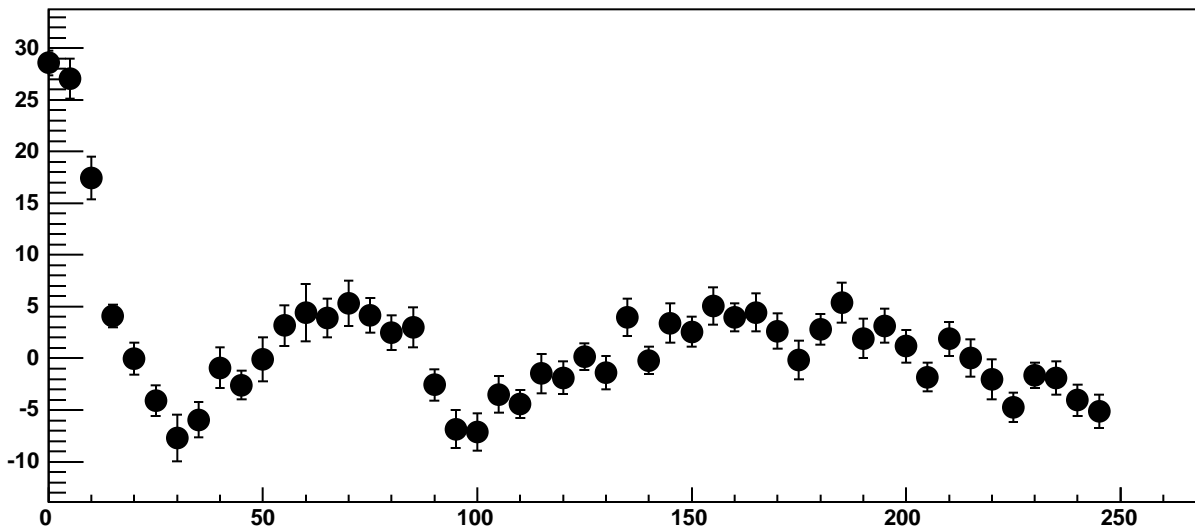


χ^2 / ndf	203.2 / 41
p0	-172.7 ± 0.5779
p1	97.94 ± 0.6322
p2	$-3.553\text{e}+07 \pm 5.219\text{e}+05$
p3	$2.186\text{e}+07 \pm 2.136\text{e}+05$
p4	1.602 ± 0.01551

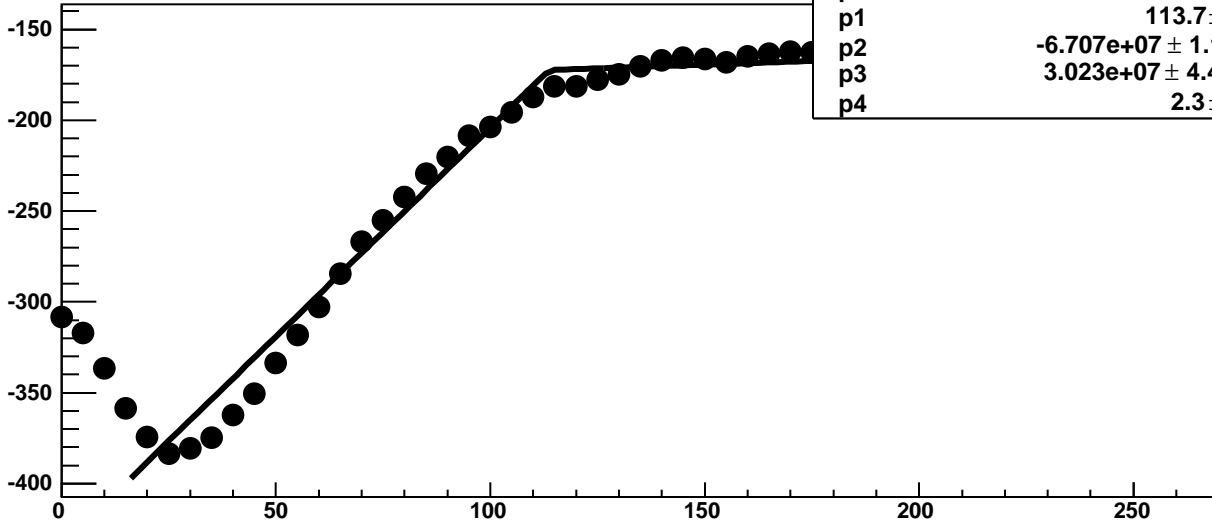
Chip 7, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold

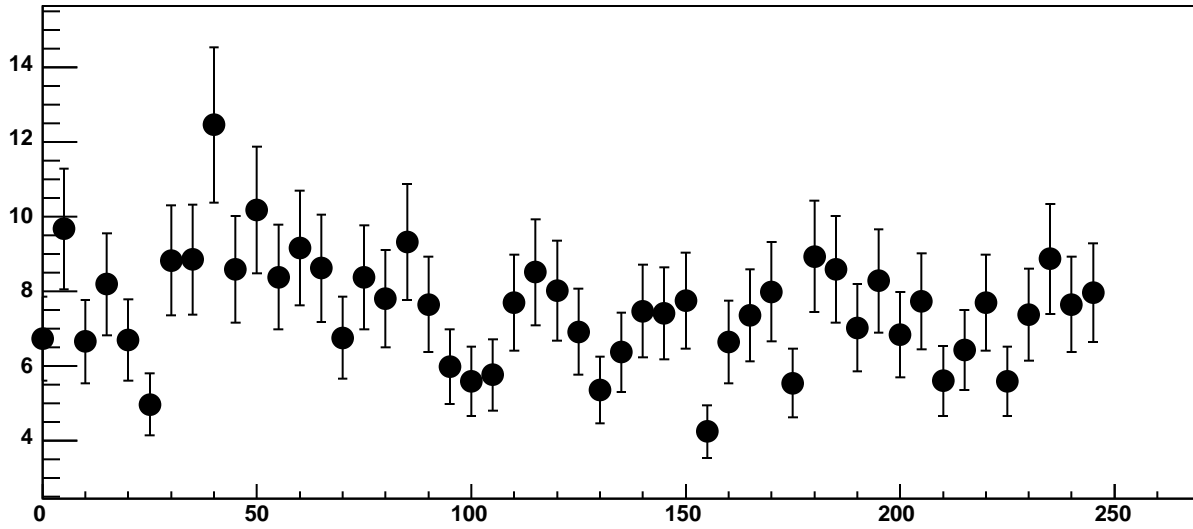


Chip 7, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

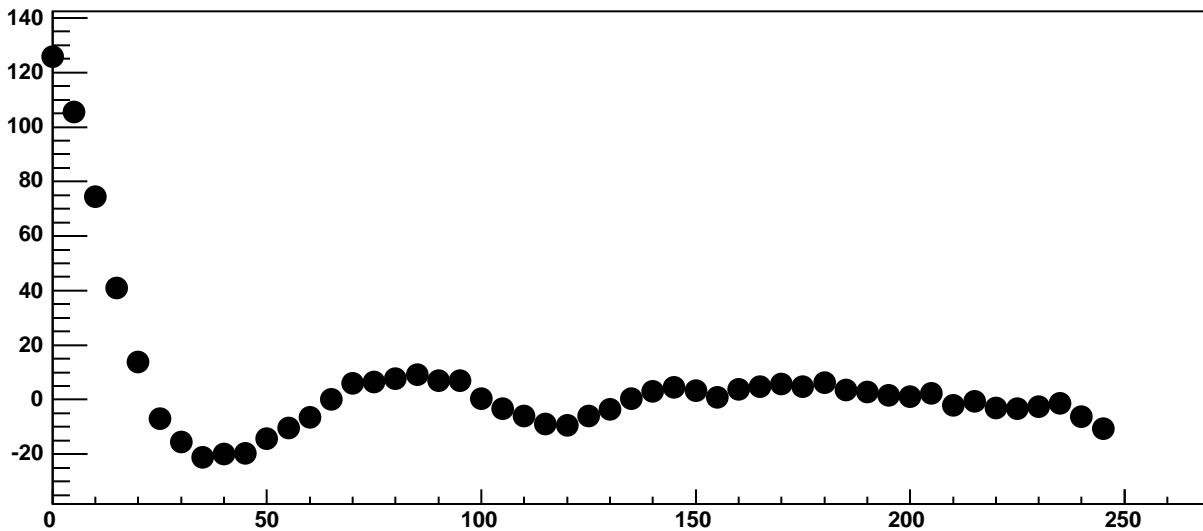


χ^2 / ndf	1284 / 41
p0	-172.4 ± 0.6336
p1	113.7 ± 0.4232
p2	$-6.707\text{e}+07 \pm 1.186\text{e}+06$
p3	$3.023\text{e}+07 \pm 4.448\text{e}+05$
p4	2.3 ± 0.0123

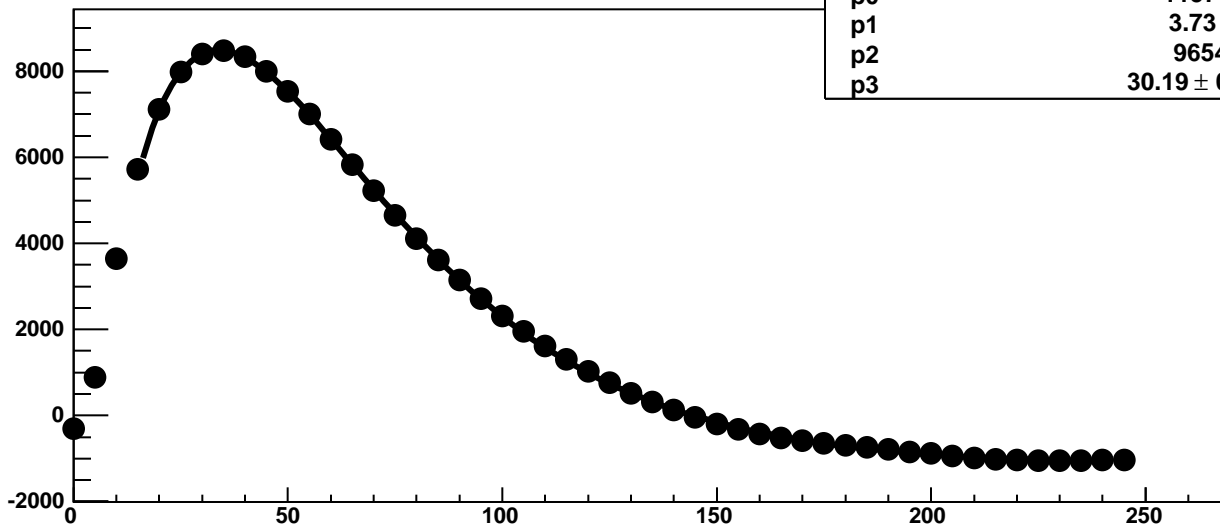
Chip 7, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

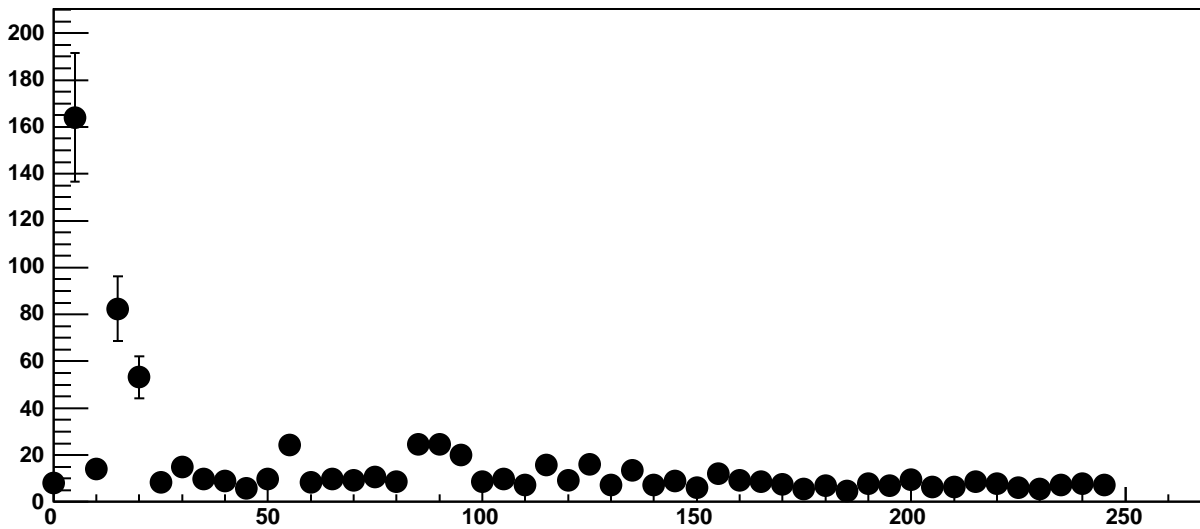


Chip 7, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold

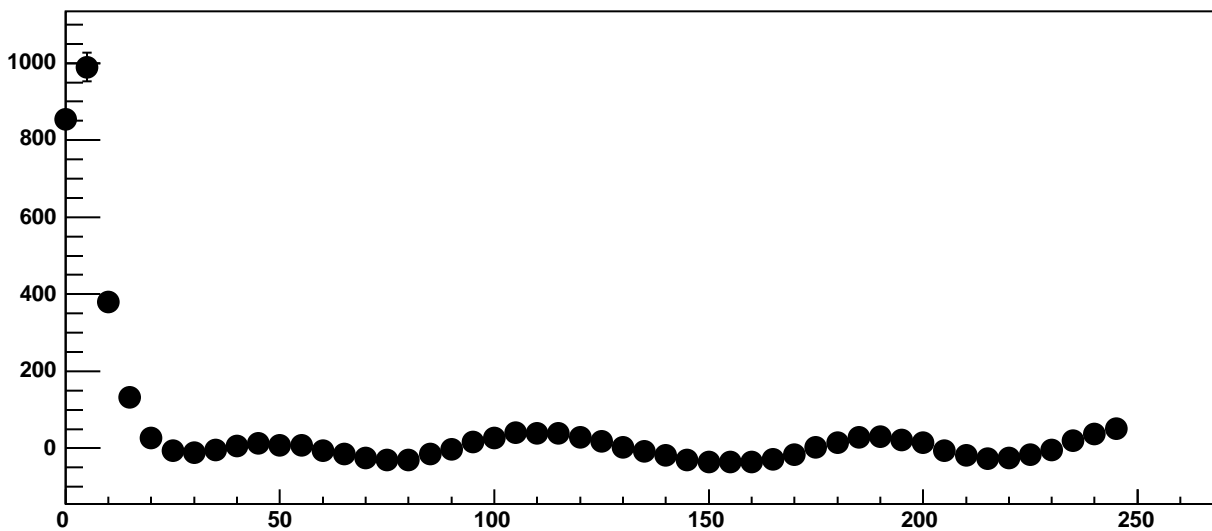


χ^2 / ndf	6429 / 42
p0	-1157 ± 0.5372
p1	3.73 ± 0.0139
p2	9654 ± 0.949
p3	30.19 ± 0.006992

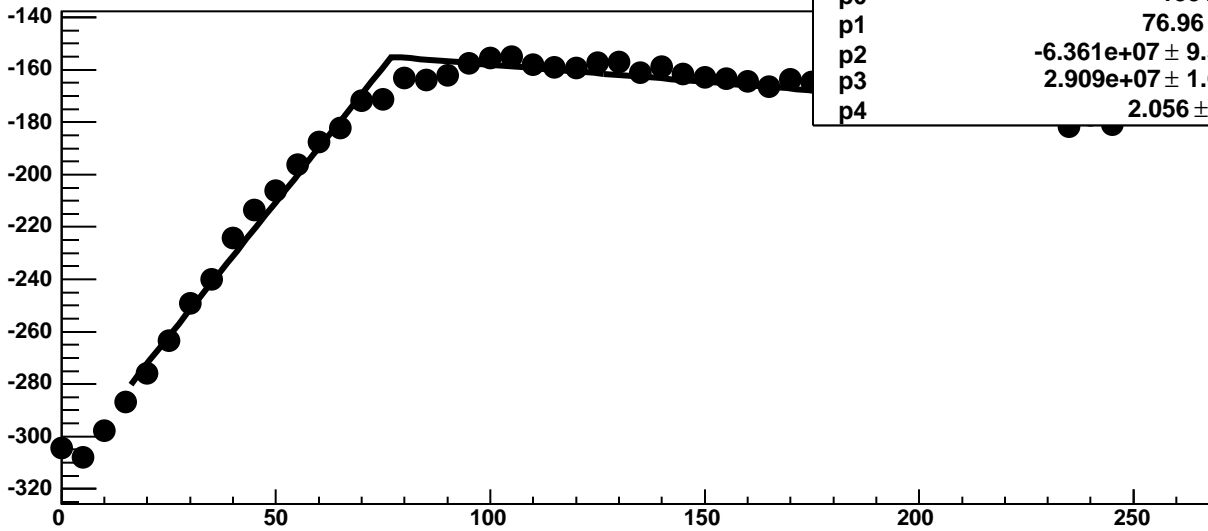
Chip 7, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold

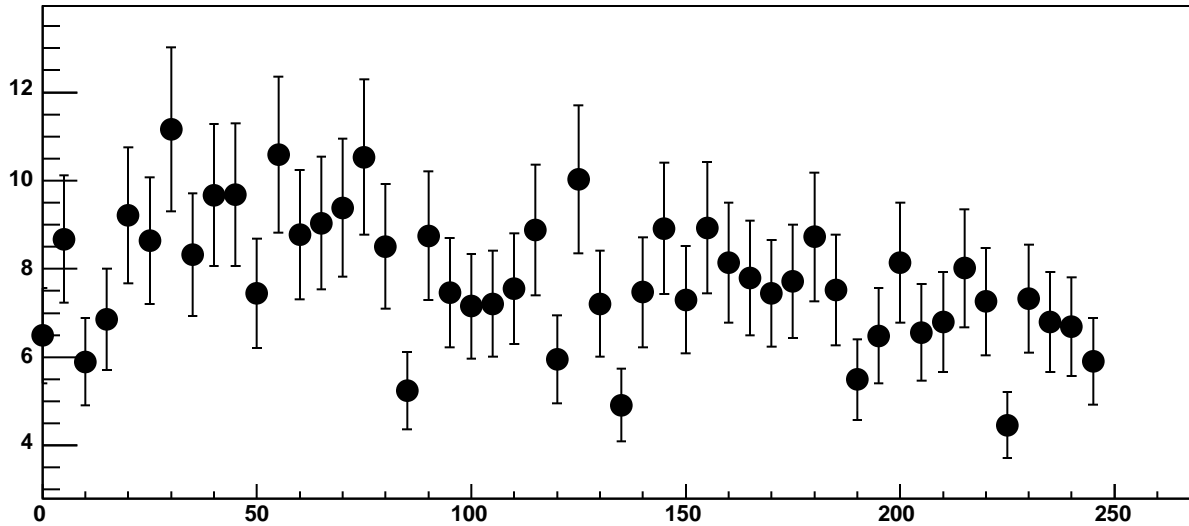


Chip 7, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

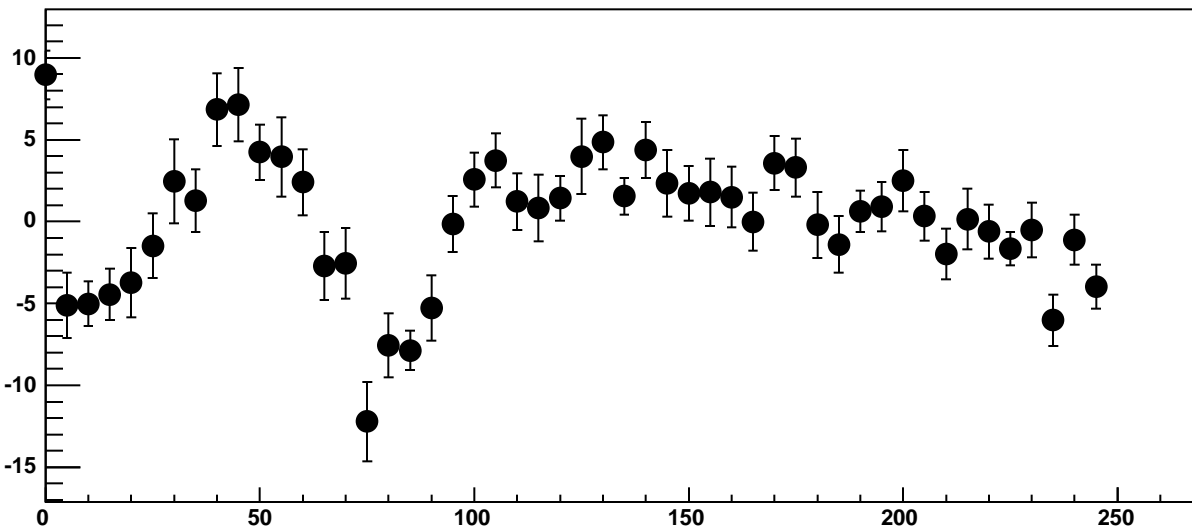


χ^2 / ndf	200.7 / 41
p0	-155 ± 0.5345
p1	76.96 ± 0.5857
p2	$-6.361\text{e}+07 \pm 9.541\text{e}+05$
p3	$2.909\text{e}+07 \pm 1.699\text{e}+05$
p4	2.056 ± 0.02983

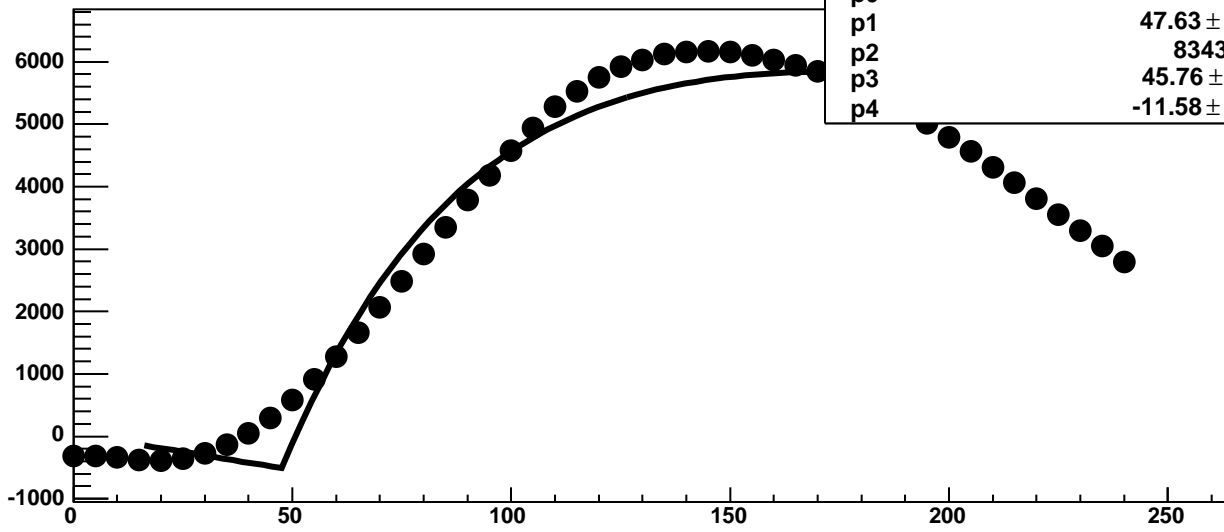
Chip 7, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

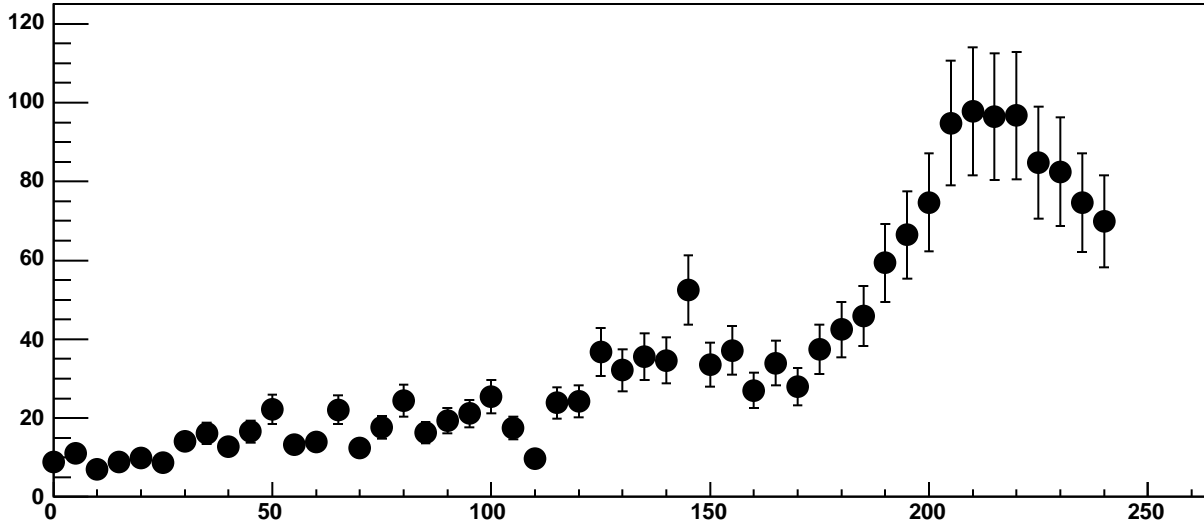


Chip 7, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold

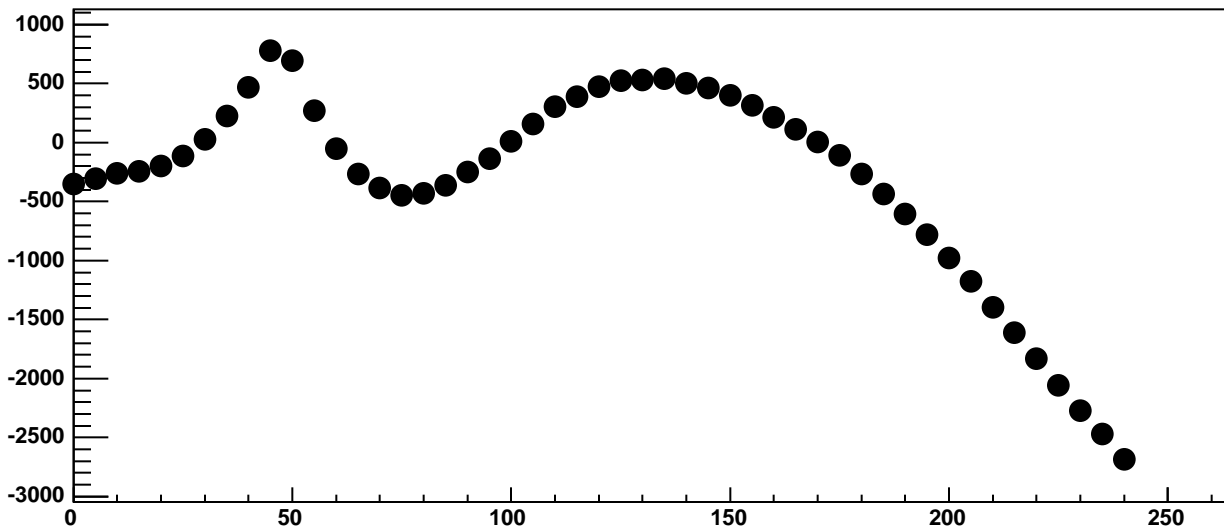


χ^2 / ndf	3.364e+05 / 41
p0	-509.3 ± 1.707
p1	47.63 ± 0.01897
p2	8343 ± 12.82
p3	45.76 ± 0.07231
p4	-11.58 ± 0.07058

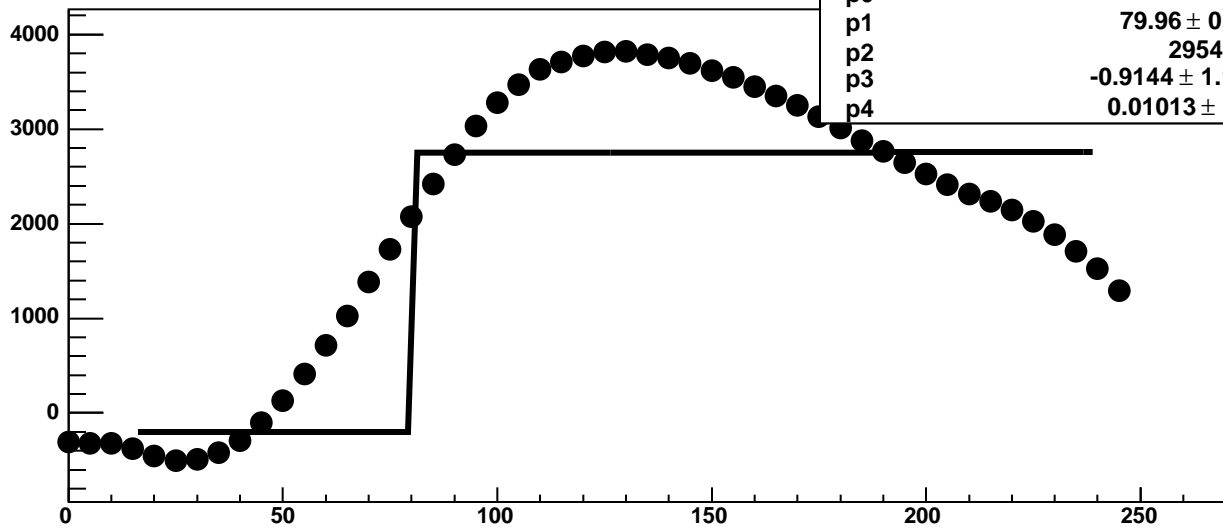
Chip 7, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

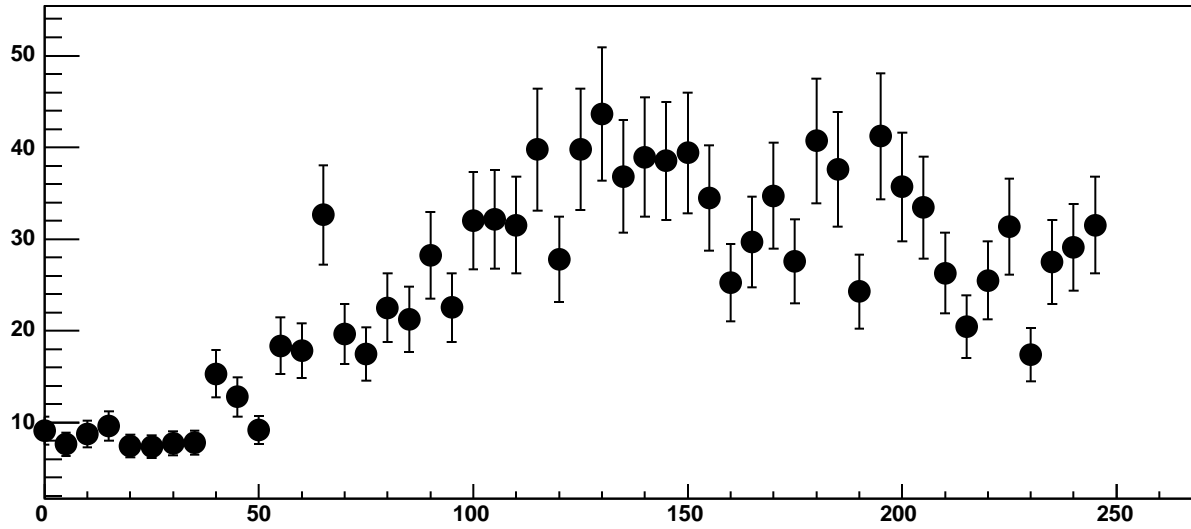


Chip 7, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

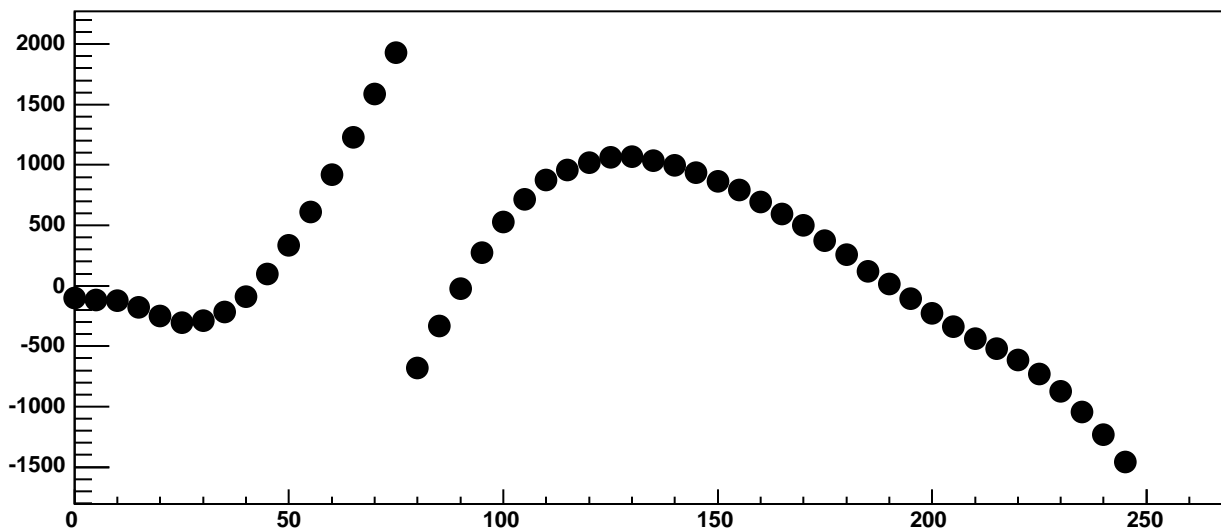


χ^2 / ndf	9.382e+05 / 41
p0	-200.2 ± 1.351
p1	79.96 ± 0.000736
p2	2954 ± 3.686
p3	-0.9144 ± 1.153e-06
p4	0.01013 ± 0.02653

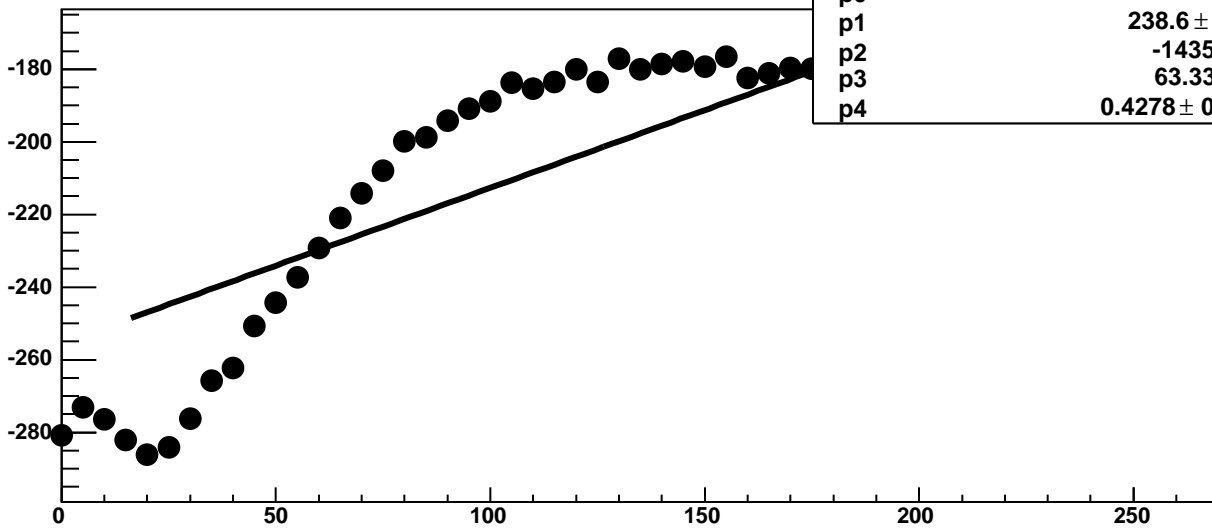
Chip 7, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

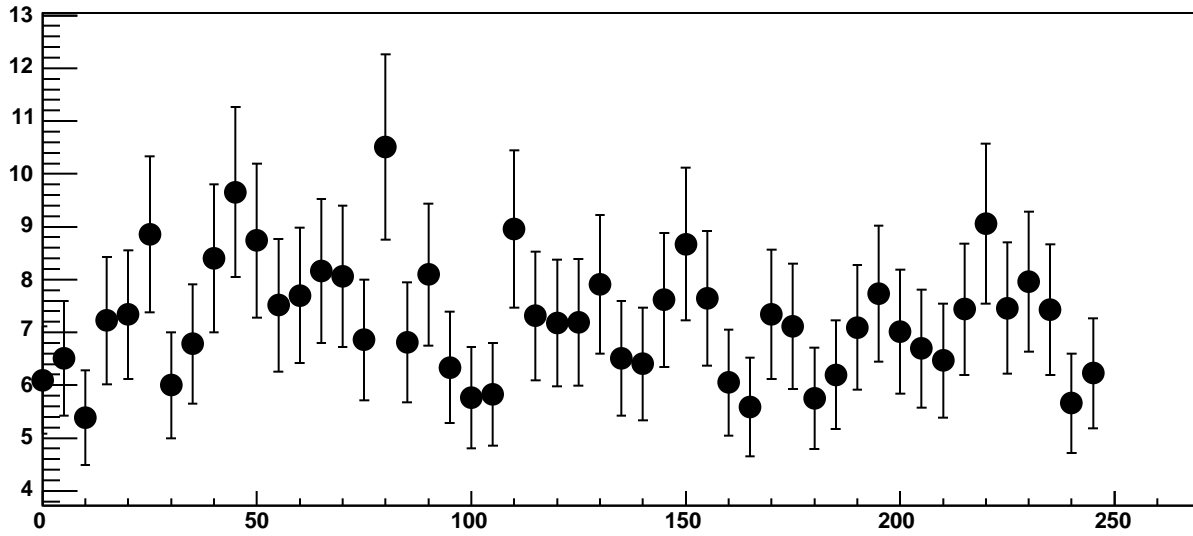


Chip 7, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold

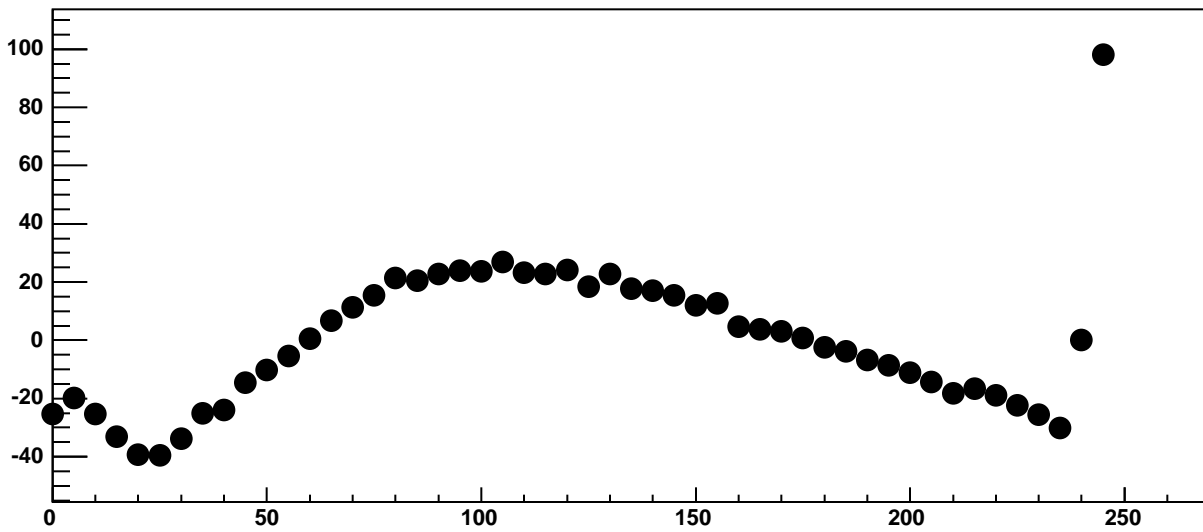


χ^2 / ndf	6419 / 41
p0	-153.4 ± 0.4811
p1	238.6 ± 0.04769
p2	-1435 ± 18.19
p3	63.33 ± 0.168
p4	0.4278 ± 0.003854

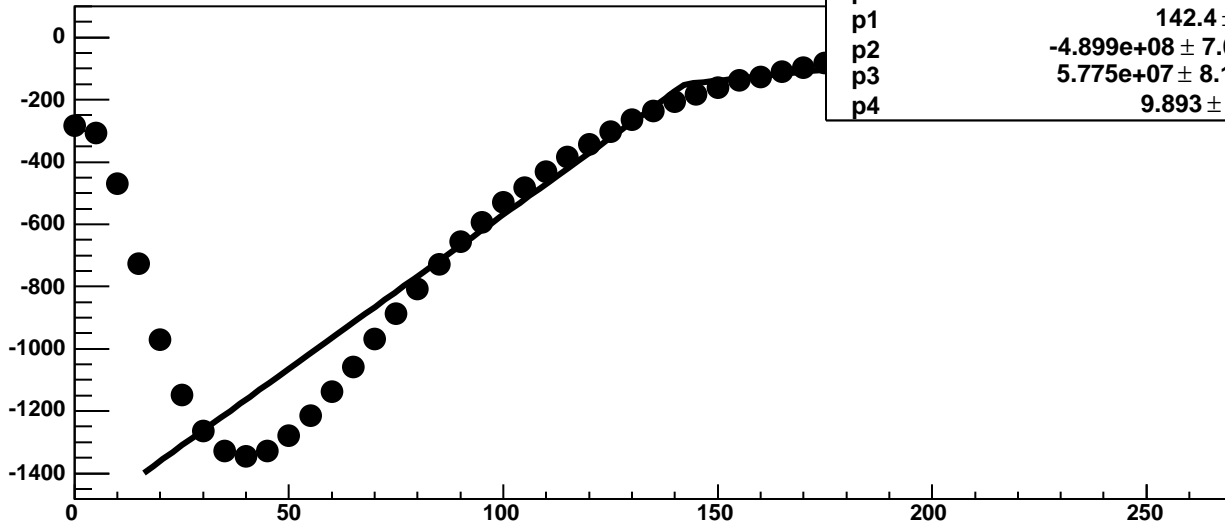
Chip 7, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold

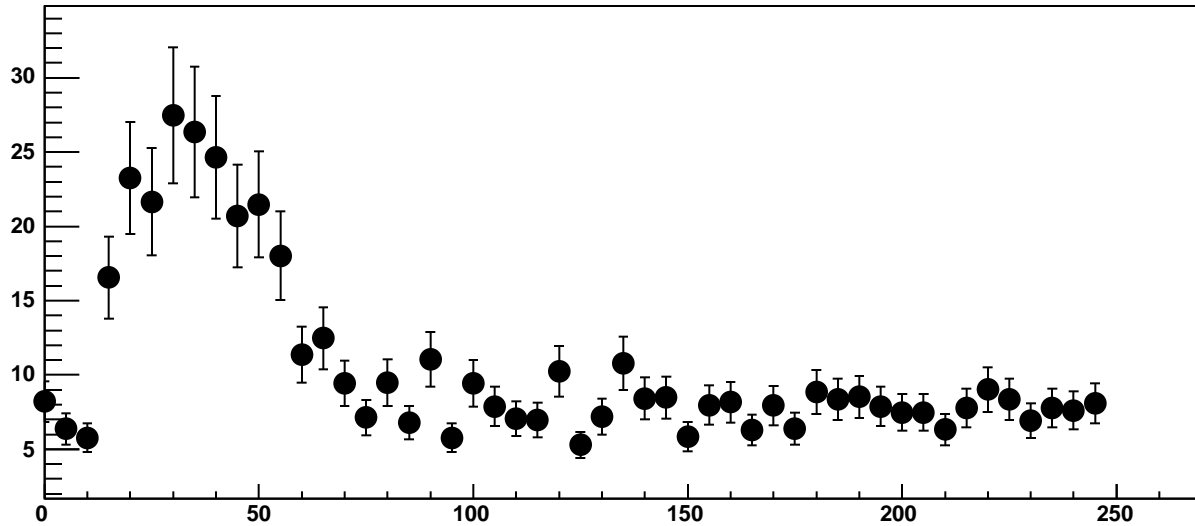


Chip 7, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

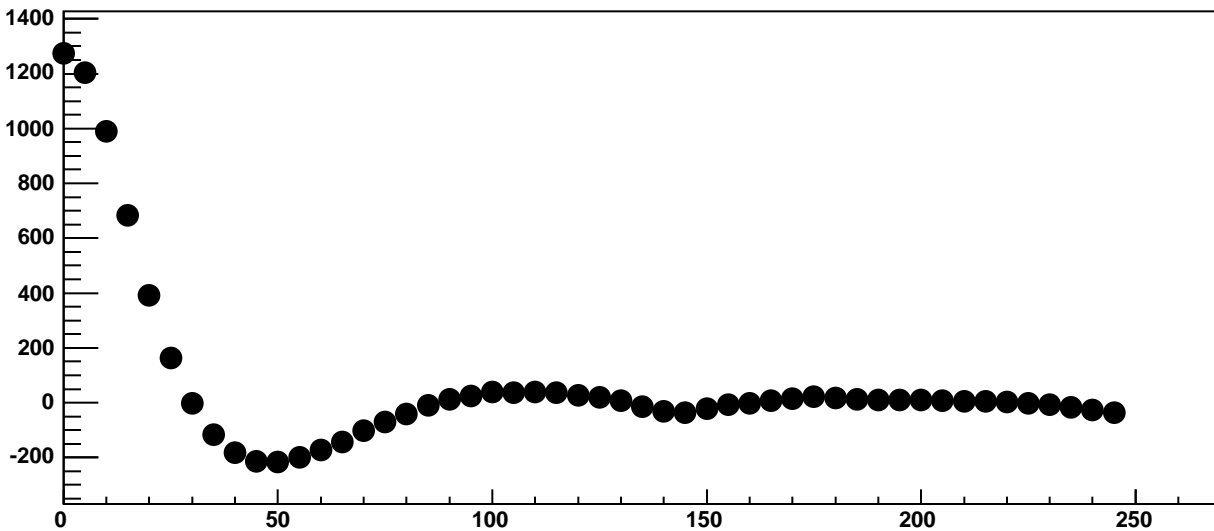


χ^2 / ndf	6.266e+04 / 41
p0	-149.5 ± 0.883
p1	142.4 ± 0.1279
p2	-4.899e+08 ± 7.046e+06
p3	5.775e+07 ± 8.127e+05
p4	9.893 ± 0.01529

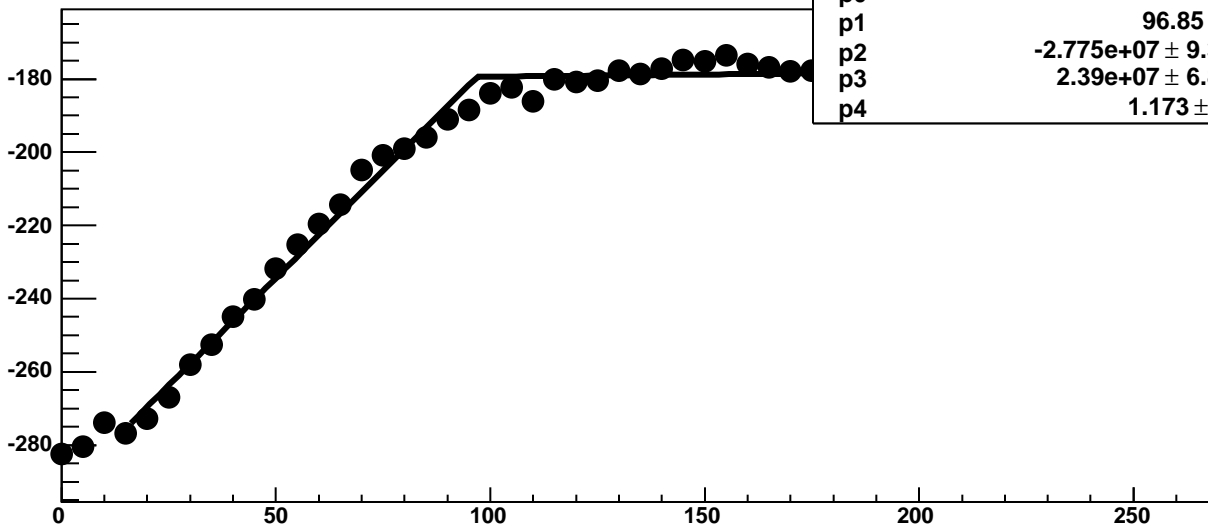
Chip 7, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

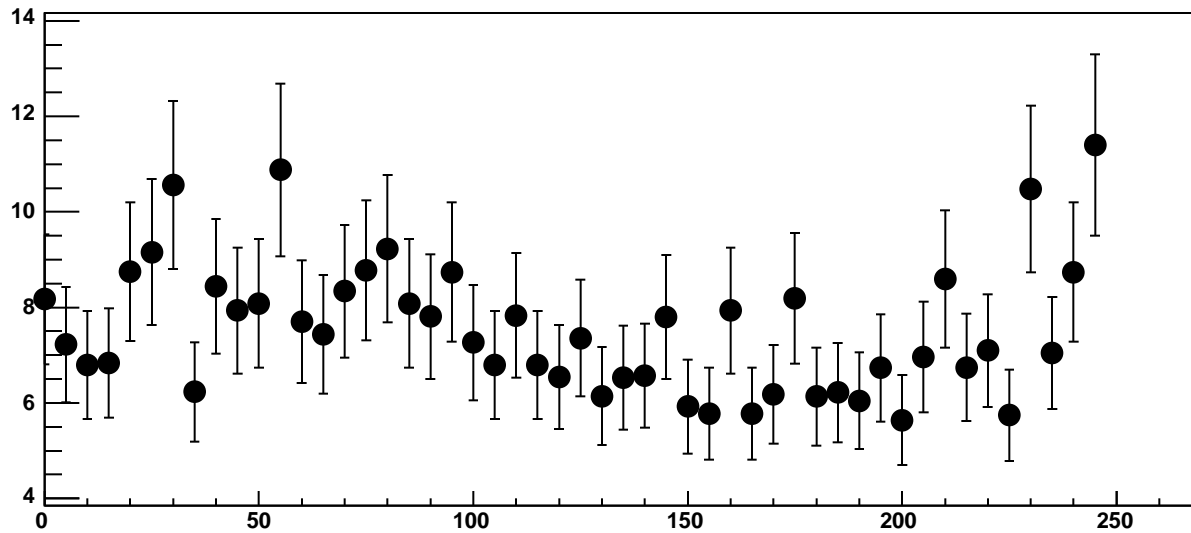


Chip 7, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

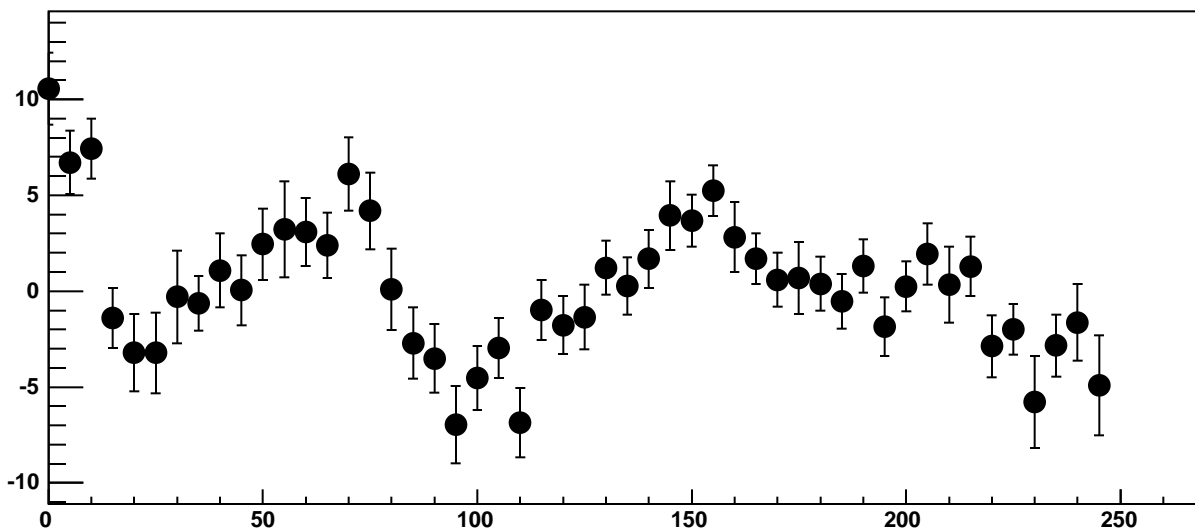


χ^2 / ndf	129.1 / 41
p0	-179.4 ± 0.6011
p1	96.85 ± 0.9388
p2	$-2.775\text{e}+07 \pm 9.307\text{e}+05$
p3	$2.39\text{e}+07 \pm 6.825\text{e}+05$
p4	1.173 ± 0.01846

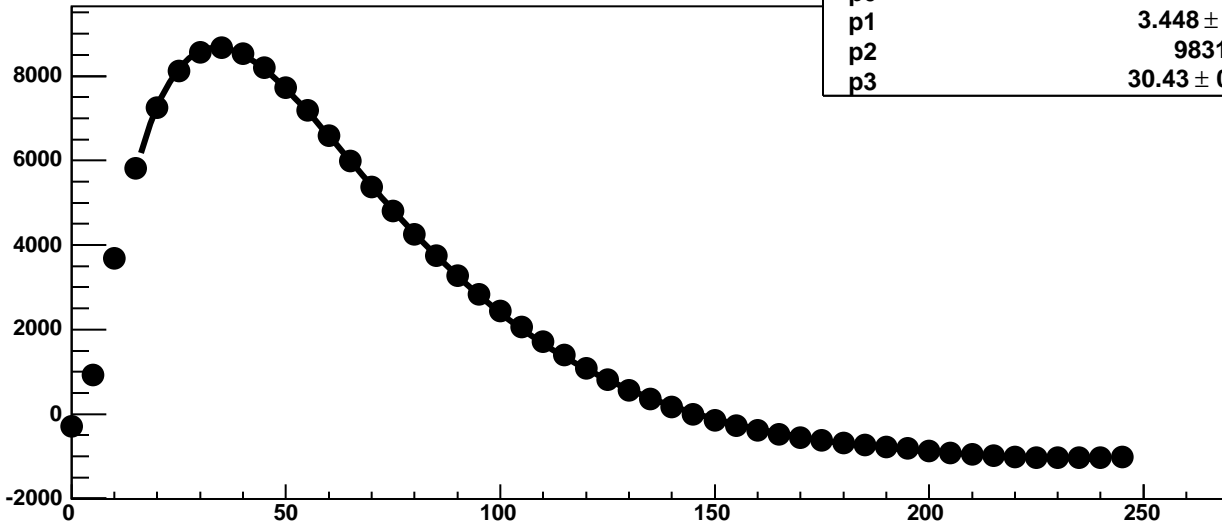
Chip 7, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

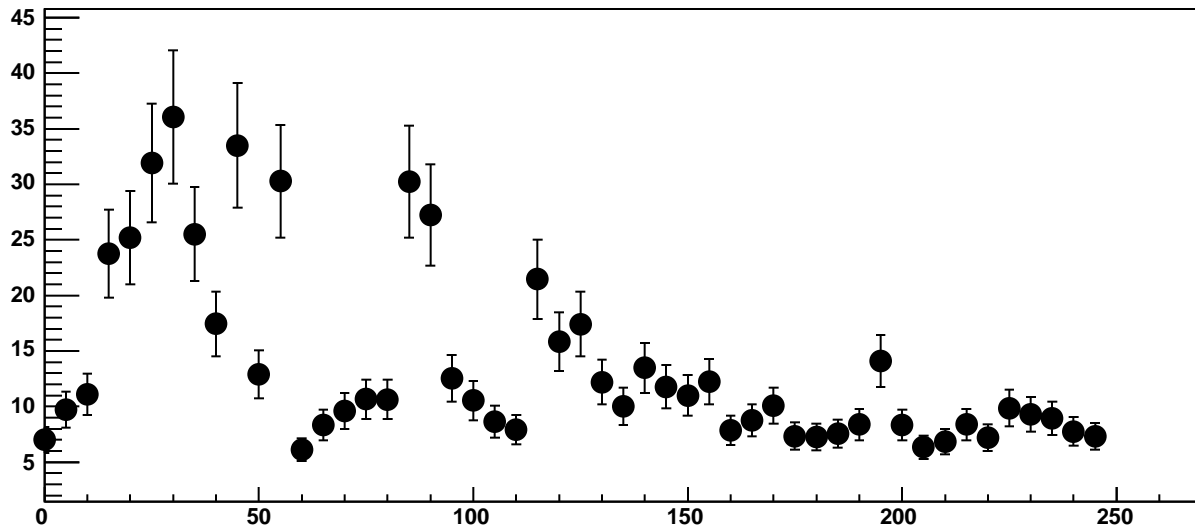


Chip 7, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold

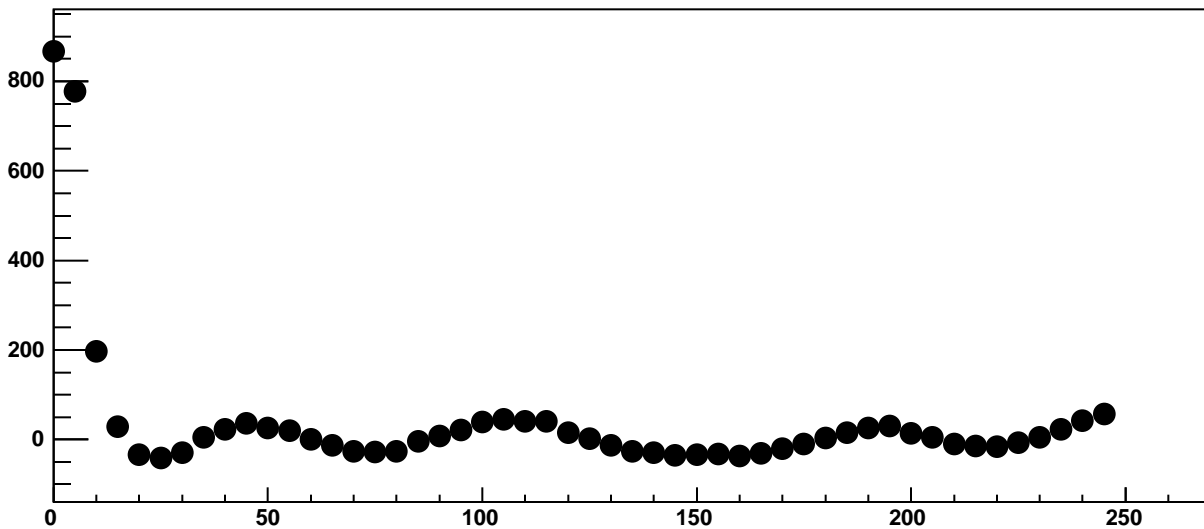


χ^2 / ndf	4906 / 42
p0	-1154 ± 0.636
p1	3.448 ± 0.01313
p2	9831 ± 1.569
p3	30.43 ± 0.007921

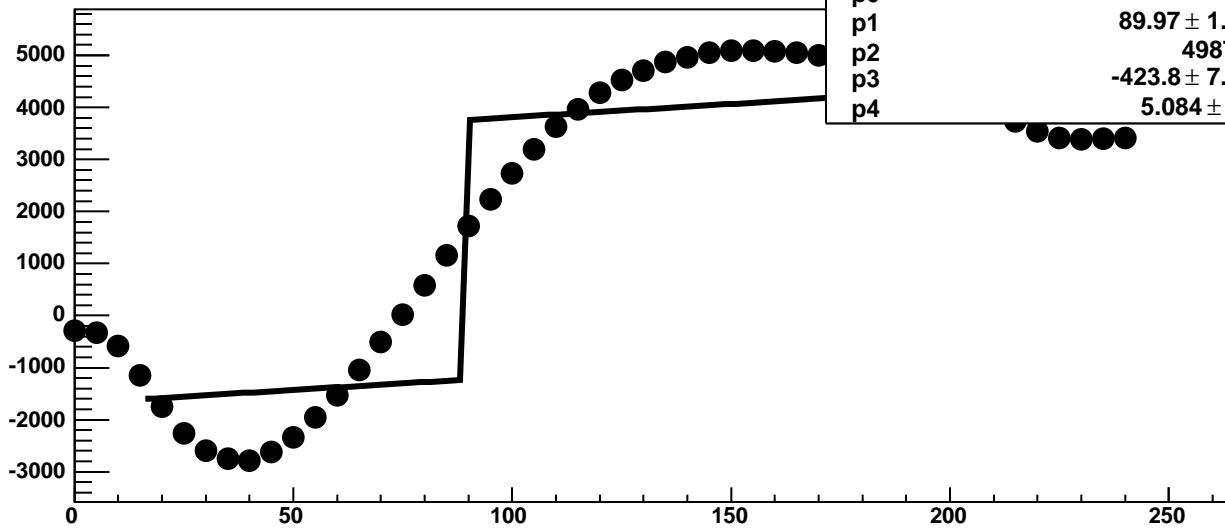
Chip 7, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold

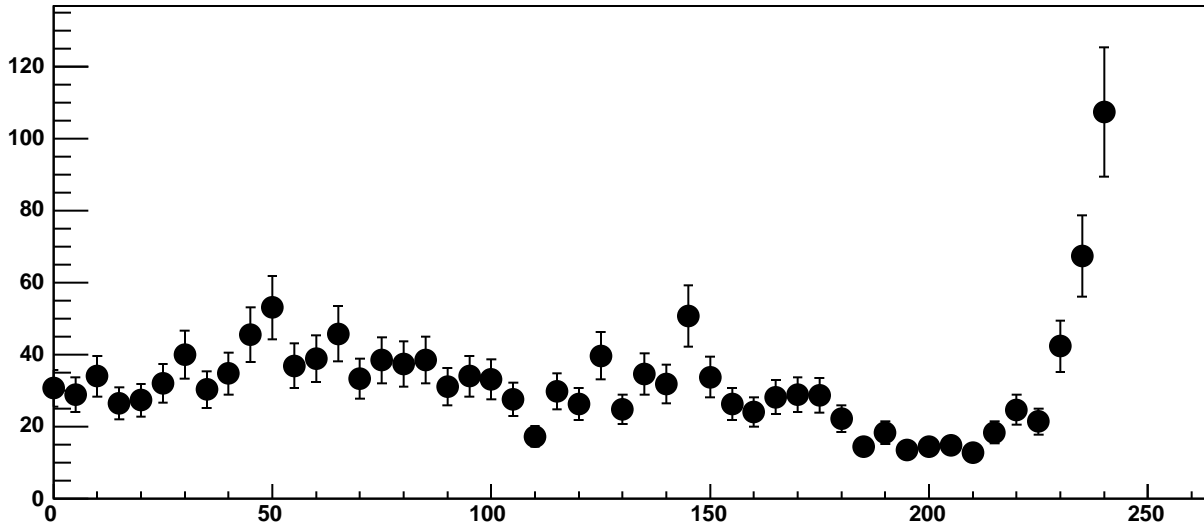


Chip 7, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

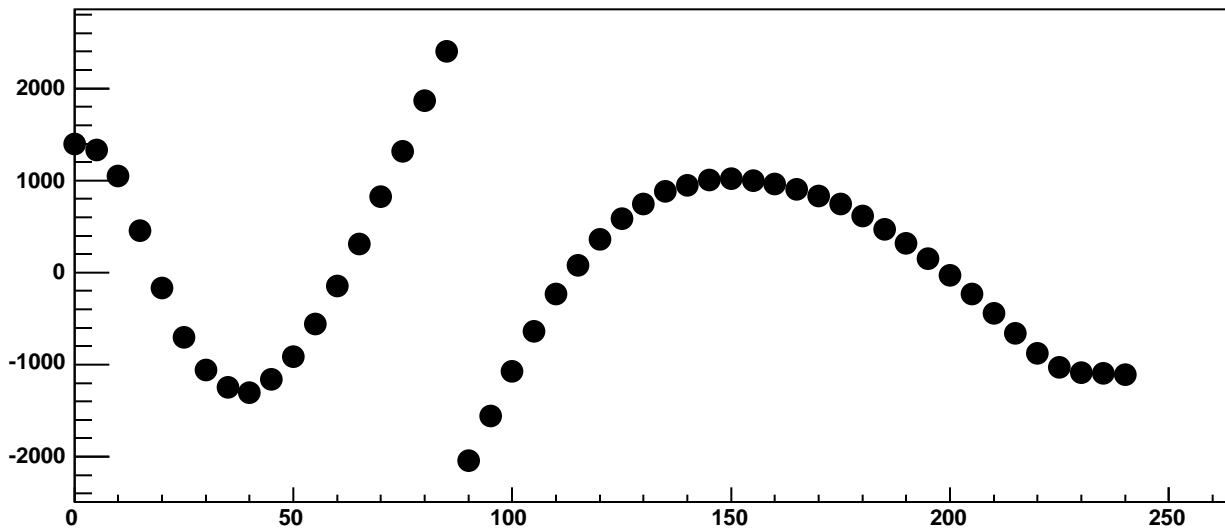


χ^2 / ndf	7.849e+05 / 41
p0	-1226 ± 1.414
p1	89.97 ± 1.666e-05
p2	4987 ± 2.16
p3	-423.8 ± 7.198e-05
p4	5.084 ± 0.02323

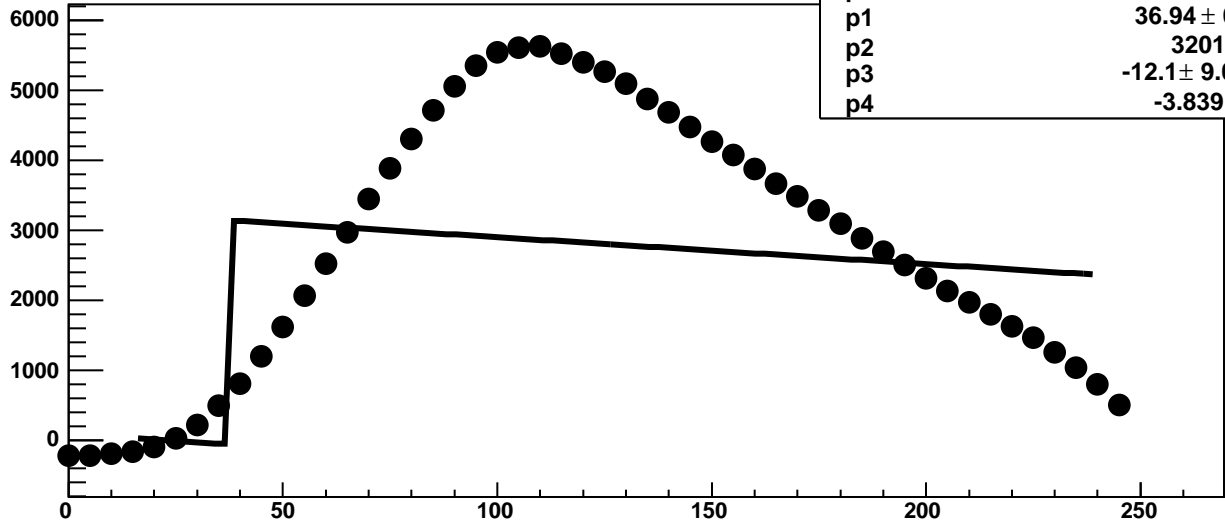
Chip 7, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold

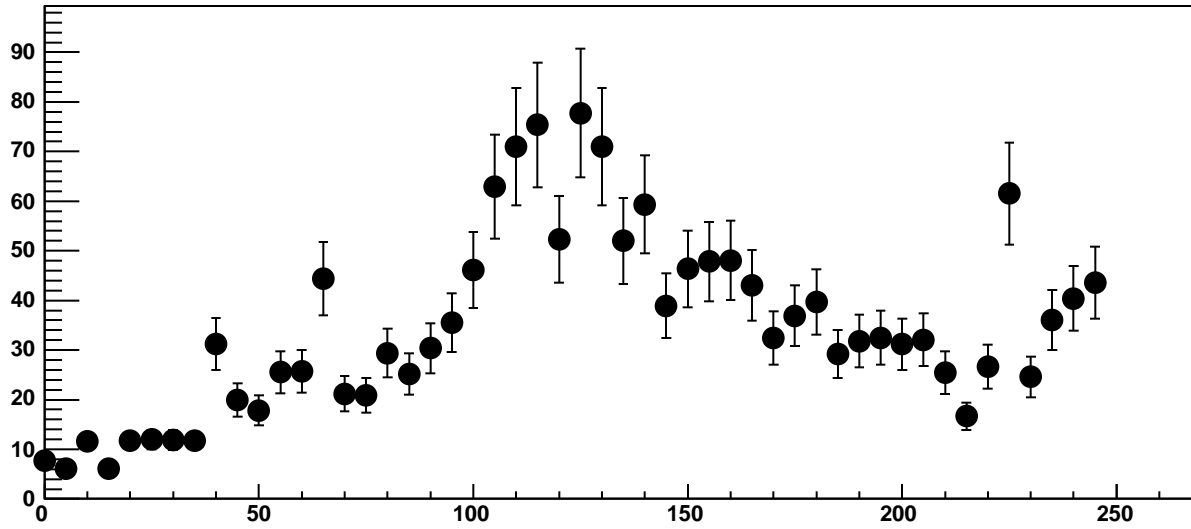


Chip 7, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold

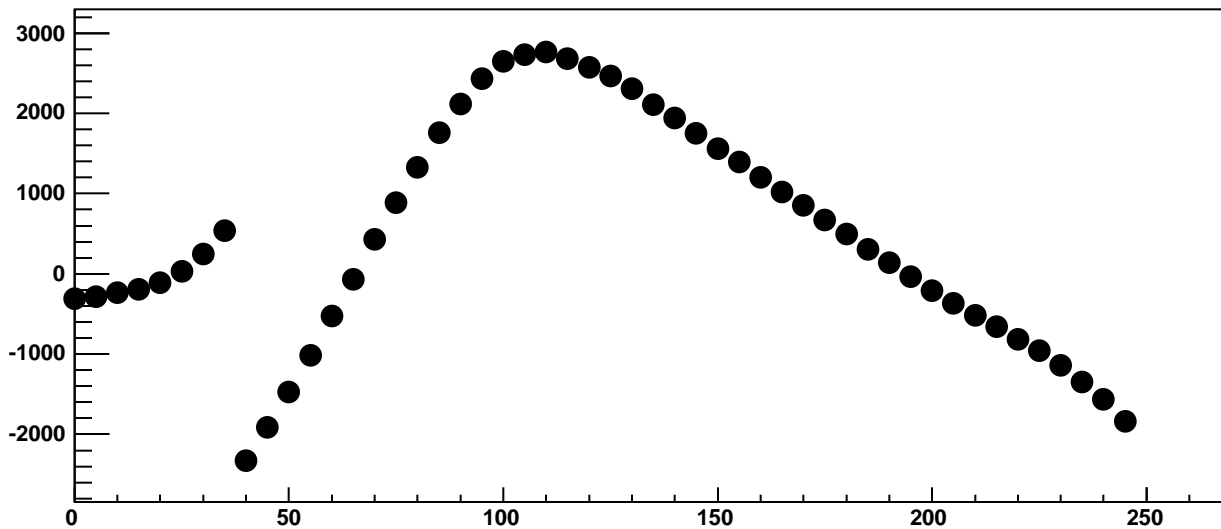


χ^2 / ndf	1.445e+06 / 41
p0	-57.6 ± 0.9883
p1	36.94 ± 0.01017
p2	3201 ± 2.308
p3	-12.1 ± 9.025e-08
p4	-3.839 ± 0.016

Chip 7, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold

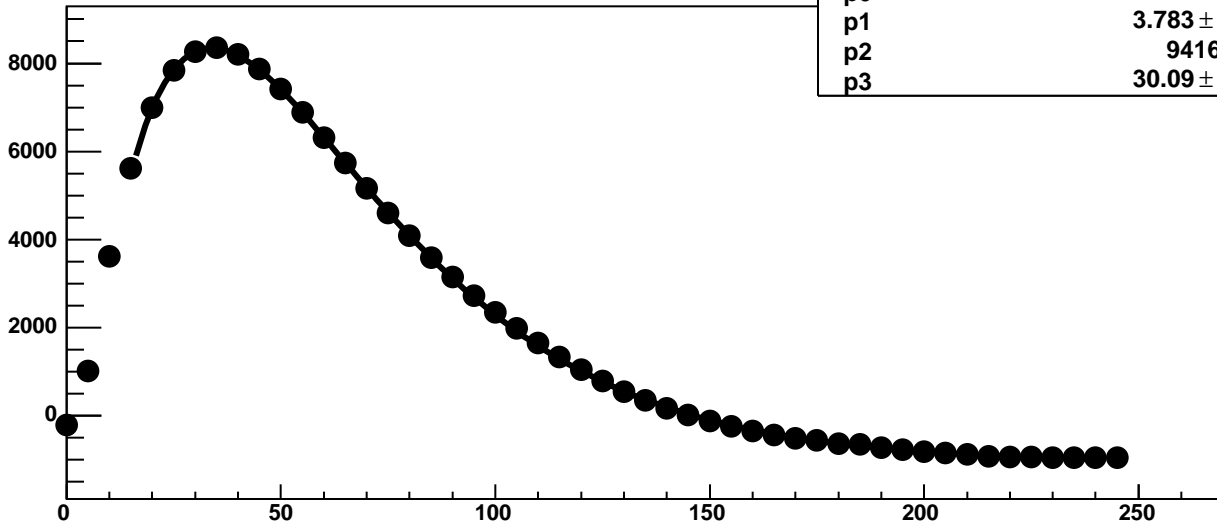


Chip 7, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold

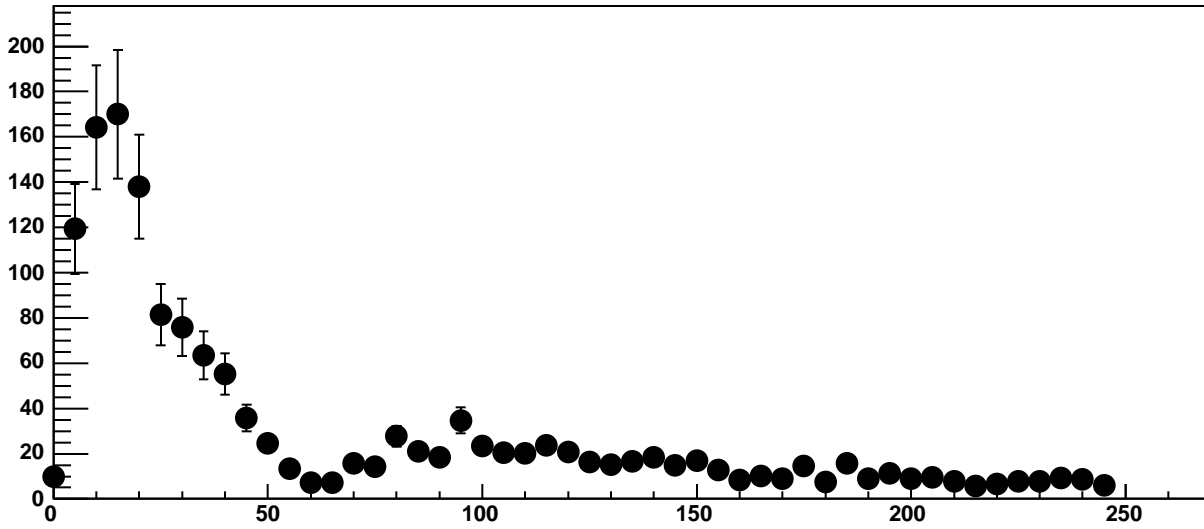


Chip 7, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold

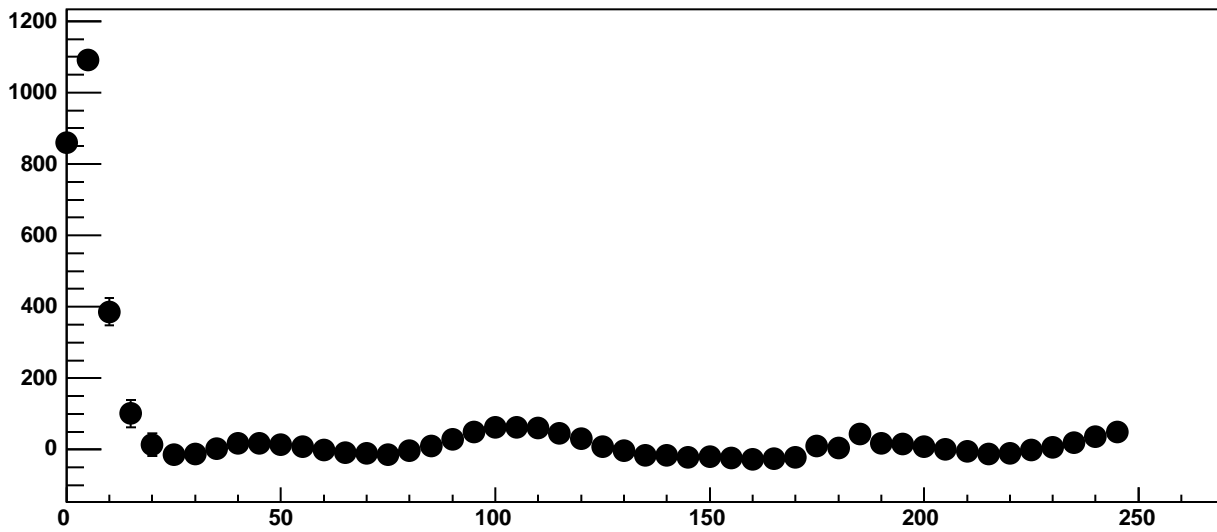
χ^2 / ndf	2212 / 42
p0	-1062 ± 0.7357
p1	3.783 ± 0.04837
p2	9416 ± 4.787
p3	30.09 ± 0.01484



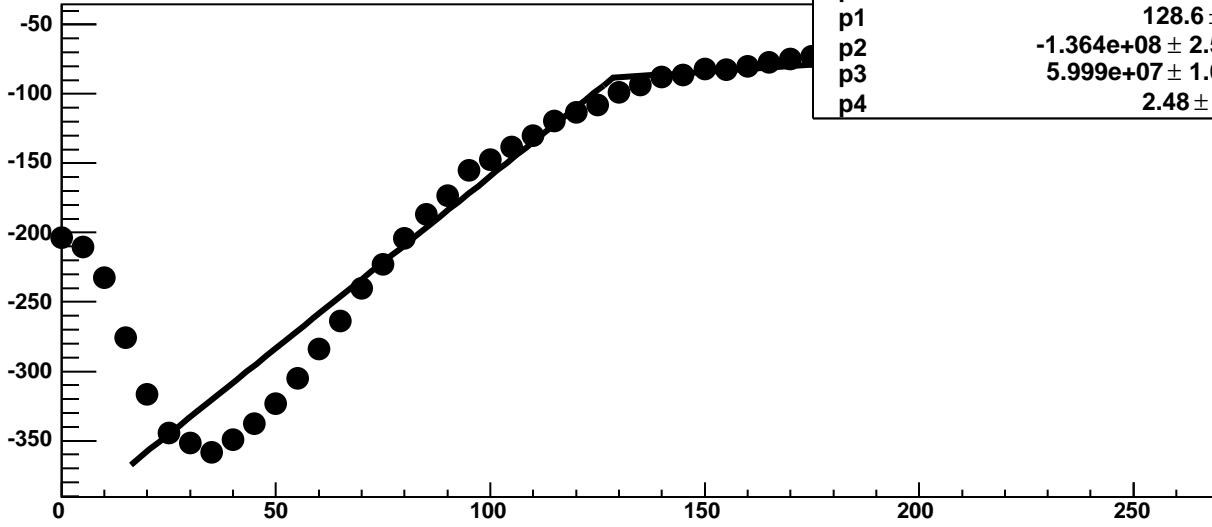
Chip 7, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold

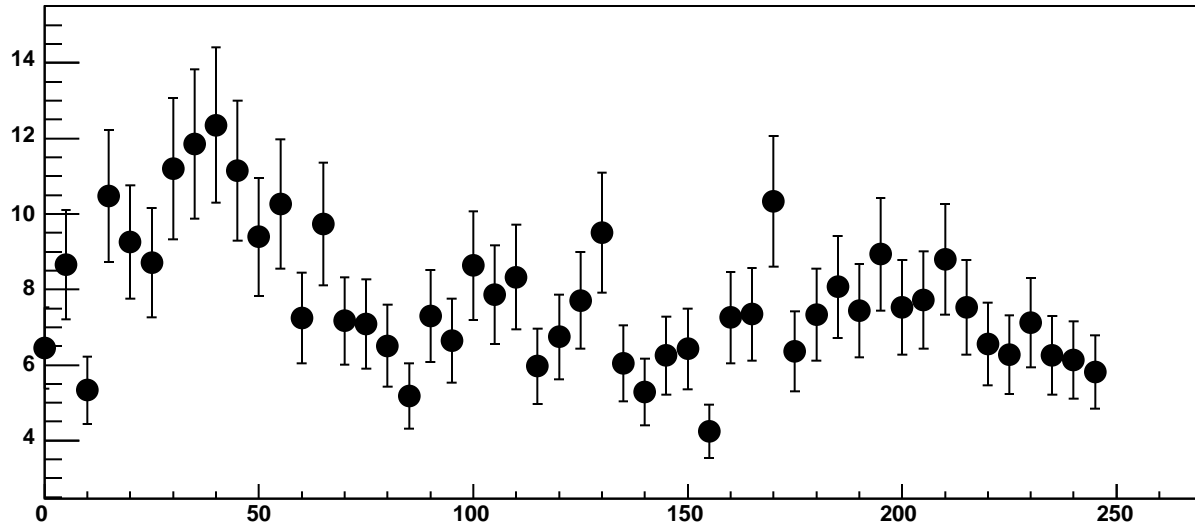


Chip 7, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

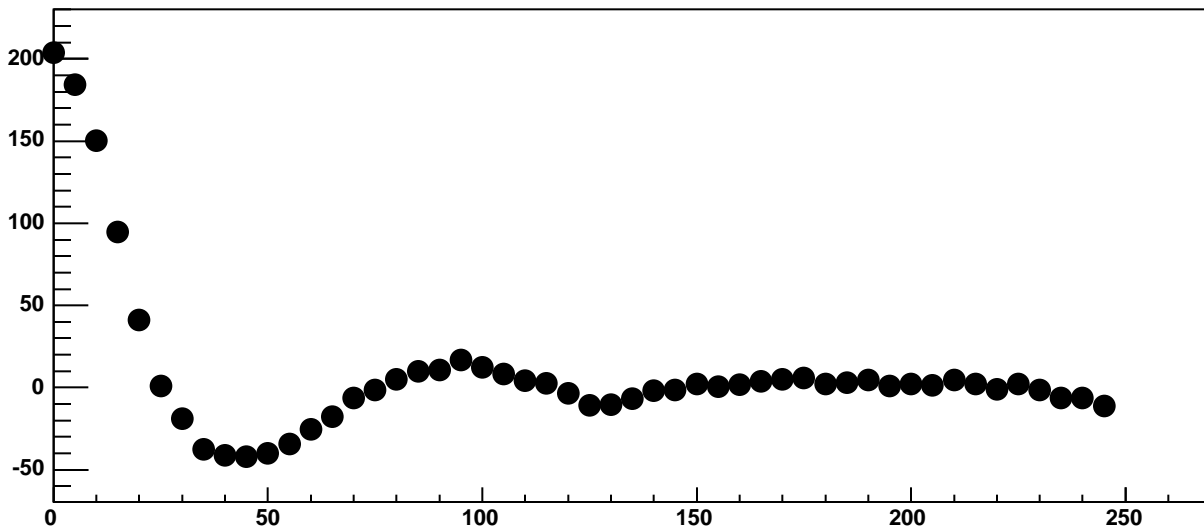


χ^2 / ndf	4033 / 41
p0	-88.45 ± 0.6544
p1	128.6 ± 0.4103
p2	-1.364e+08 ± 2.518e+06
p3	5.999e+07 ± 1.029e+06
p4	2.48 ± 0.01234

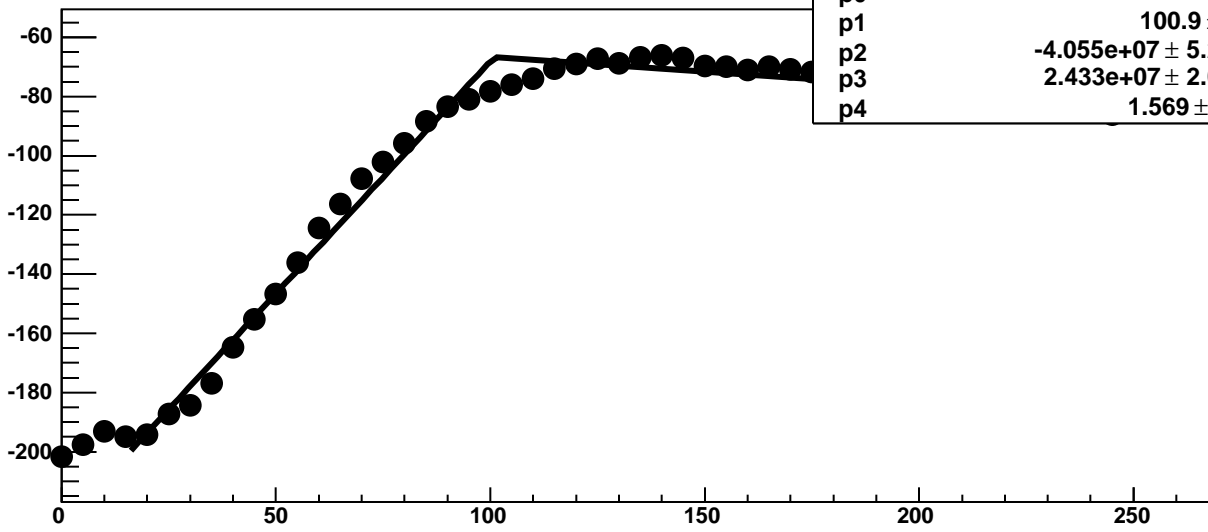
Chip 7, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

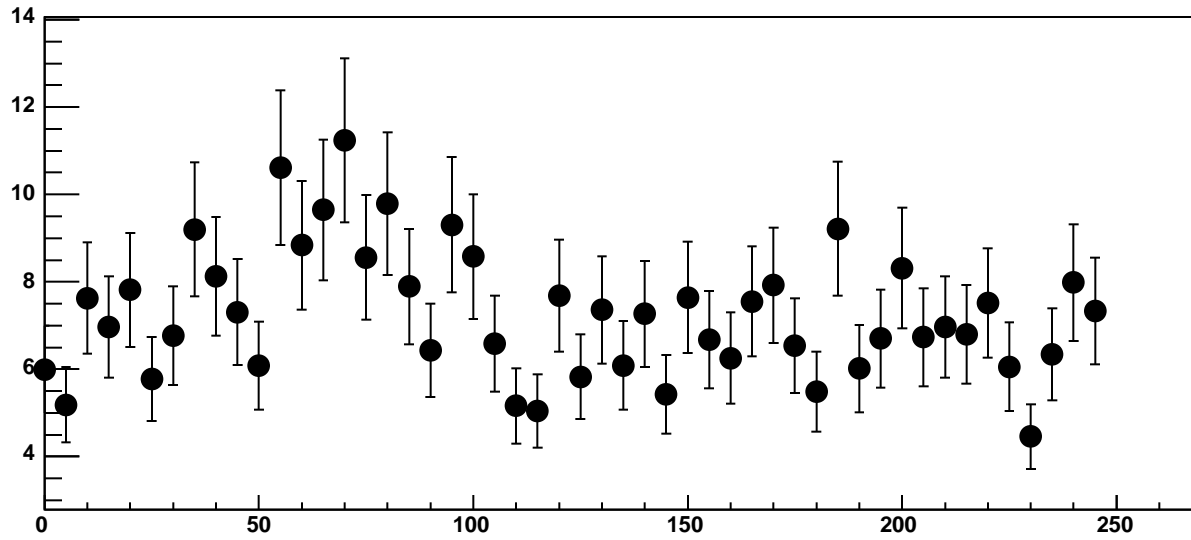


Chip 7, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

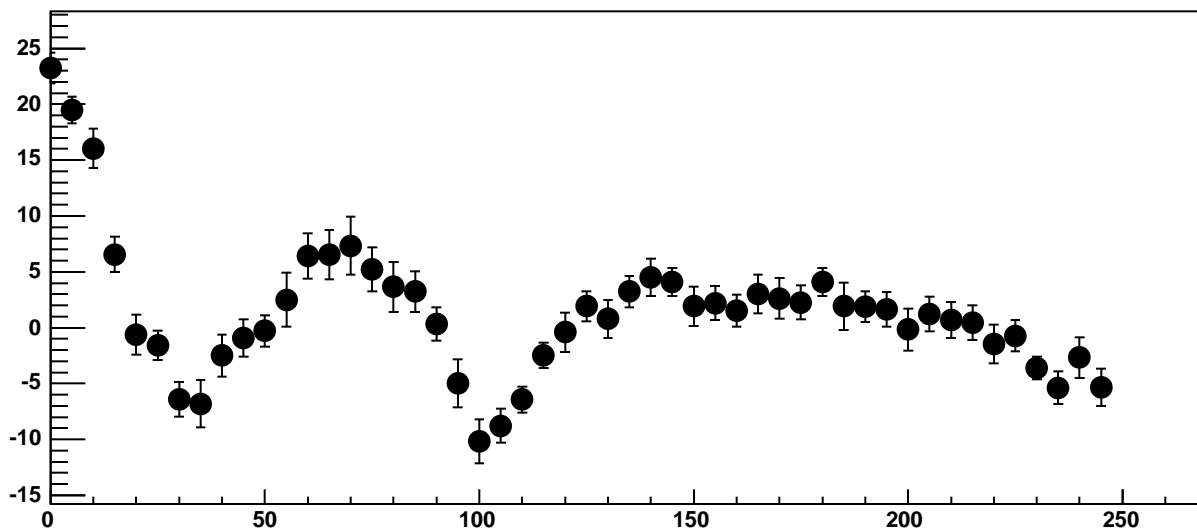


χ^2 / ndf	270.9 / 41
p0	-66.72 ± 0.5093
p1	100.9 ± 0.6105
p2	$-4.055\text{e}+07 \pm 5.273\text{e}+05$
p3	$2.433\text{e}+07 \pm 2.022\text{e}+05$
p4	1.569 ± 0.01587

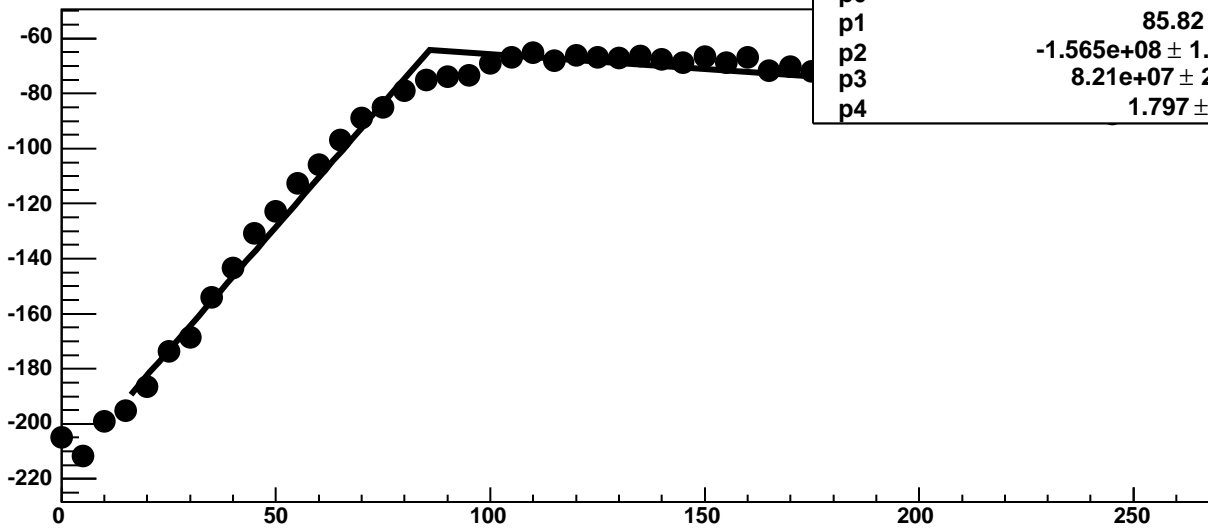
Chip 7, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

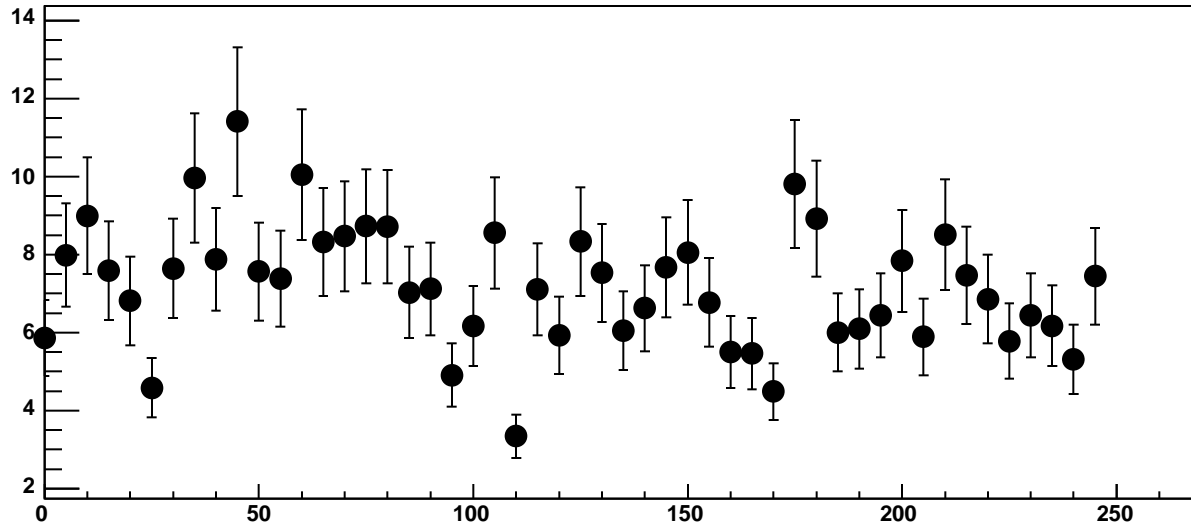


Chip 7, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold

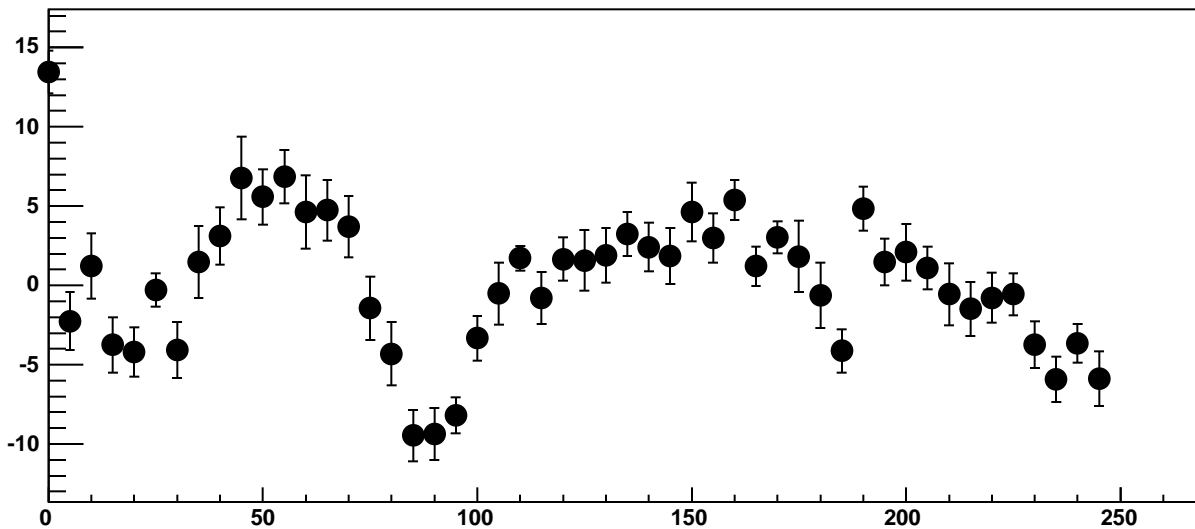


χ^2 / ndf	313.7 / 41
p0	-64.12 ± 0.4587
p1	85.82 ± 0.5409
p2	$-1.565\text{e}+08 \pm 1.774\text{e}+06$
p3	$8.21\text{e}+07 \pm 2.64\text{e}+05$
p4	1.797 ± 0.01994

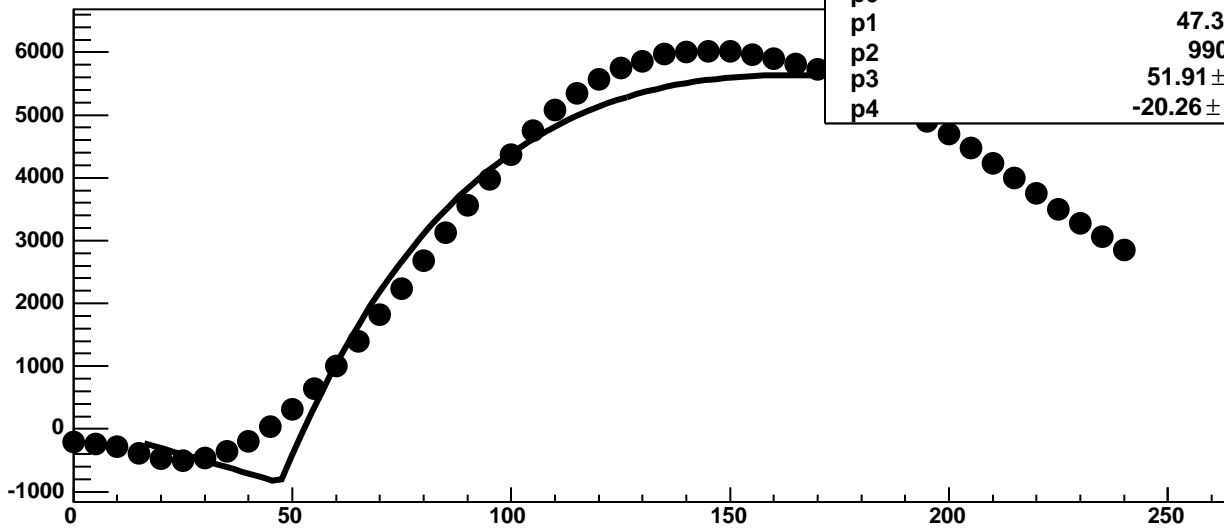
Chip 7, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold

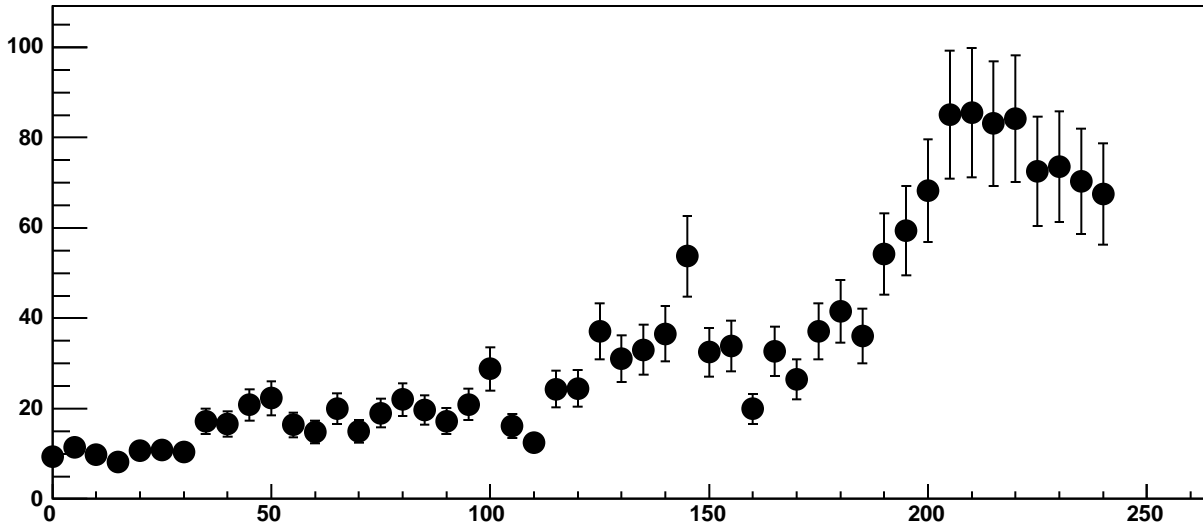


Chip 7, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

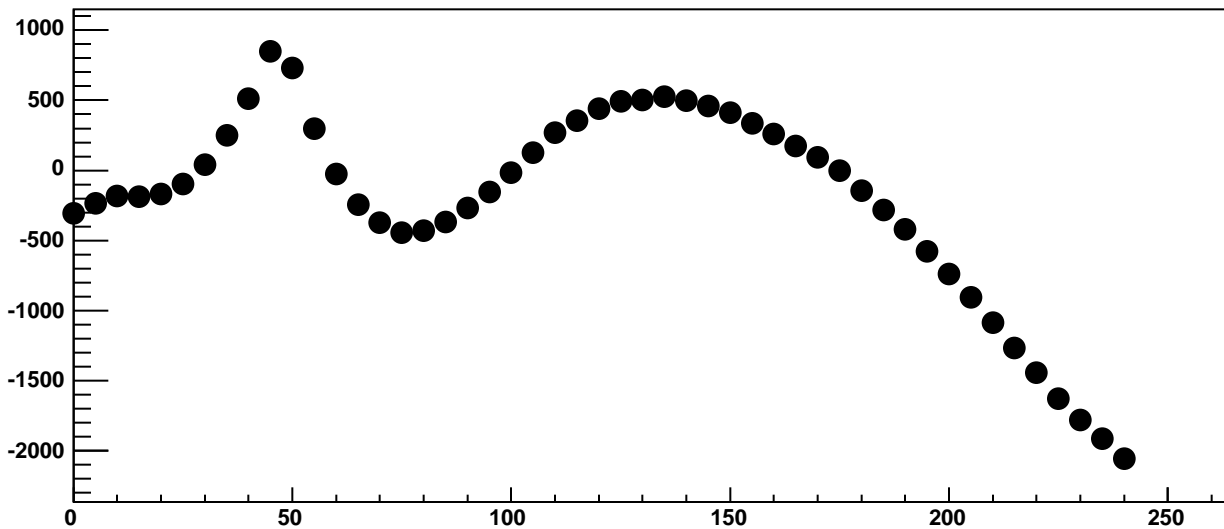


χ^2 / ndf	2.626e+05 / 41
p0	-859.4 ± 1.867
p1	47.31 ± 0.02
p2	9909 ± 16
p3	51.91 ± 0.08211
p4	-20.26 ± 0.08048

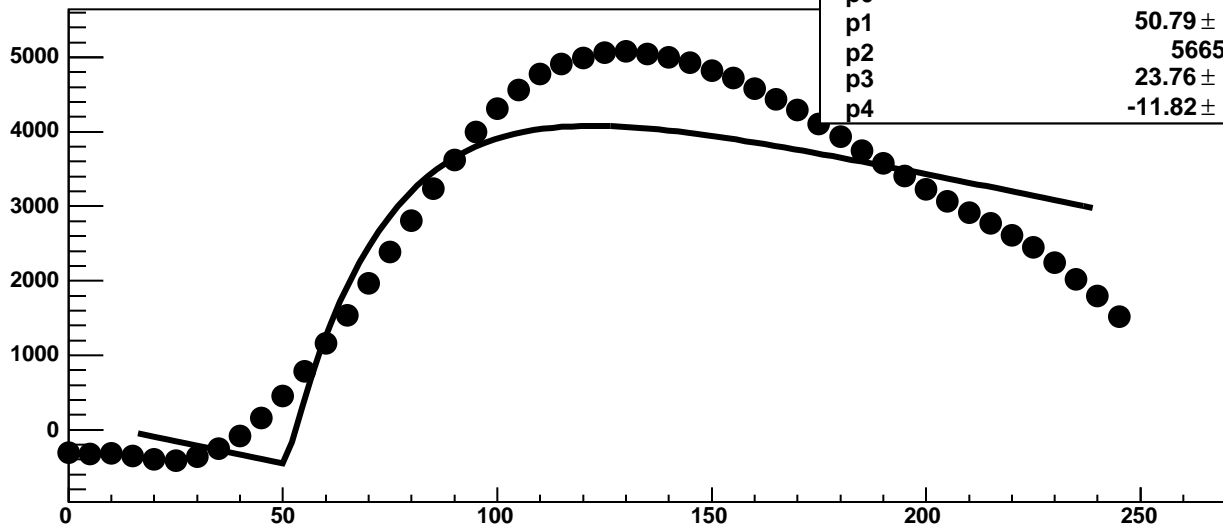
Chip 7, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold

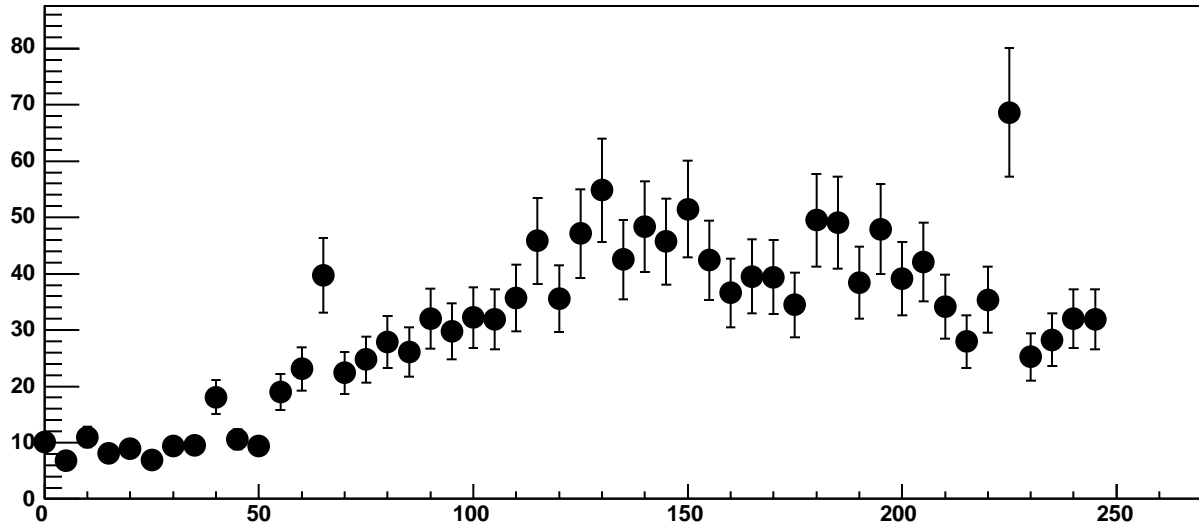


Chip 7, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

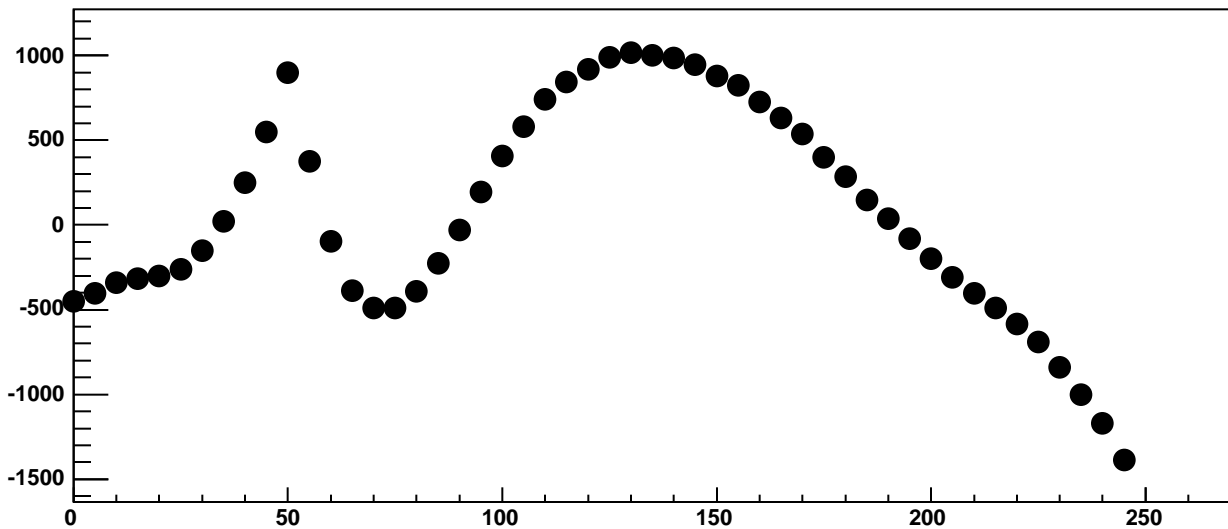


χ^2 / ndf	5.402e+05 / 41
p0	-460.5 ± 1.038
p1	50.79 ± 0.02163
p2	5665 ± 6.031
p3	23.76 ± 0.04412
p4	-11.82 ± 0.03585

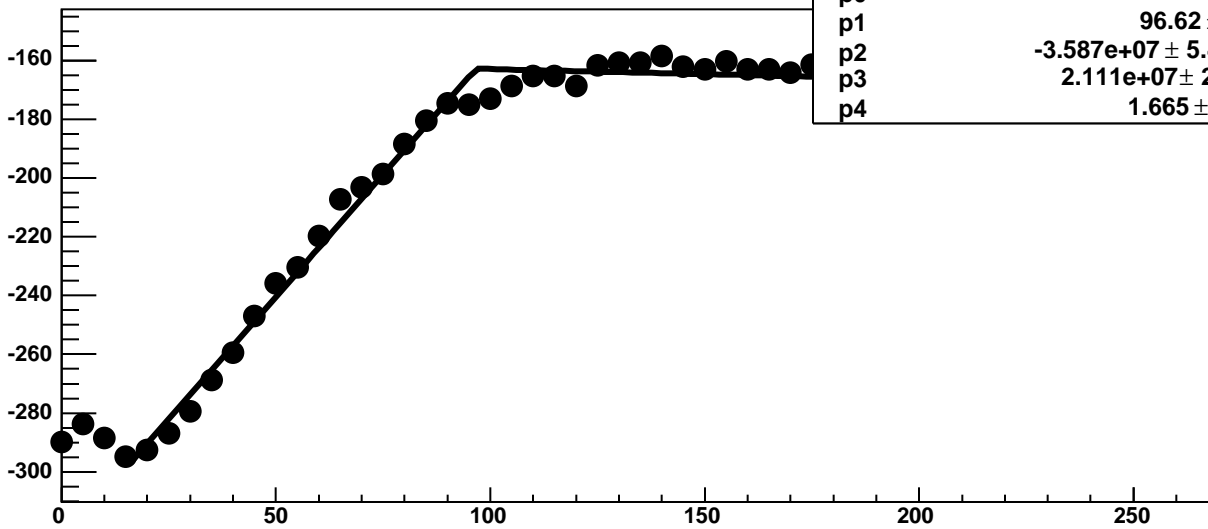
Chip 7, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

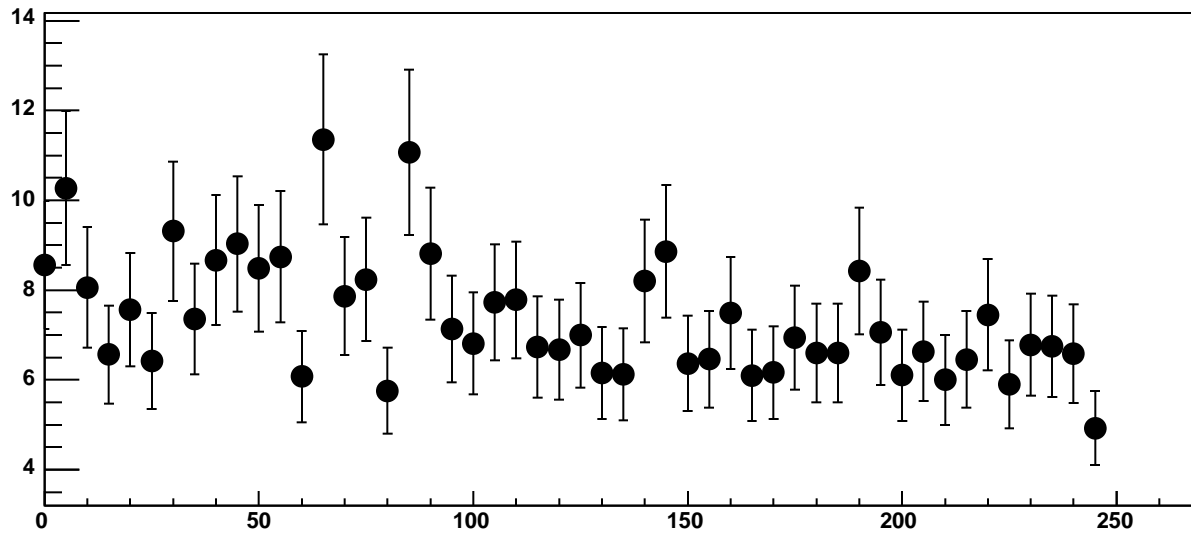


Chip 7, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold

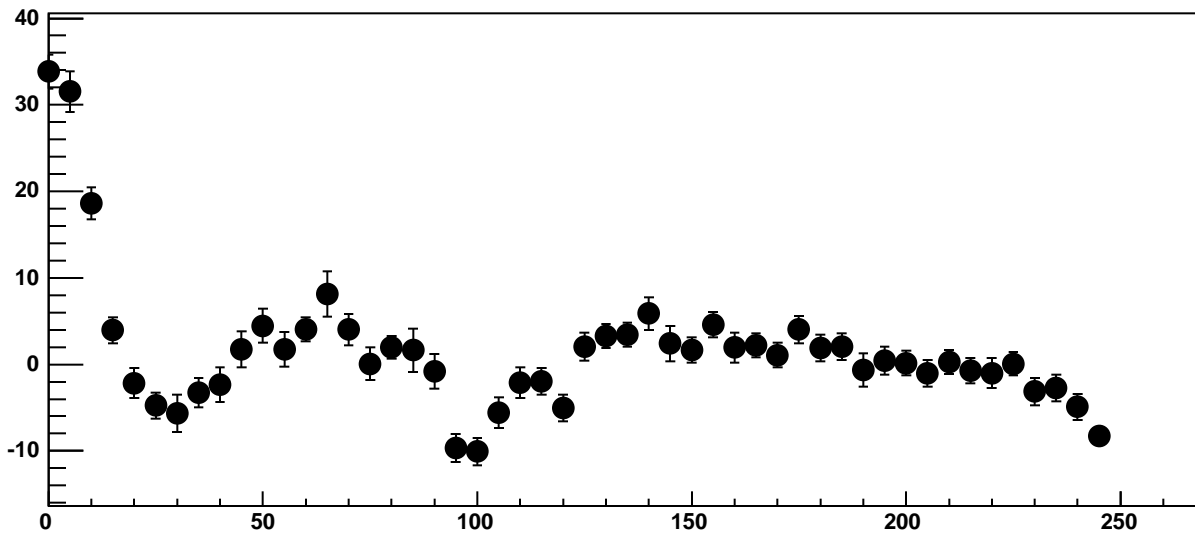


χ^2 / ndf	232.1 / 41
p0	-162.8 ± 0.5841
p1	96.62 ± 0.6085
p2	$-3.587\text{e}+07 \pm 5.452\text{e}+05$
p3	$2.111\text{e}+07 \pm 2.28\text{e}+05$
p4	1.665 ± 0.01695

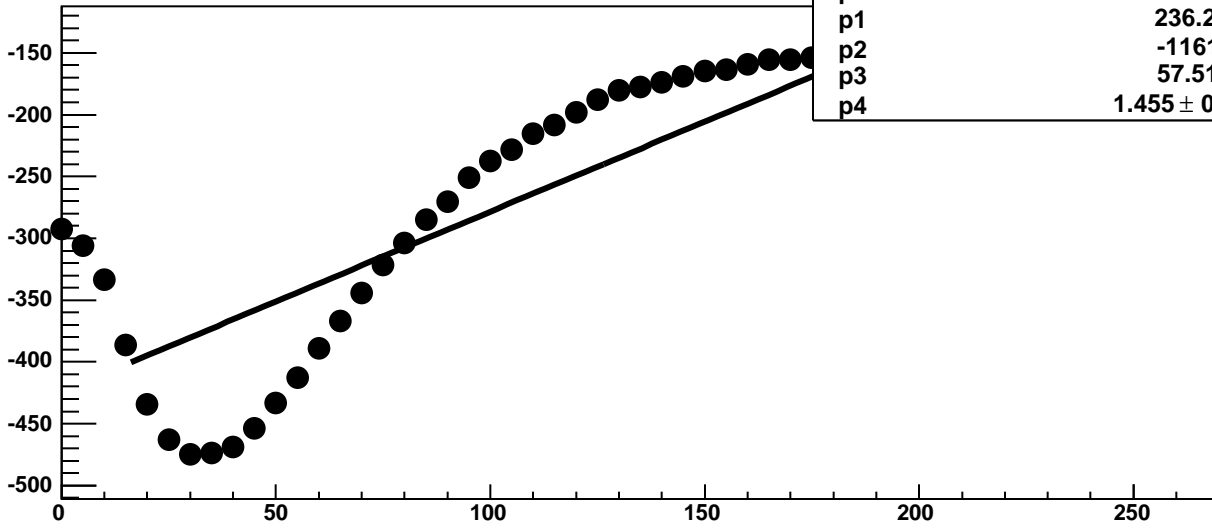
Chip 7, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold

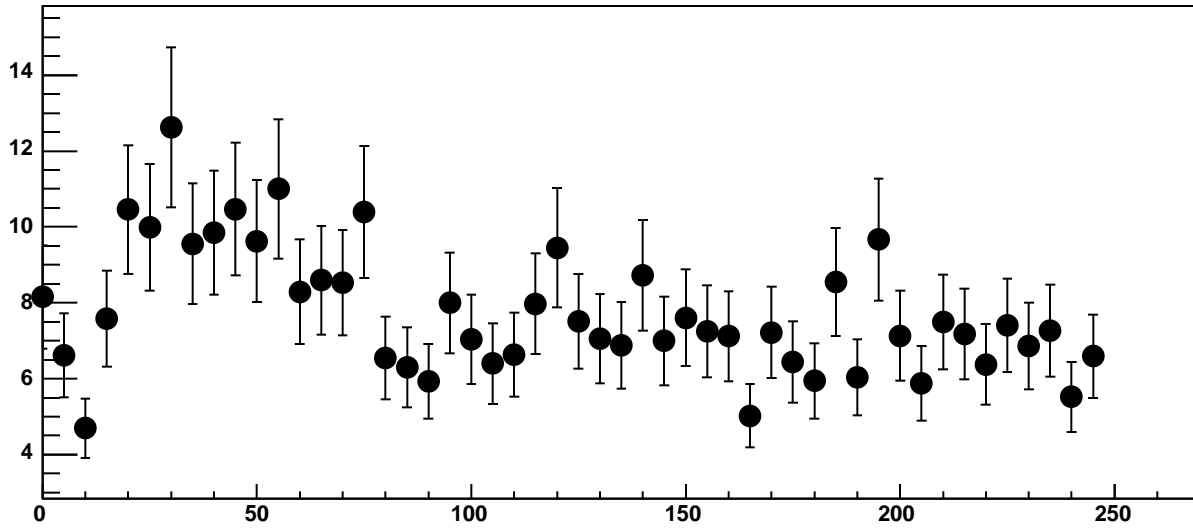


Chip 7, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

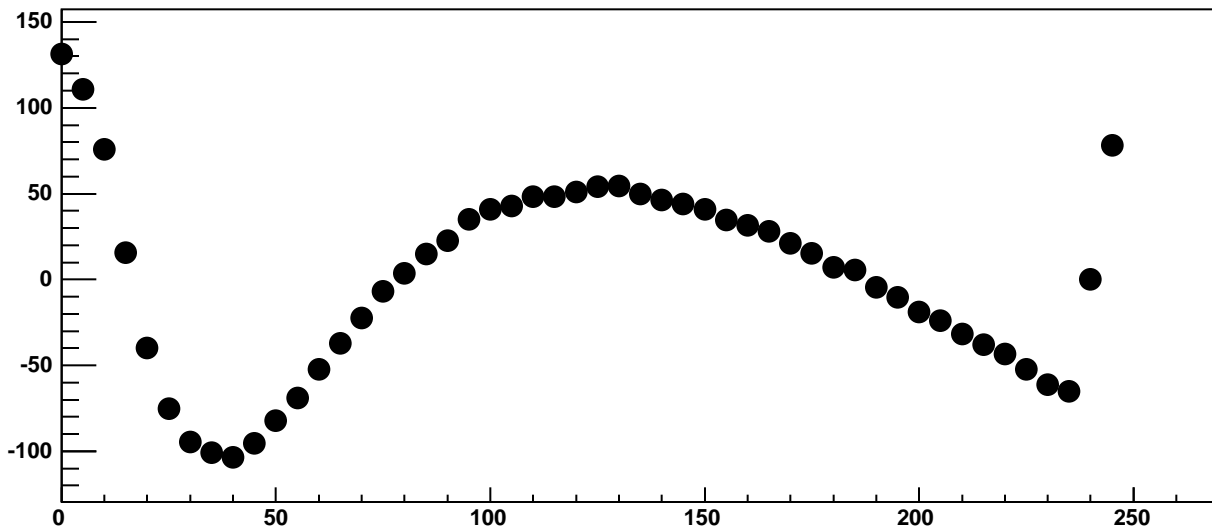


χ^2 / ndf	2.908e+04 / 41
p0	-80.23 ± 1.545
p1	236.2 ± 1.012
p2	-1161 ± 291.7
p3	57.51 ± 12.23
p4	1.455 ± 0.004193

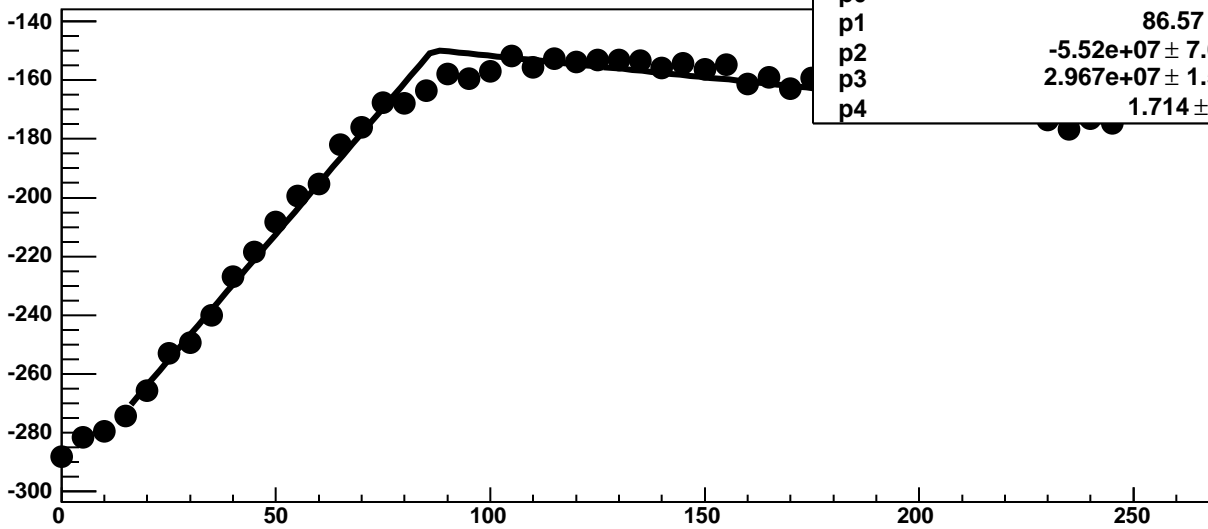
Chip 7, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

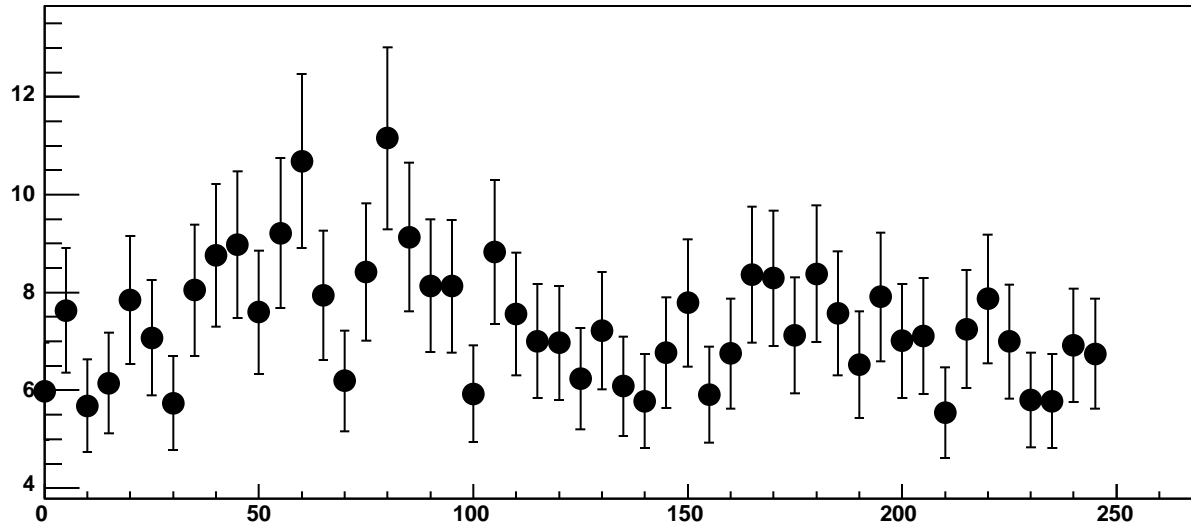


Chip 7, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

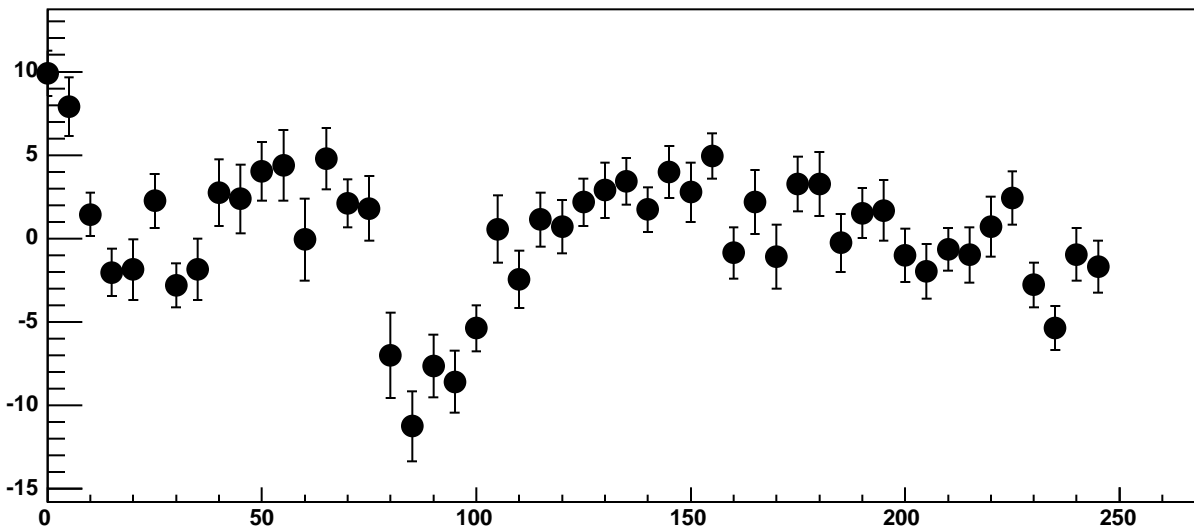


χ^2 / ndf	198.1 / 41
p0	-149.7 ± 0.5439
p1	86.57 ± 0.6097
p2	$-5.52\text{e}+07 \pm 7.085\text{e}+05$
p3	$2.967\text{e}+07 \pm 1.592\text{e}+05$
p4	1.714 ± 0.02106

Chip 7, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold

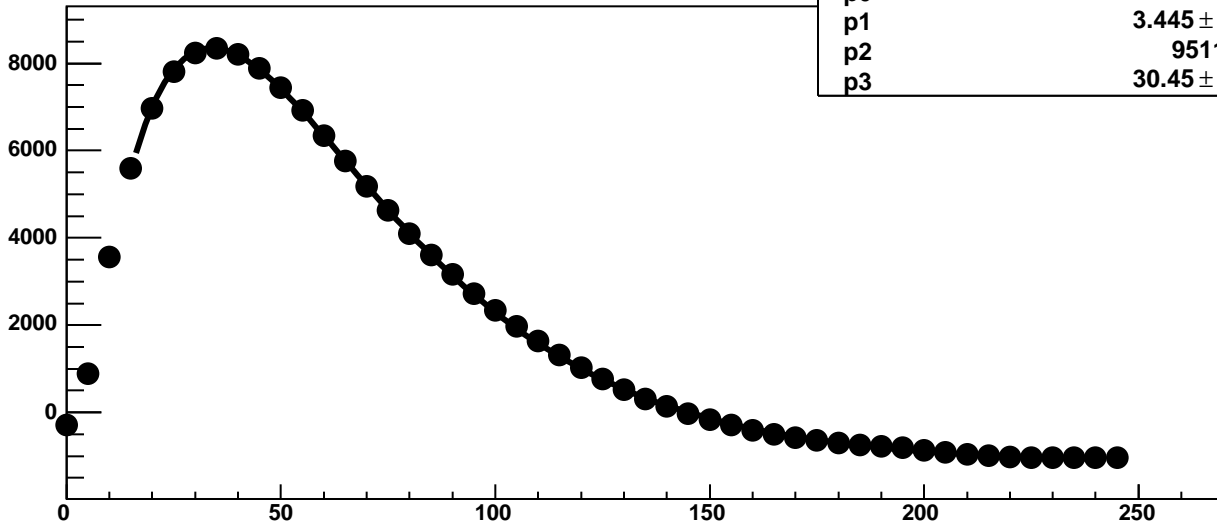


Chip 7, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

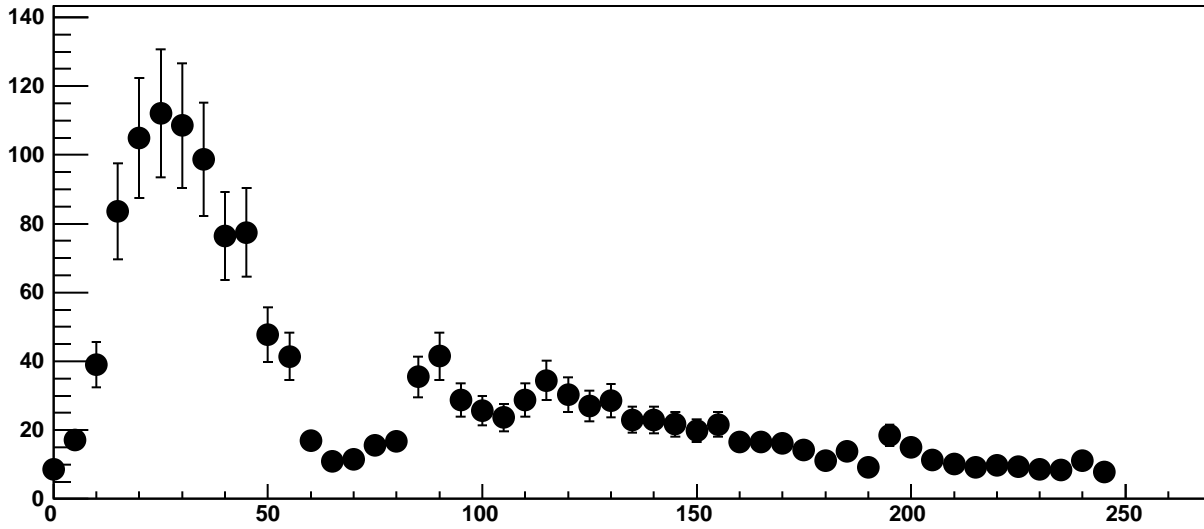


Chip 7, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold

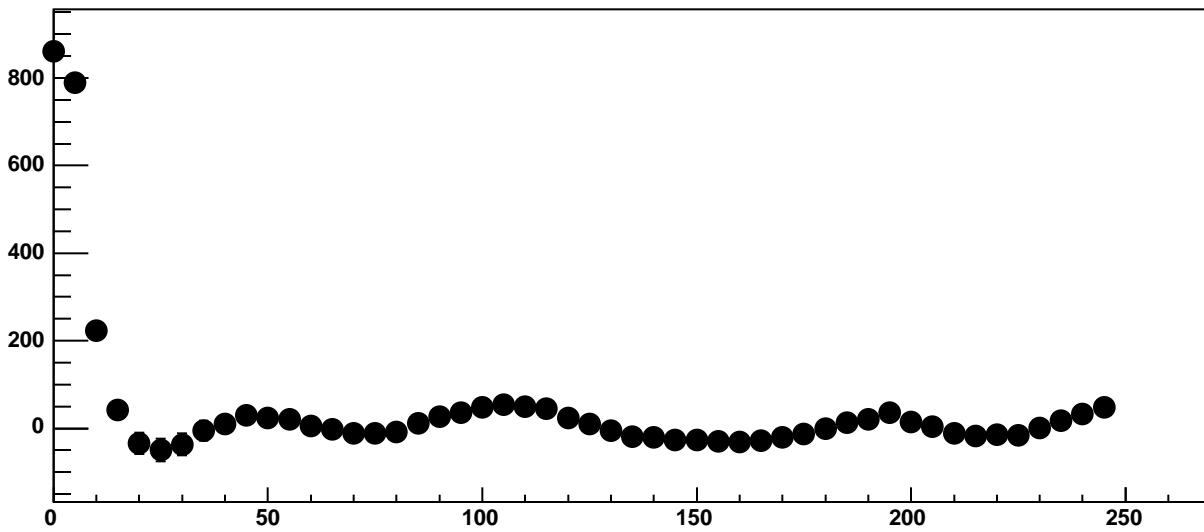
χ^2 / ndf	1305 / 42
p0	-1152 ± 0.9407
p1	3.445 ± 0.04108
p2	9511 ± 4.812
p3	30.45 ± 0.01828



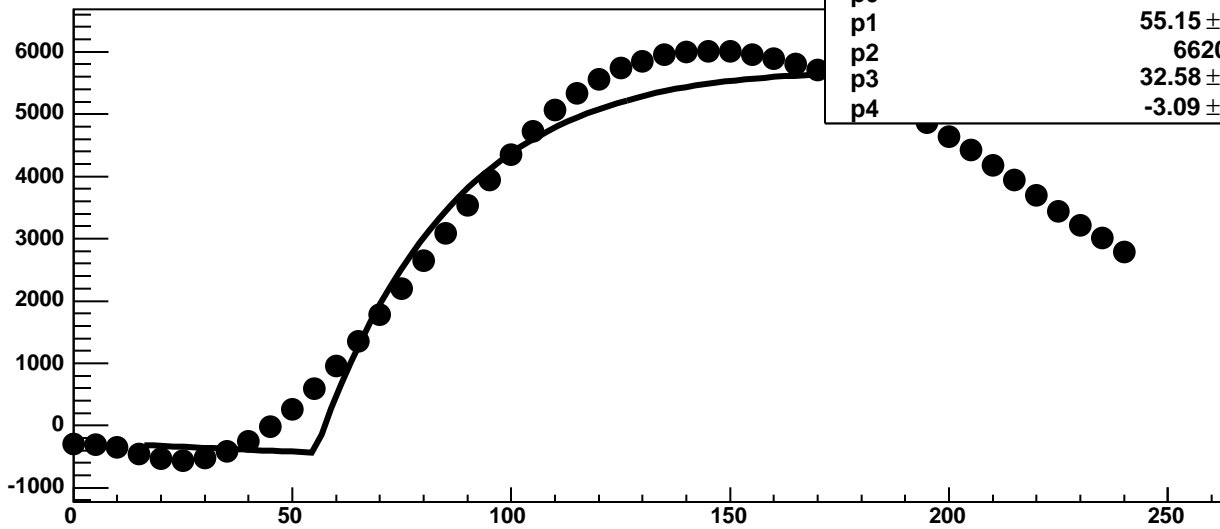
Chip 7, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold

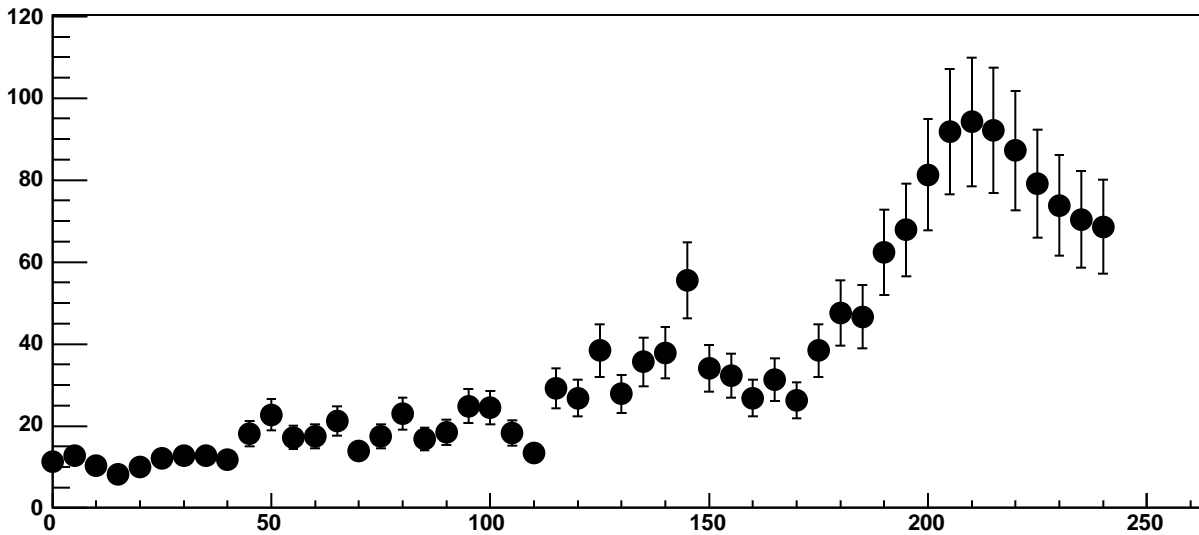


Chip 7, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

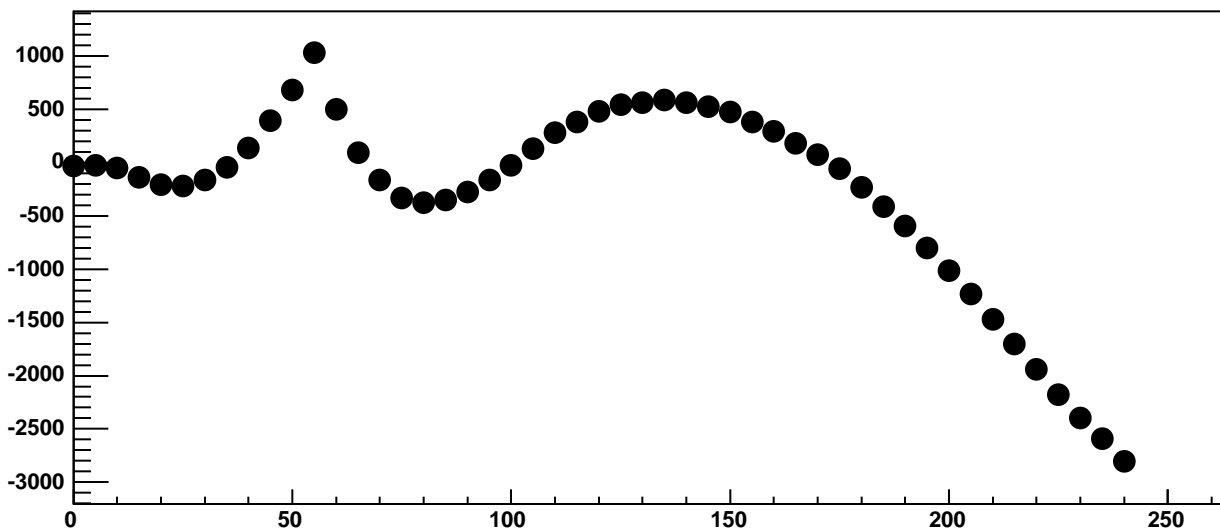


χ^2 / ndf	3.395e+05 / 41
p0	-435 ± 1.718
p1	55.15 ± 0.02186
p2	6620 ± 8.847
p3	32.58 ± 0.05547
p4	-3.09 ± 0.05524

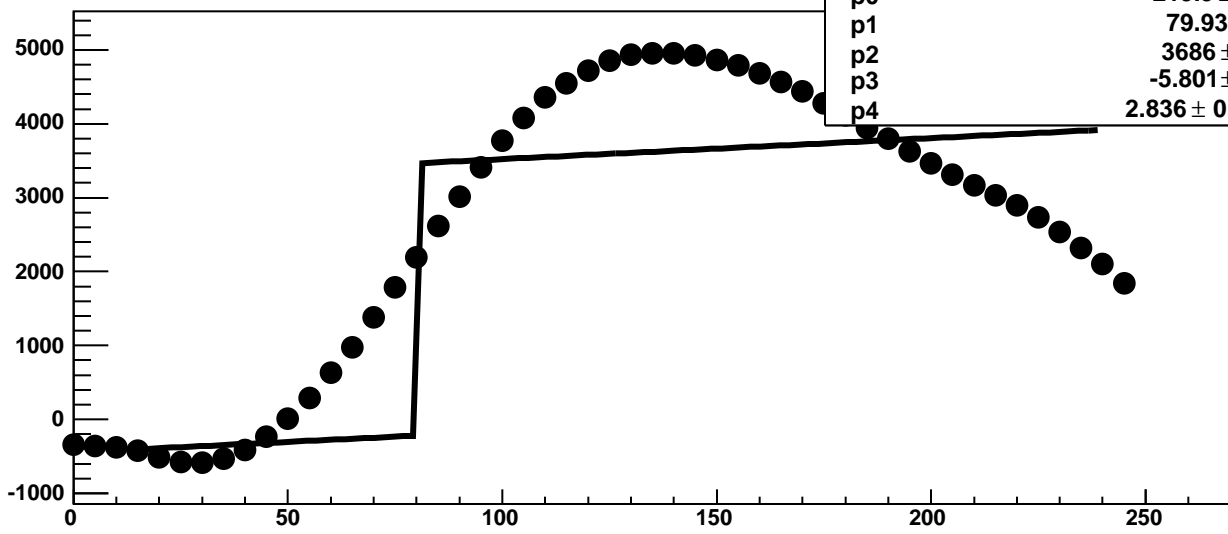
Chip 7, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

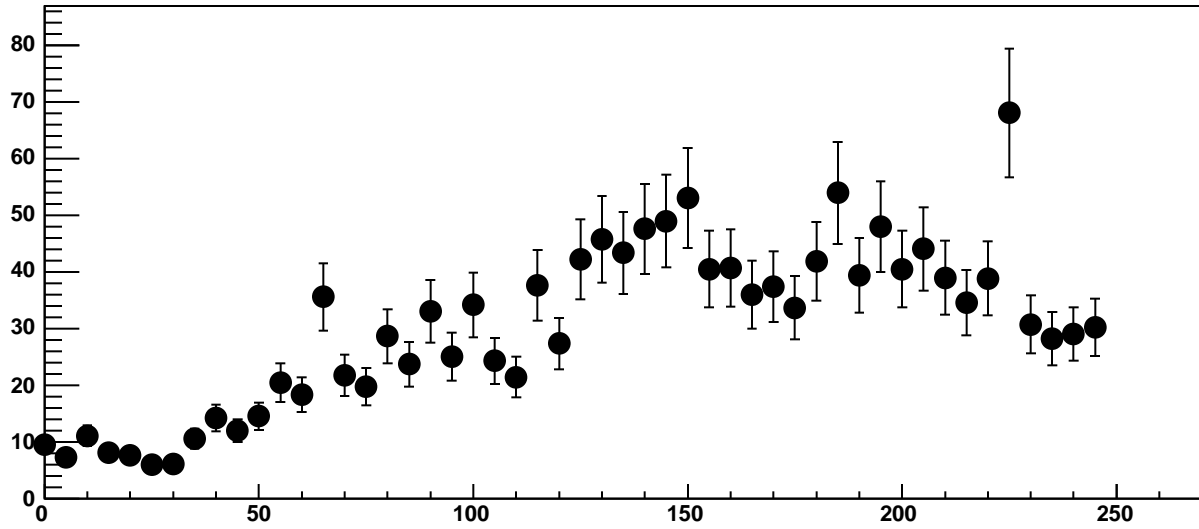


Chip 7, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

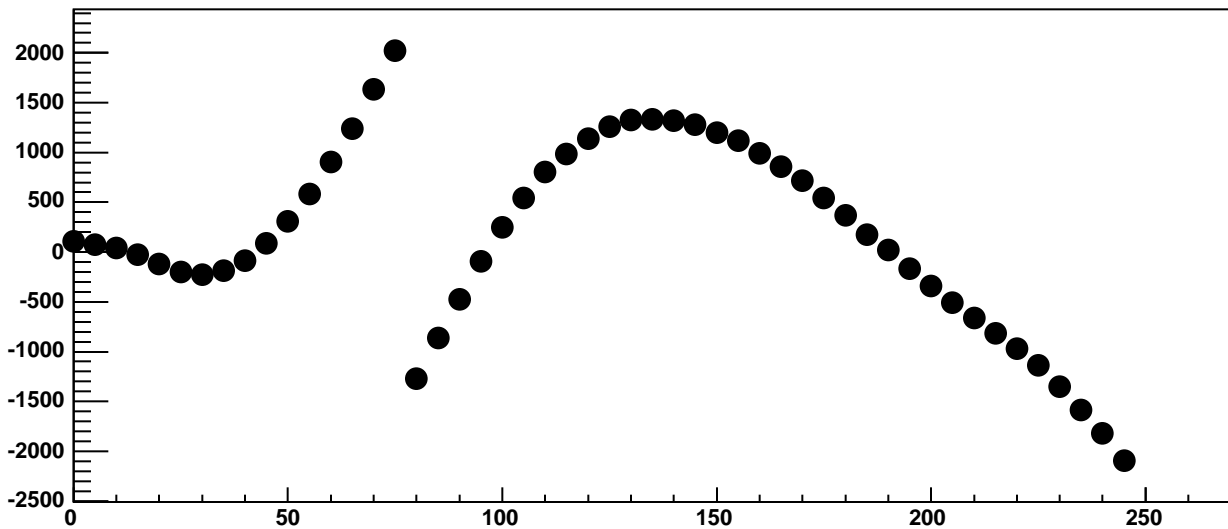


χ^2 / ndf	9.546e+05 / 41
p0	-219.9 ± 0.1936
p1	79.93 ± 1.157
p2	3686 ± 0.3537
p3	-5.801 ± 0.2527
p4	2.836 ± 0.003188

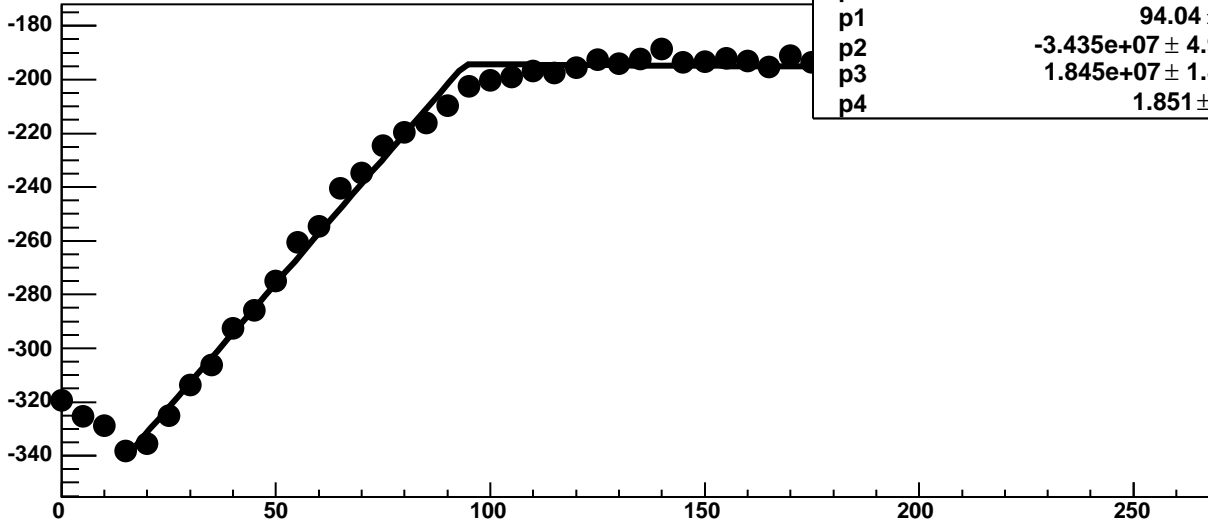
Chip 7, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold

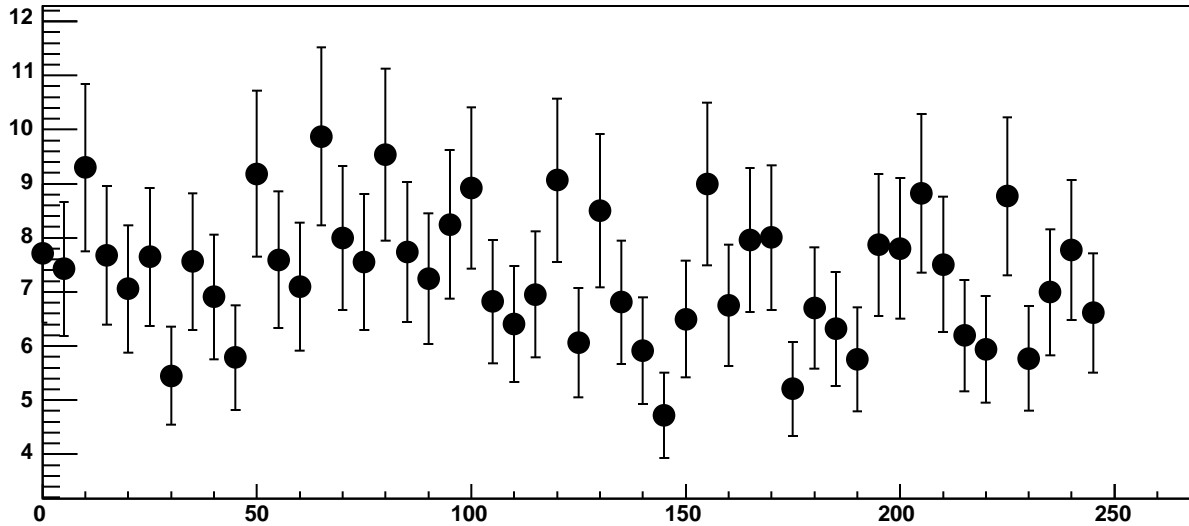


Chip 7, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

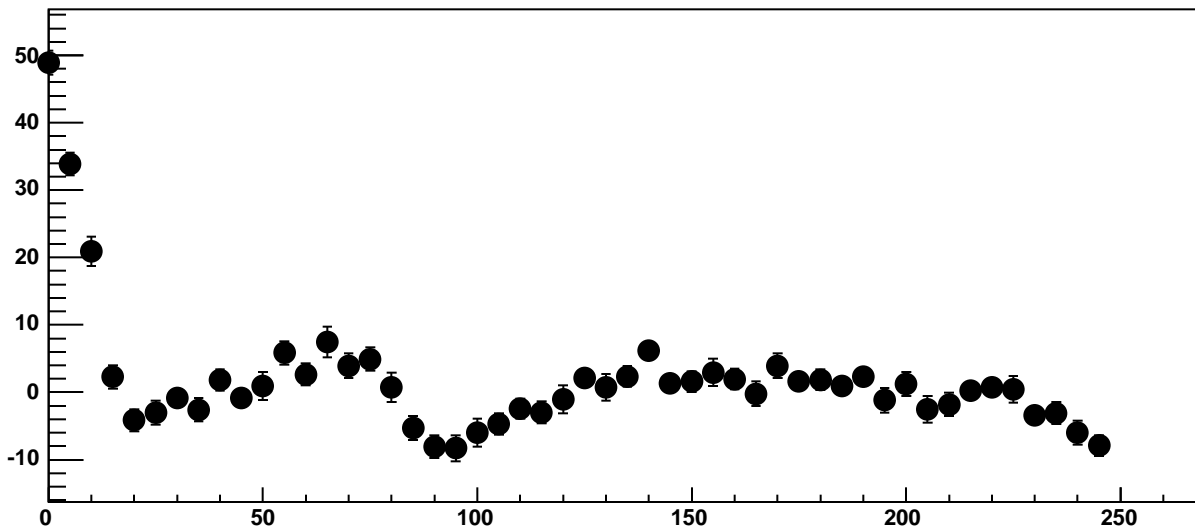


χ^2 / ndf	197.7 / 41
p0	-194.3 ± 0.5836
p1	94.04 ± 0.5885
p2	-3.435e+07 ± 4.996e+05
p3	1.845e+07 ± 1.825e+05
p4	1.851 ± 0.01861

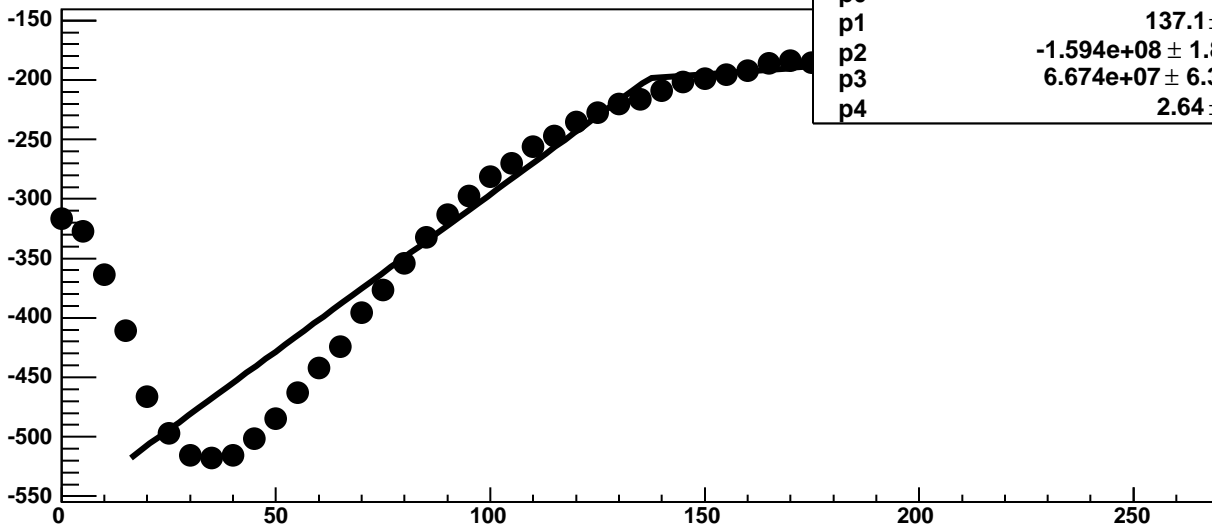
Chip 7, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

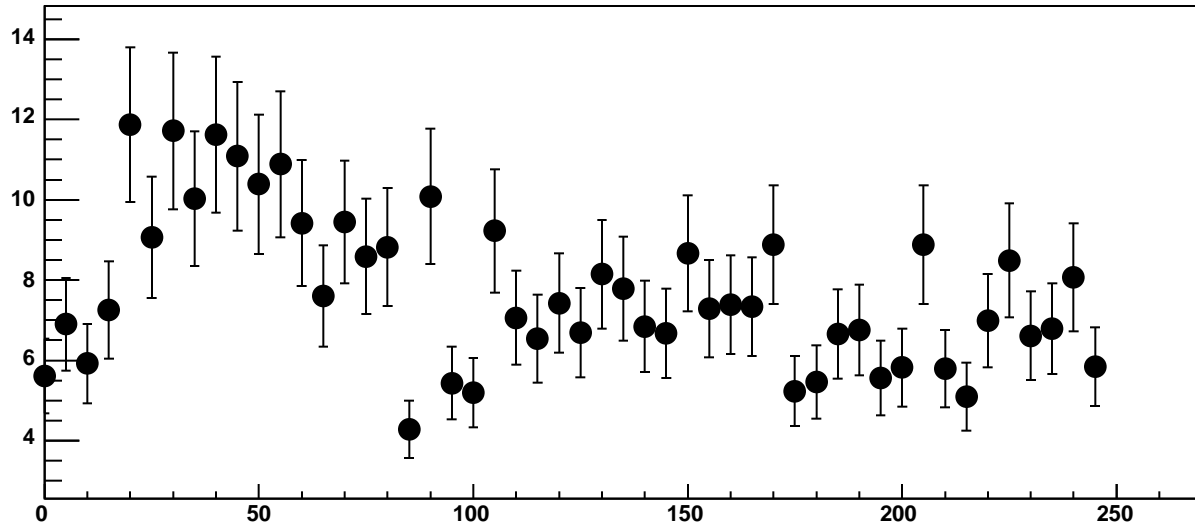


Chip 7, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold

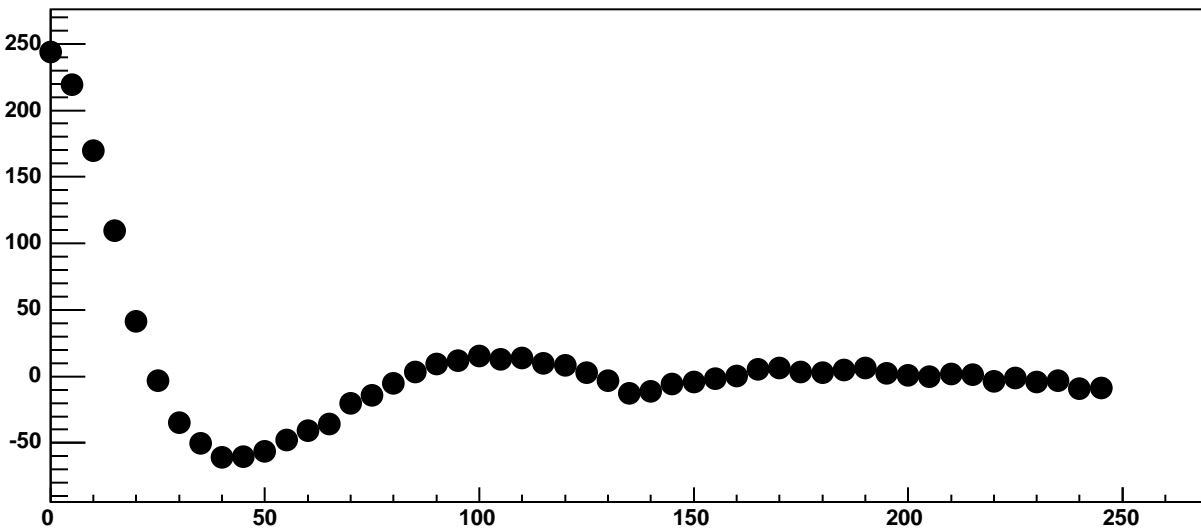


χ^2 / ndf	8842 / 41
p0	-198.3 ± 0.7899
p1	137.1 ± 0.4083
p2	$-1.594\text{e}+08 \pm 1.858\text{e}+06$
p3	$6.674\text{e}+07 \pm 6.399\text{e}+05$
p4	2.64 ± 0.0107

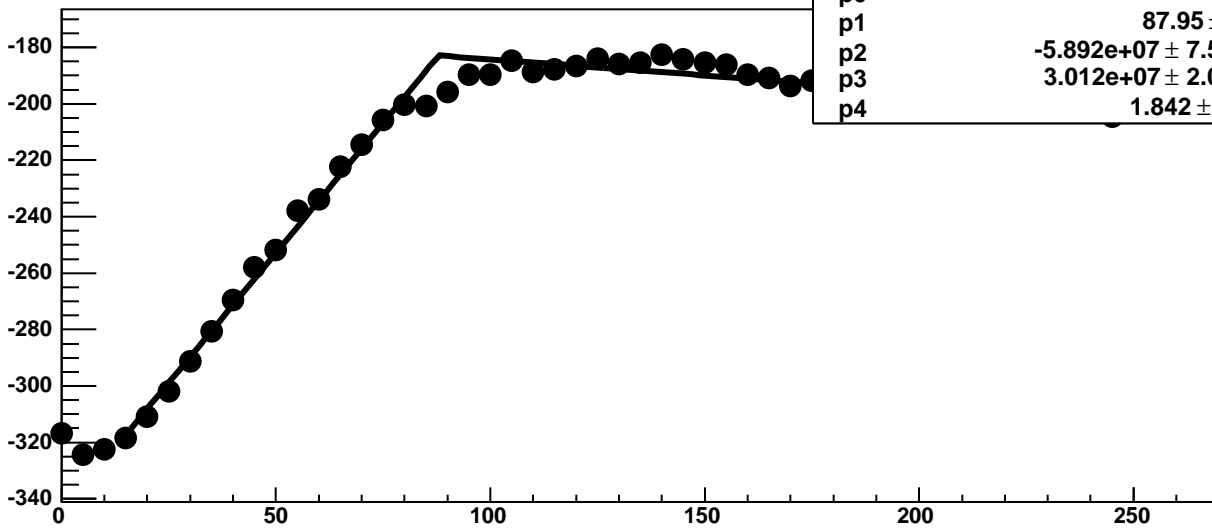
Chip 7, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold

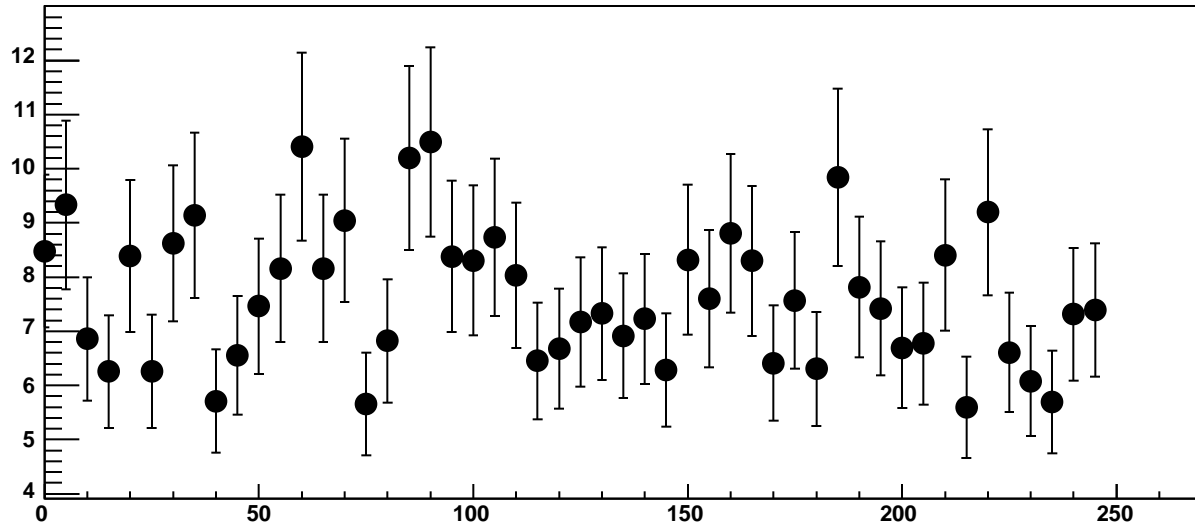


Chip 7, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

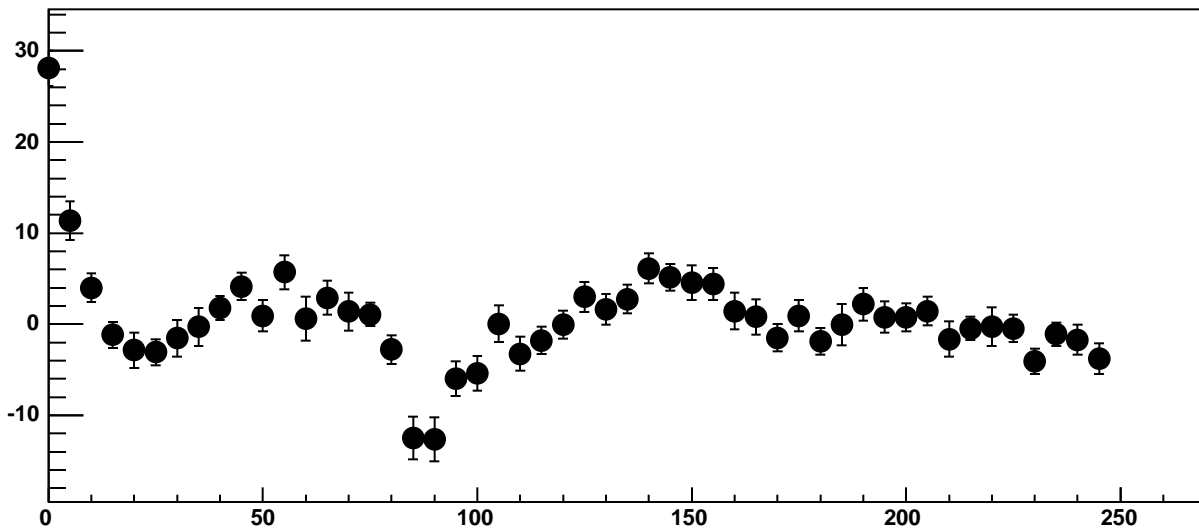


χ^2 / ndf	175.3 / 41
p0	-182.9 ± 0.5949
p1	87.95 ± 0.5623
p2	$-5.892\text{e}+07 \pm 7.593\text{e}+05$
p3	$3.012\text{e}+07 \pm 2.068\text{e}+05$
p4	1.842 ± 0.02001

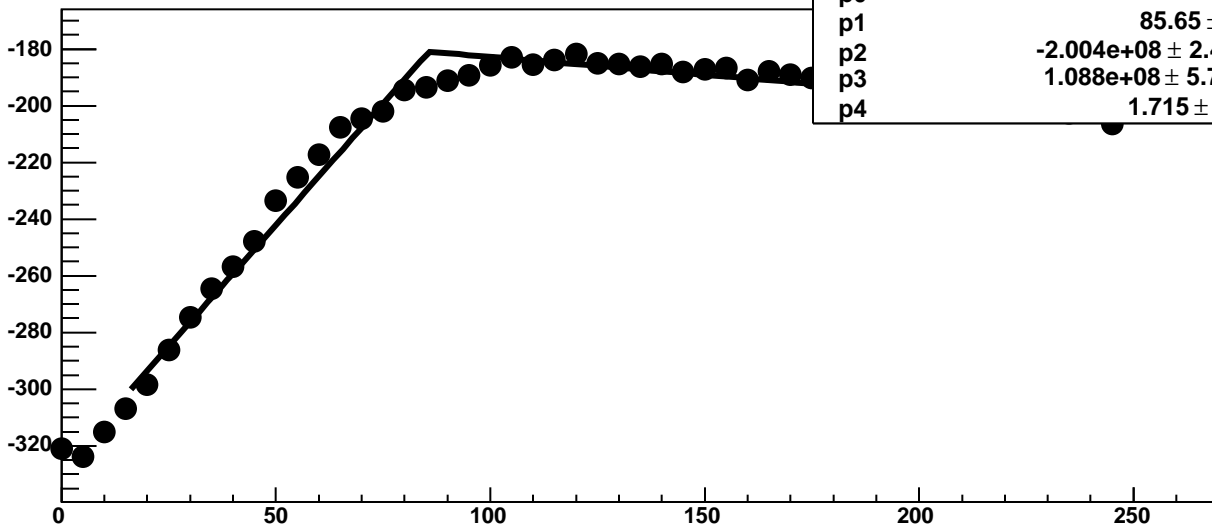
Chip 7, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

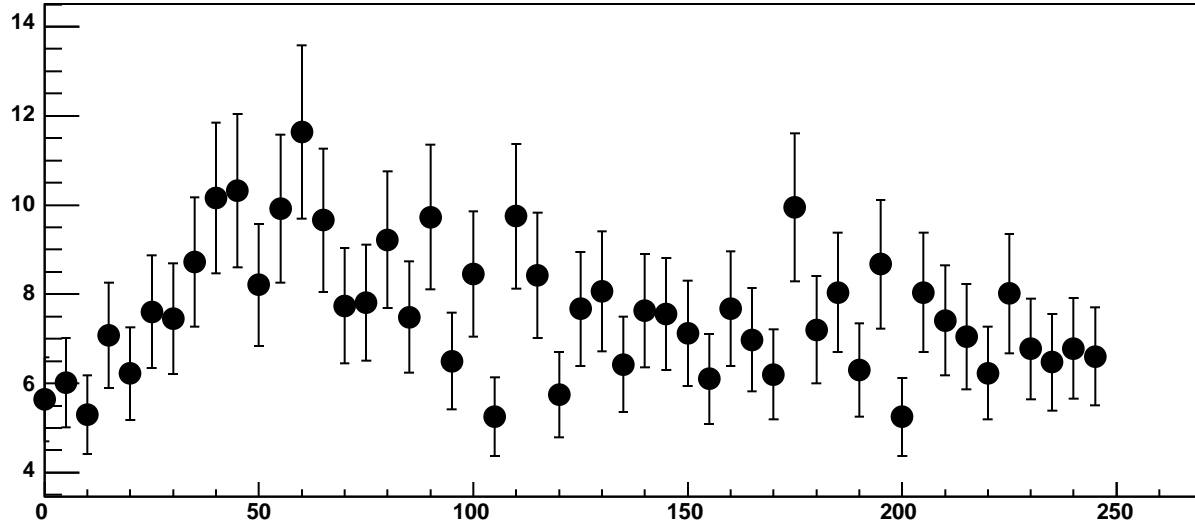


Chip 7, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

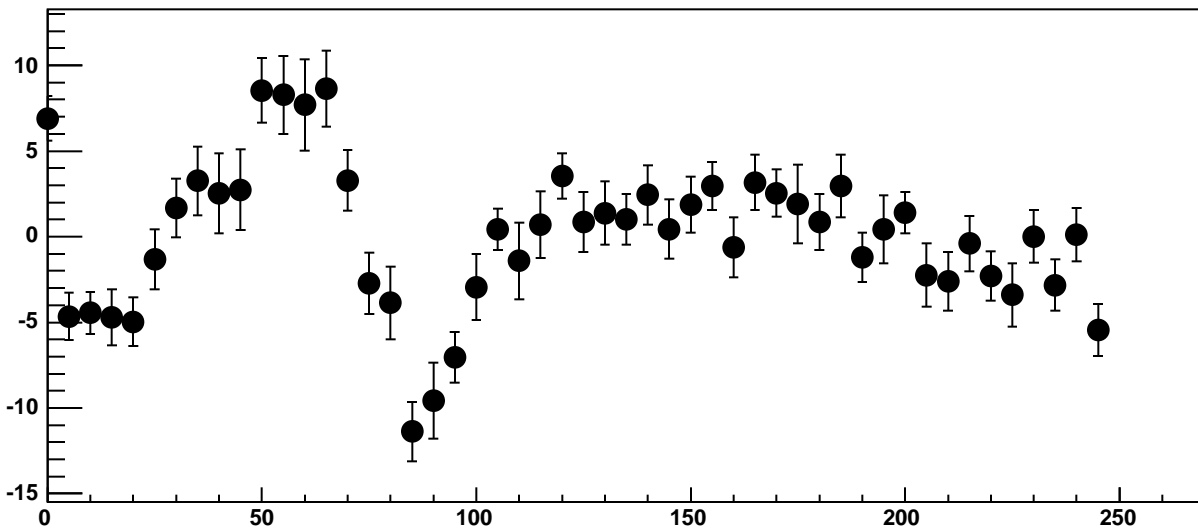


χ^2 / ndf	223.9 / 41
p0	-180.9 ± 0.5552
p1	85.65 ± 0.6044
p2	$-2.004\text{e}+08 \pm 2.446\text{e}+06$
p3	$1.088\text{e}+08 \pm 5.777\text{e}+04$
p4	1.715 ± 0.02122

Chip 7, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold

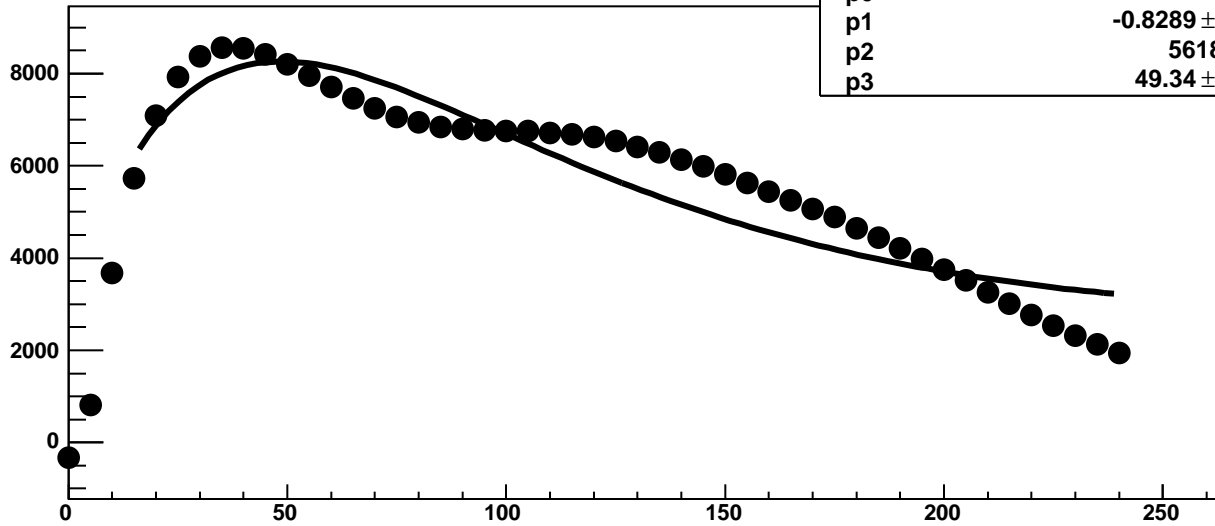


Chip 7, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

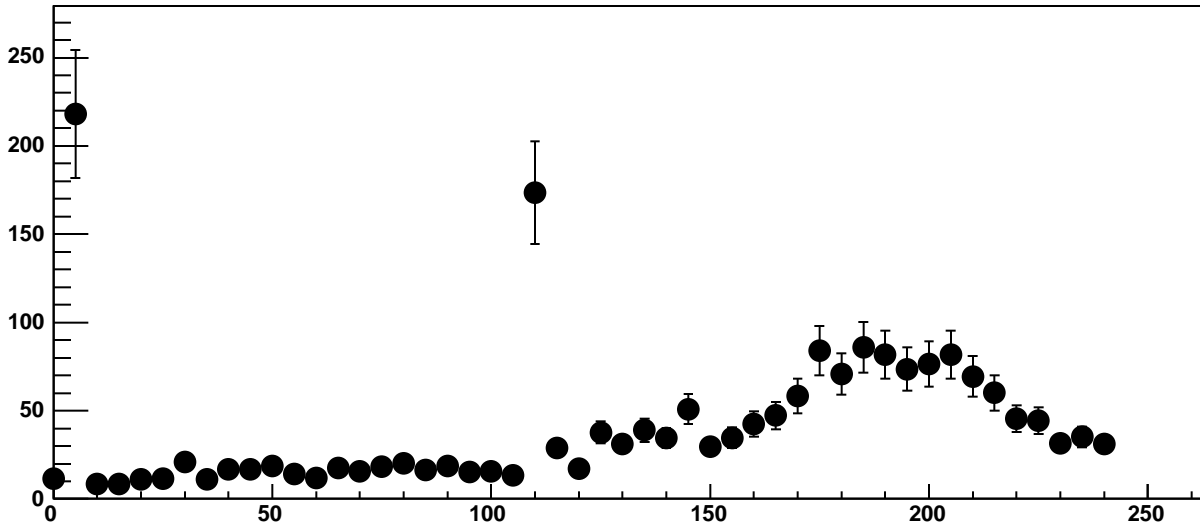


Chip 7, Channel 15, Enable 5!, DAC=4000, ADC Mean vs Hold

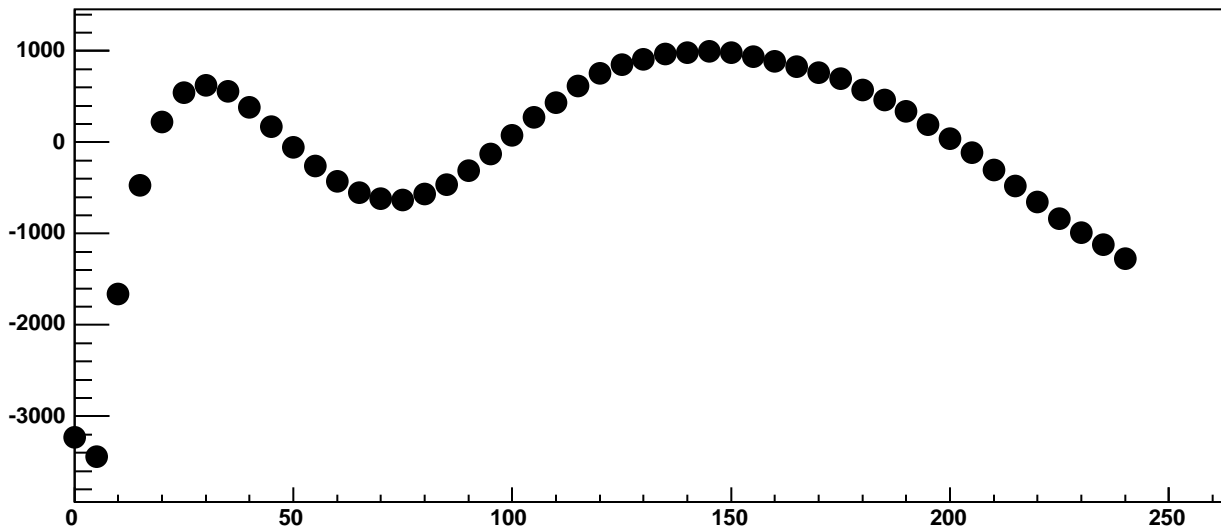
χ^2 / ndf	5.72e+05 / 42
p0	2646 ± 6.542
p1	-0.8289 ± 0.04477
p2	5618 ± 6.087
p3	49.34 ± 0.06047



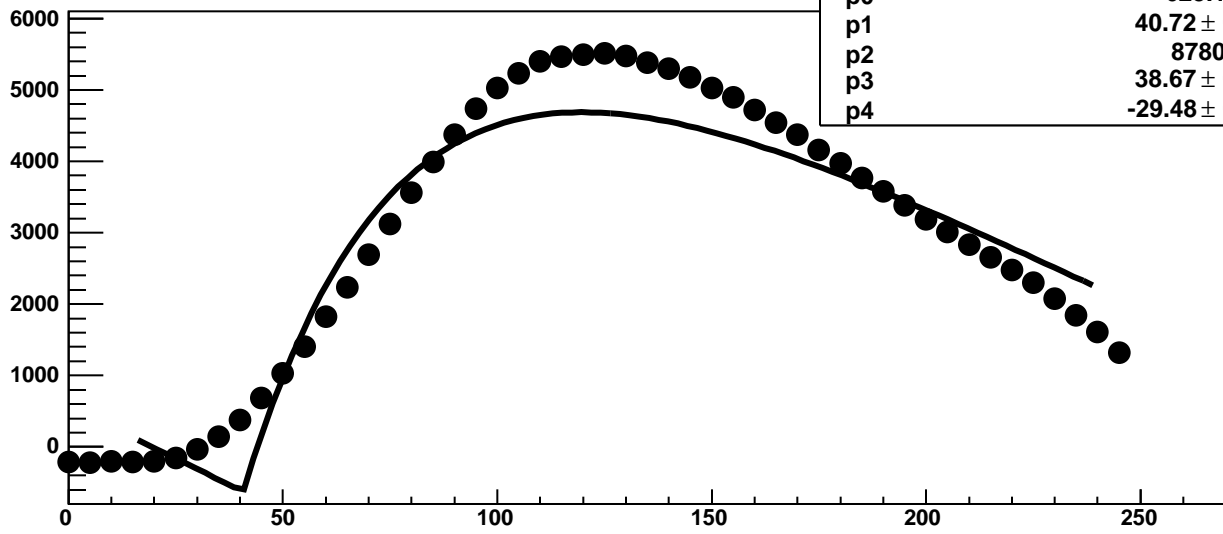
Chip 7, Channel 15, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 15, Enable 5!, DAC=4000, ADC Residuals vs Hold

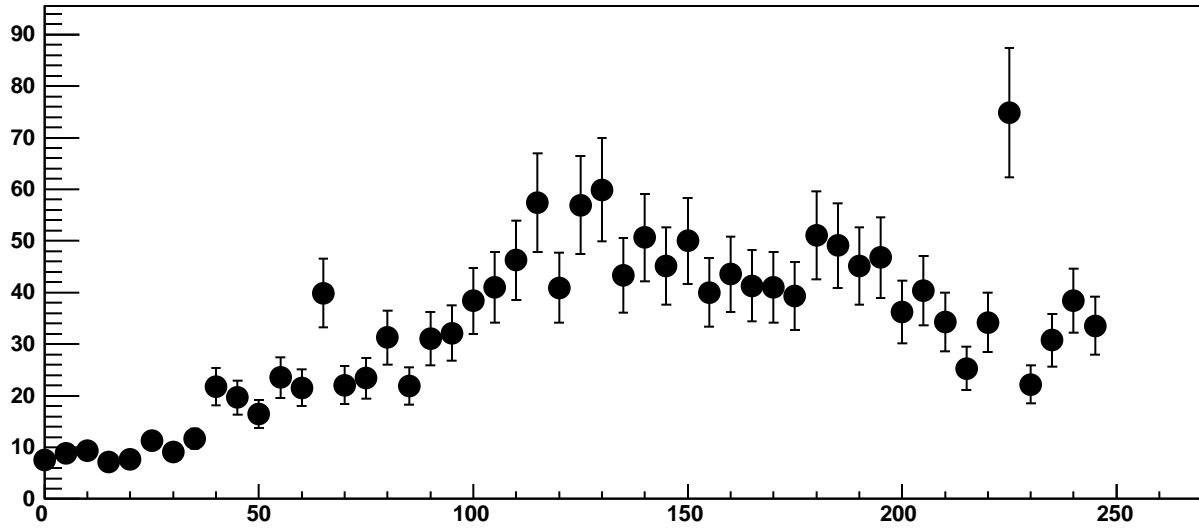


Chip 7, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

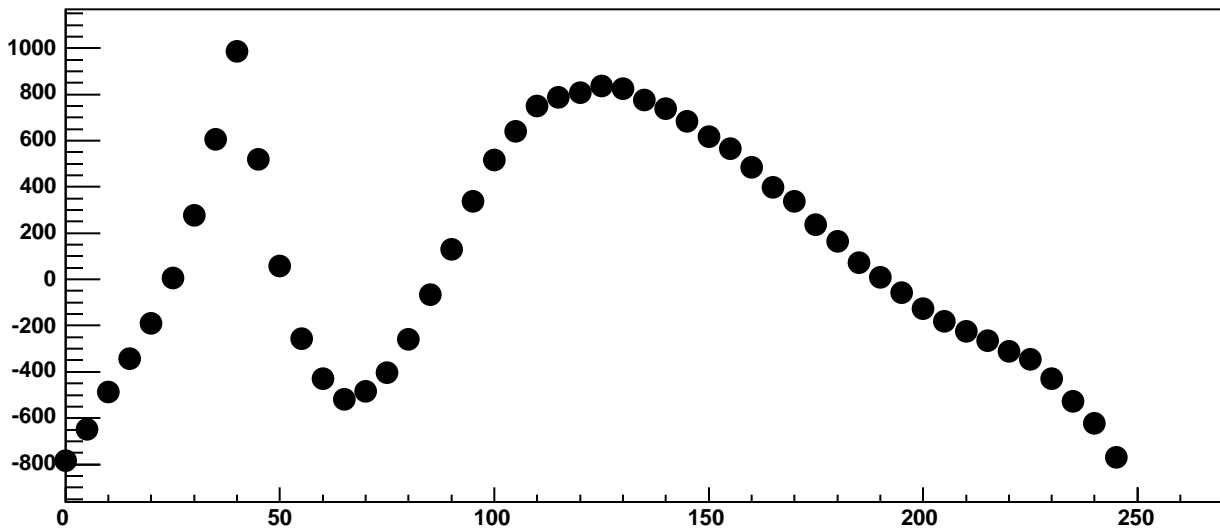


χ^2 / ndf	2.914e+05 / 41
p0	-625.1 ± 1.213
p1	40.72 ± 0.01842
p2	8780 ± 11.22
p3	38.67 ± 0.06408
p4	-29.48 ± 0.05685

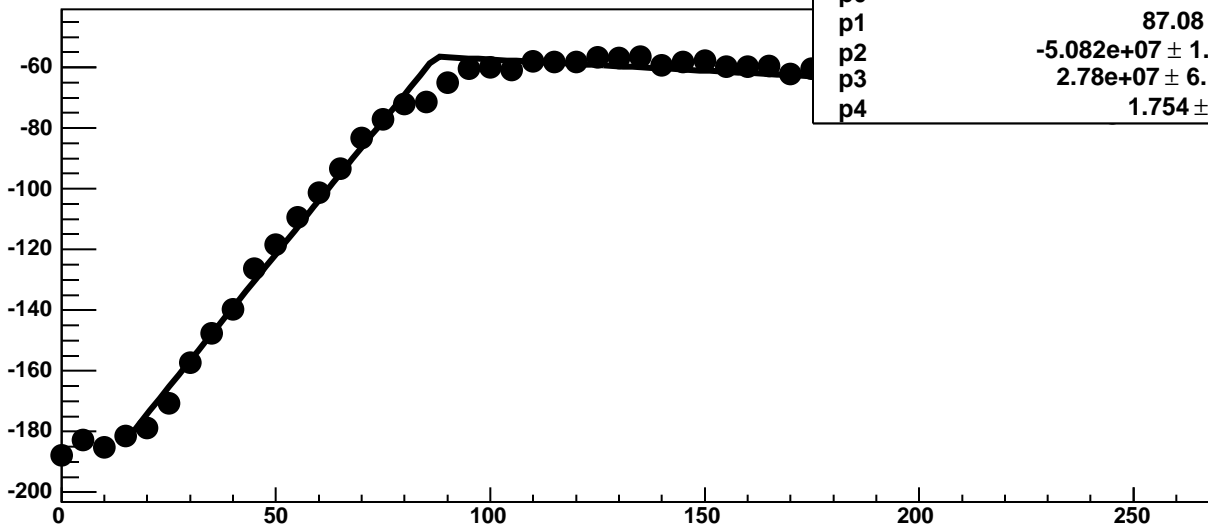
Chip 7, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

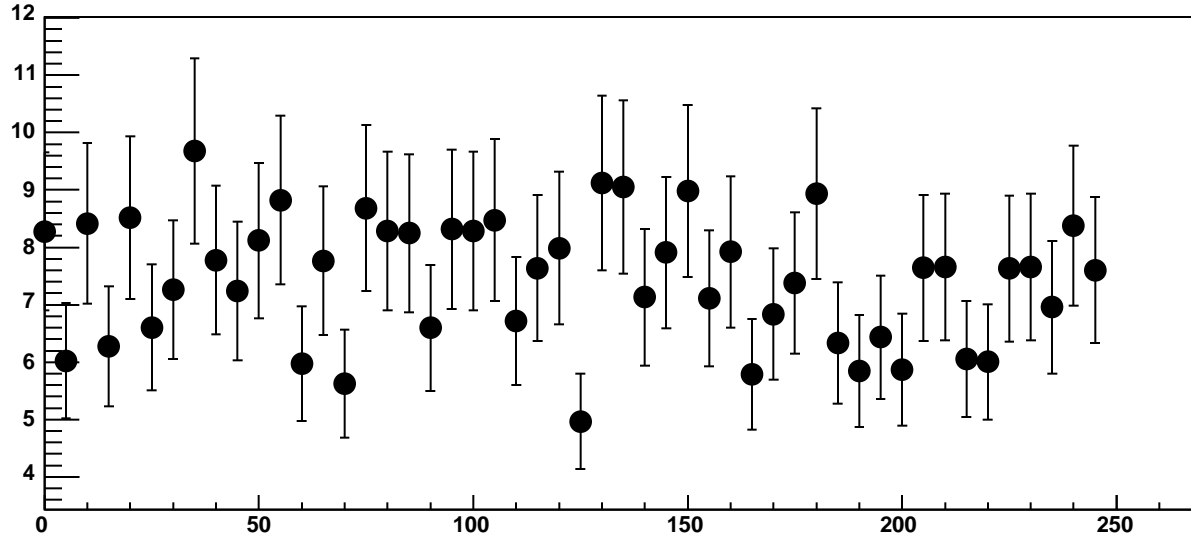


Chip 7, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold

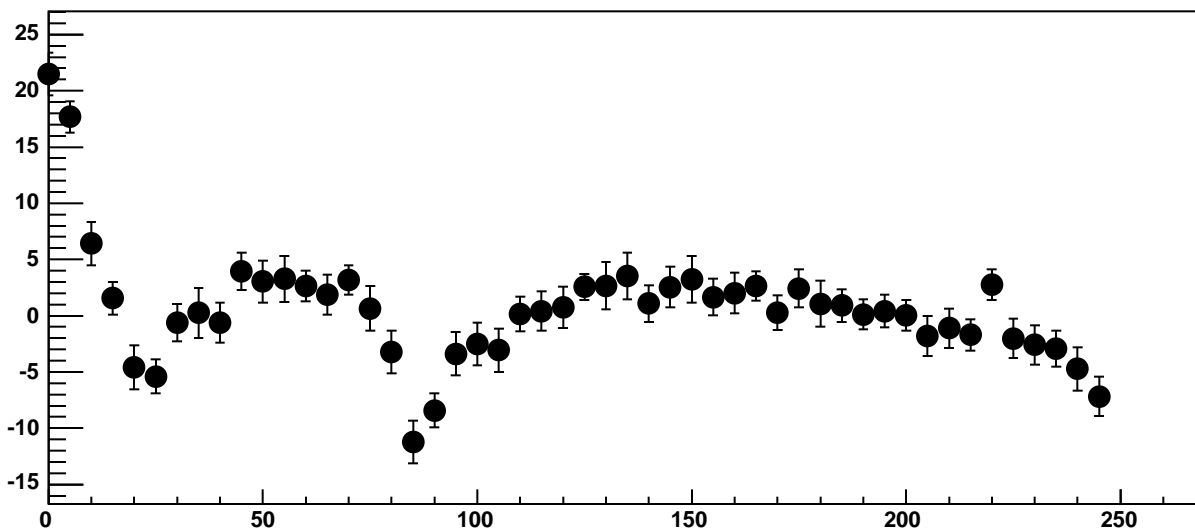


χ^2 / ndf	161.6 / 41
p0	-56.47 ± 0.5749
p1	87.08 ± 0.5399
p2	$-5.082\text{e}+07 \pm 1.063\text{e}+06$
p3	$2.78\text{e}+07 \pm 6.152\text{e}+05$
p4	1.754 ± 0.01752

Chip 7, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold

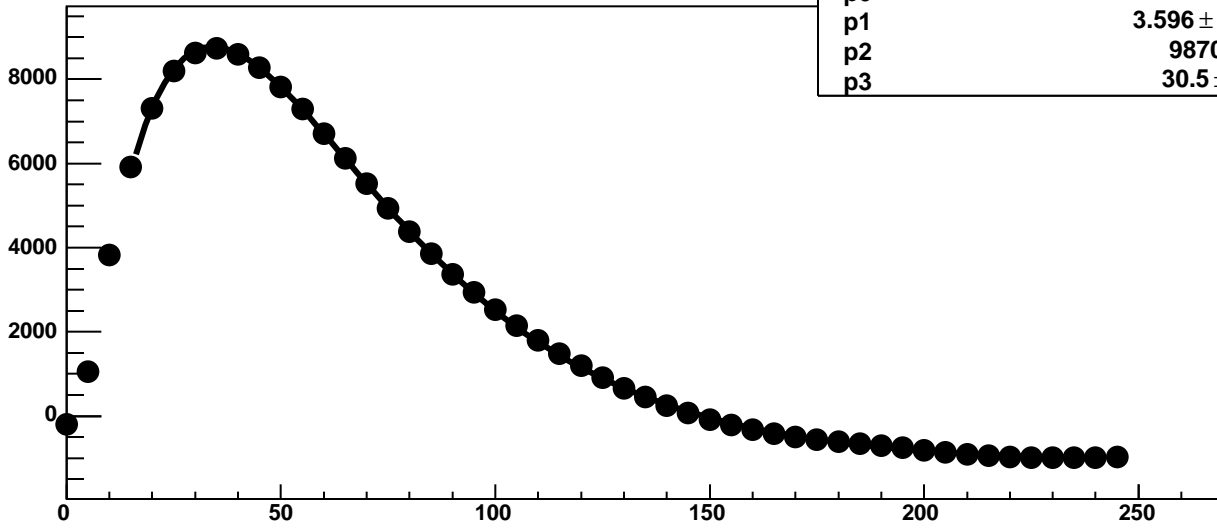


Chip 7, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold

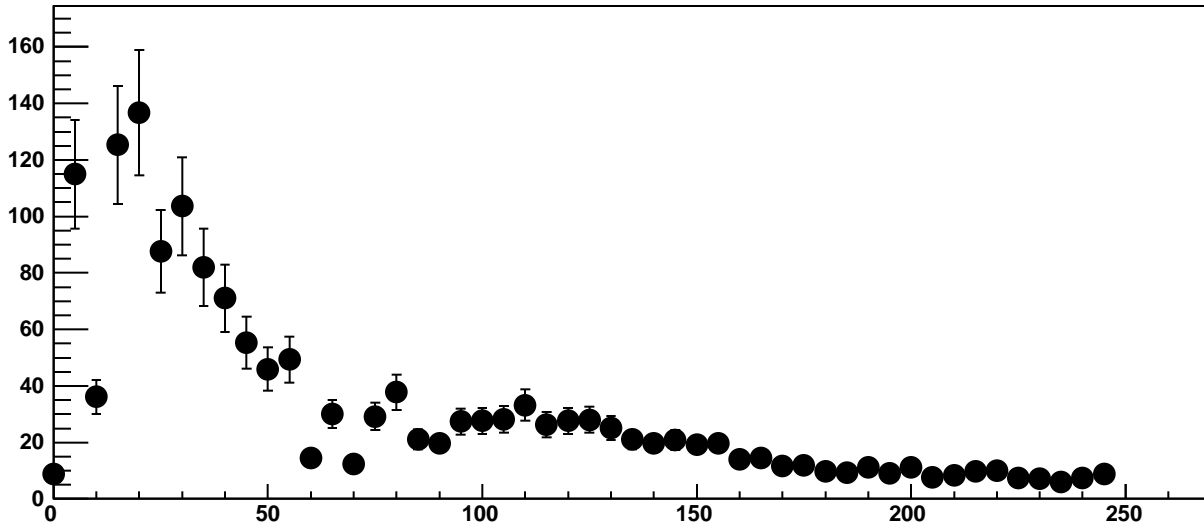


Chip 7, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold

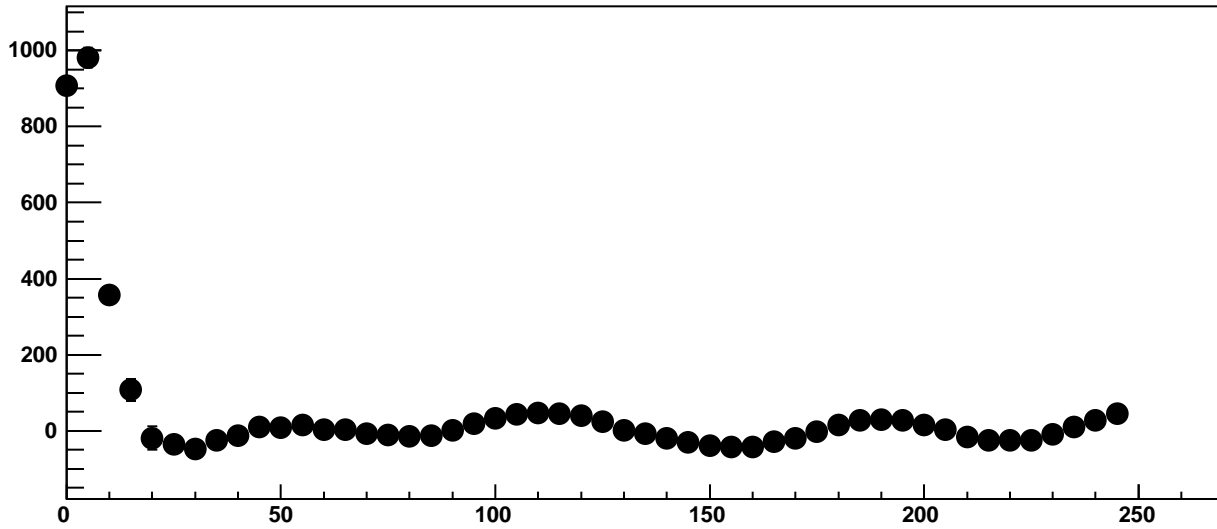
χ^2 / ndf	2232 / 42
p0	-1101 ± 0.7766
p1	3.596 ± 0.04665
p2	9870 ± 5.221
p3	30.5 ± 0.0169



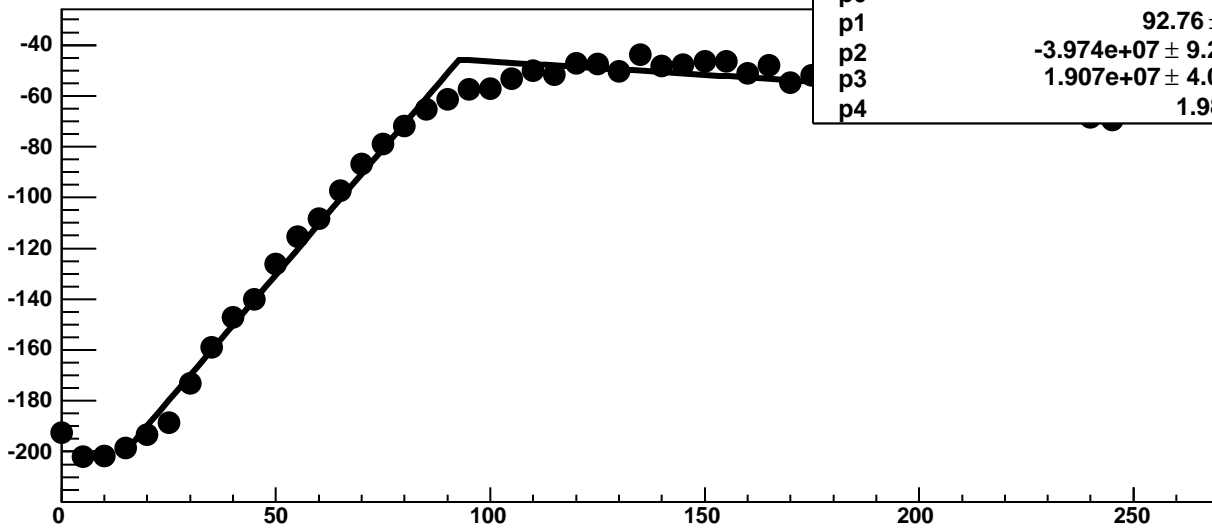
Chip 7, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold

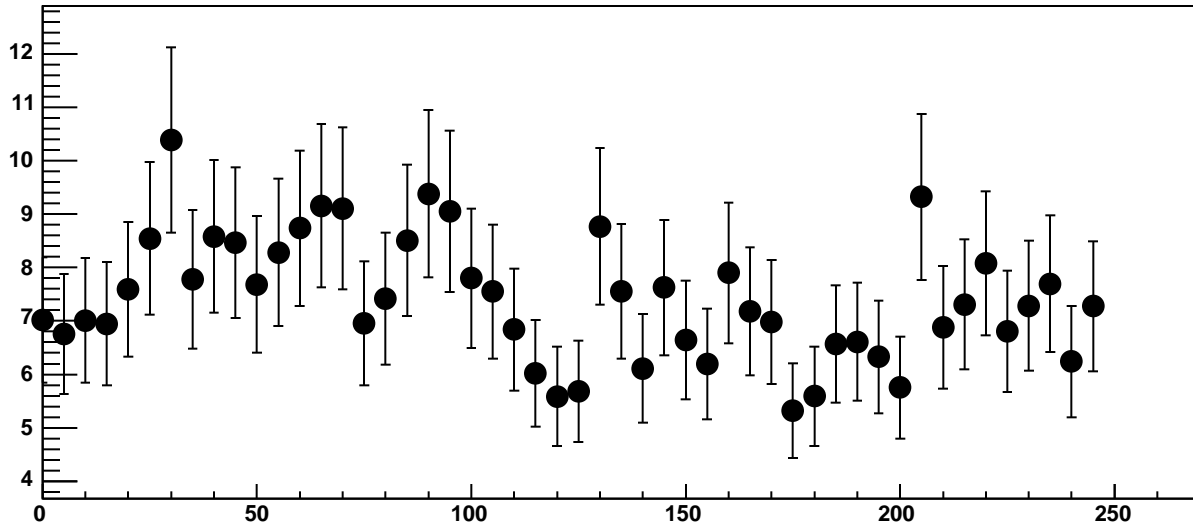


Chip 7, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

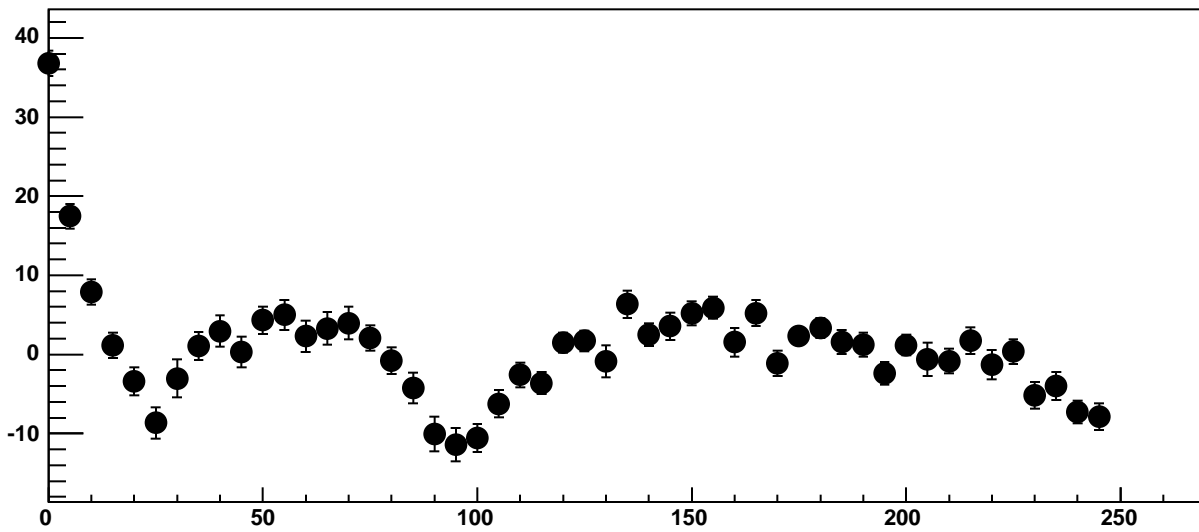


χ^2 / ndf	286.7 / 41
p0	-45.75 ± 0.5565
p1	92.76 ± 0.5339
p2	$-3.974\text{e}+07 \pm 9.265\text{e}+05$
p3	$1.907\text{e}+07 \pm 4.064\text{e}+05$
p4	1.98 ± 0.02

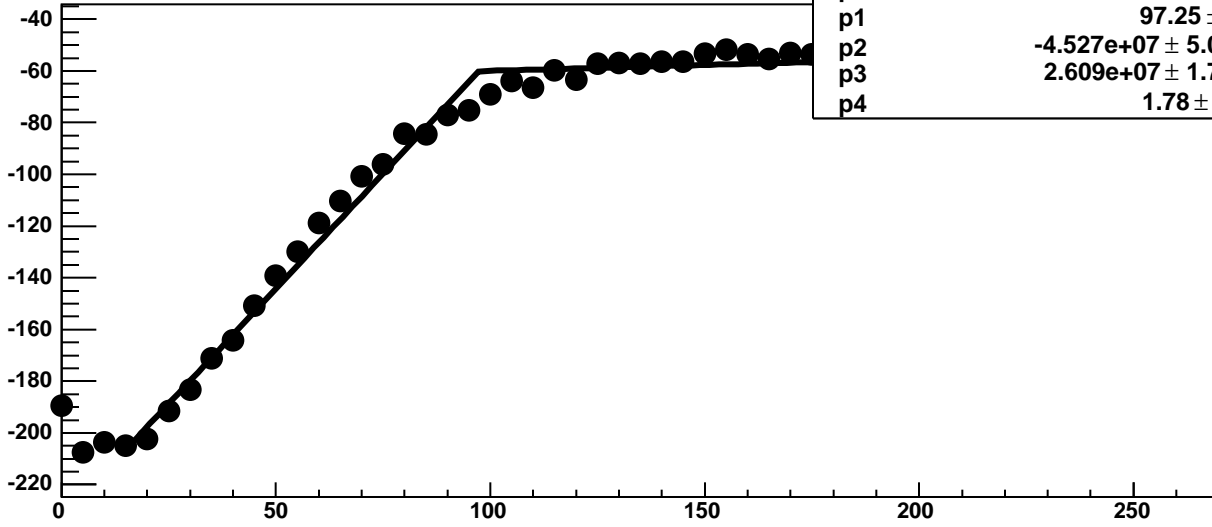
Chip 7, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

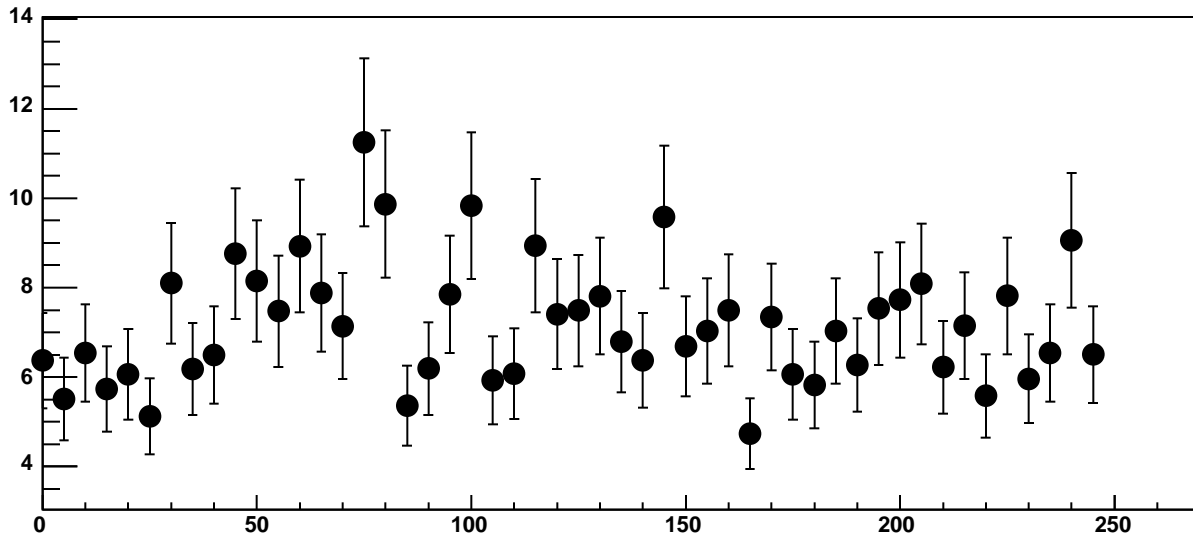


Chip 7, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

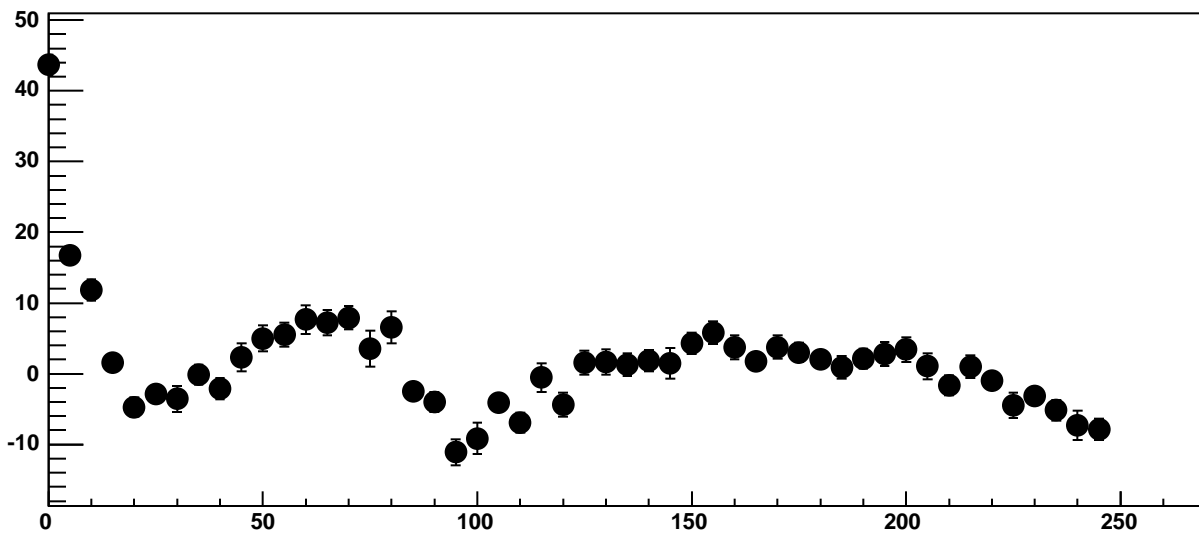


χ^2 / ndf	305.4 / 41
p0	-60.09 ± 0.6302
p1	97.25 ± 0.5754
p2	$-4.527\text{e}+07 \pm 5.095\text{e}+05$
p3	$2.609\text{e}+07 \pm 1.788\text{e}+05$
p4	1.78 ± 0.01477

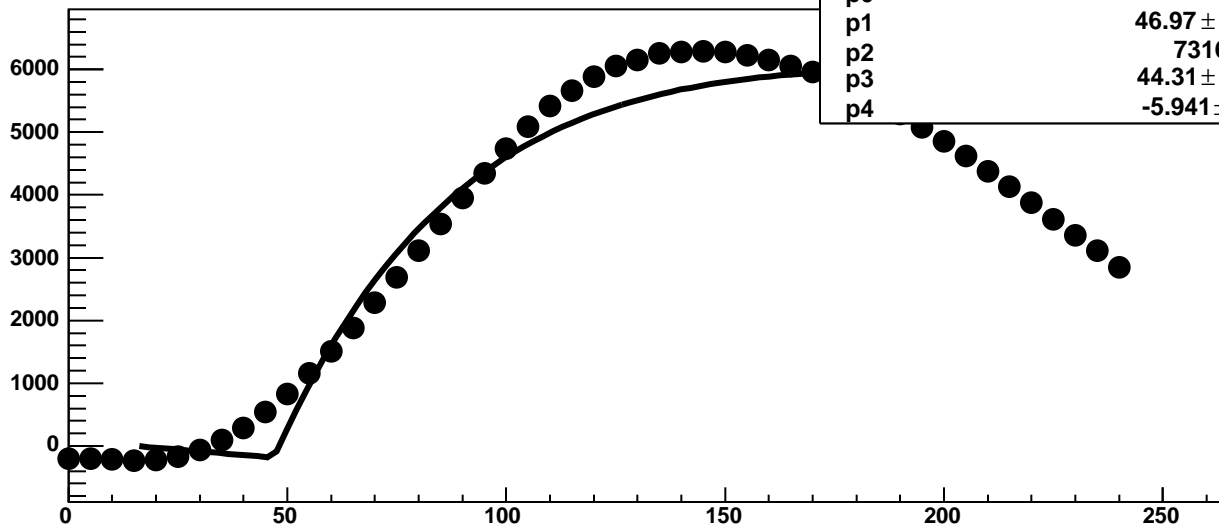
Chip 7, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

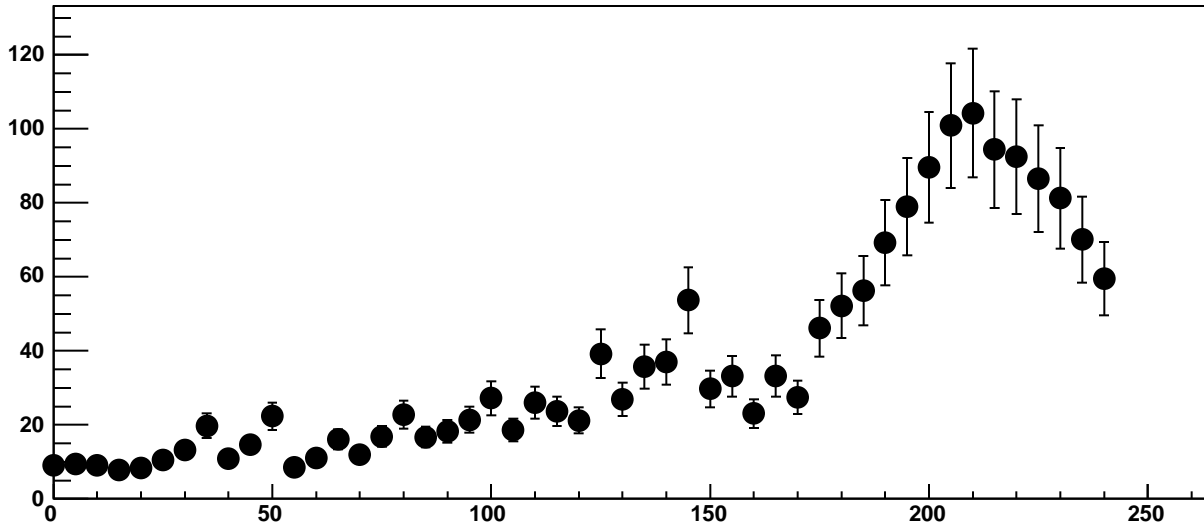


Chip 7, Channel 16, Enable 5, DAC=4000, ADC Mean vs Hold

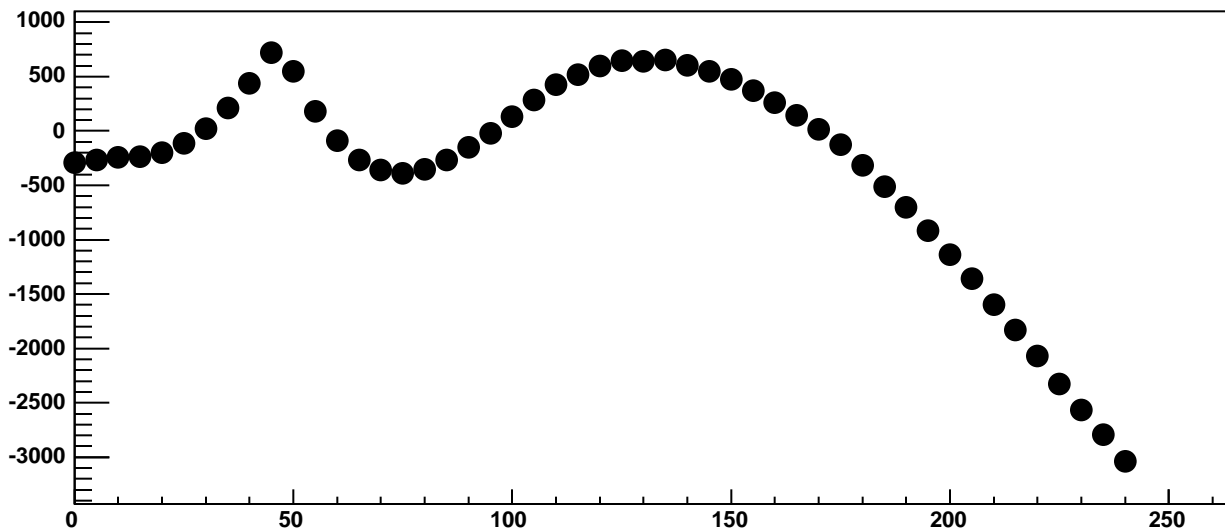


χ^2 / ndf	3.92e+05 / 41
p0	-187.1 ± 1.598
p1	46.97 ± 0.01806
p2	7316 ± 11.41
p3	44.31 ± 0.07167
p4	-5.941 ± 0.0644

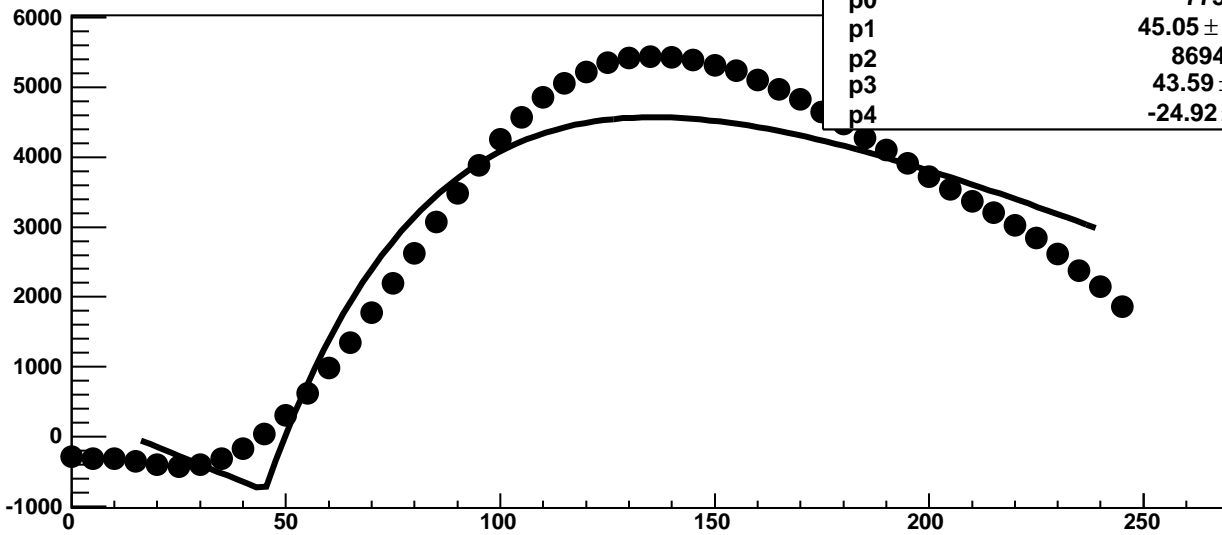
Chip 7, Channel 16, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 16, Enable 5, DAC=4000, ADC Residuals vs Hold

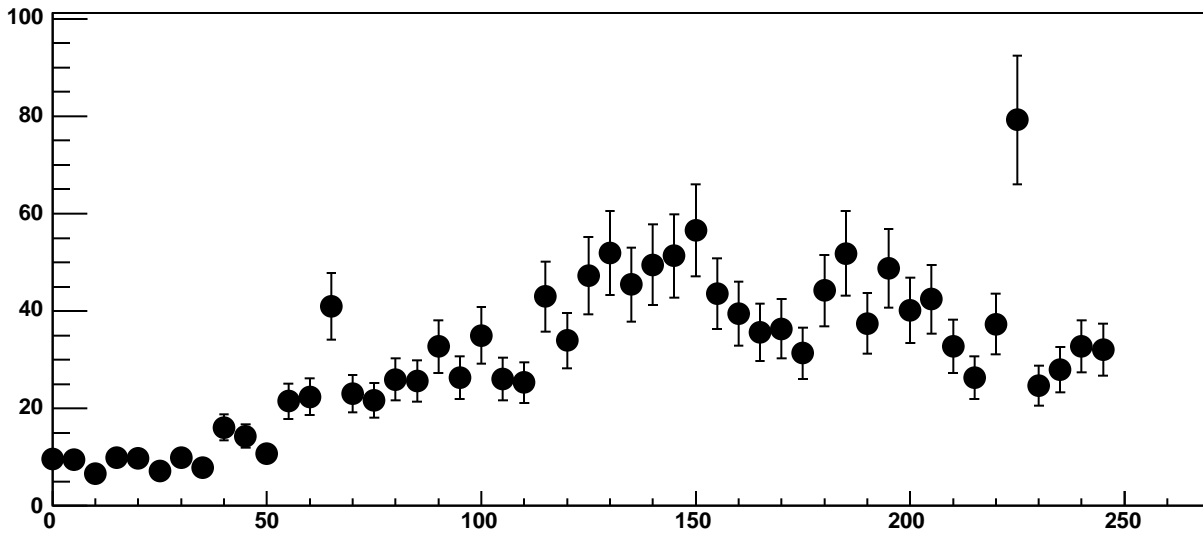


Chip 7, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

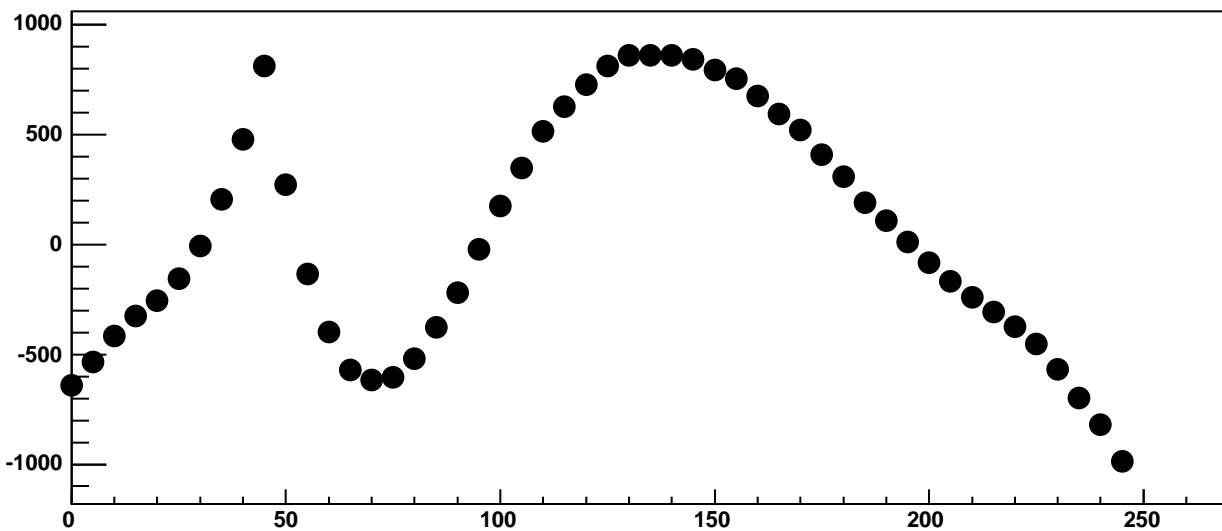


χ^2 / ndf	3.202e+05 / 41
p0	-775 ± 1.177
p1	45.05 ± 0.01623
p2	8694 ± 11.54
p3	43.59 ± 0.0677
p4	-24.92 ± 0.0581

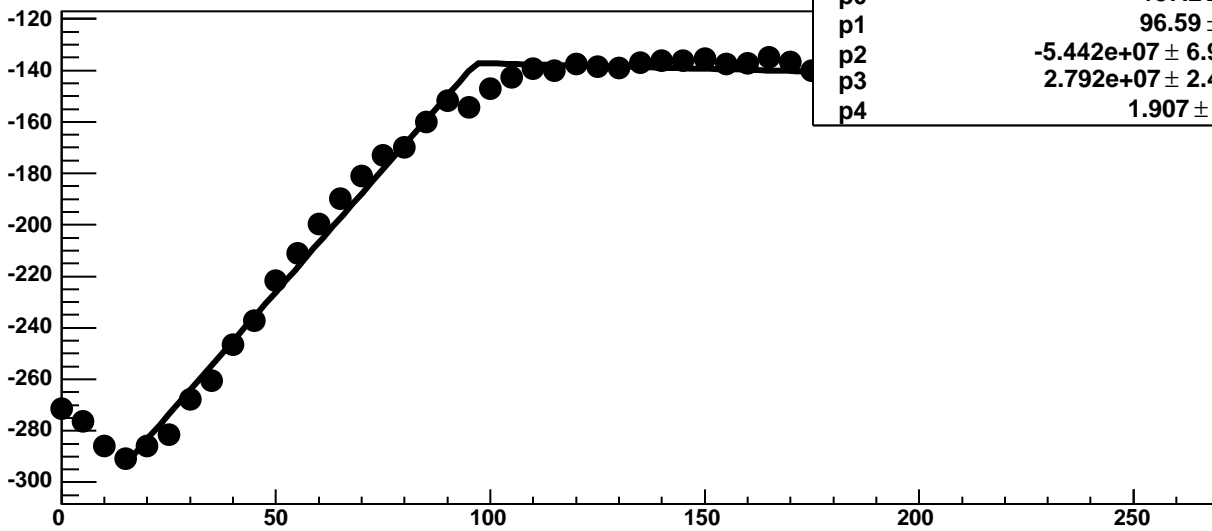
Chip 7, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

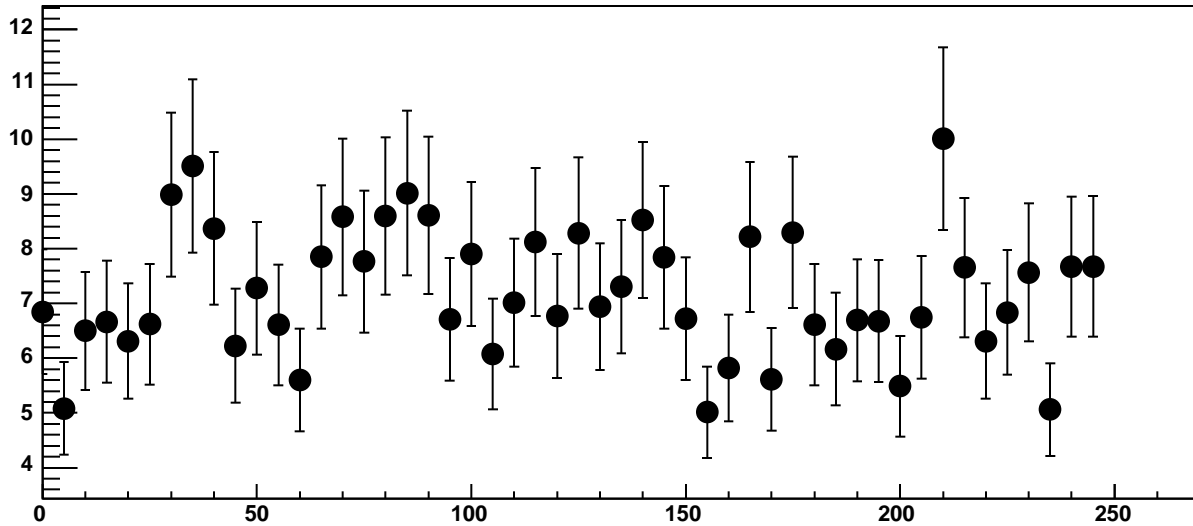


Chip 7, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

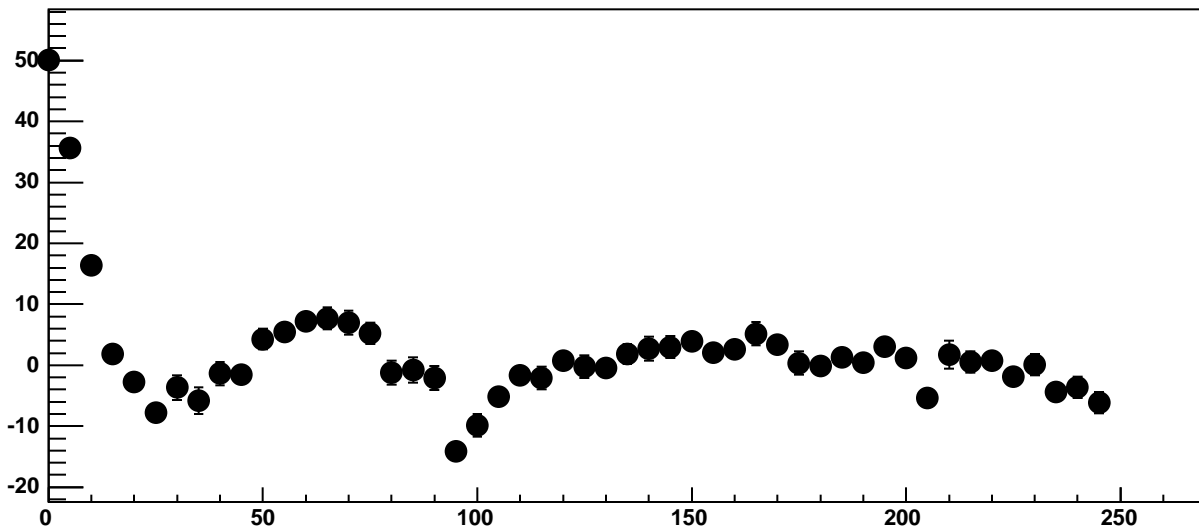


χ^2 / ndf	338.3 / 41
p0	-137.2 ± 0.5916
p1	96.59 ± 0.5295
p2	$-5.442\text{e}+07 \pm 6.919\text{e}+05$
p3	$2.792\text{e}+07 \pm 2.465\text{e}+05$
p4	1.907 ± 0.01673

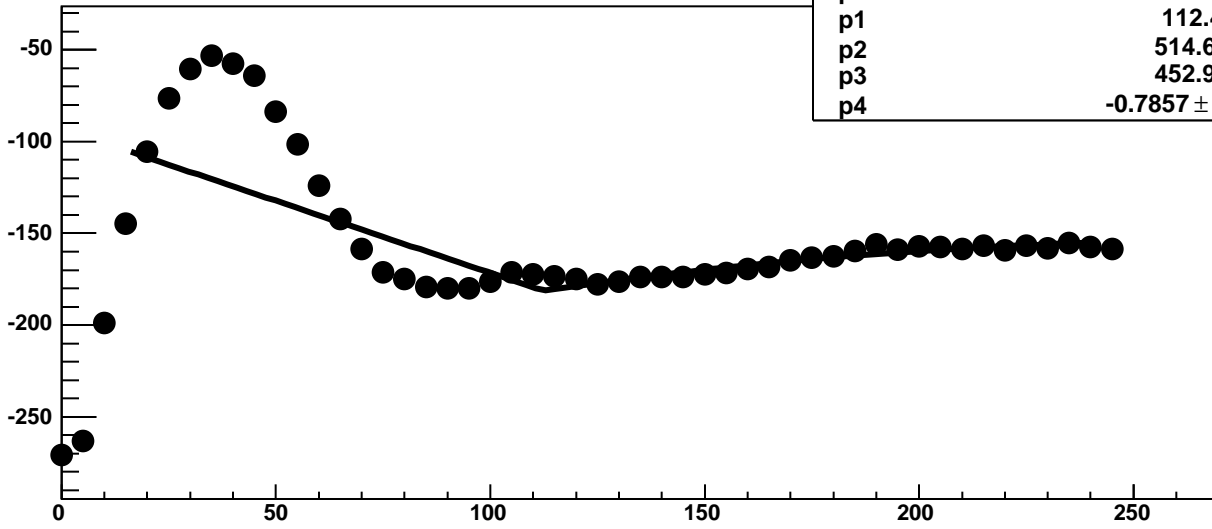
Chip 7, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

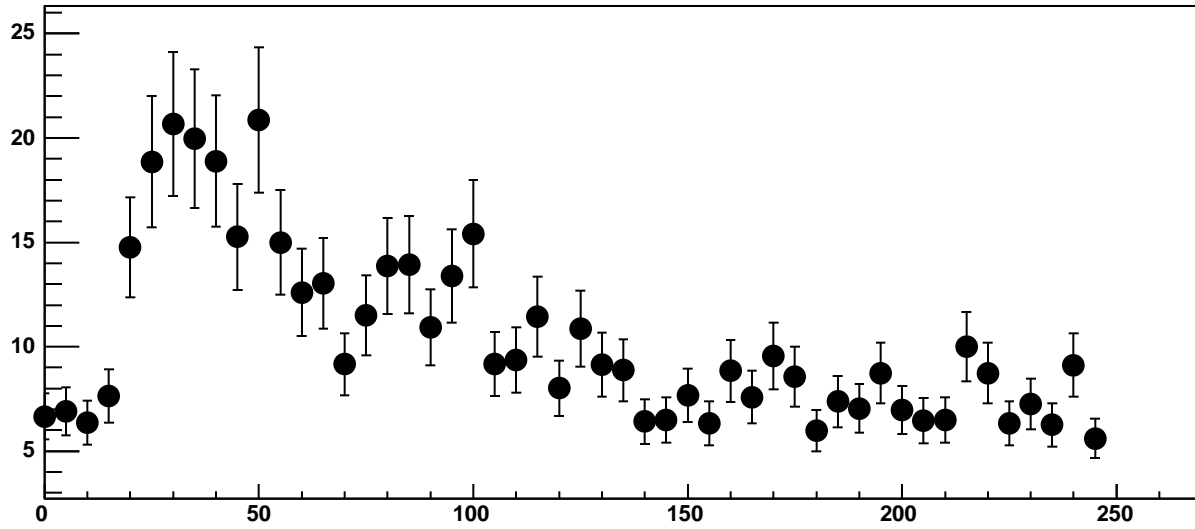


Chip 7, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold

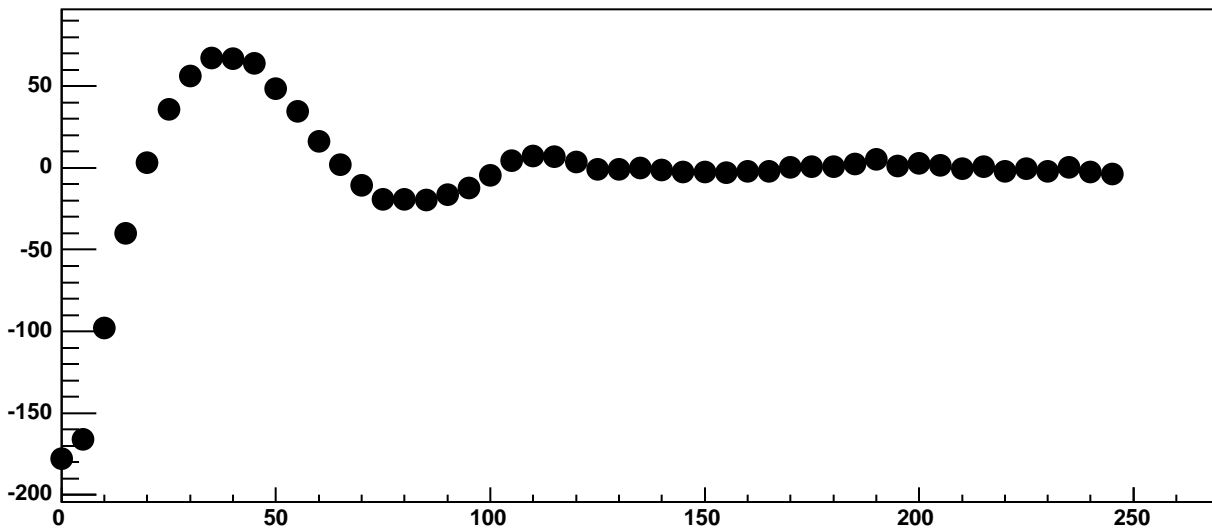


χ^2 / ndf	2035 / 41
p0	-181.3 ± 0.8359
p1	112.4 ± 1.35
p2	514.6 ± 92.88
p3	452.9 ± 93.36
p4	-0.7857 ± 0.02023

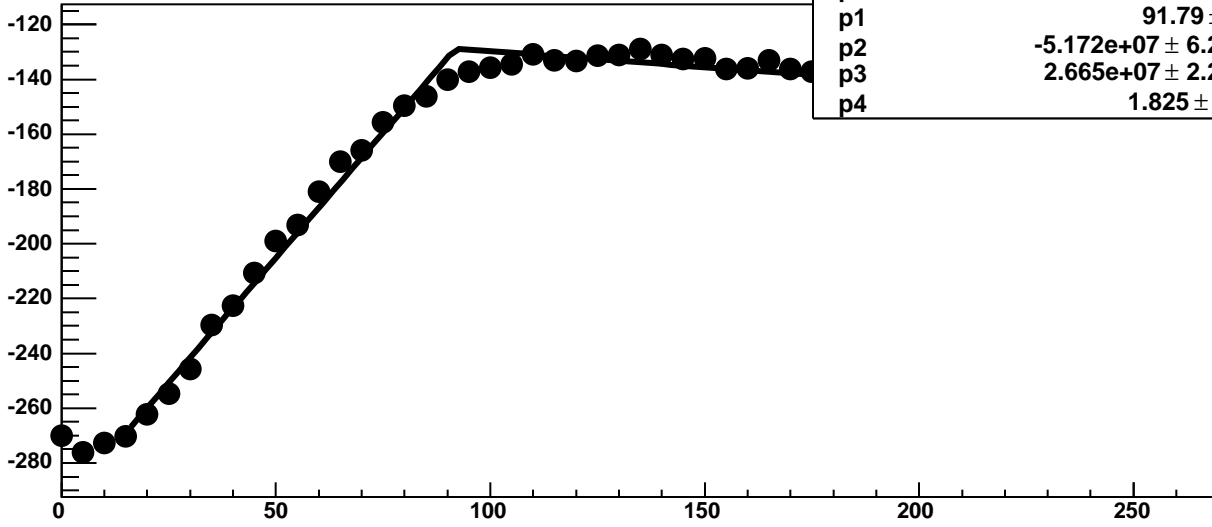
Chip 7, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold

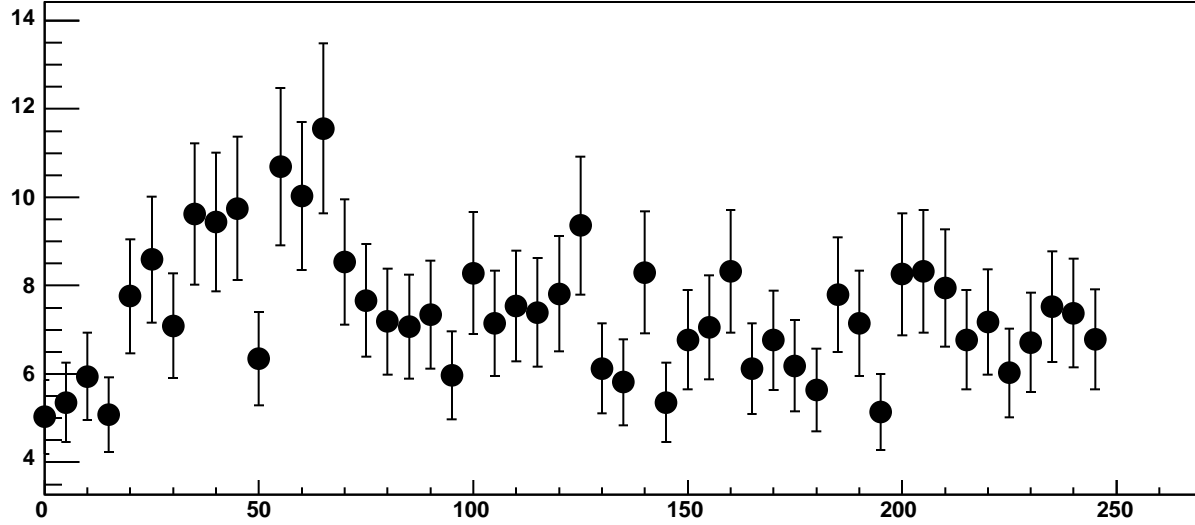


Chip 7, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold

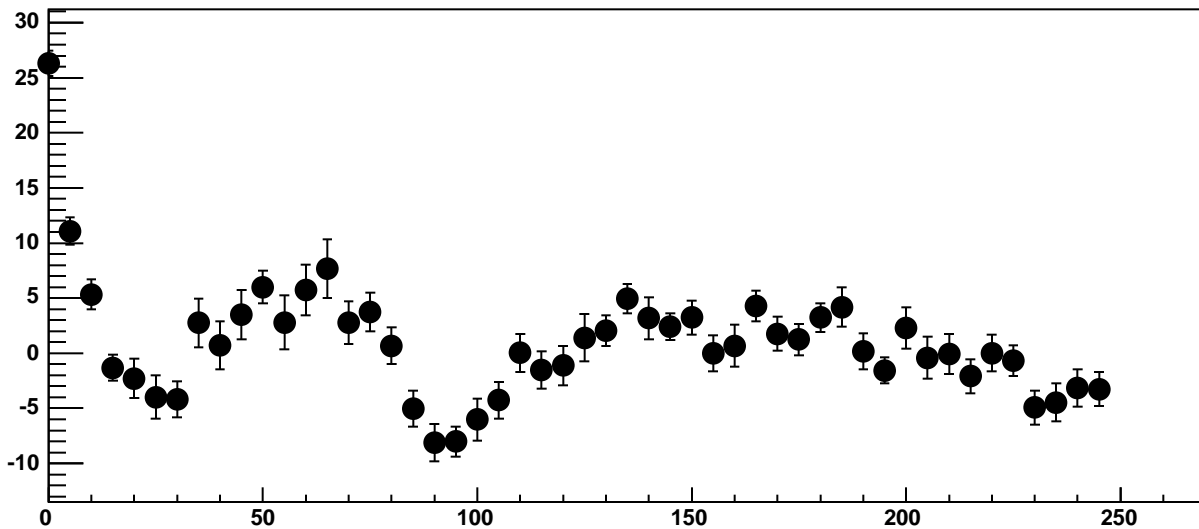


χ^2 / ndf	218.6 / 41
p0	-128.8 ± 0.5619
p1	91.79 ± 0.5399
p2	$-5.172\text{e}+07 \pm 6.287\text{e}+05$
p3	$2.665\text{e}+07 \pm 2.206\text{e}+05$
p4	1.825 ± 0.01747

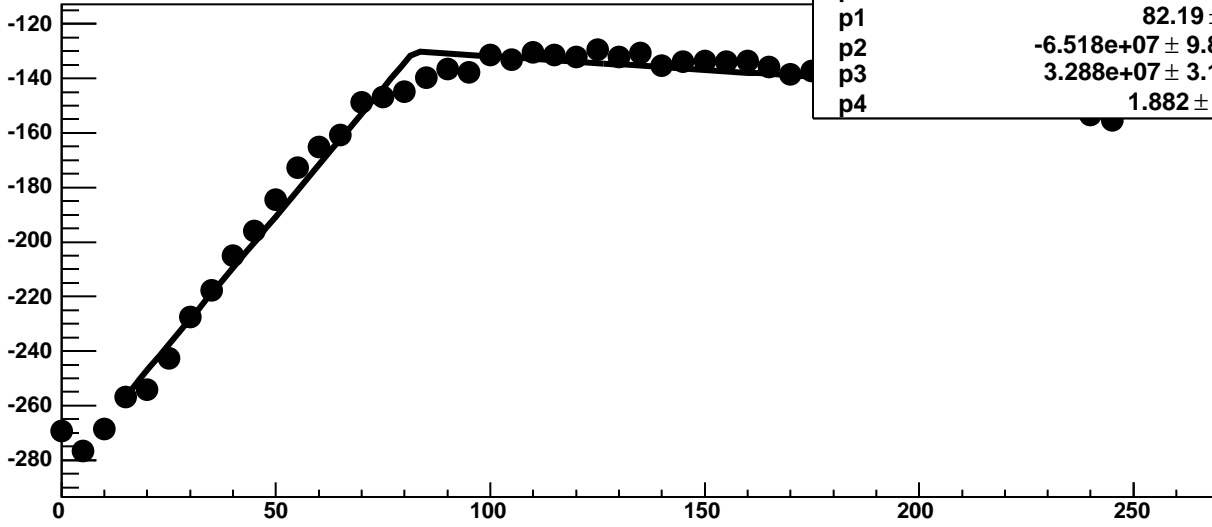
Chip 7, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold

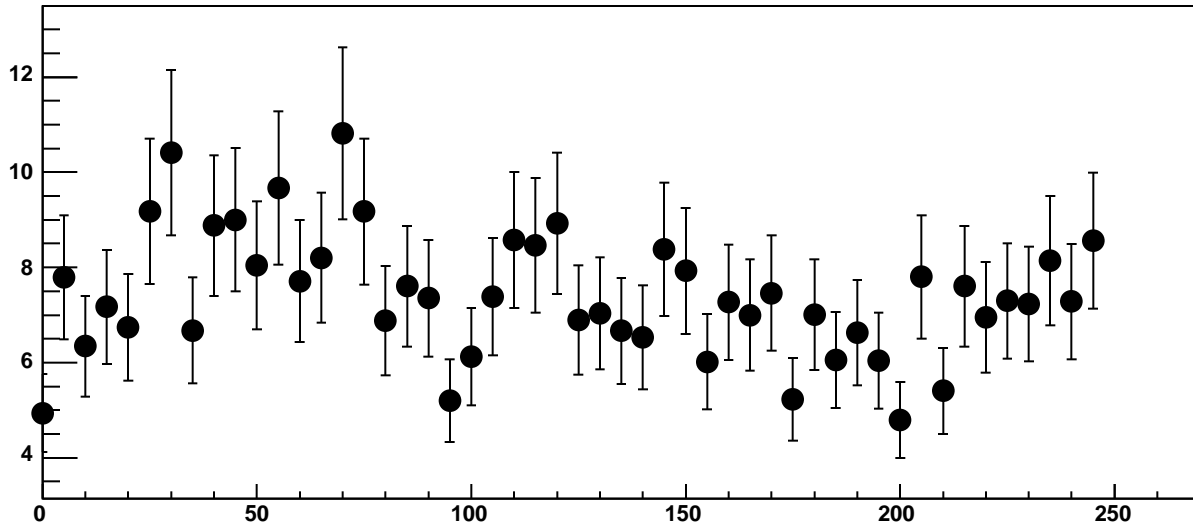


Chip 7, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold

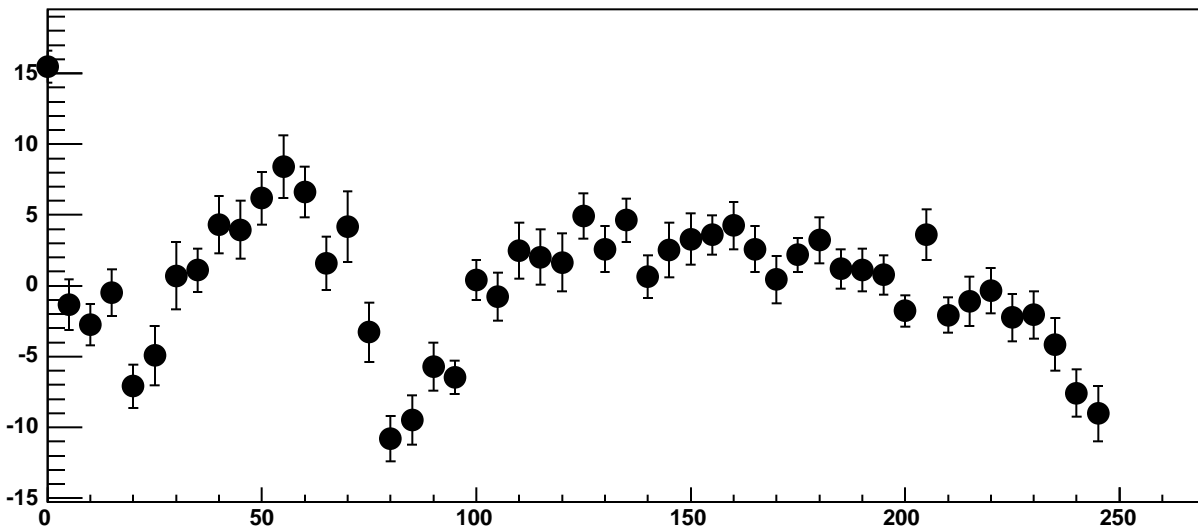


χ^2 / ndf	291.8 / 41
p0	-130 ± 0.5427
p1	82.19 ± 0.5767
p2	$-6.518\text{e}+07 \pm 9.844\text{e}+05$
p3	$3.288\text{e}+07 \pm 3.192\text{e}+05$
p4	1.882 ± 0.02377

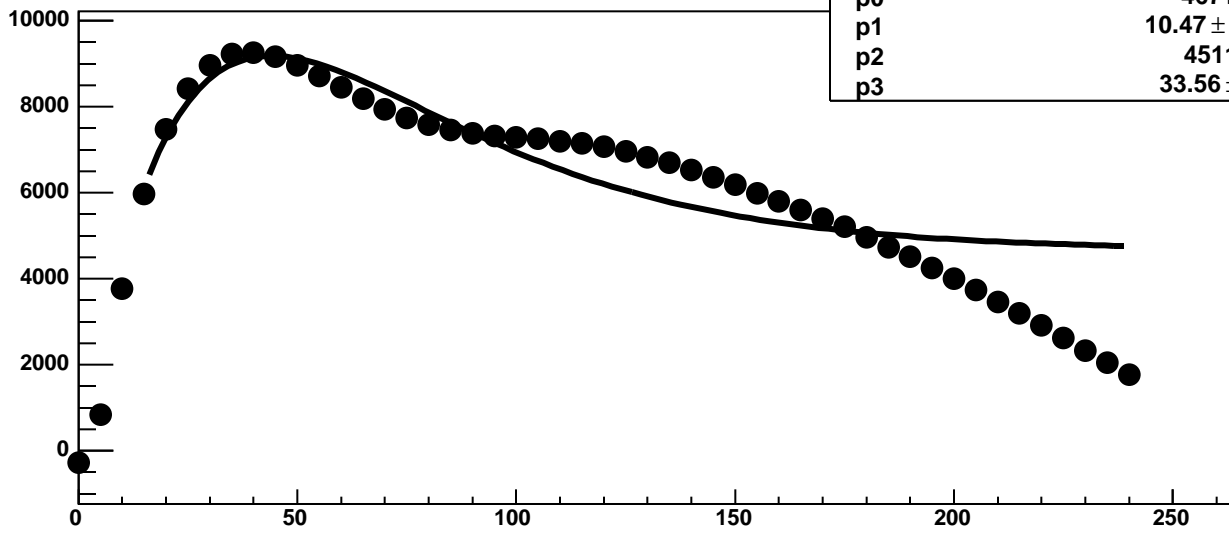
Chip 7, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

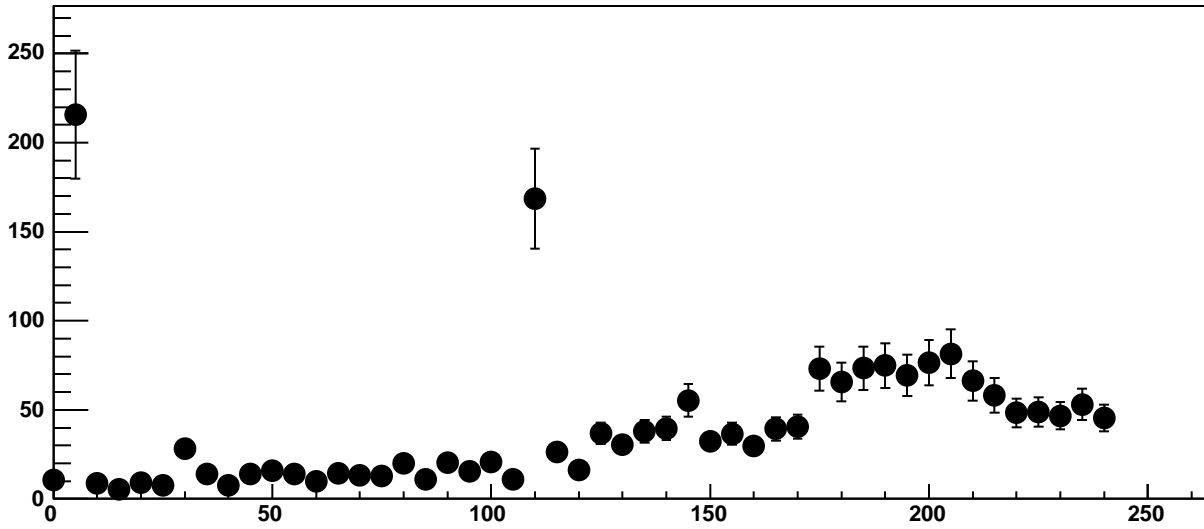


Chip 7, Channel 17, Enable 5!, DAC=4000, ADC Mean vs Hold

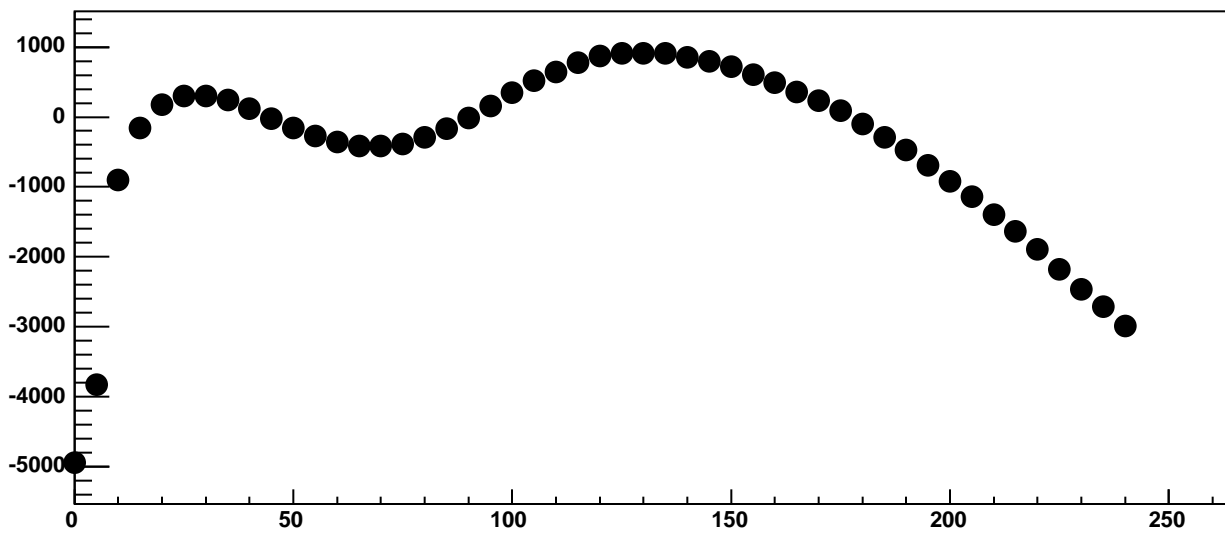


χ^2 / ndf	6.381e+05 / 42
p0	4671 ± 4.056
p1	10.47 ± 0.01613
p2	4511 ± 3.983
p3	33.56 ± 0.0292

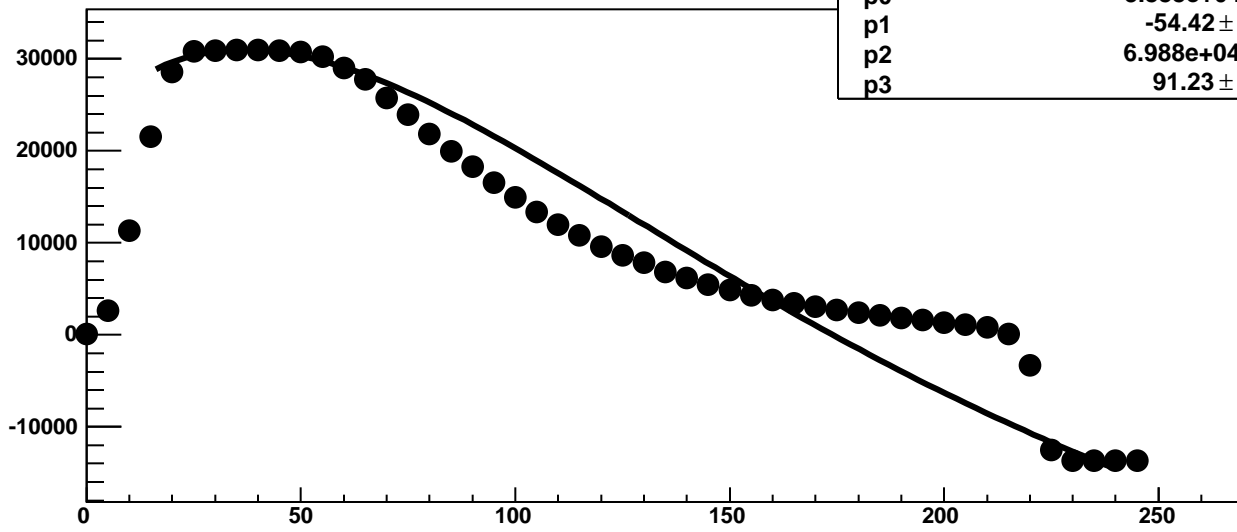
Chip 7, Channel 17, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 7, Channel 17, Enable 5!, DAC=4000, ADC Residuals vs Hold

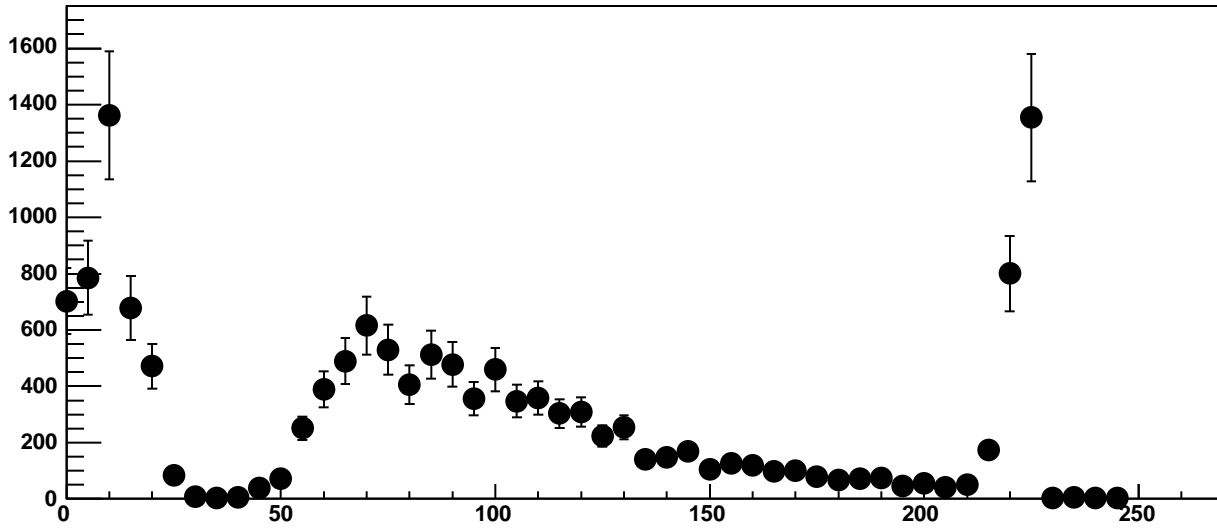


Chip 8, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold

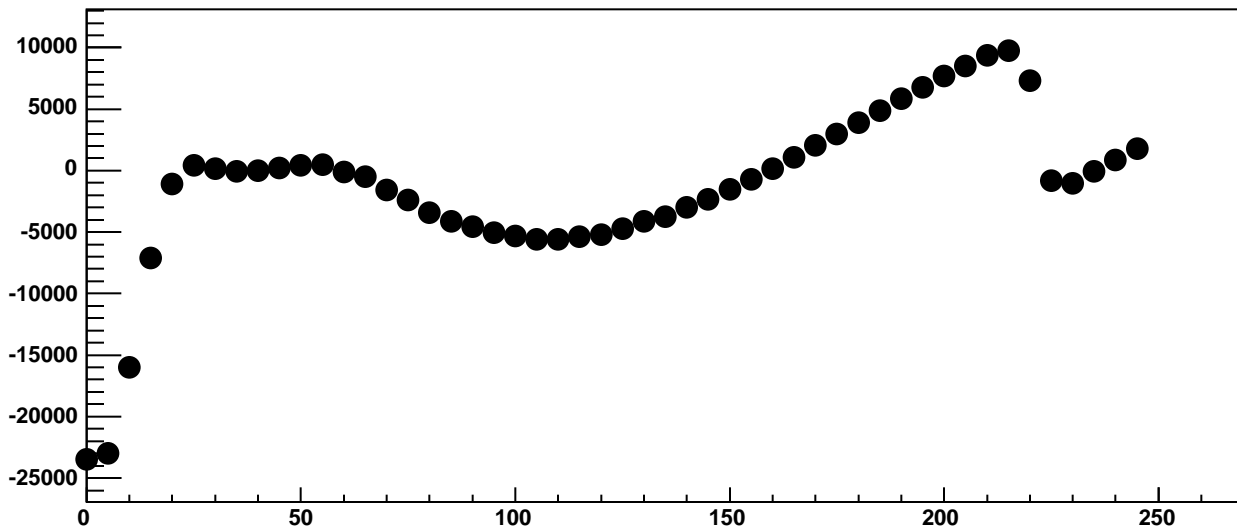


χ^2 / ndf	4.467e+06 / 42
p0	-3.888e+04 ± 26.58
p1	-54.42 ± 0.06063
p2	6.988e+04 ± 26.46
p3	91.23 ± 0.04565

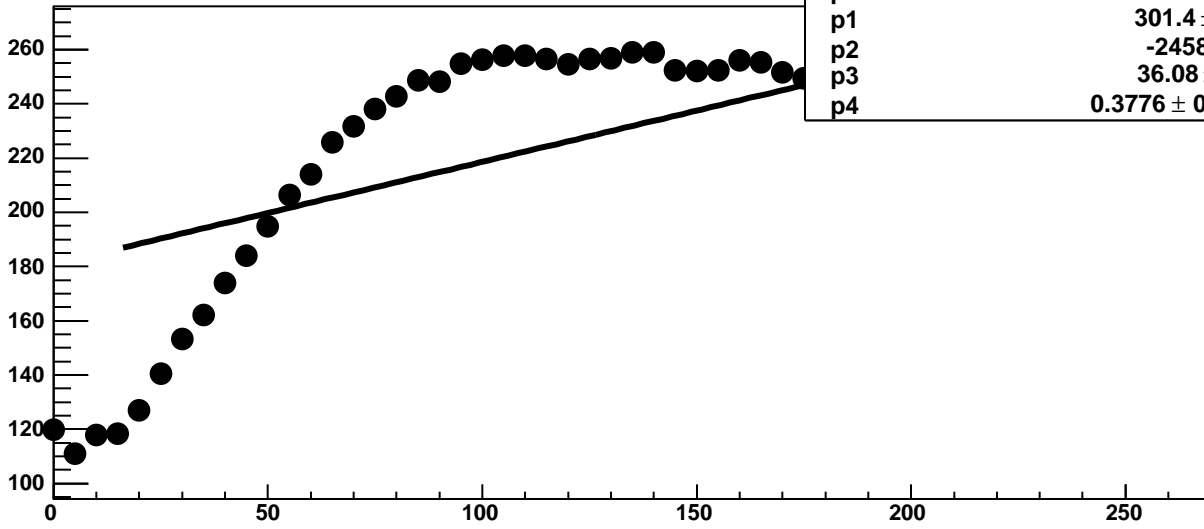
Chip 8, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold

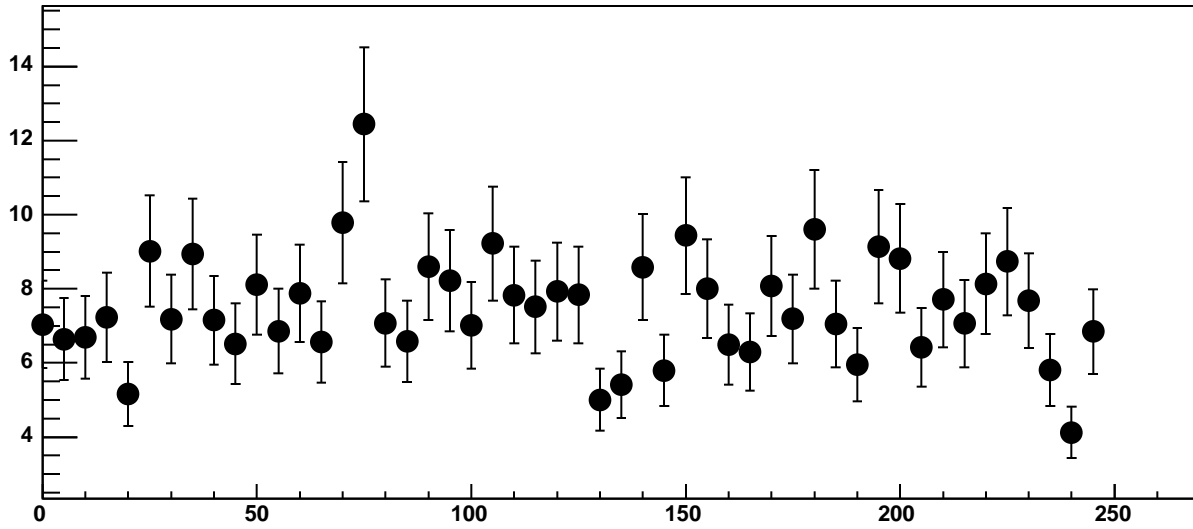


Chip 8, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

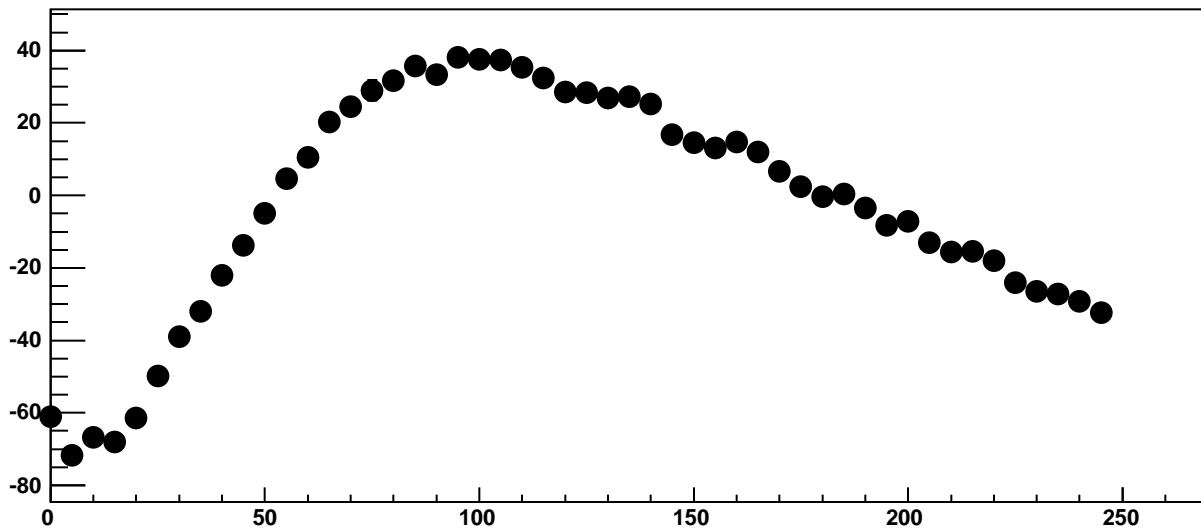


χ^2 / ndf	1.395e+04 / 41
p0	294.7 ± 0.4212
p1	301.4 ± 0.6989
p2	-2458 ± 37.65
p3	36.08 ± 0.7101
p4	0.3776 ± 0.003469

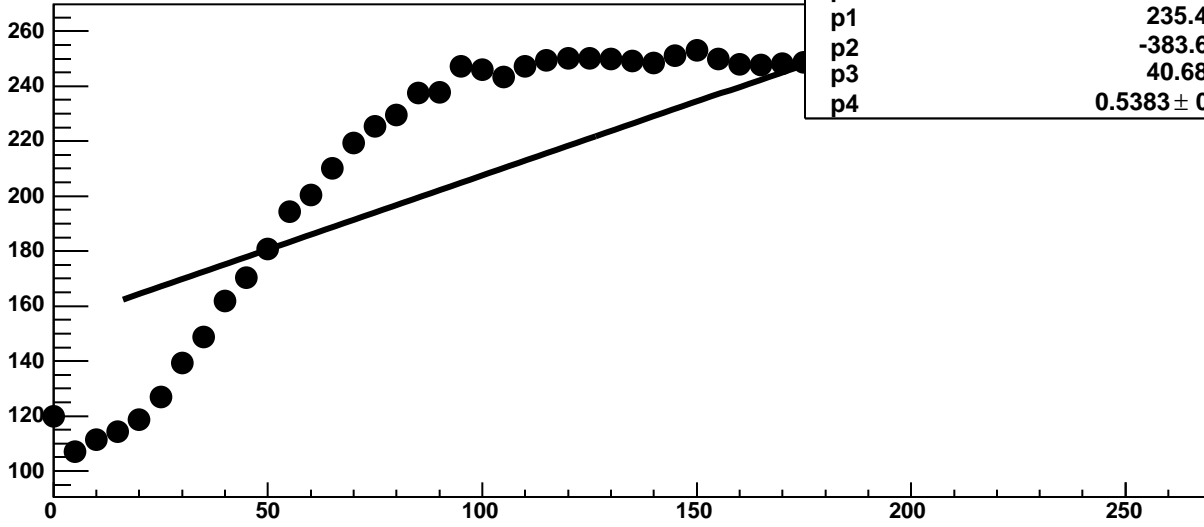
Chip 8, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold

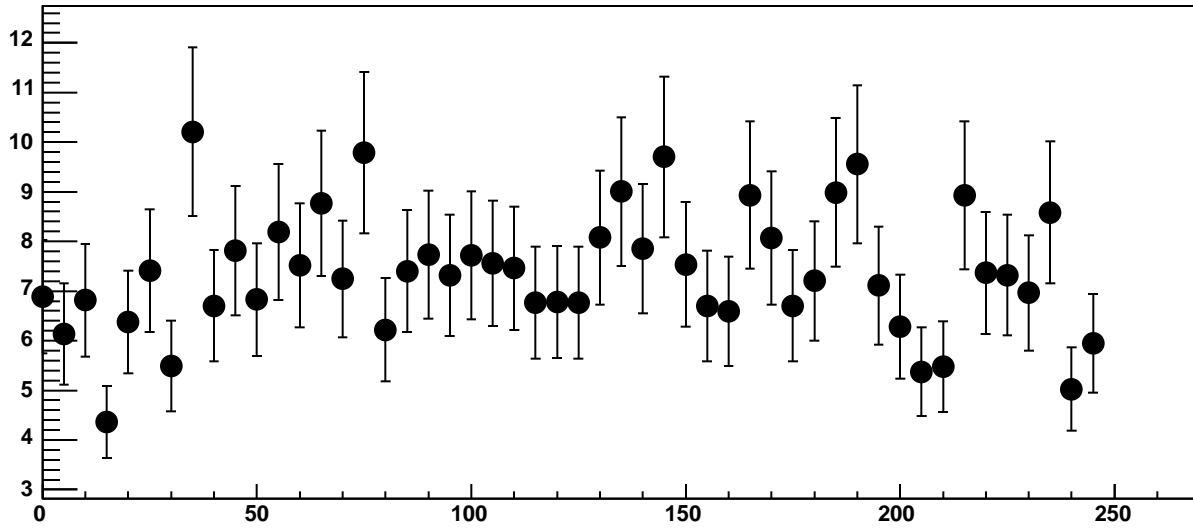


Chip 8, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

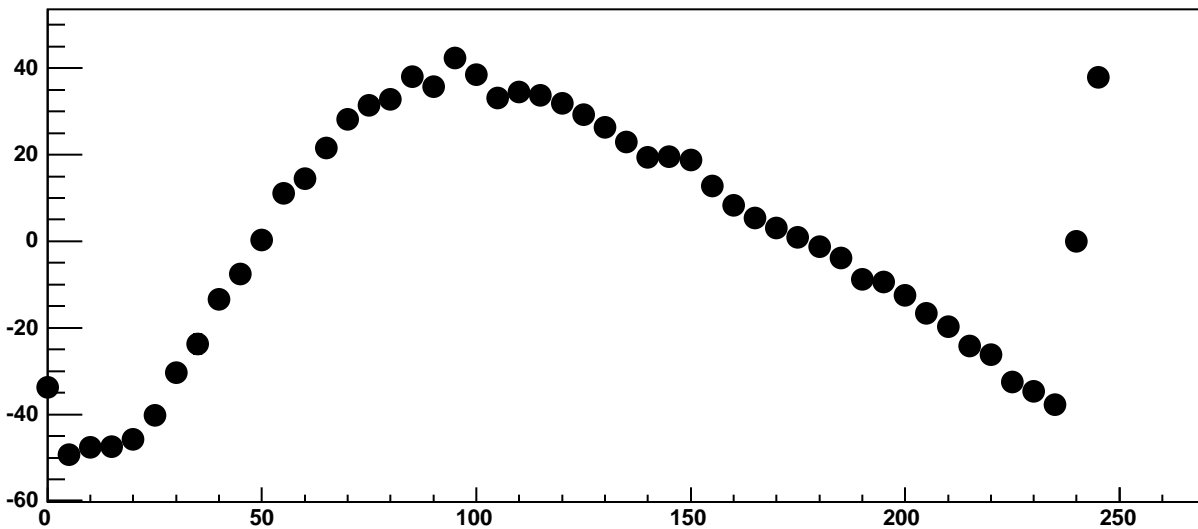


χ^2 / ndf	1.279e+04 / 41
p0	280.3 ± 1.173
p1	235.4 ± 2.004
p2	-383.6 ± 150.4
p3	40.68 ± 13.98
p4	0.5383 ± 0.003562

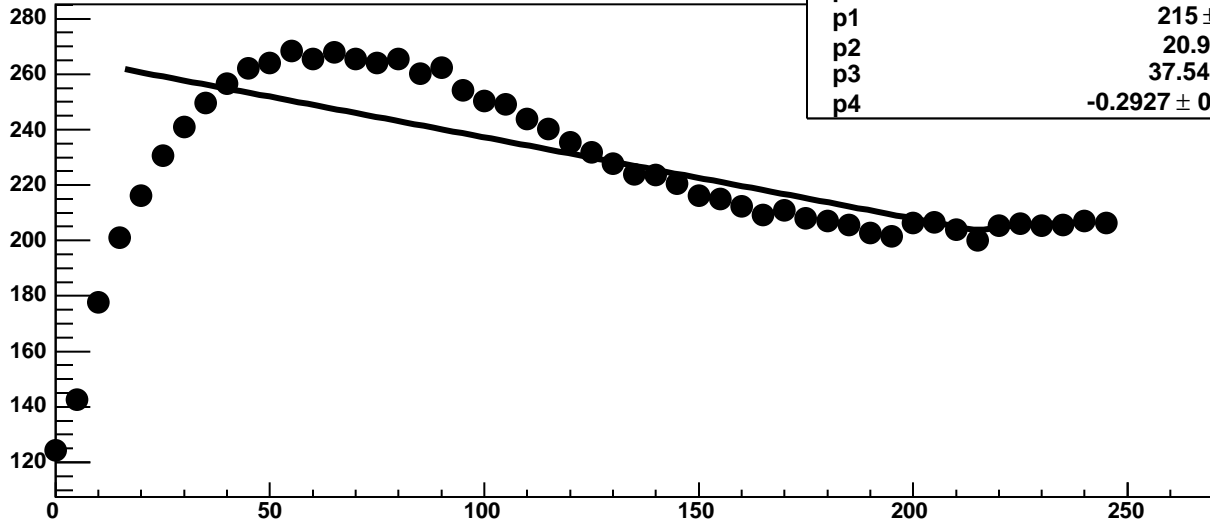
Chip 8, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



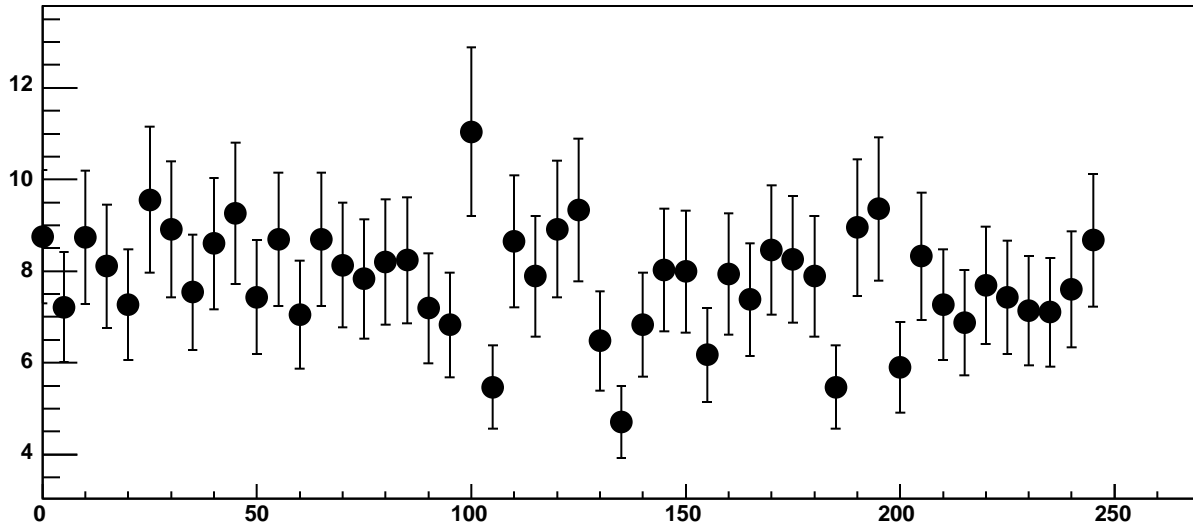
Chip 8, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold



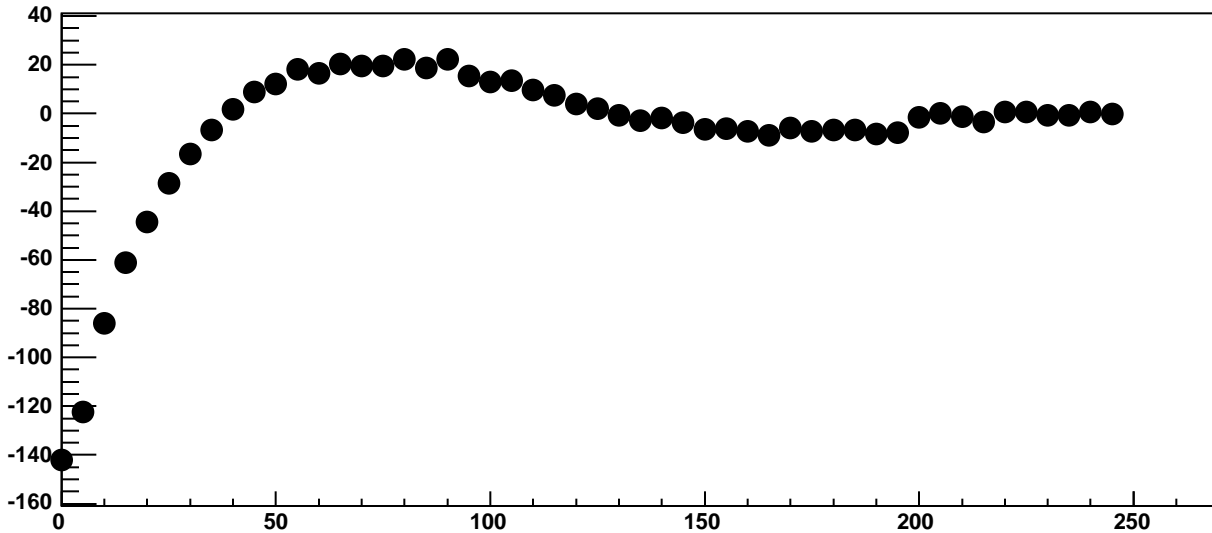
Chip 8, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold



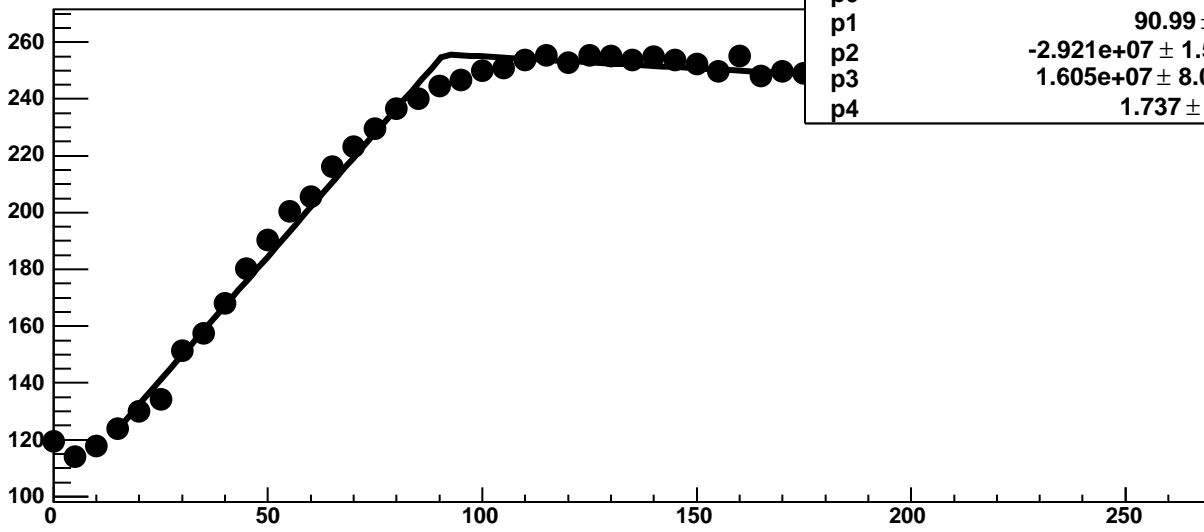
Chip 8, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold

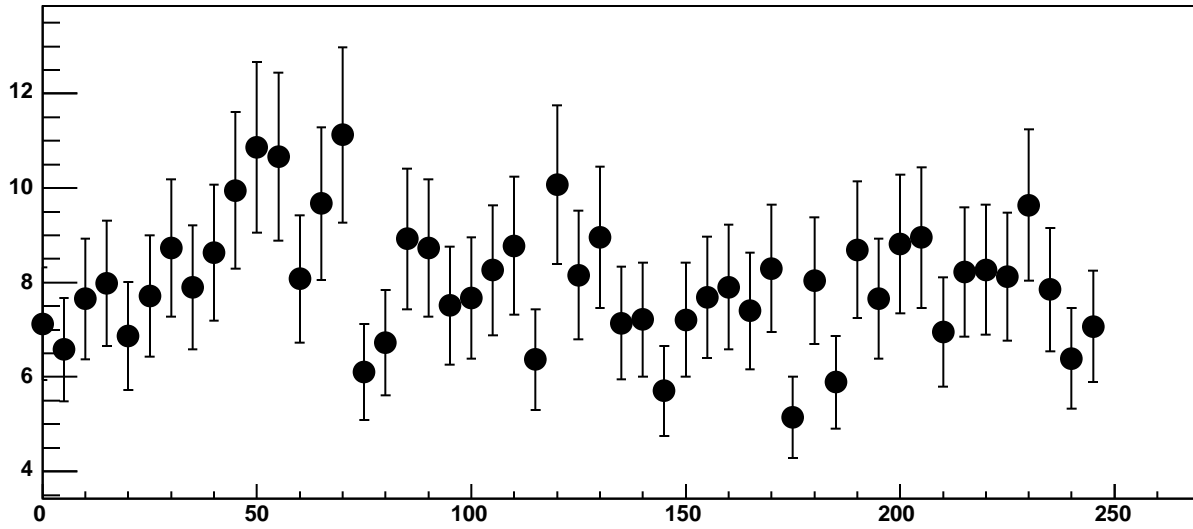


Chip 8, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

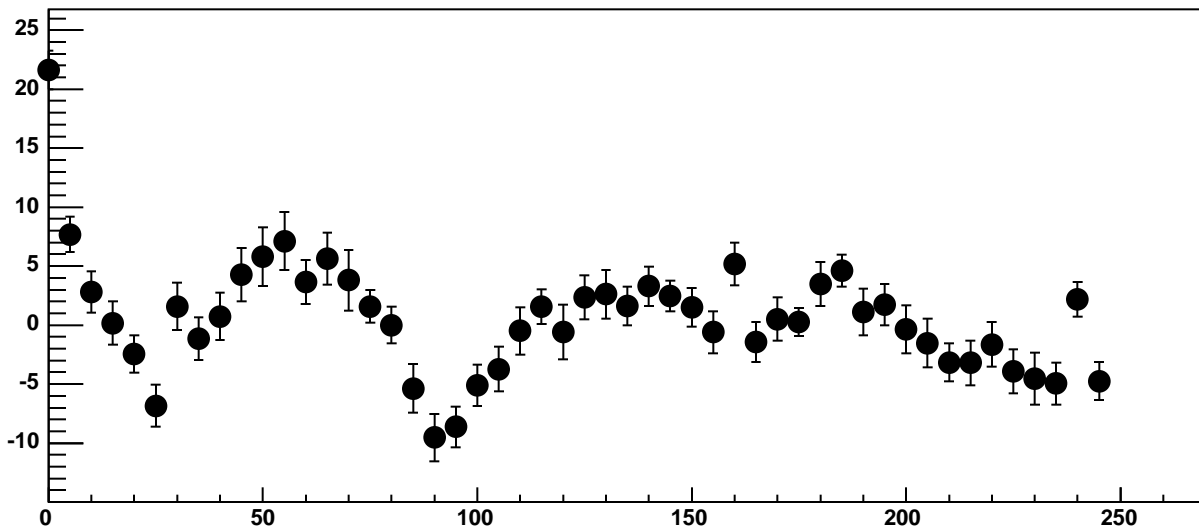


χ^2 / ndf	182.5 / 41
p0	255.8 ± 0.6107
p1	90.99 ± 0.5933
p2	$-2.921\text{e}+07 \pm 1.514\text{e}+06$
p3	$1.605\text{e}+07 \pm 8.069\text{e}+05$
p4	1.737 ± 0.01934

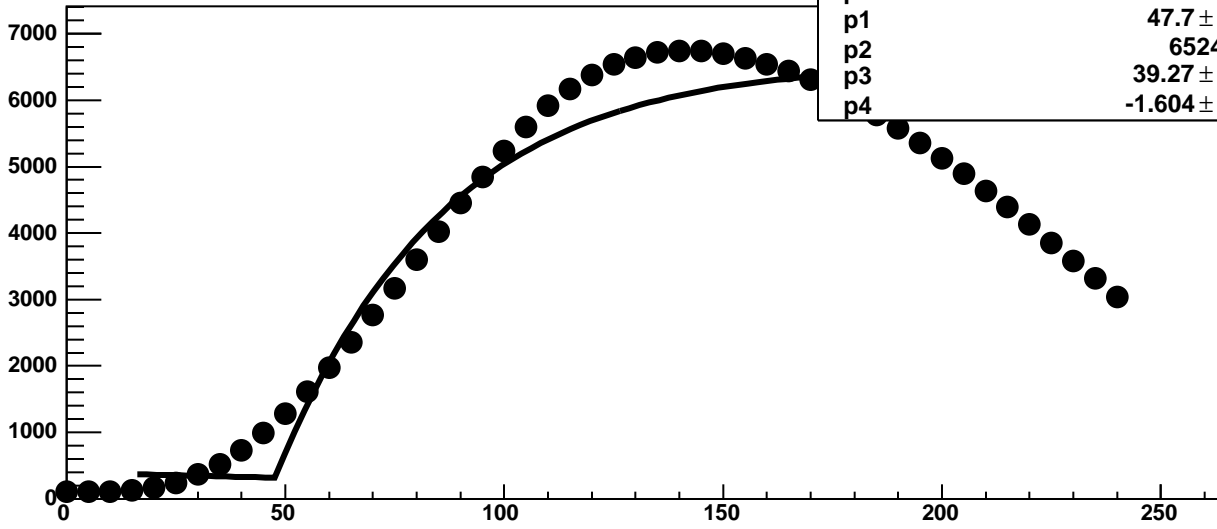
Chip 8, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

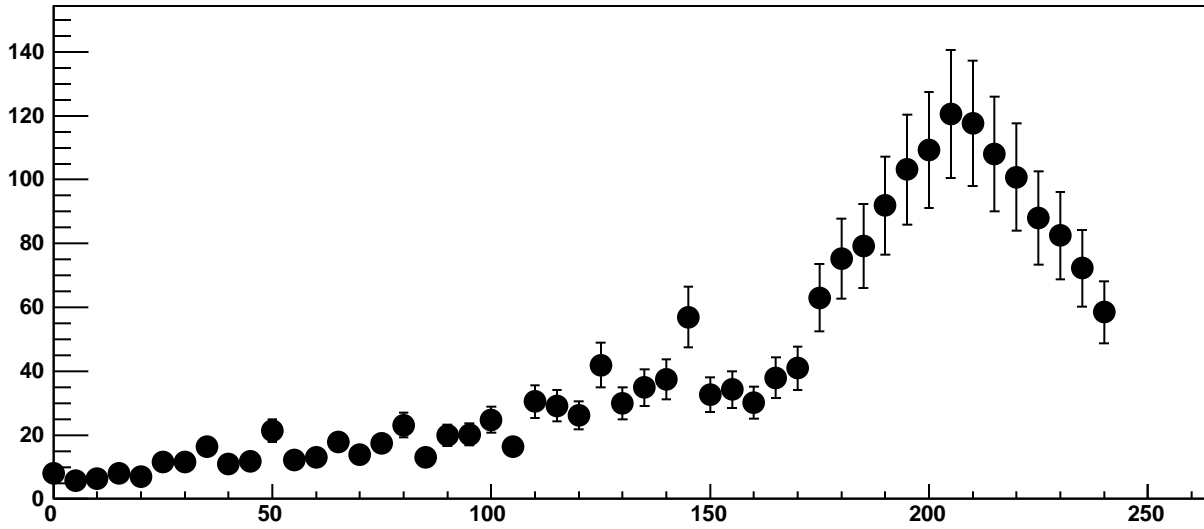


Chip 8, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold

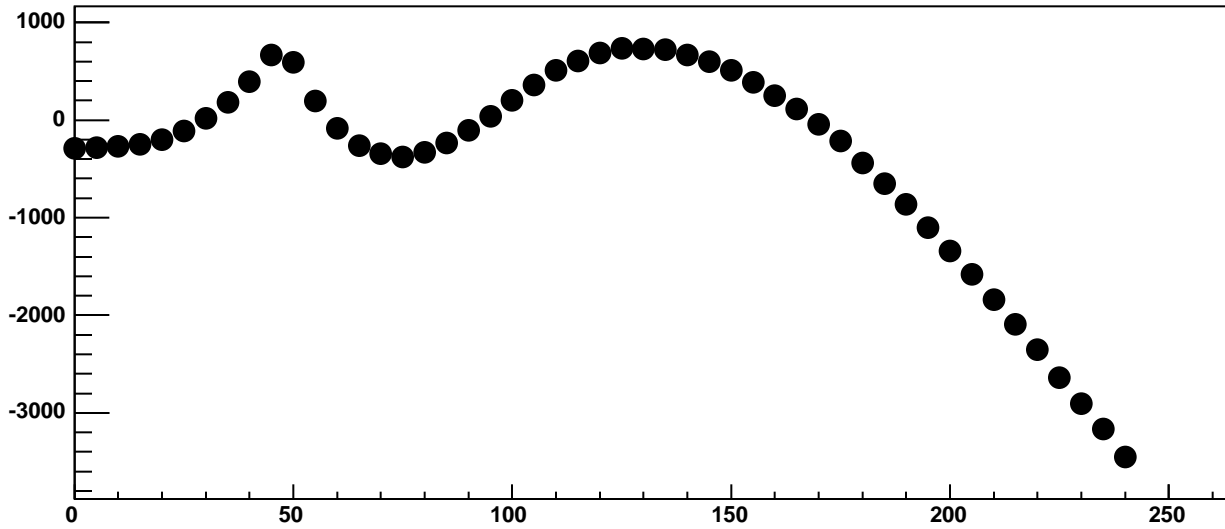


χ^2 / ndf	4.354e+05 / 41
p0	319.5 ± 1.521
p1	47.7 ± 0.01973
p2	6524 ± 9.851
p3	39.27 ± 0.06569
p4	-1.604 ± 0.05943

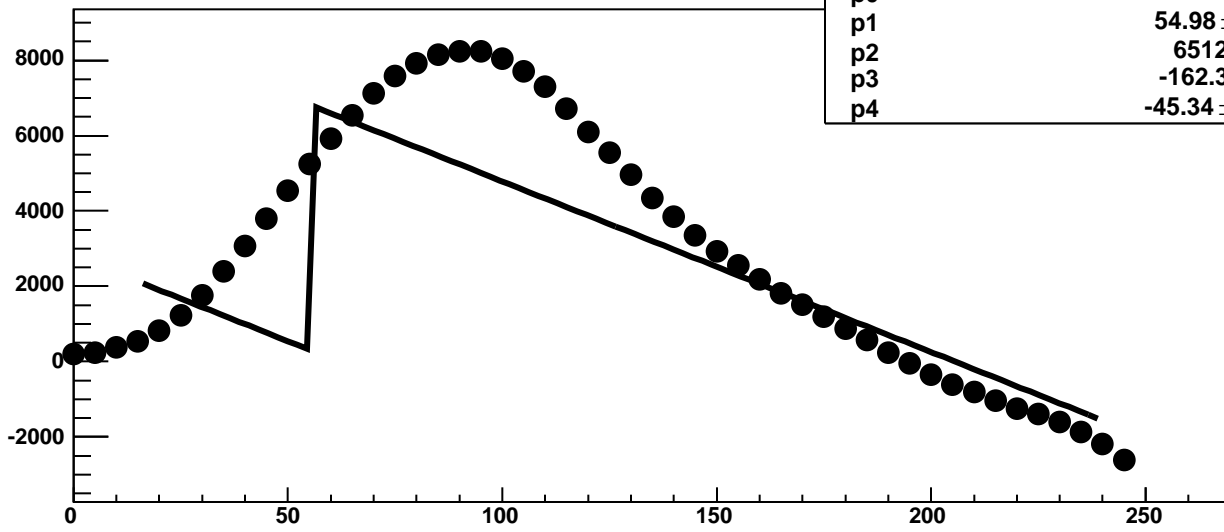
Chip 8, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold

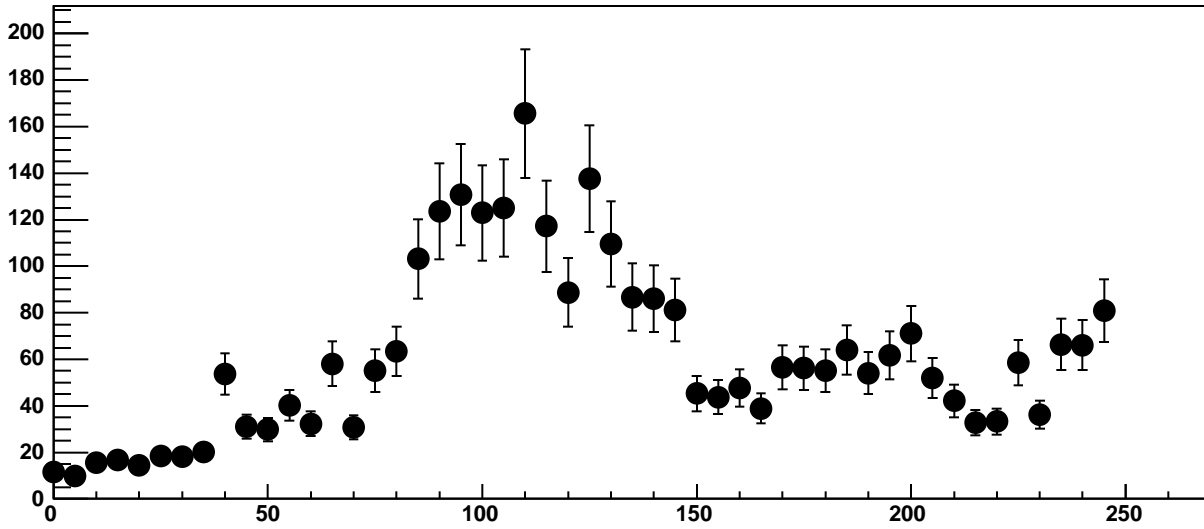


Chip 8, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold

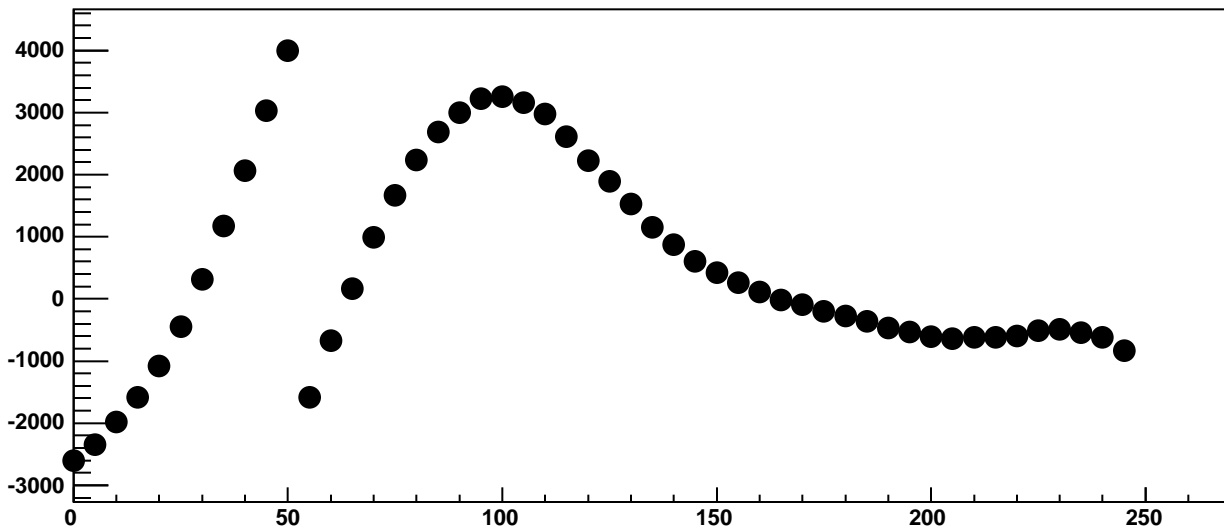


χ^2 / ndf	1.148e+06 / 41
p0	315 ± 12.91
p1	54.98 ± 0.1299
p2	6512 ± 10.77
p3	-162.3 ± 2.032
p4	-45.34 ± 0.1295

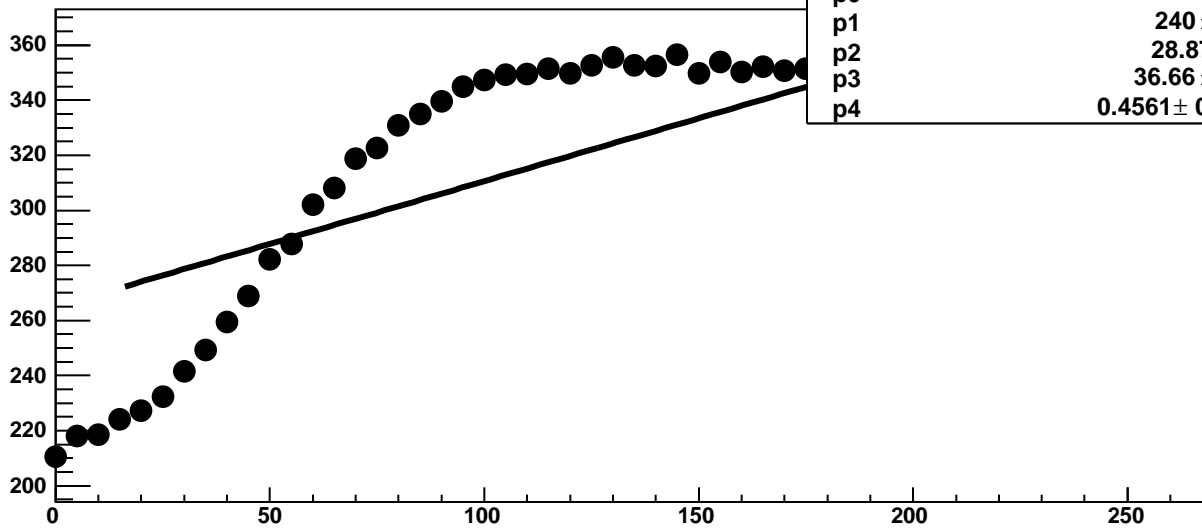
Chip 8, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold

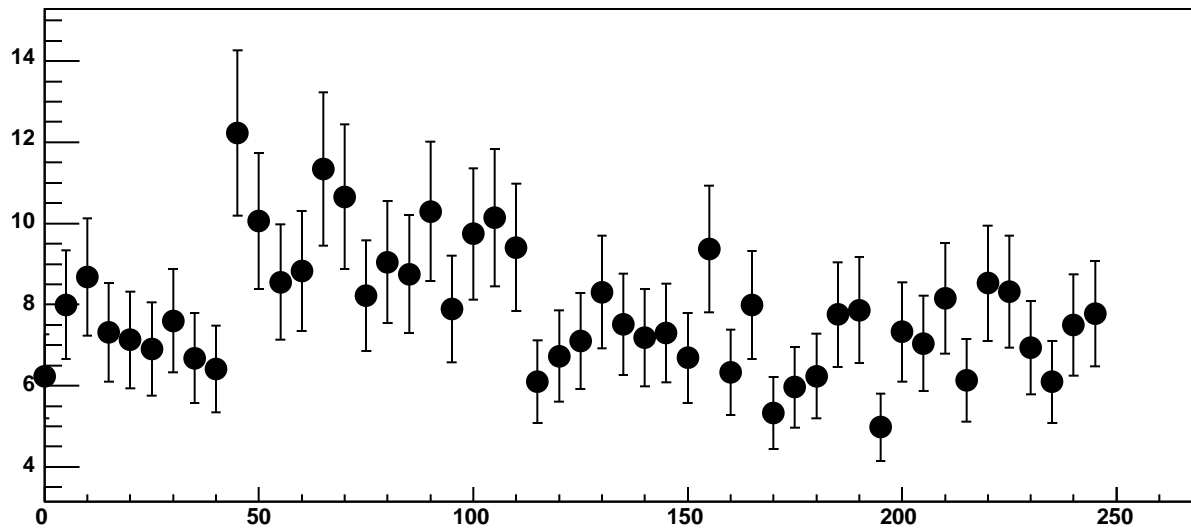


Chip 8, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

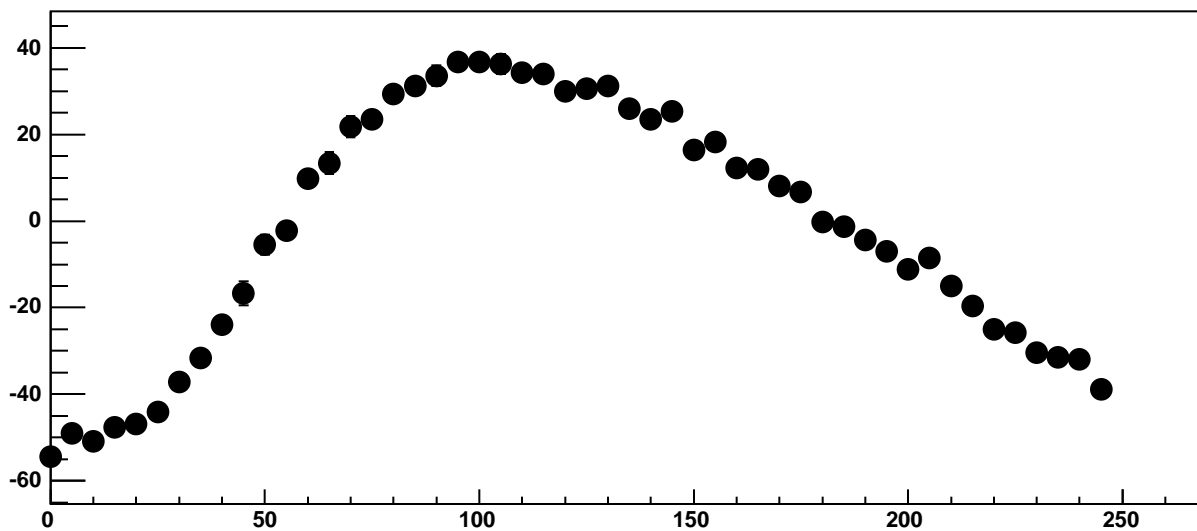


χ^2 / ndf	1.025e+04 / 41
p0	374.5 ± 0.4683
p1	240 ± 0.1025
p2	28.87 ± 2.671
p3	36.66 ± 0.2285
p4	0.4561 ± 0.003767

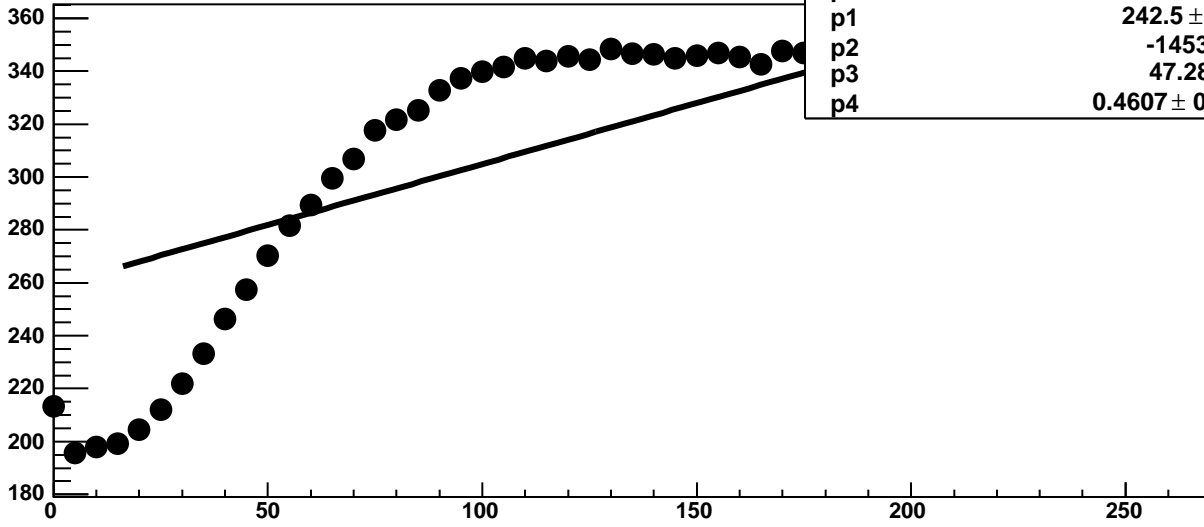
Chip 8, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

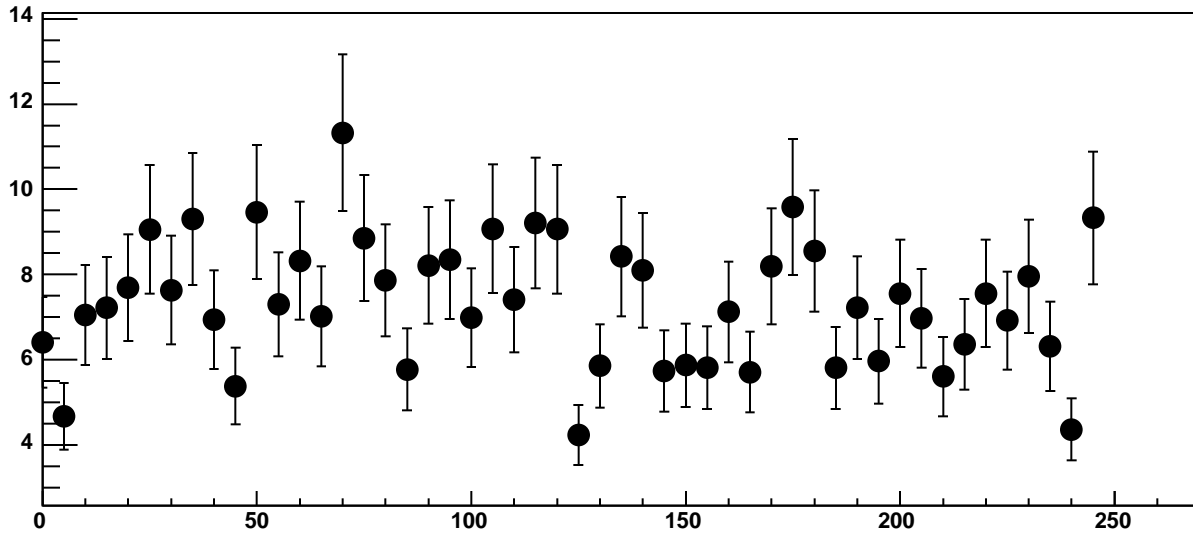


Chip 8, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

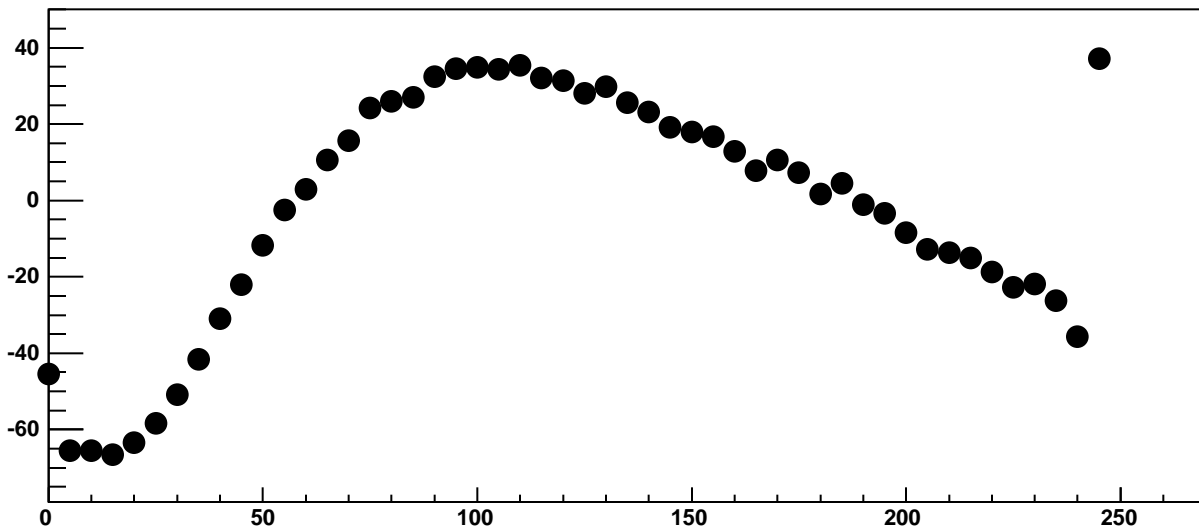


χ^2 / ndf	1.358e+04 / 41
p0	370.5 ± 0.4292
p1	242.5 ± 0.07391
p2	-1453 ± 48.82
p3	47.28 ± 1.241
p4	0.4607 ± 0.003517

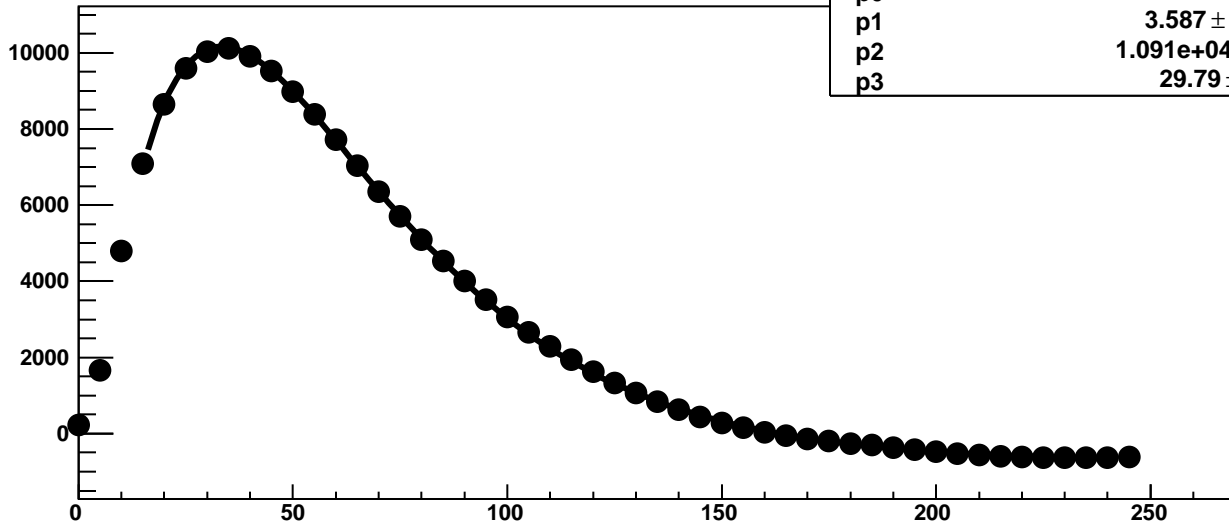
Chip 8, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

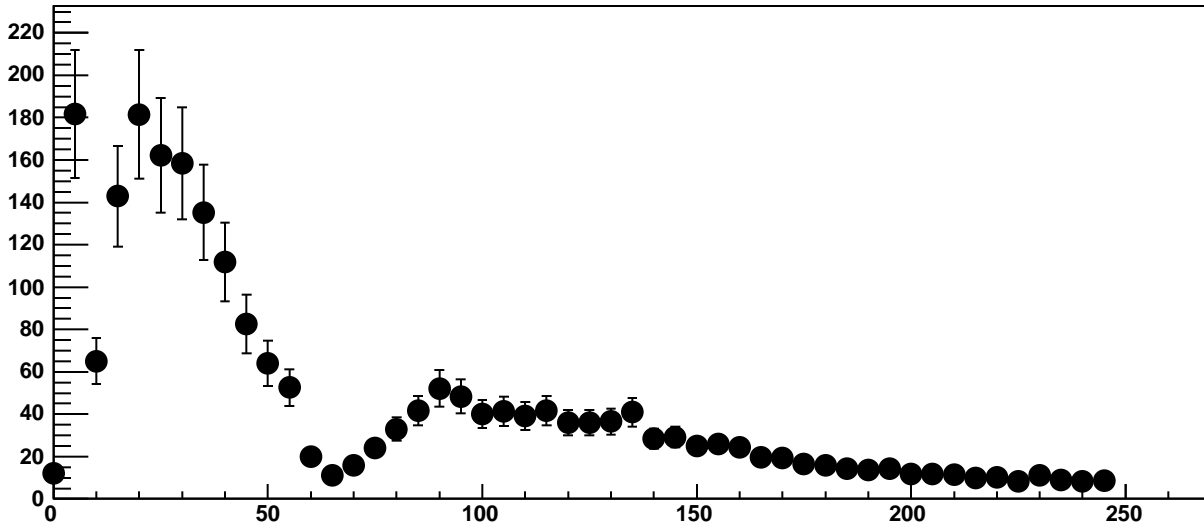


Chip 8, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold

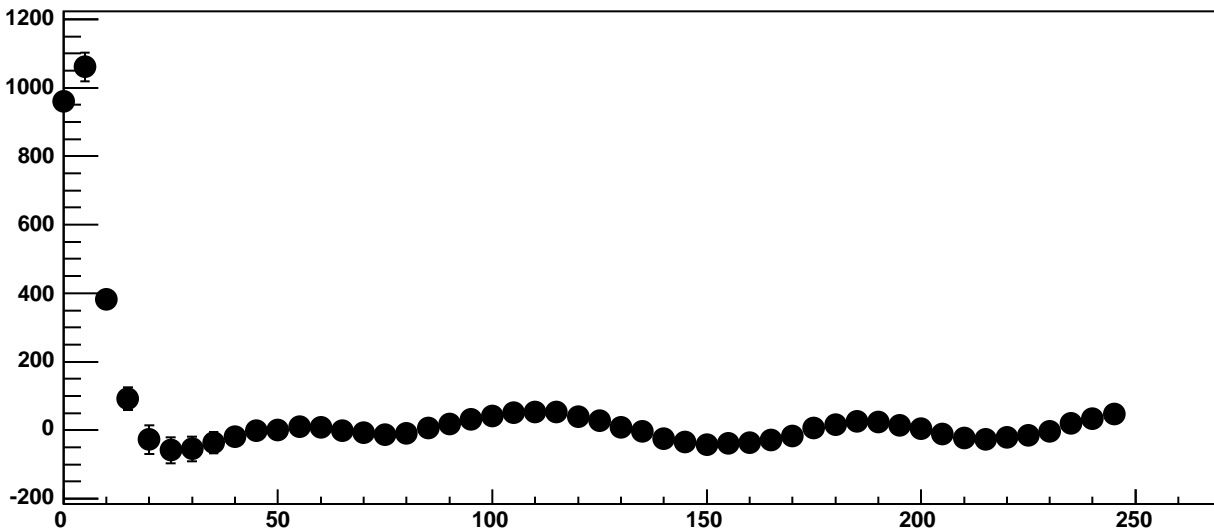


χ^2 / ndf	1379 / 42
p0	-743.6 ± 0.996
p1	3.587 ± 0.05504
p2	$1.091\text{e}+04 \pm 7.386$
p3	29.79 ± 0.0209

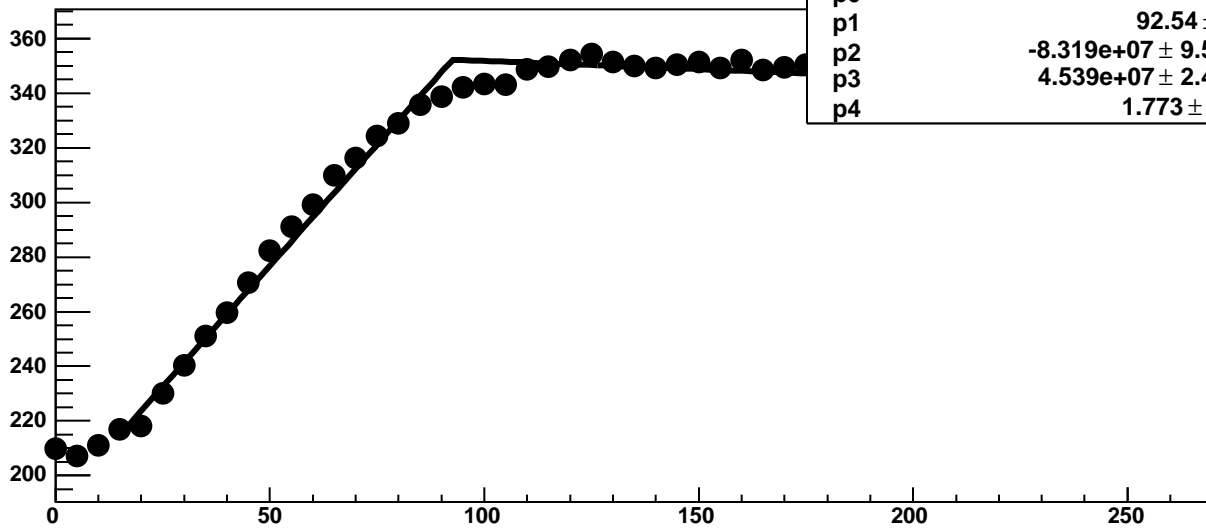
Chip 8, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold

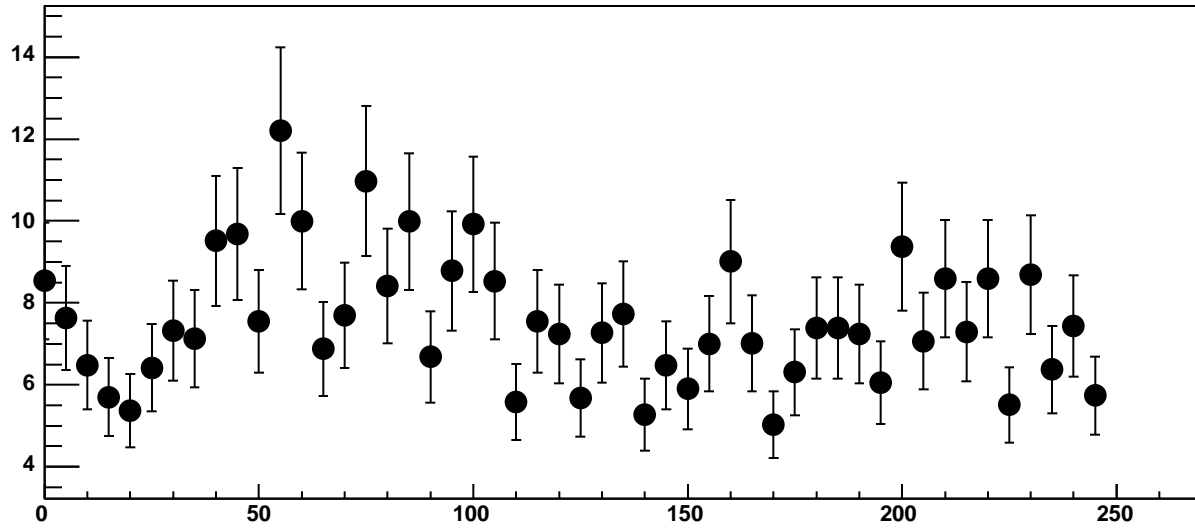


Chip 8, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

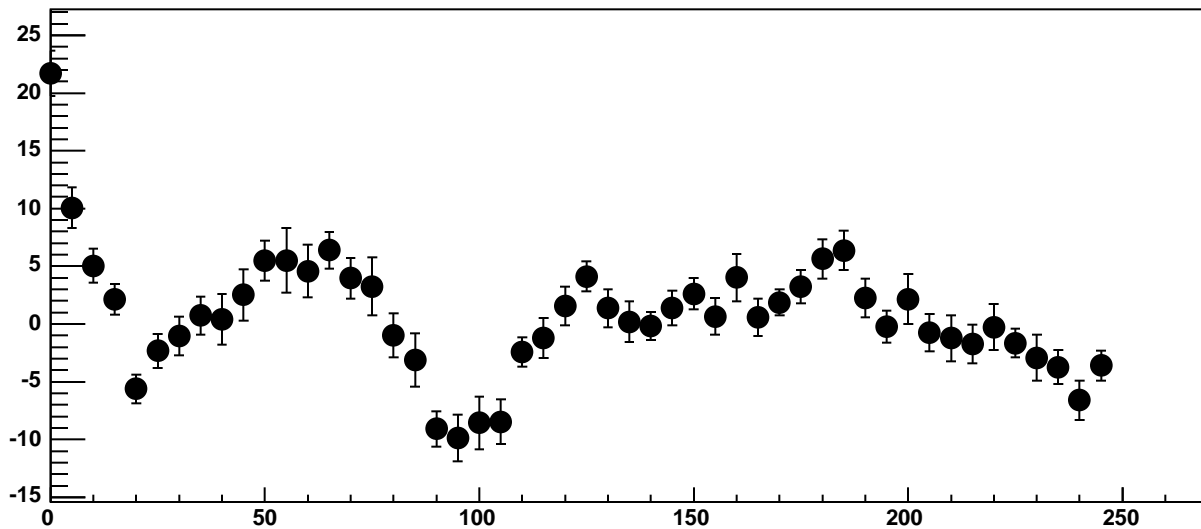


χ^2 / ndf	249.6 / 41
p0	352.3 ± 0.5797
p1	92.54 ± 0.5947
p2	$-8.319\text{e}+07 \pm 9.577\text{e}+05$
p3	$4.539\text{e}+07 \pm 2.478\text{e}+05$
p4	1.773 ± 0.01745

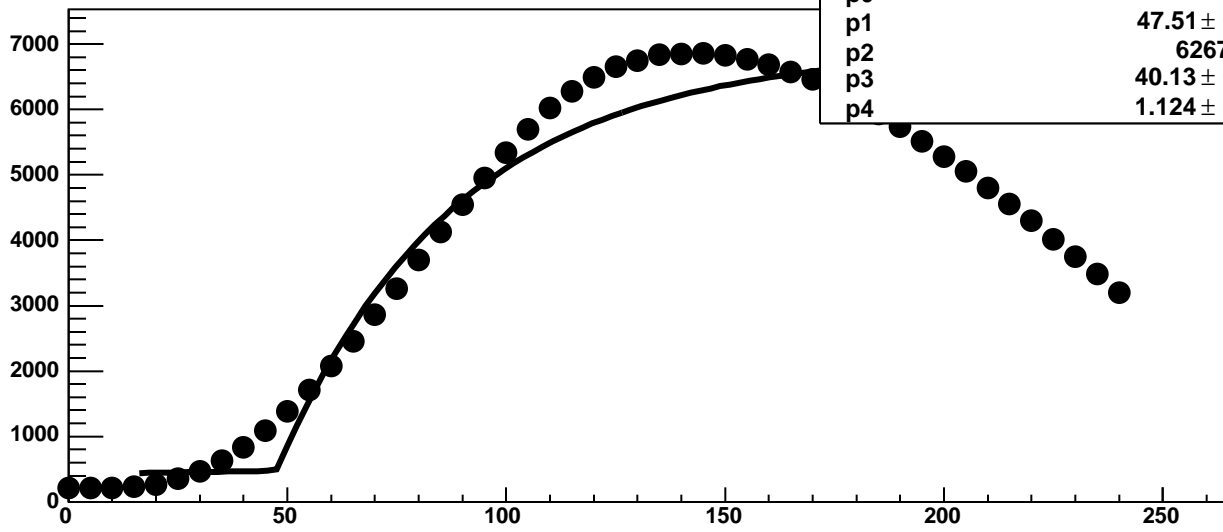
Chip 8, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

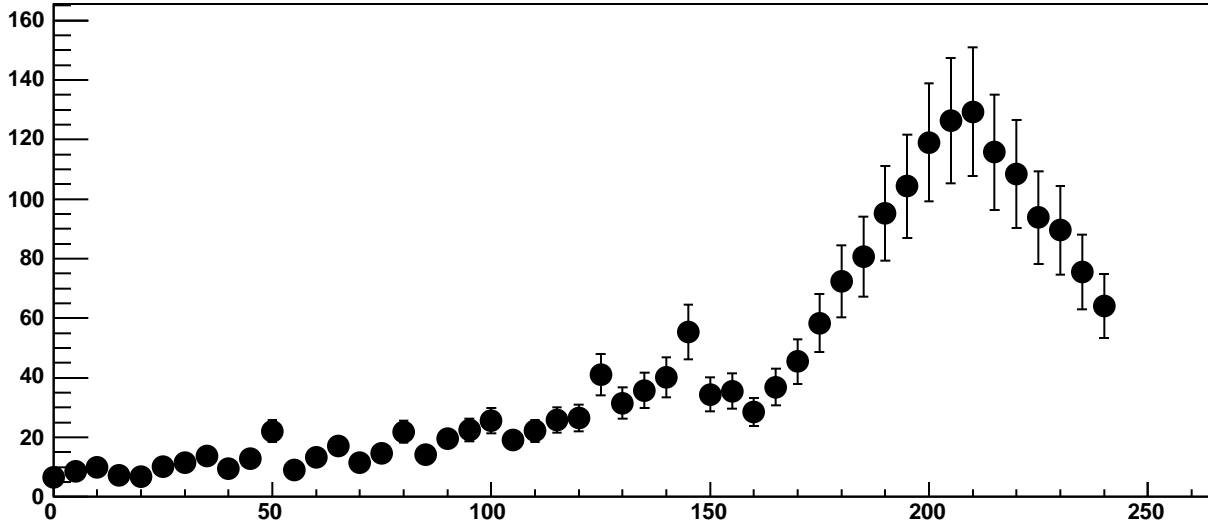


Chip 8, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

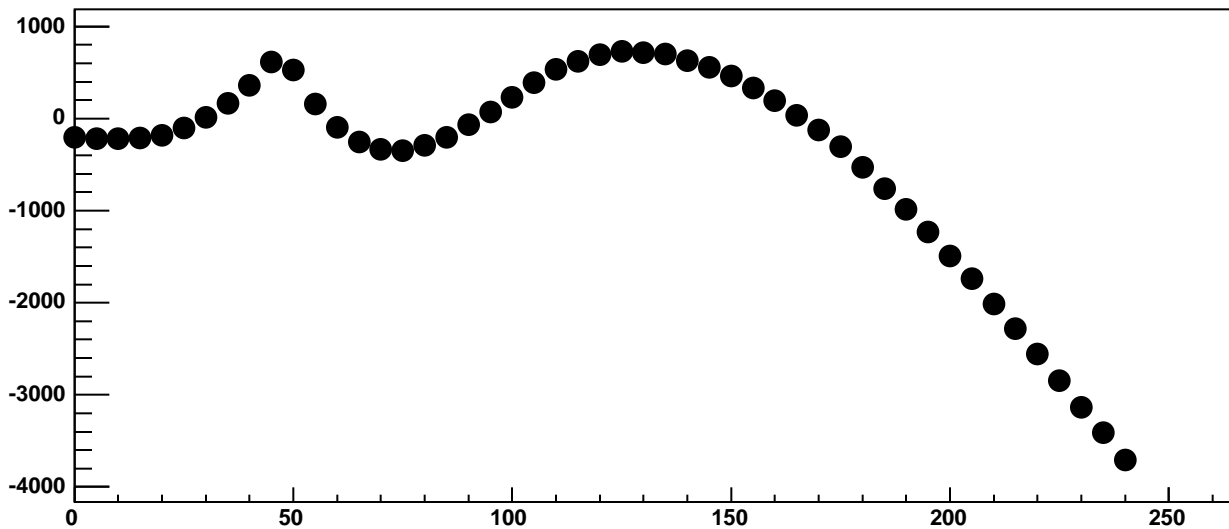


χ^2 / ndf	4.218e+05 / 41
p0	474.9 ± 1.535
p1	47.51 ± 0.01889
p2	6267 ± 9.86
p3	40.13 ± 0.06818
p4	1.124 ± 0.05977

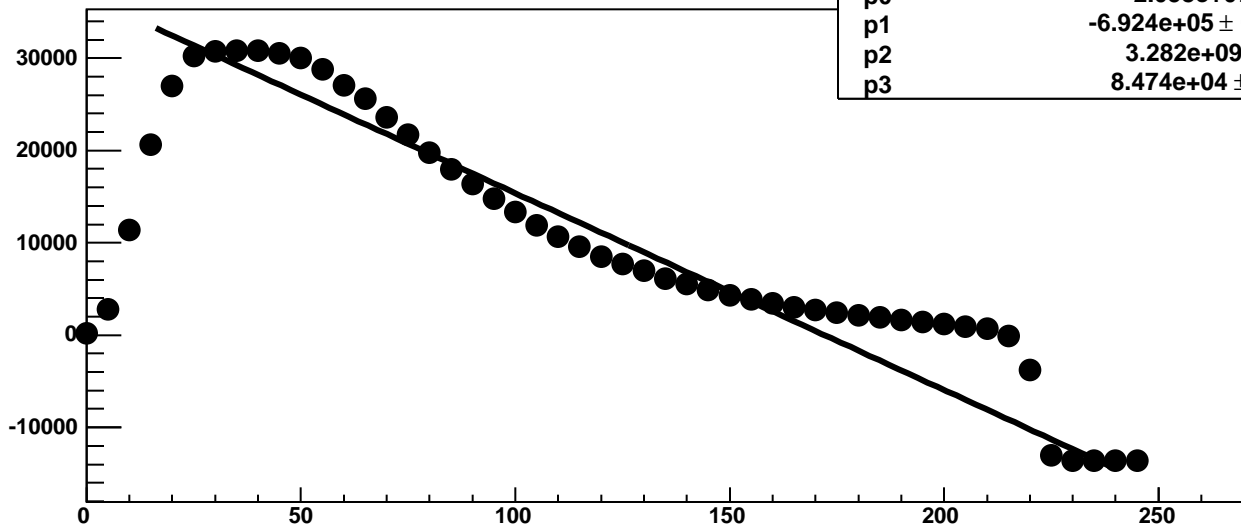
Chip 8, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold

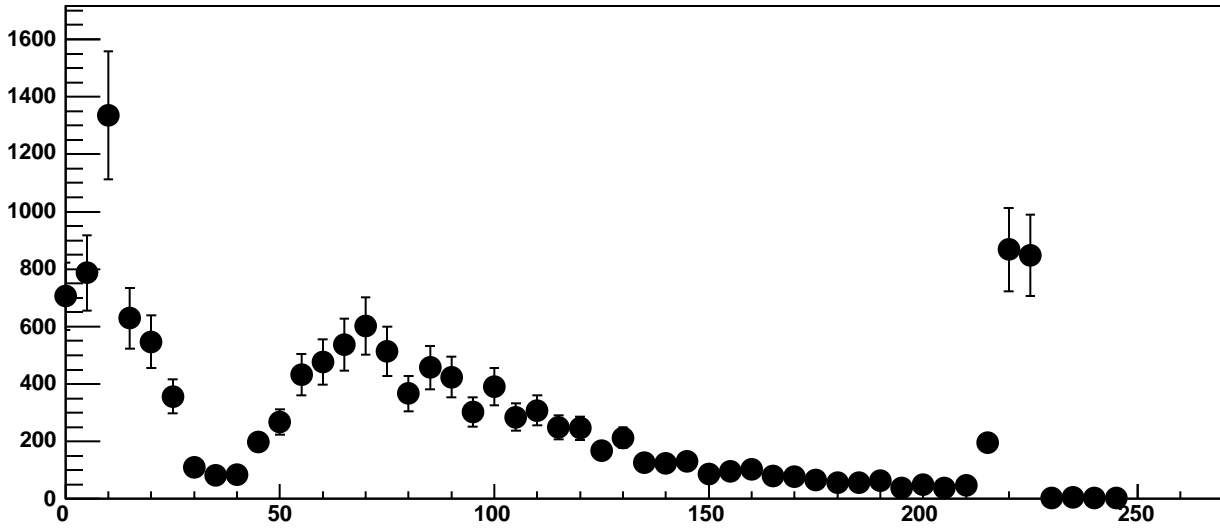


Chip 8, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold

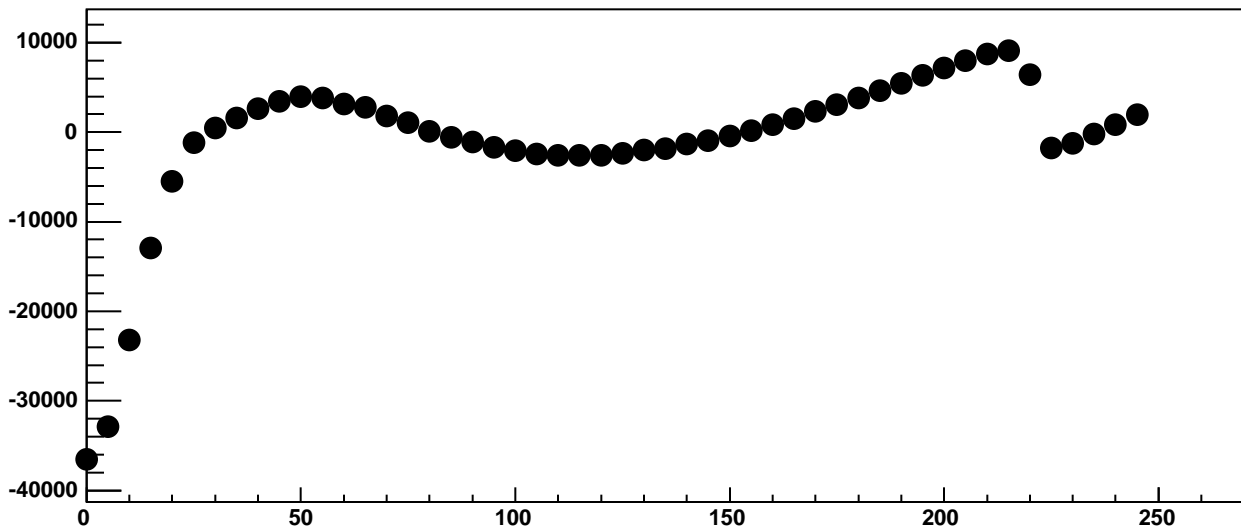


χ^2 / ndf	6.702e+06 / 42
p0	-2.058e+07 ± 6.61
p1	-6.924e+05 ± 0.03106
p2	3.282e+09 ± 1055
p3	8.474e+04 ± 0.0038

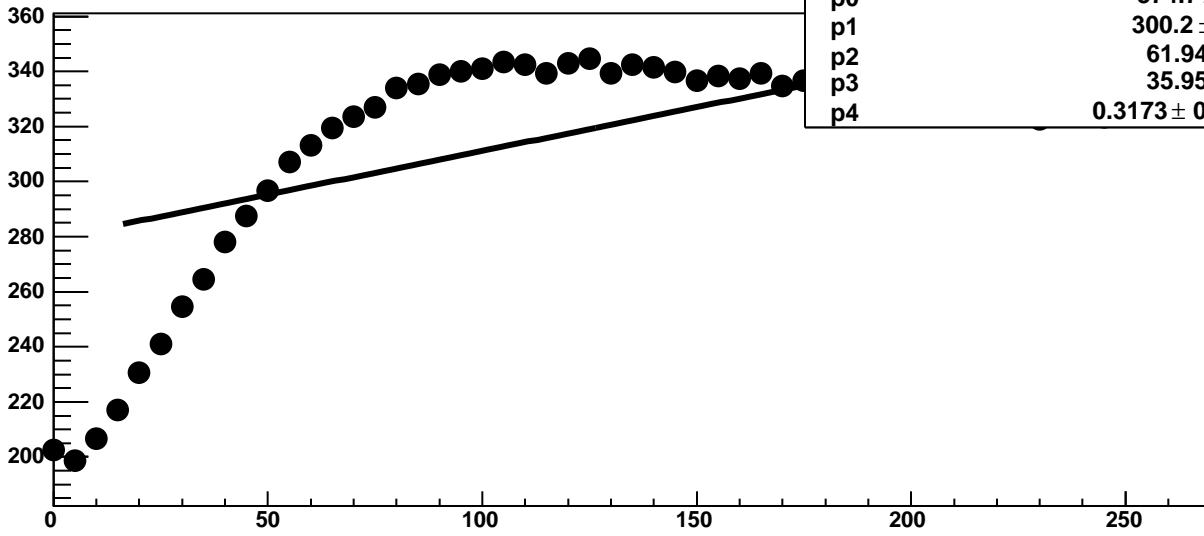
Chip 8, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold



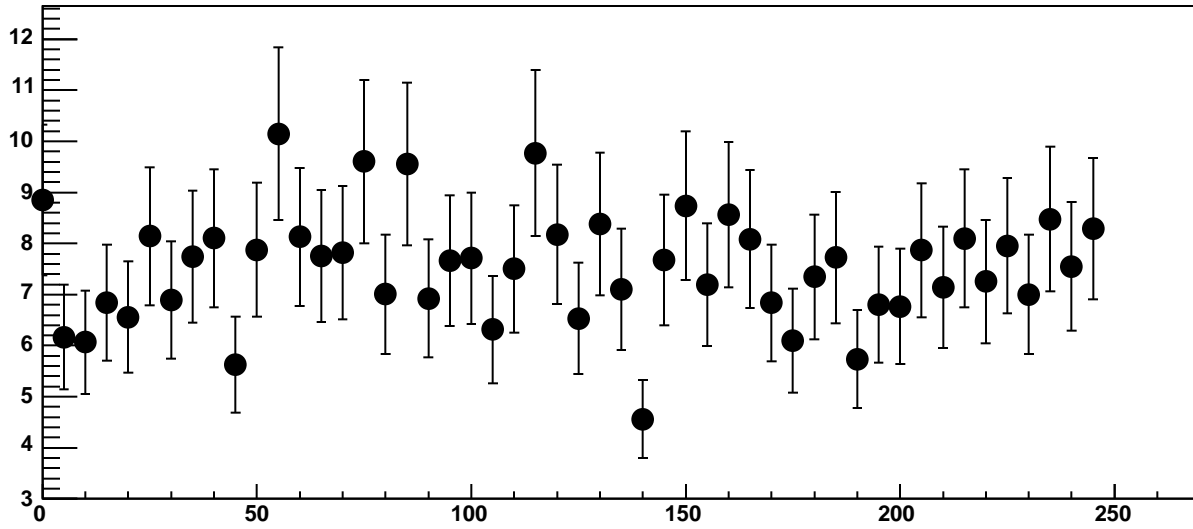
Chip 8, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold



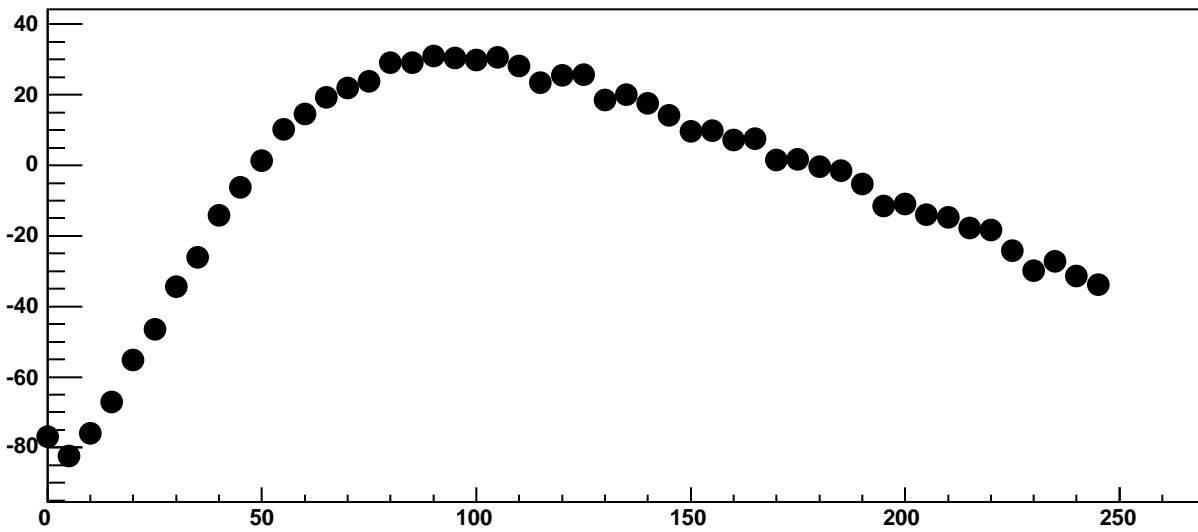
Chip 8, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold



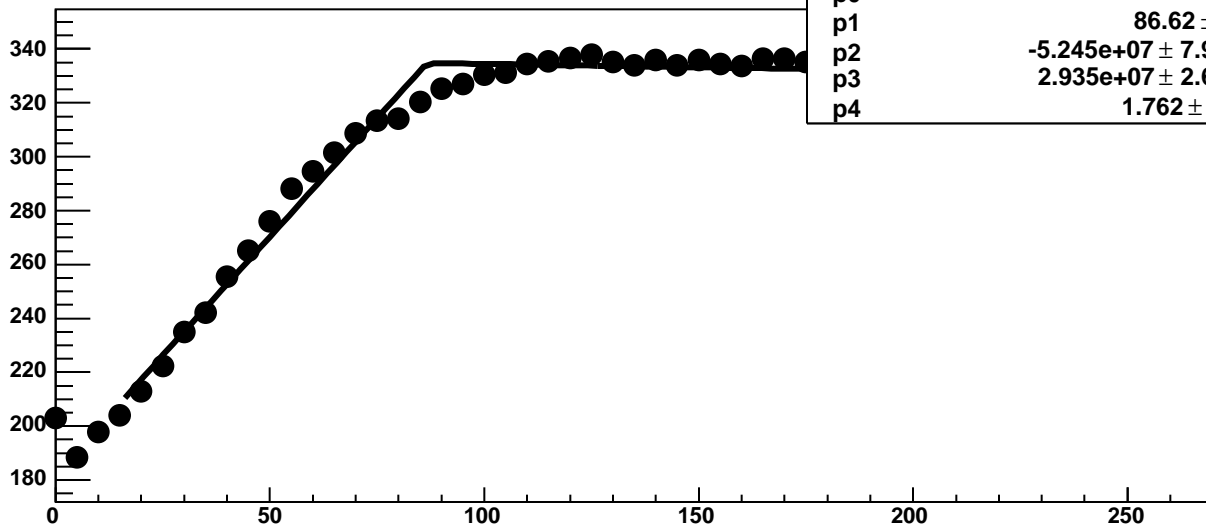
Chip 8, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold

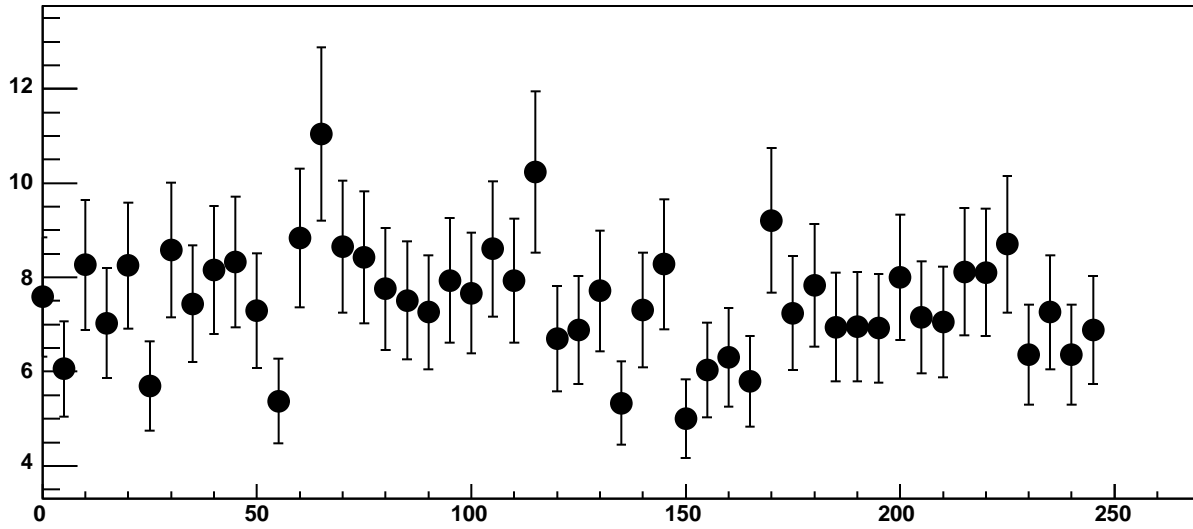


Chip 8, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

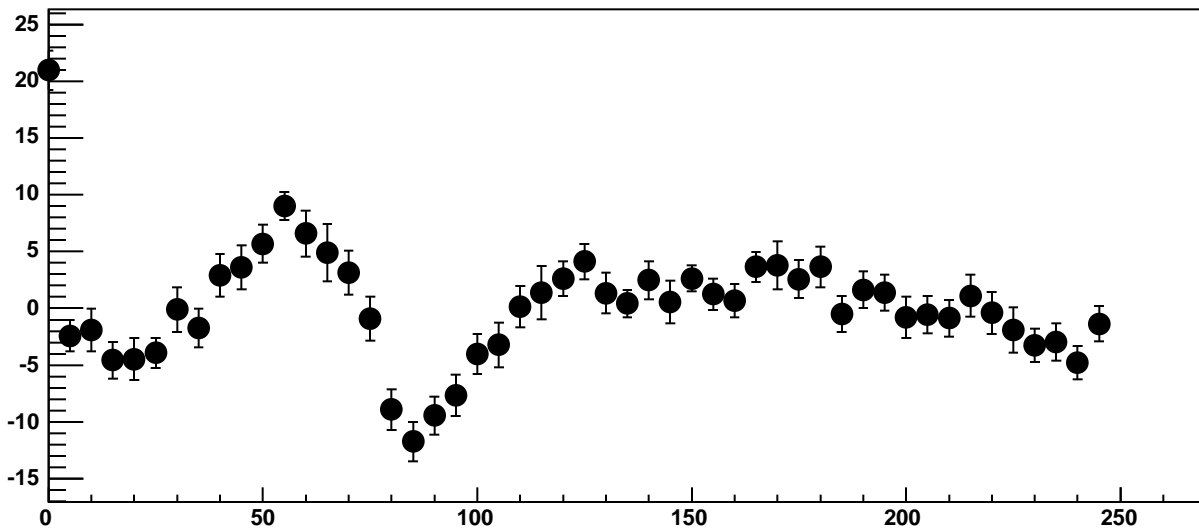


χ^2 / ndf	301.1 / 41
p0	334.8 ± 0.6038
p1	86.62 ± 0.6146
p2	$-5.245e+07 \pm 7.985e+05$
p3	$2.935e+07 \pm 2.642e+05$
p4	1.762 ± 0.02065

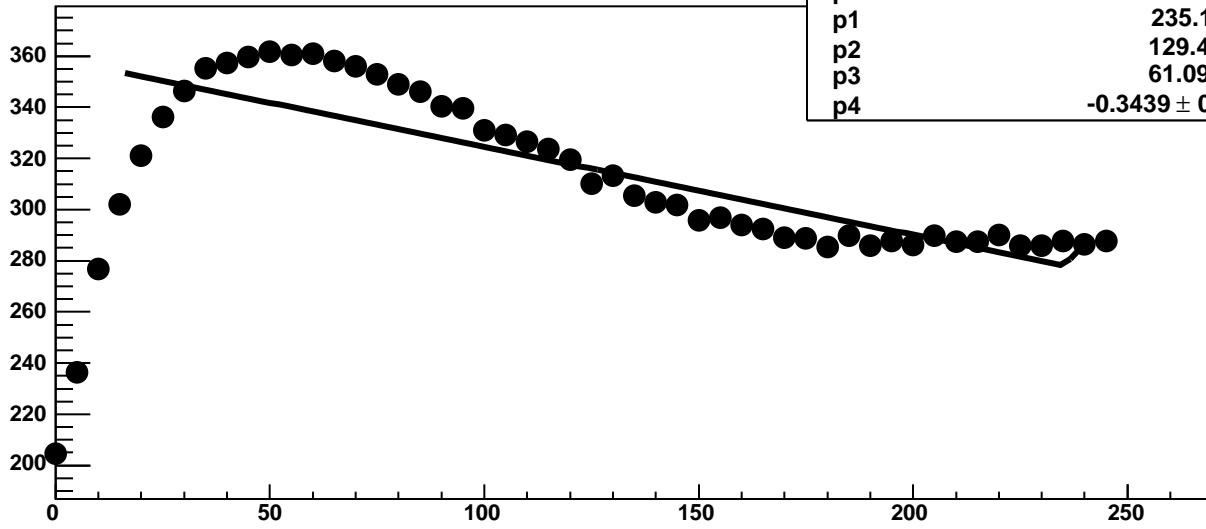
Chip 8, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



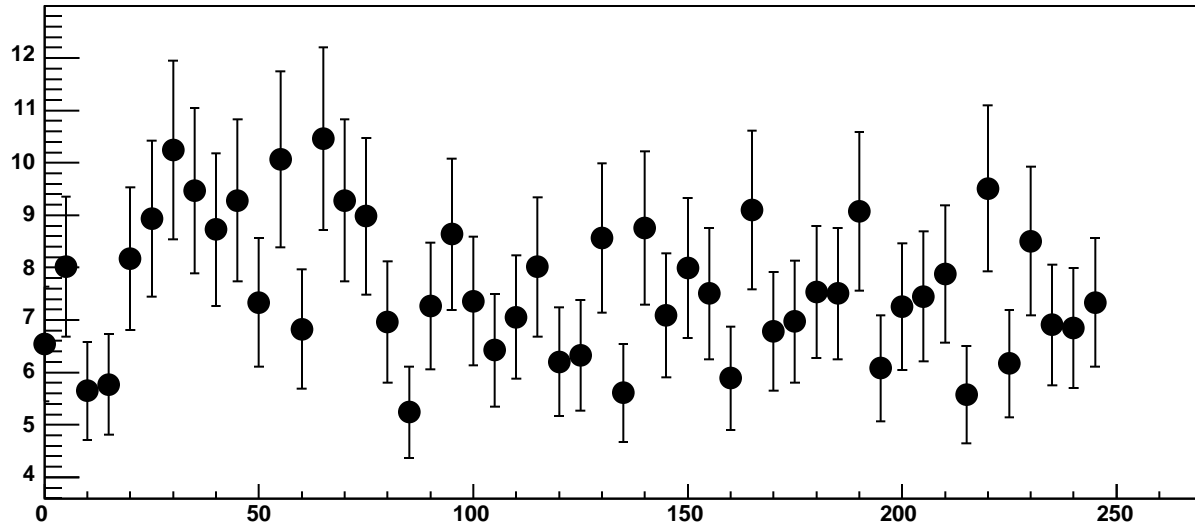
Chip 8, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold



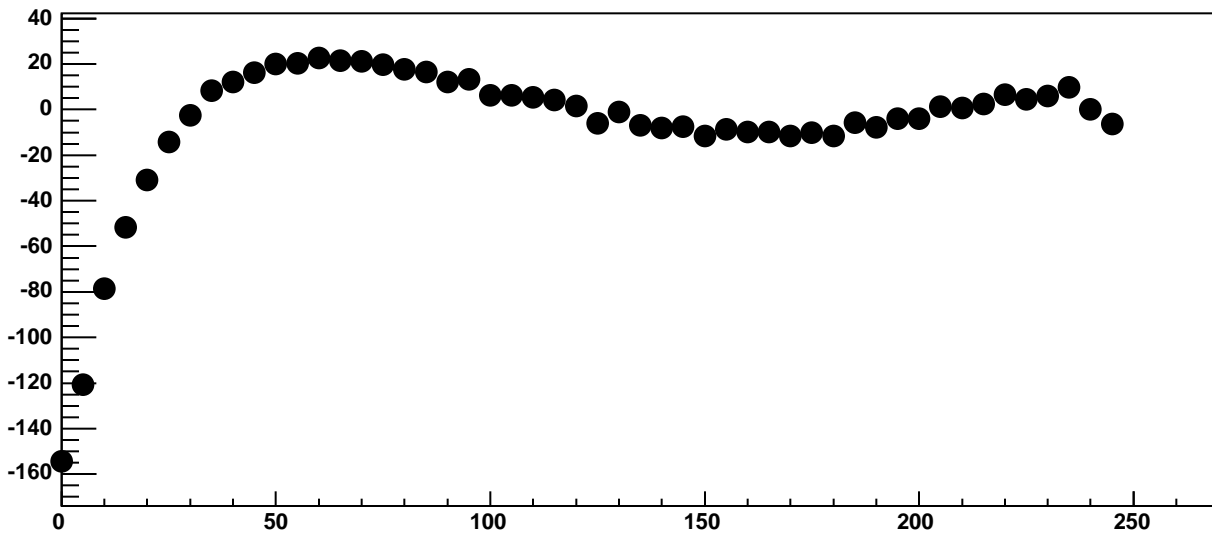
Chip 8, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold



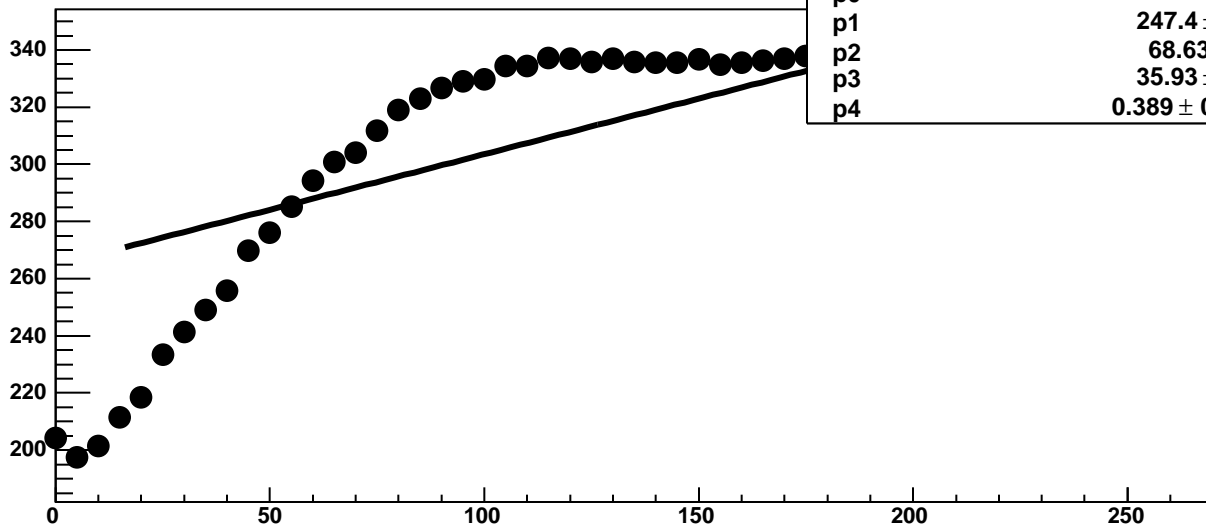
Chip 8, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold

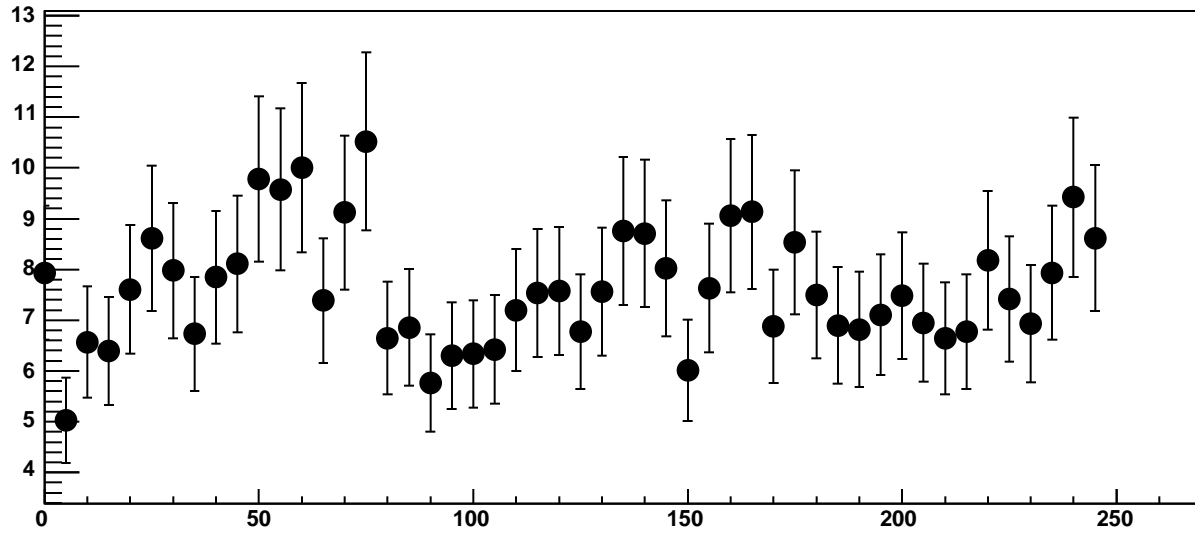


Chip 8, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold

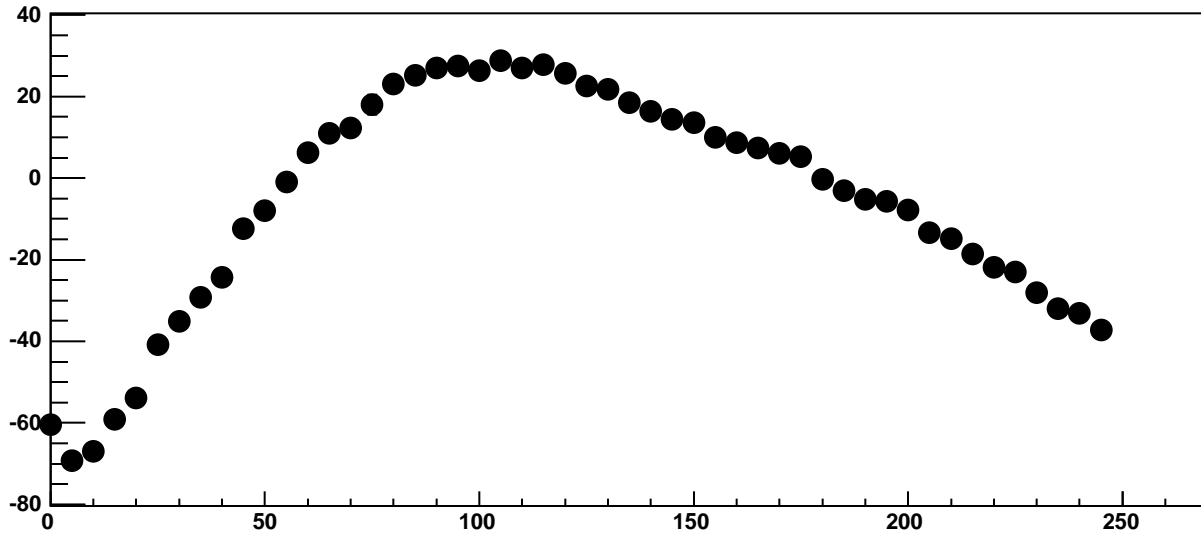


χ^2 / ndf	9124 / 41
p0	360.9 ± 0.4321
p1	247.4 ± 0.4512
p2	68.63 ± 1.976
p3	35.93 ± 0.1933
p4	0.389 ± 0.003861

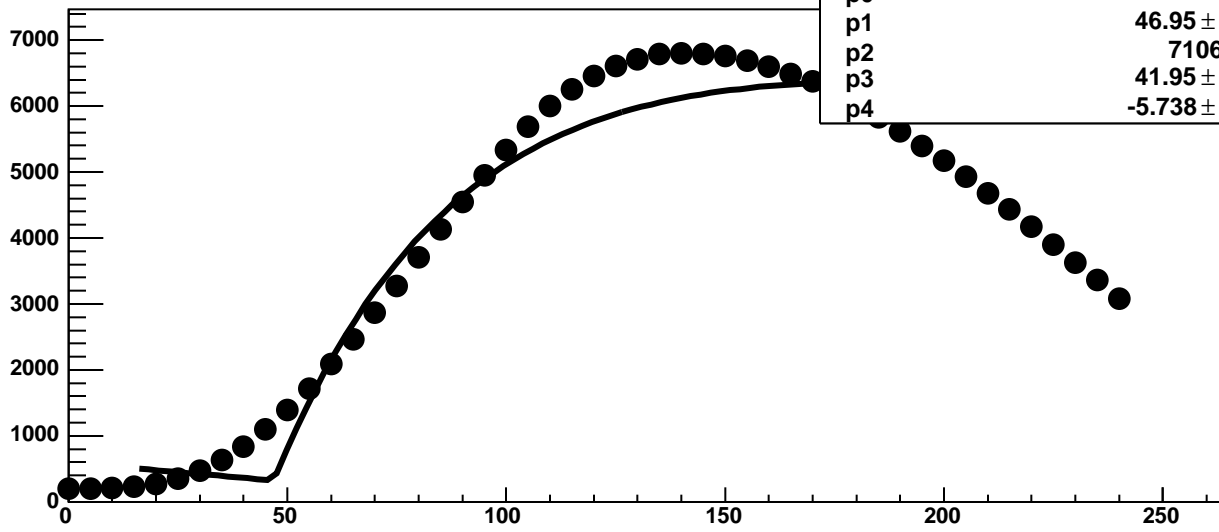
Chip 8, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

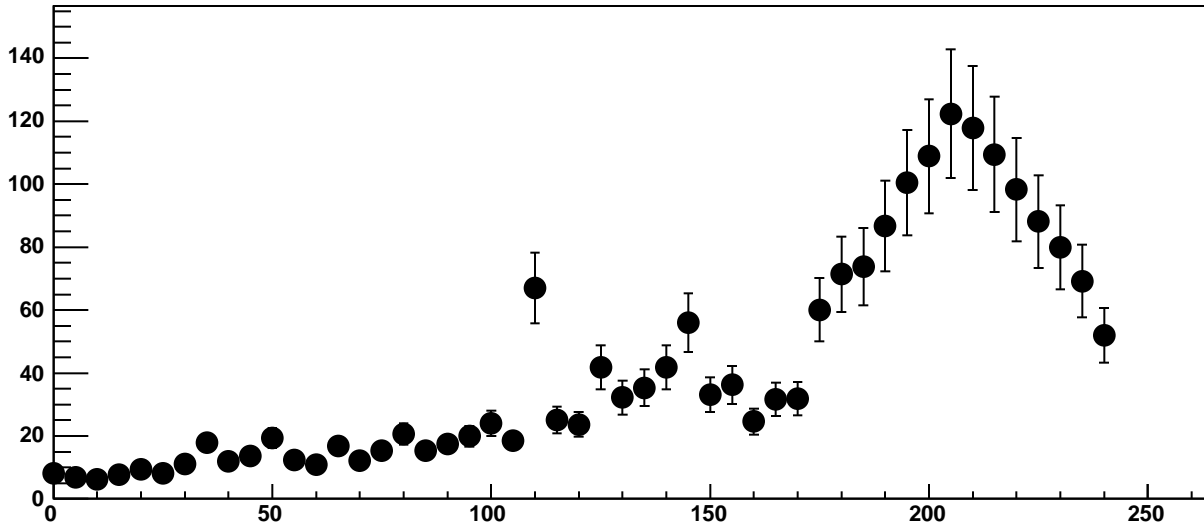


Chip 8, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

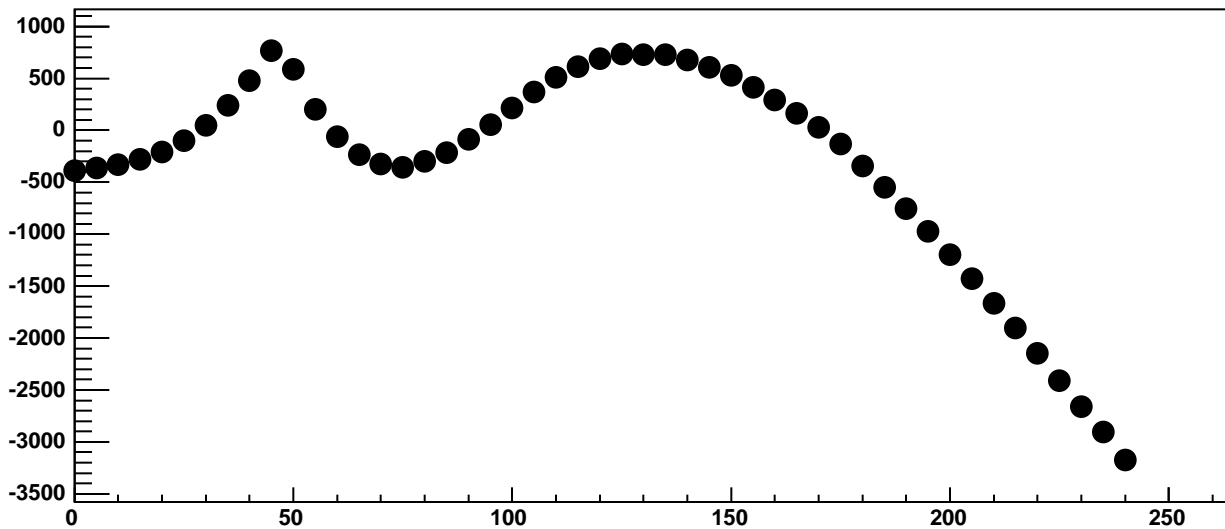


χ^2 / ndf	4.383e+05 / 41
p0	324.4 ± 1.535
p1	46.95 ± 0.01894
p2	7106 ± 10.72
p3	41.95 ± 0.06709
p4	-5.738 ± 0.06247

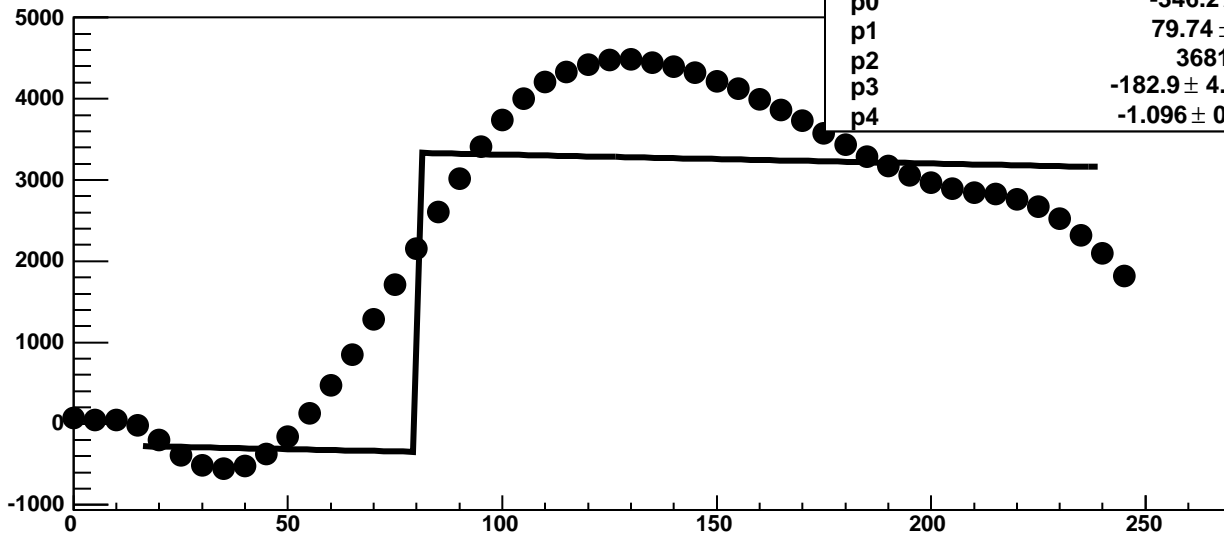
Chip 8, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

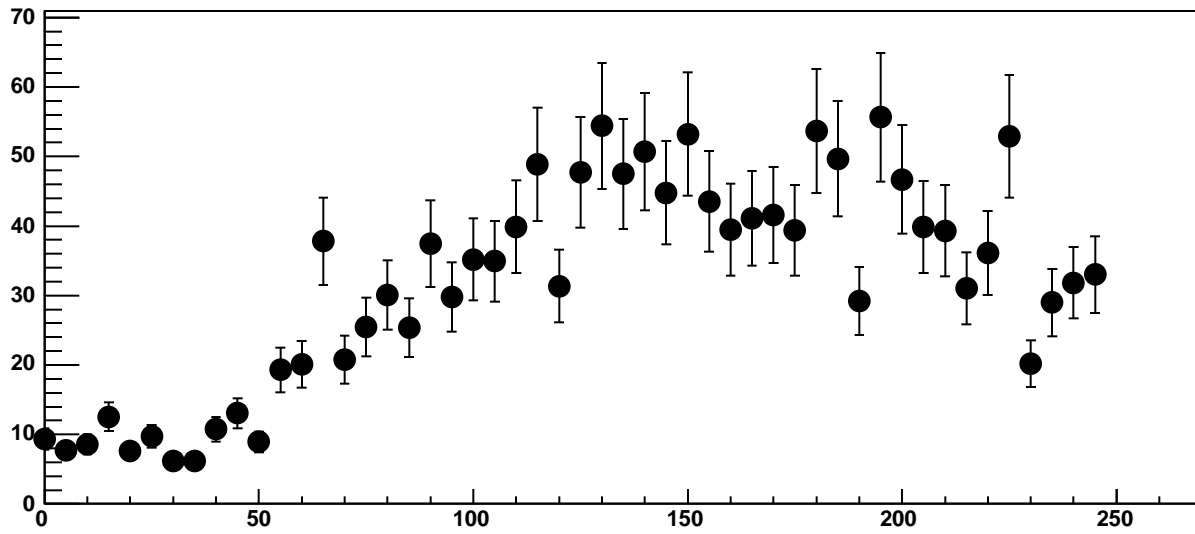


Chip 8, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold

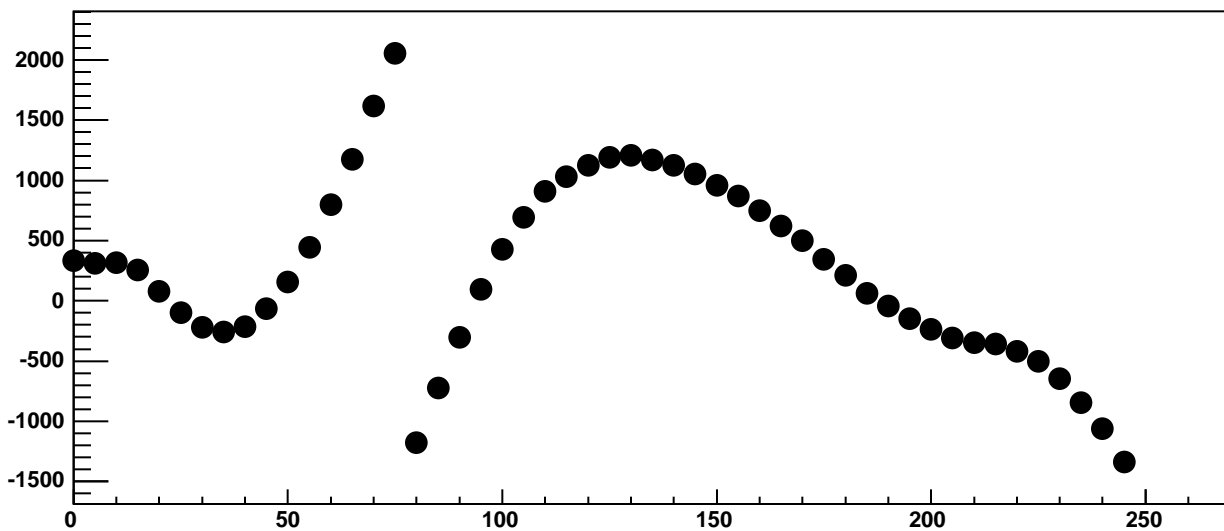


χ^2 / ndf	6.294e+05 / 41
p0	-346.2 ± 0.4781
p1	79.74 ± 0.4365
p2	3681 ± 2.099
p3	-182.9 ± 4.209e-05
p4	-1.096 ± 0.008426

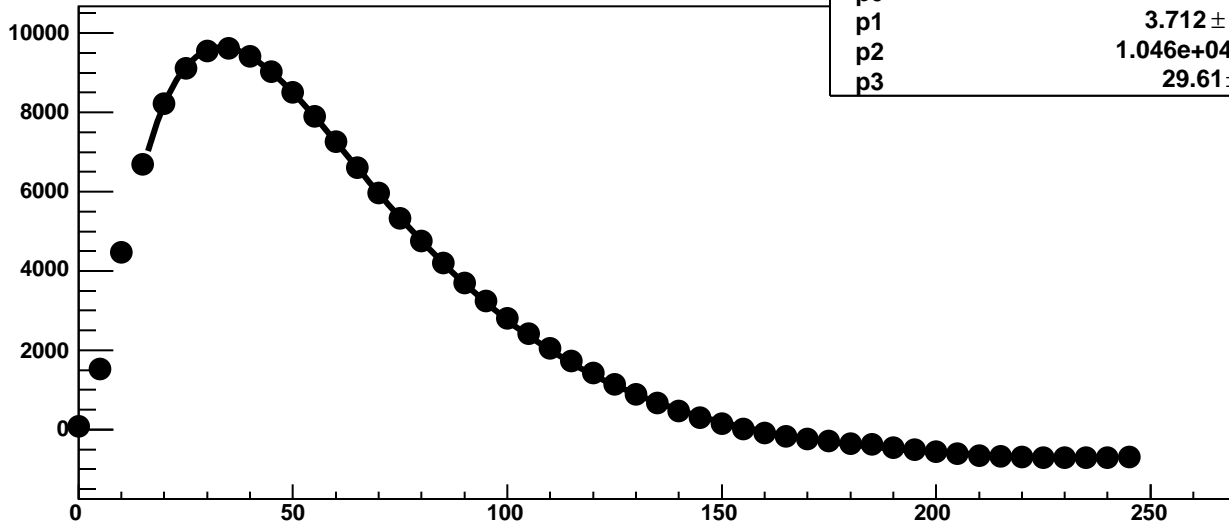
Chip 8, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

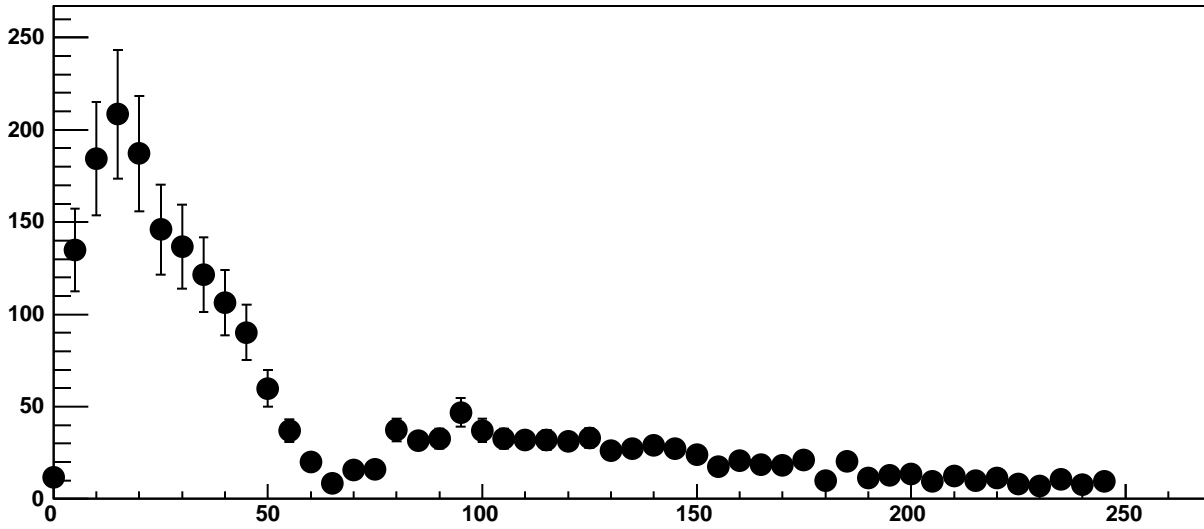


Chip 8, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold

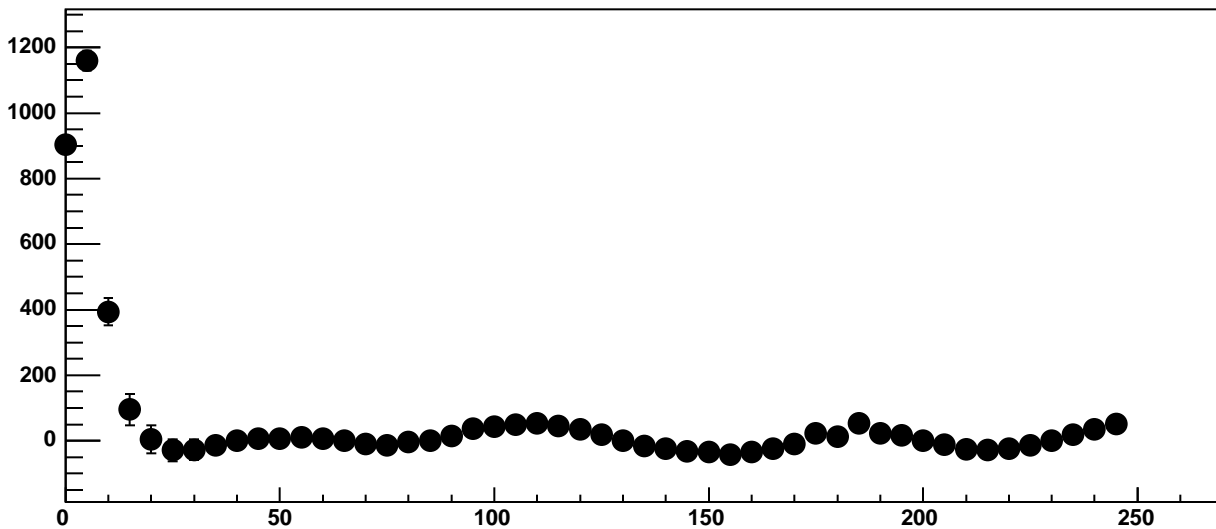


χ^2 / ndf	1671 / 42
p0	-813.4 ± 0.9157
p1	3.712 ± 0.06494
p2	1.046e+04 ± 8.082
p3	29.61 ± 0.0203

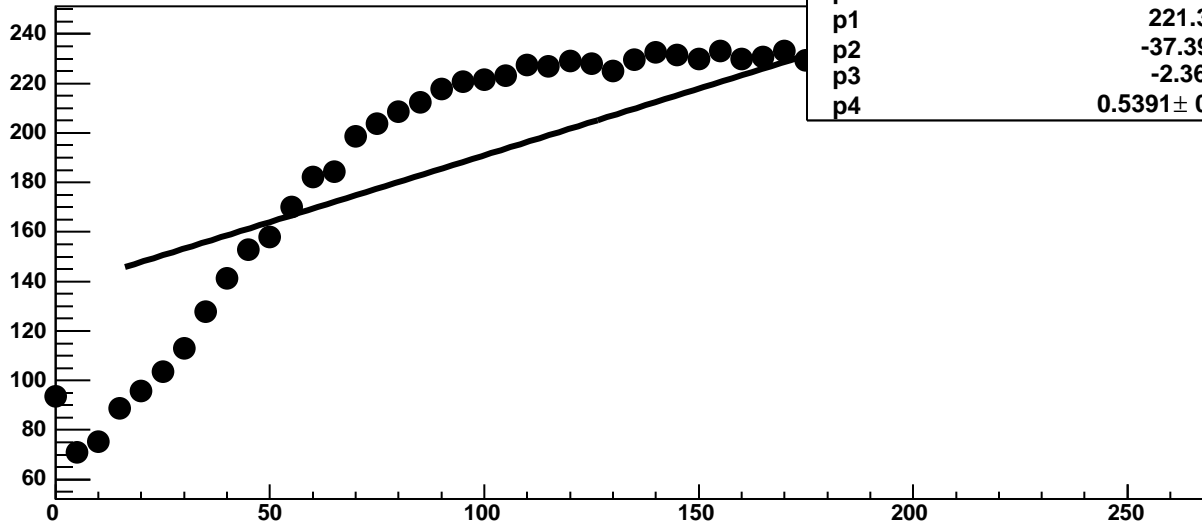
Chip 8, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold

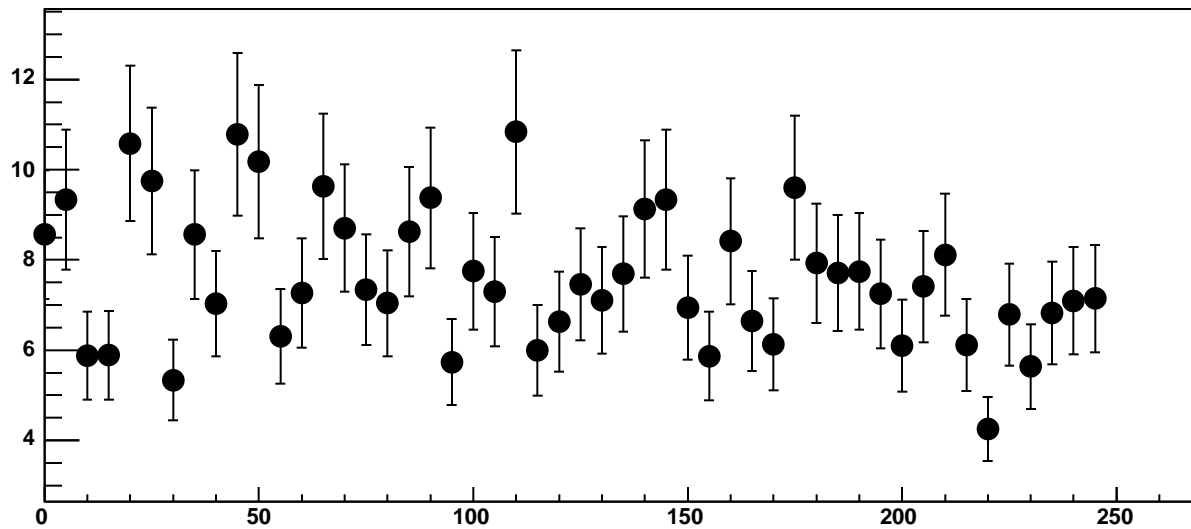


Chip 8, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

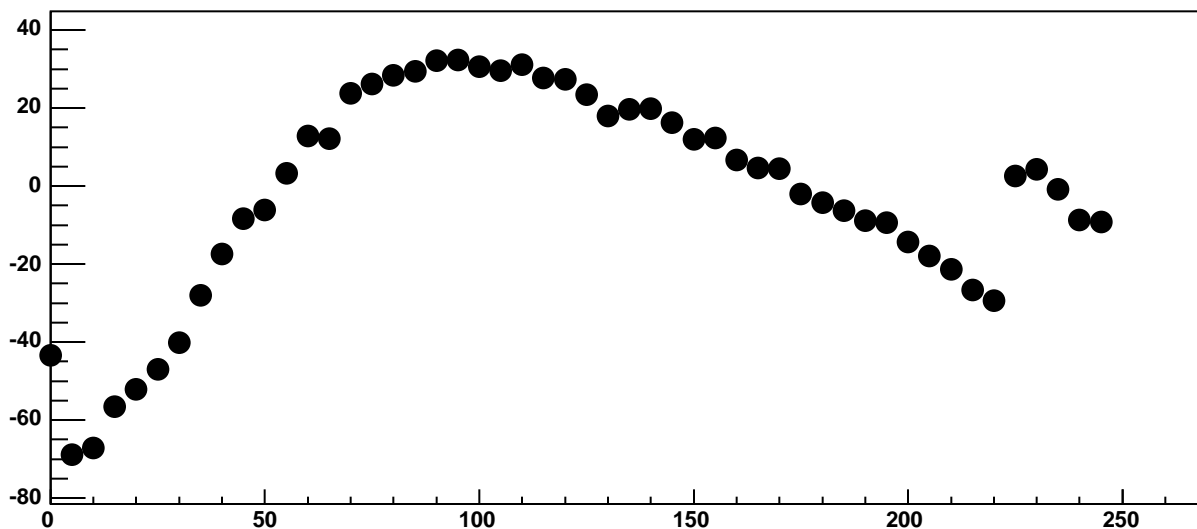


χ^2 / ndf	9919 / 41
p0	256.4 ± 2.571
p1	221.3 ± 5.447
p2	-37.39 ± 2.906
p3	-2.362 ± 10.1
p4	0.5391 ± 0.002863

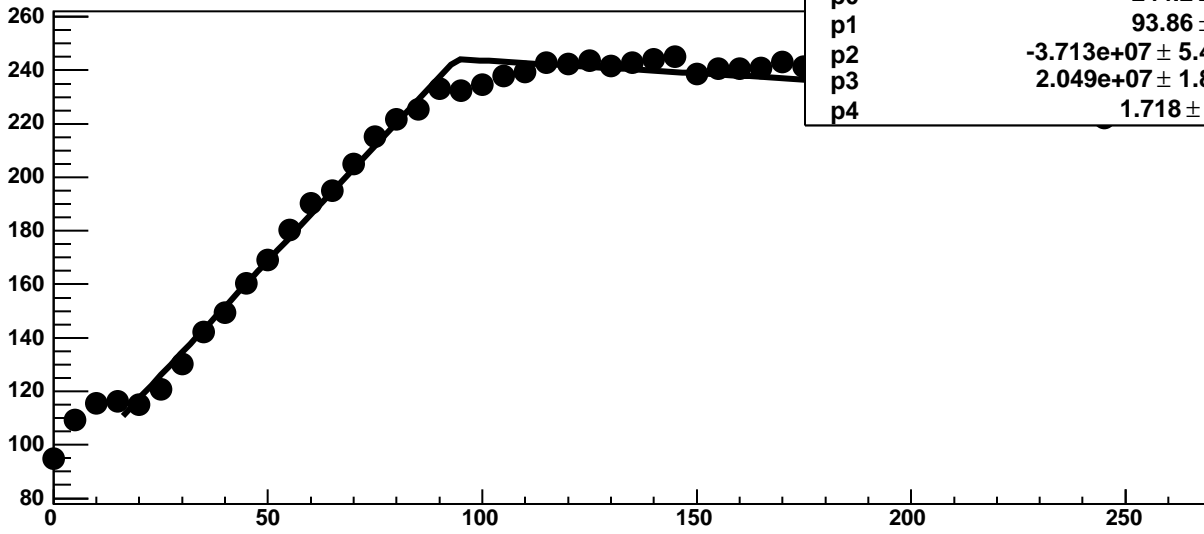
Chip 8, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

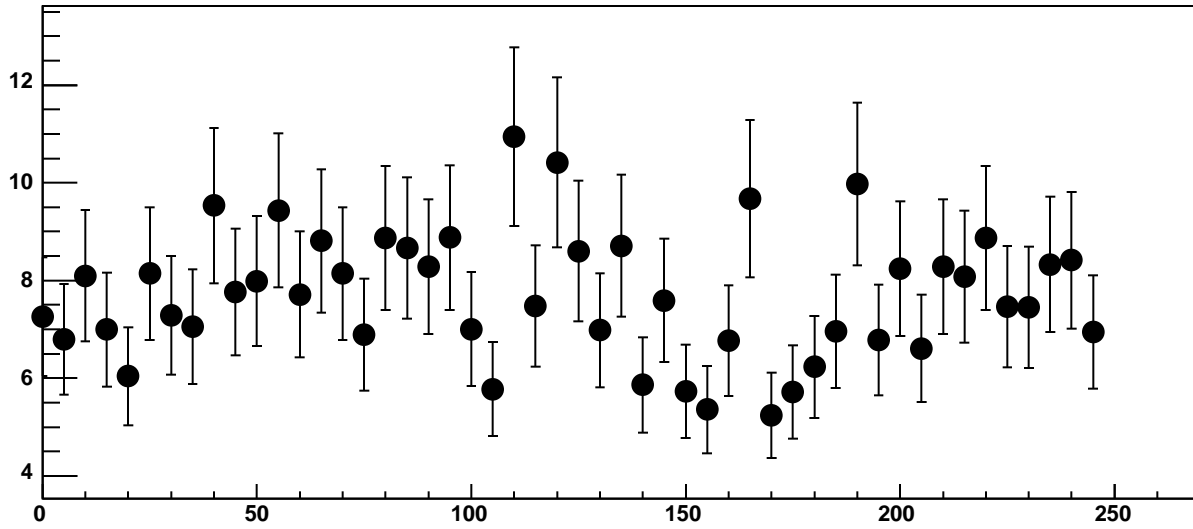


Chip 8, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

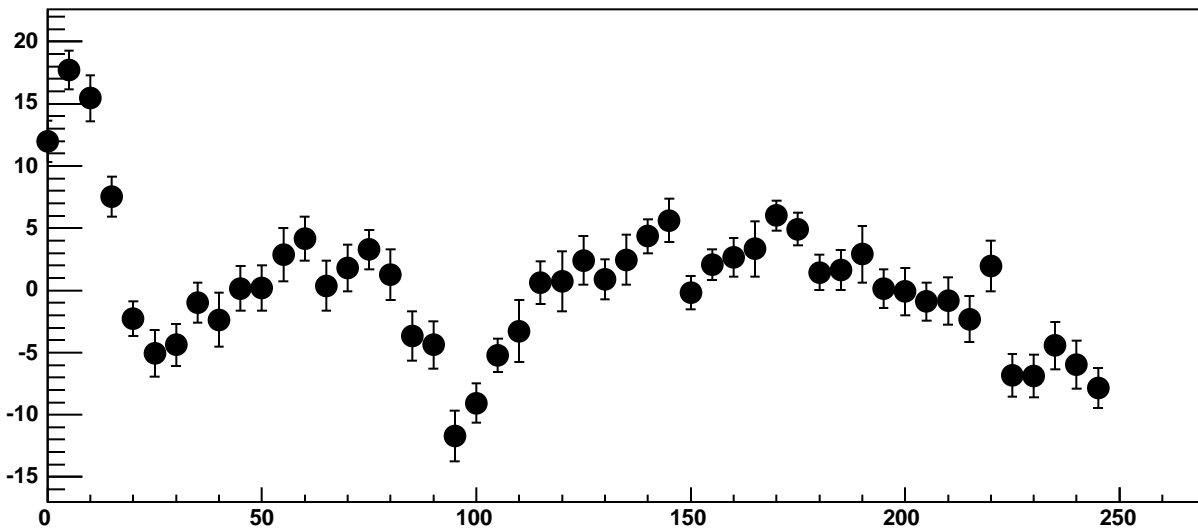


χ^2 / ndf	269.4 / 41
p0	244.2 ± 0.5915
p1	93.86 ± 0.6266
p2	$-3.713\text{e}+07 \pm 5.466\text{e}+05$
p3	$2.049\text{e}+07 \pm 1.894\text{e}+05$
p4	1.718 ± 0.01889

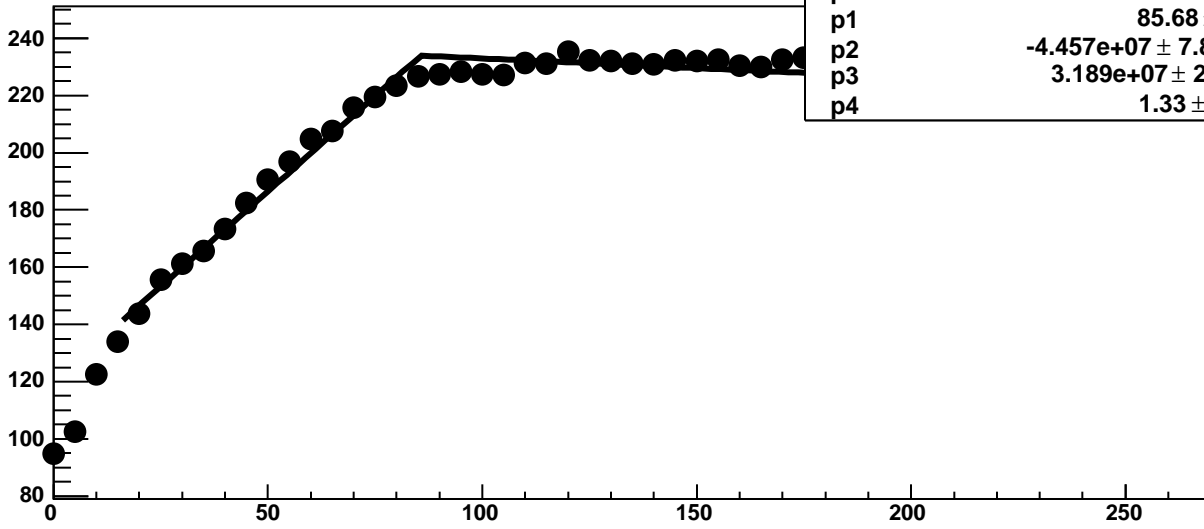
Chip 8, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

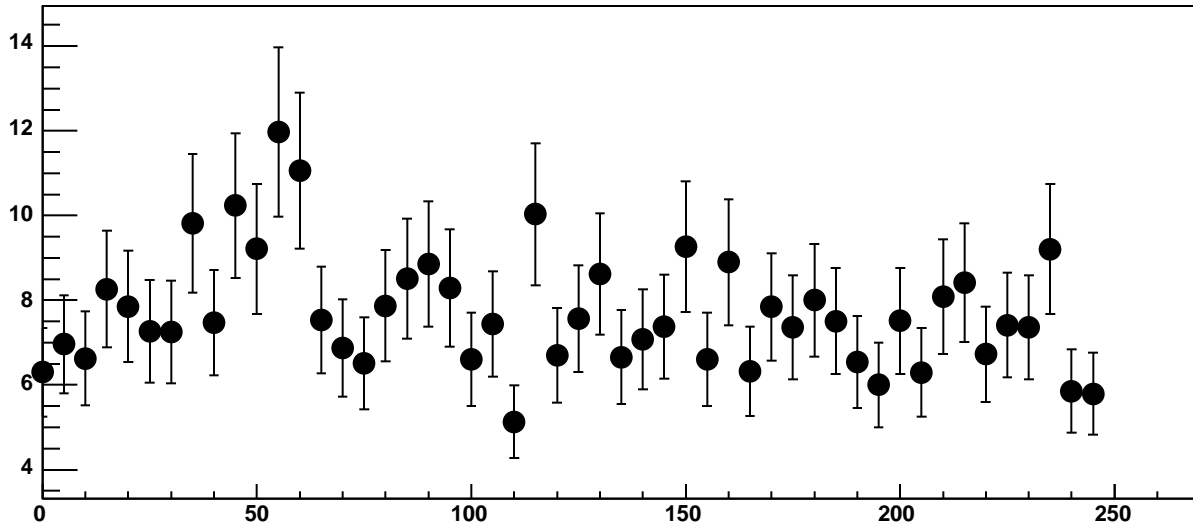


Chip 8, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold

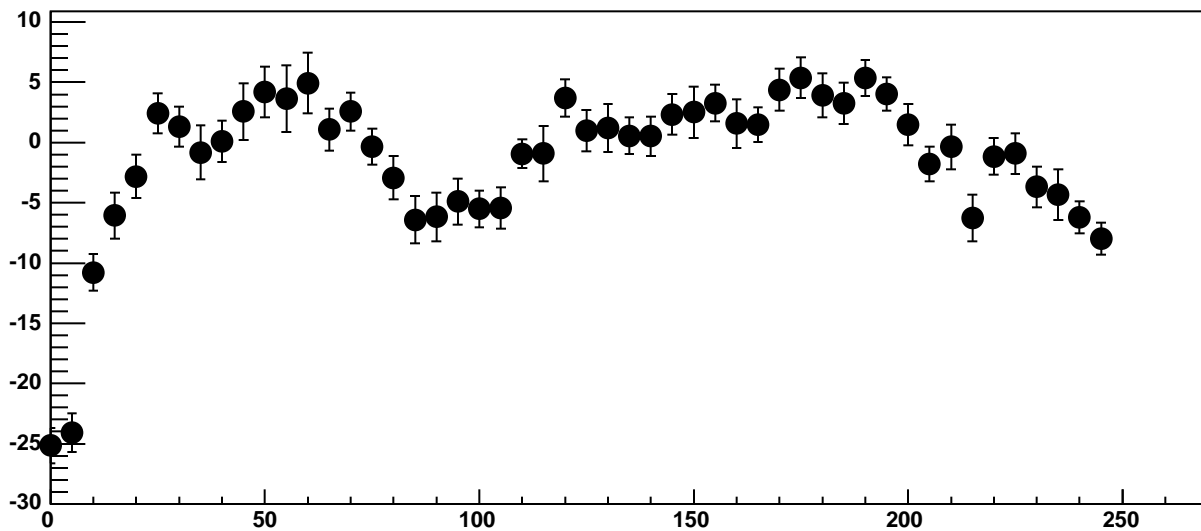


χ^2 / ndf	189 / 41
p0	233.9 ± 0.5881
p1	85.68 ± 0.7781
p2	$-4.457\text{e}+07 \pm 7.808\text{e}+05$
p3	$3.189\text{e}+07 \pm 2.61\text{e}+05$
p4	1.33 ± 0.02111

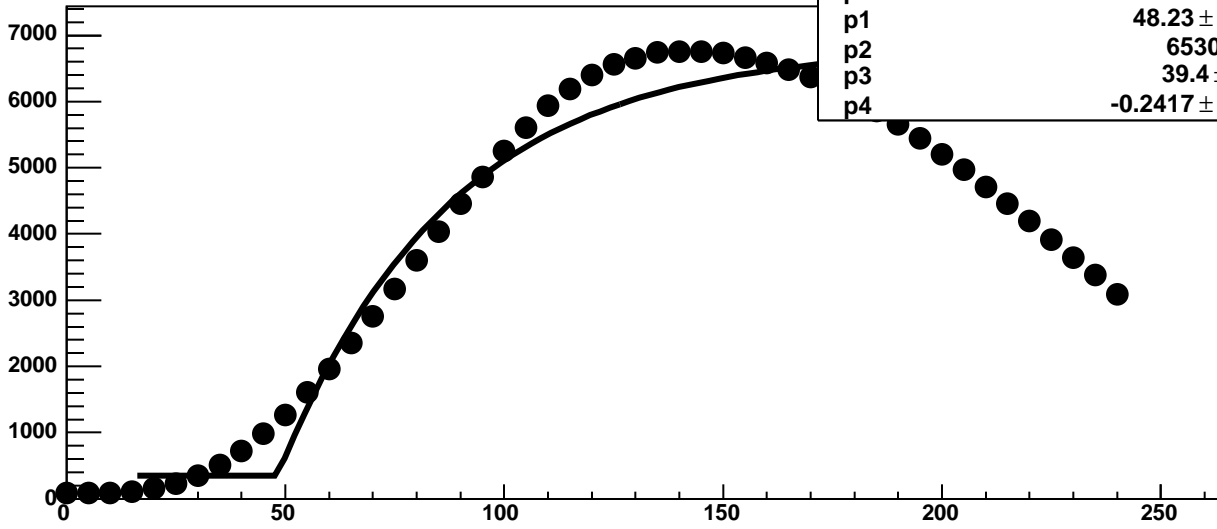
Chip 8, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold

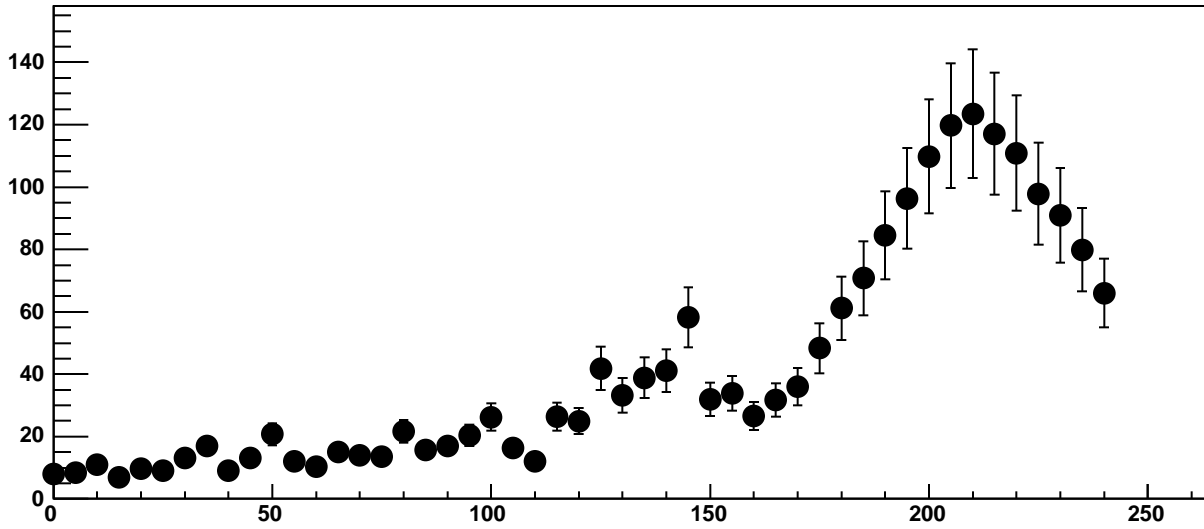


Chip 8, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold

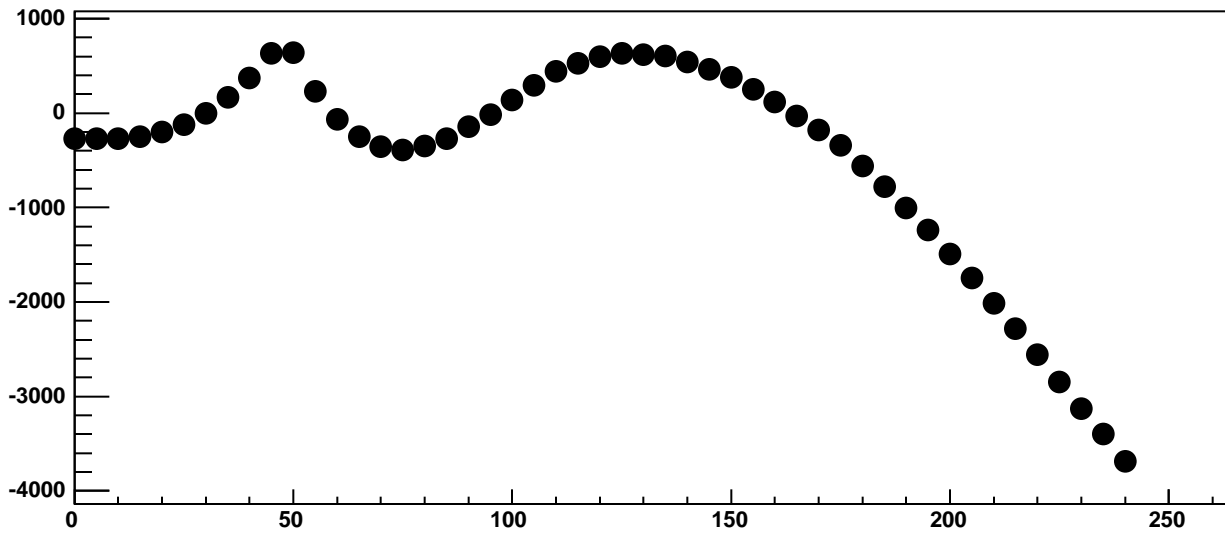


χ^2 / ndf	4.321e+05 / 41
p0	347.2 ± 1.522
p1	48.23 ± 0.01817
p2	6530 ± 9.564
p3	39.4 ± 0.0619
p4	-0.2417 ± 0.05832

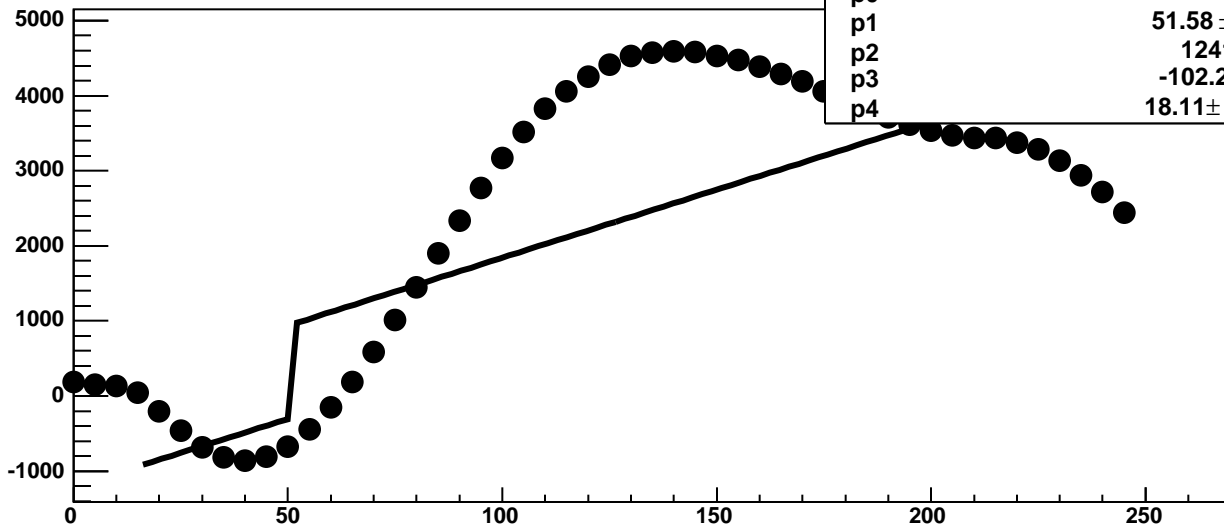
Chip 8, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



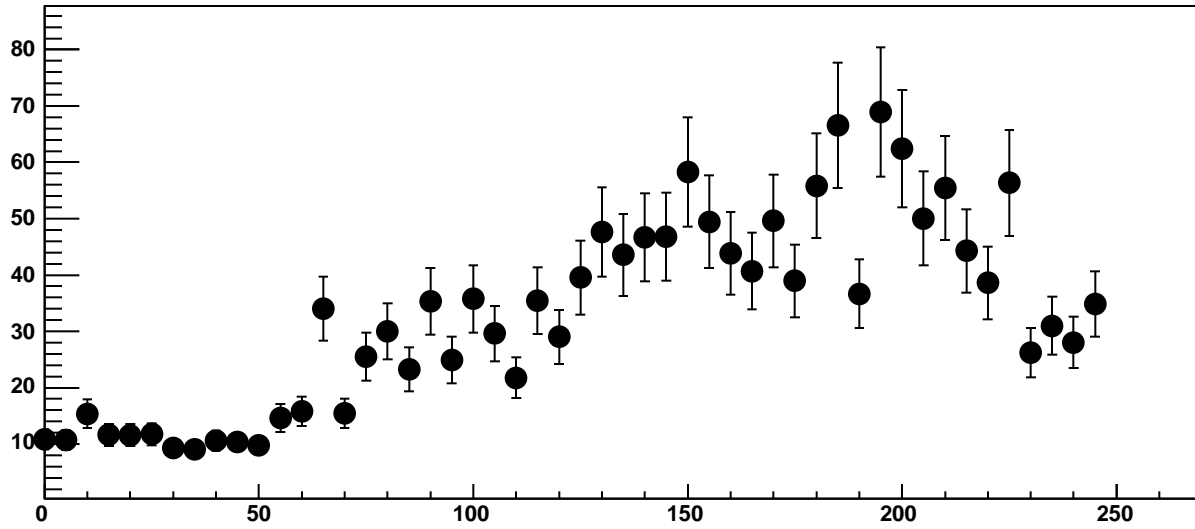
Chip 8, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold



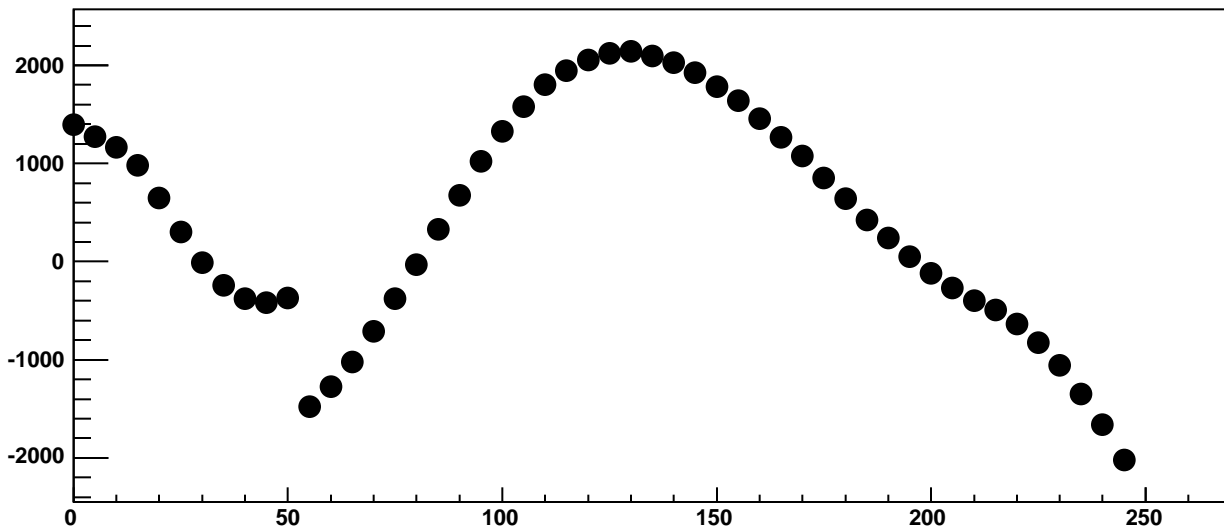
Chip 8, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold



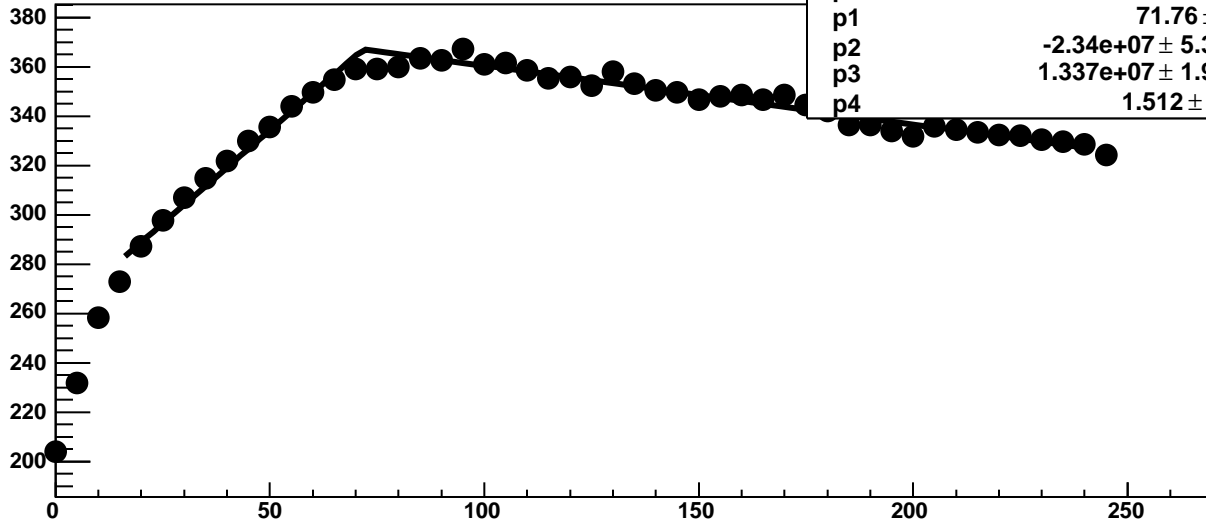
Chip 8, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

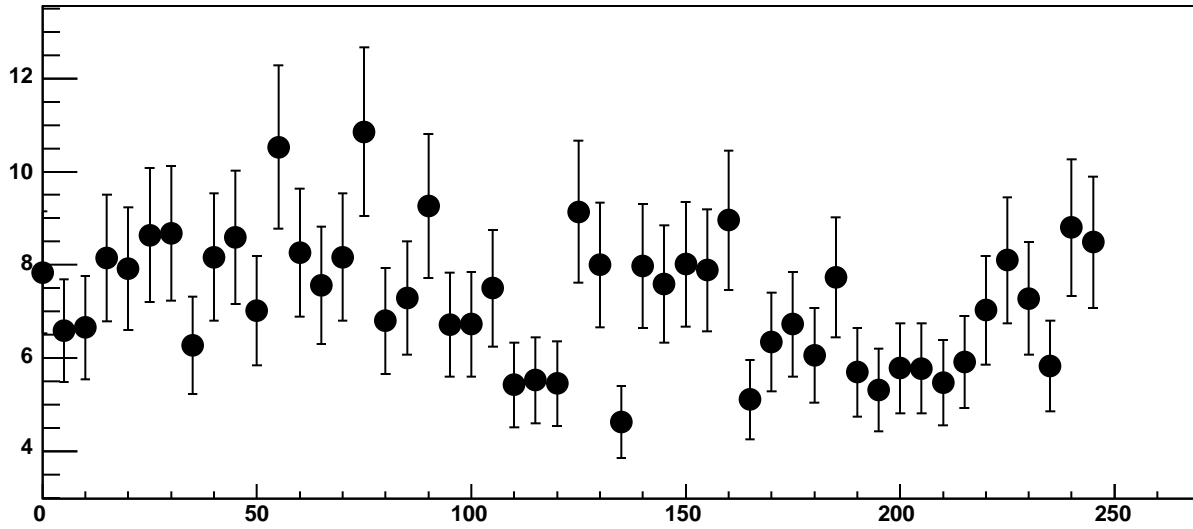


Chip 8, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold

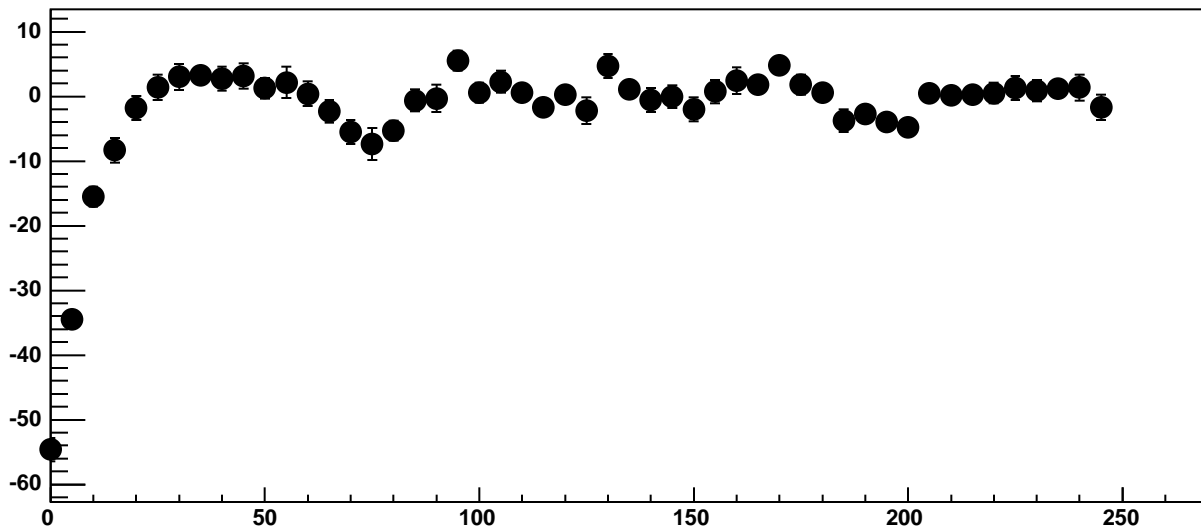


χ^2 / ndf	143.1 / 41
p0	367.2 ± 0.5017
p1	71.76 ± 0.6828
p2	$-2.34\text{e}+07 \pm 5.363\text{e}+05$
p3	$1.337\text{e}+07 \pm 1.983\text{e}+05$
p4	1.512 ± 0.03109

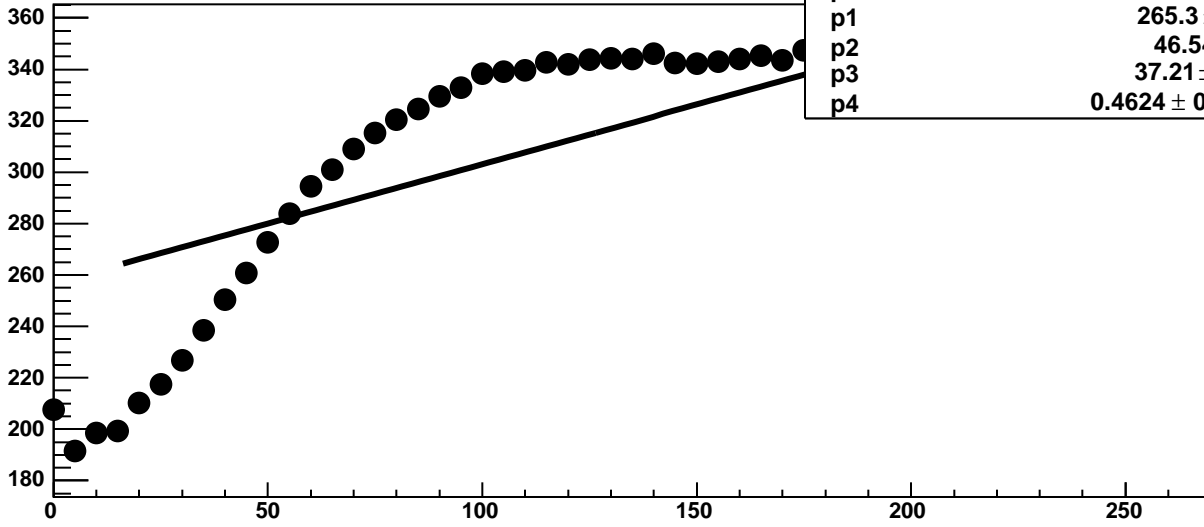
Chip 8, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold

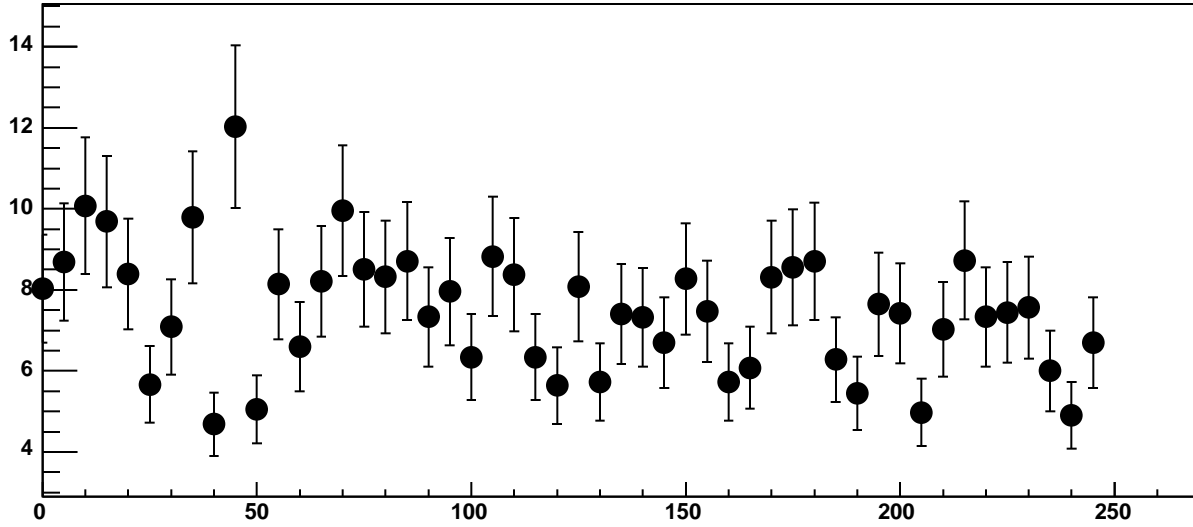


Chip 8, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

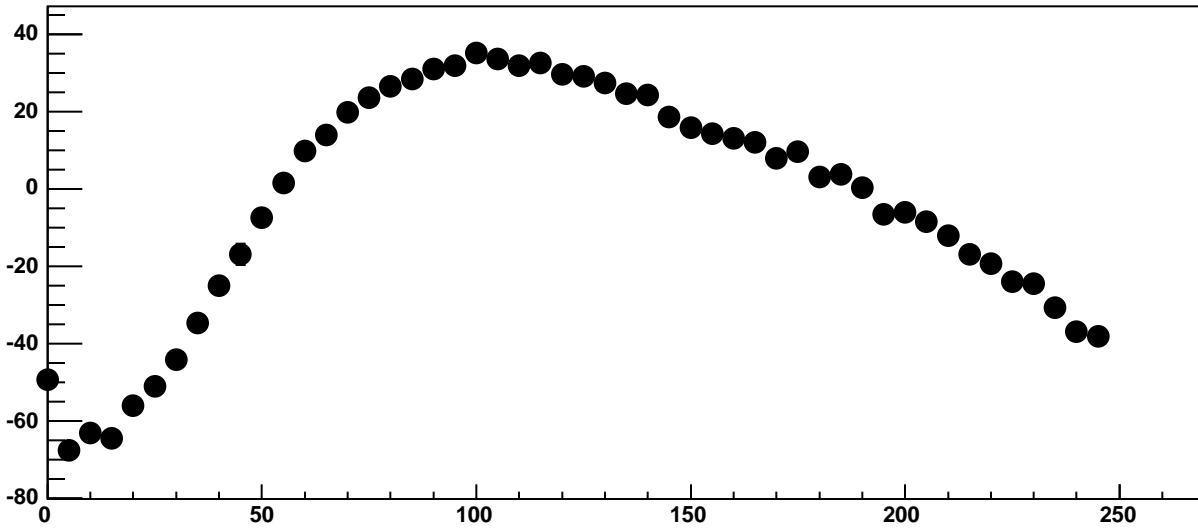


χ^2 / ndf	1.232e+04 / 41
p0	379.6 ± 0.4541
p1	265.3 ± 0.4371
p2	46.54 ± 1.92
p3	37.21 ± 0.1734
p4	0.4624 ± 0.003469

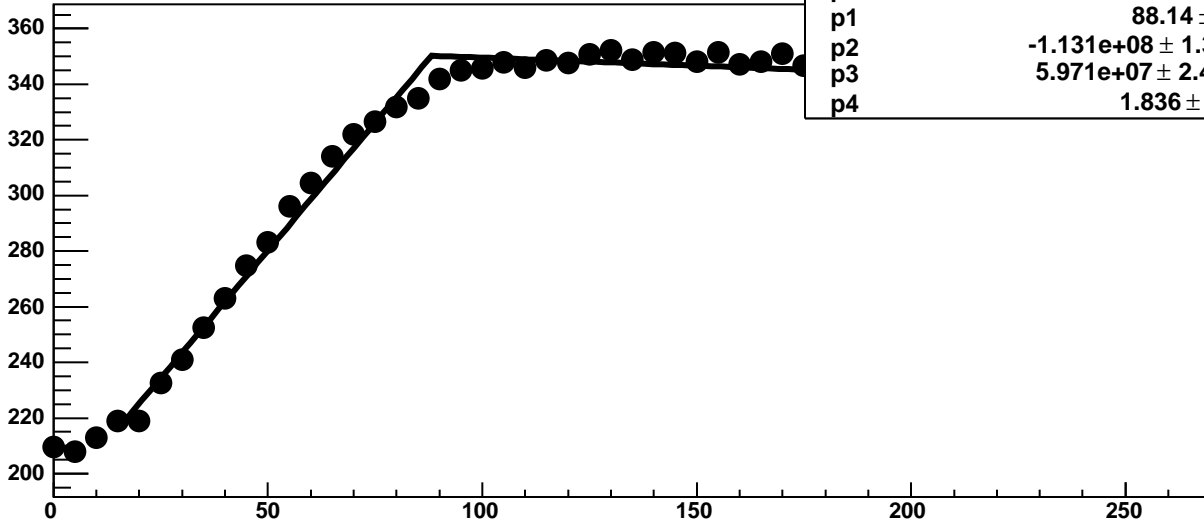
Chip 8, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

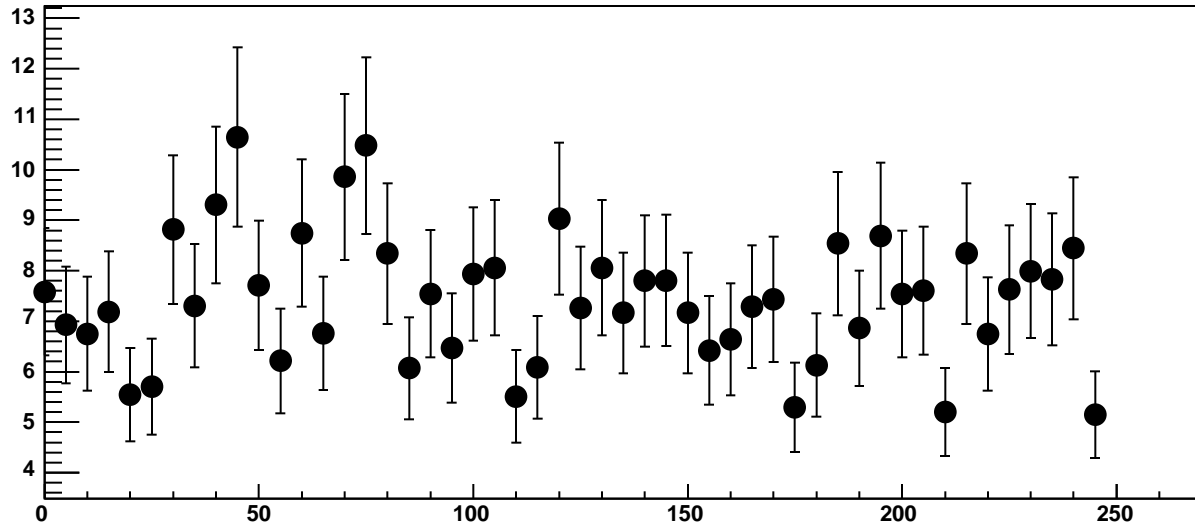


Chip 8, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

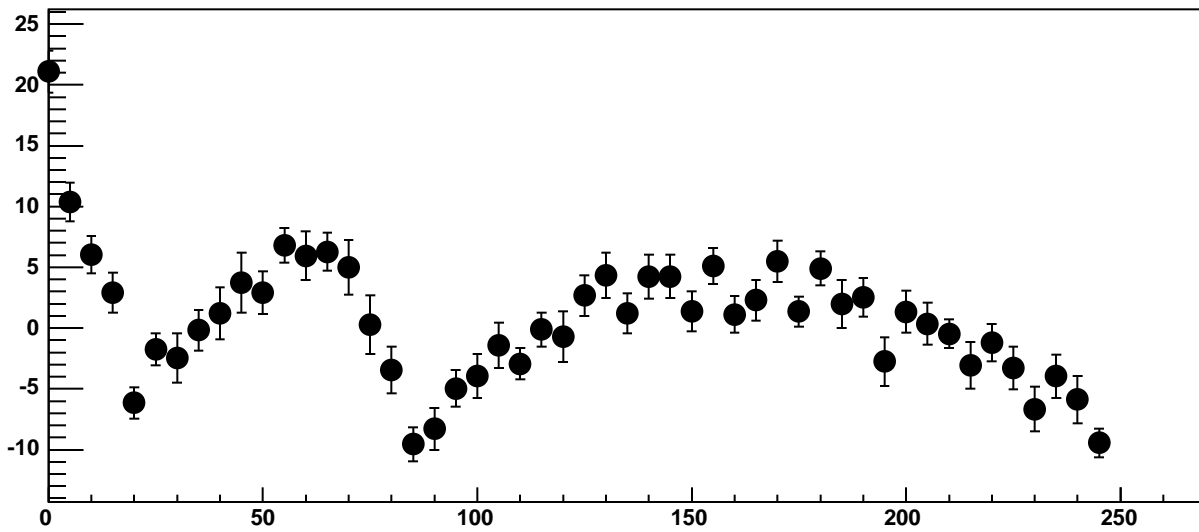


χ^2 / ndf	282.2 / 41
p0	350.2 ± 0.5706
p1	88.14 ± 0.5665
p2	$-1.131\text{e}+08 \pm 1.323\text{e}+06$
p3	$5.971\text{e}+07 \pm 2.453\text{e}+05$
p4	1.836 ± 0.01917

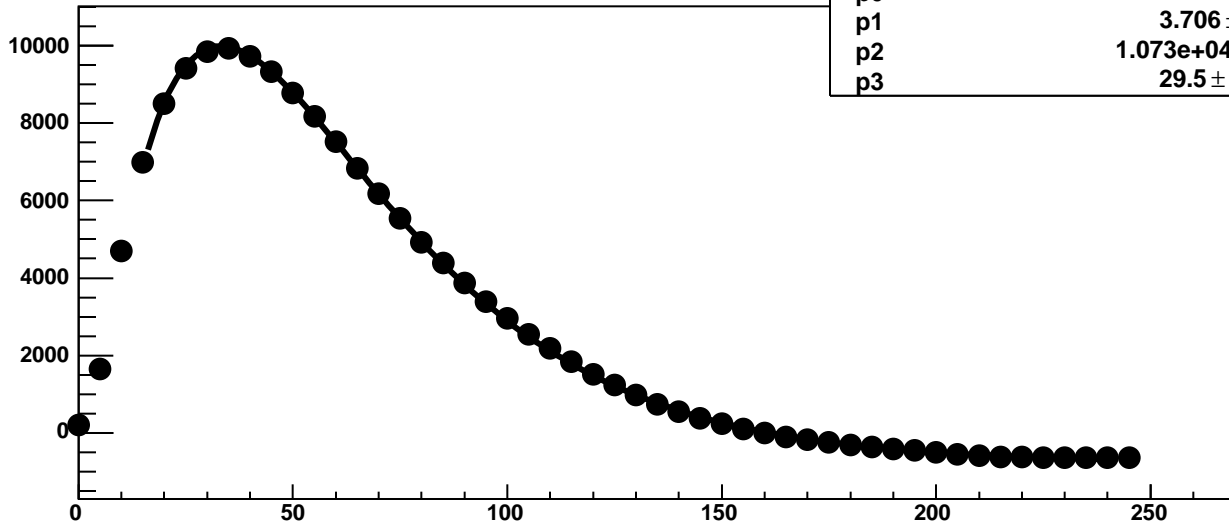
Chip 8, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

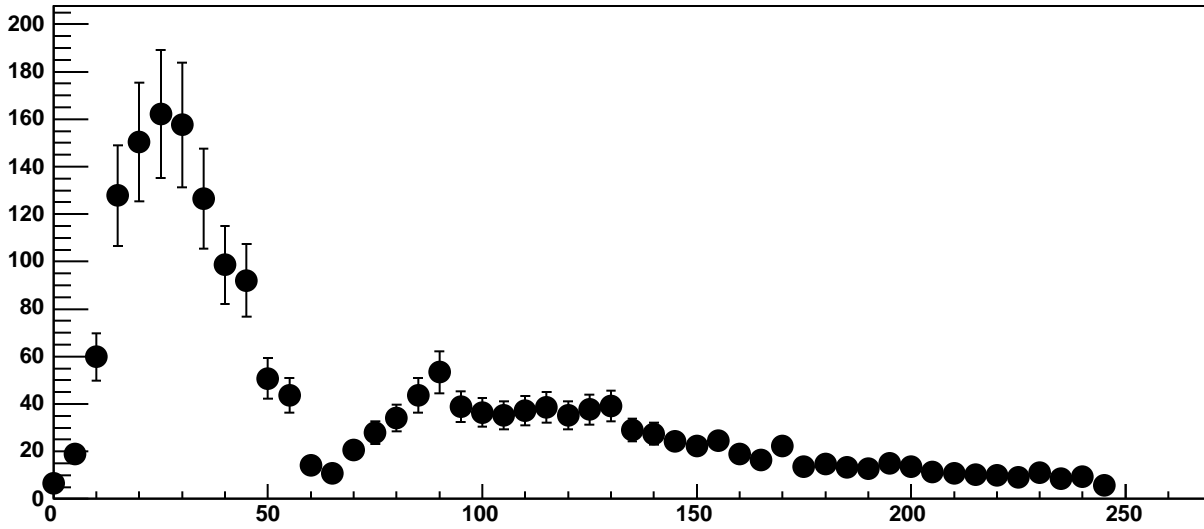


Chip 8, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold

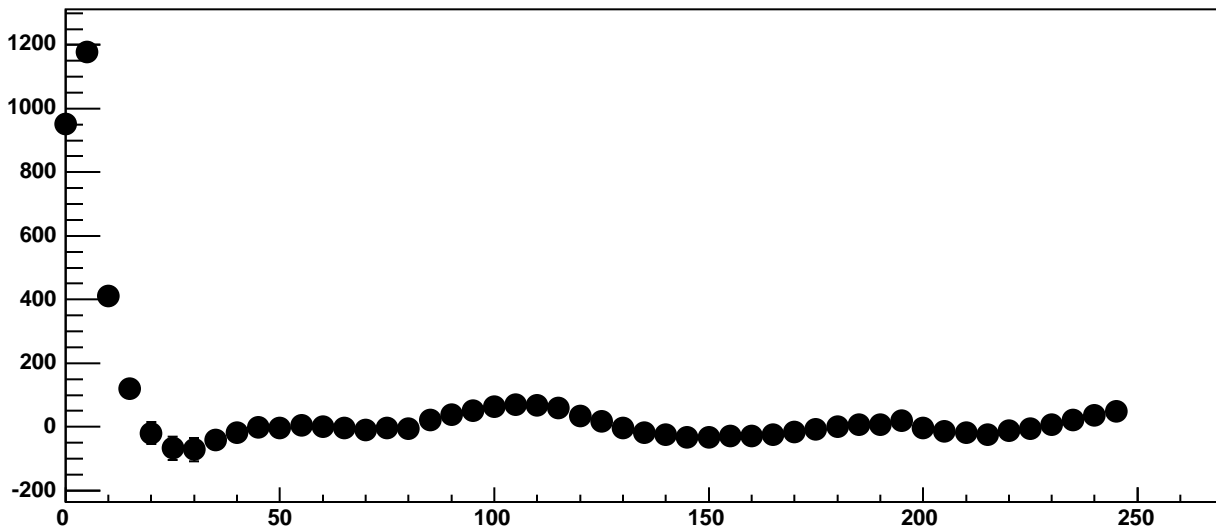


χ^2 / ndf	1261 / 42
p0	-748 ± 0.9733
p1	3.706 ± 0.0503
p2	1.073e+04 ± 6.765
p3	29.5 ± 0.01939

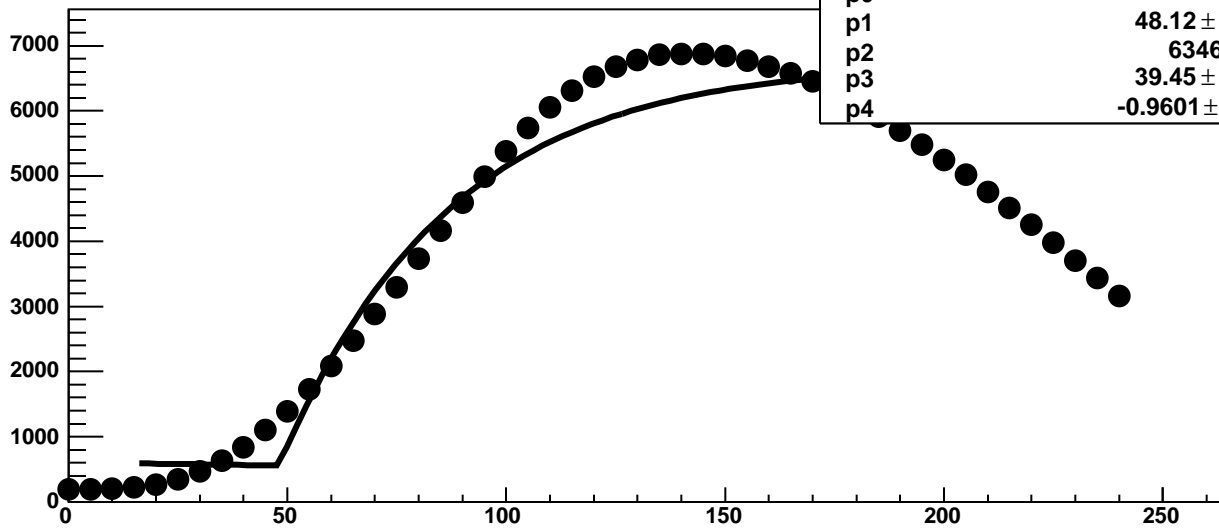
Chip 8, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold

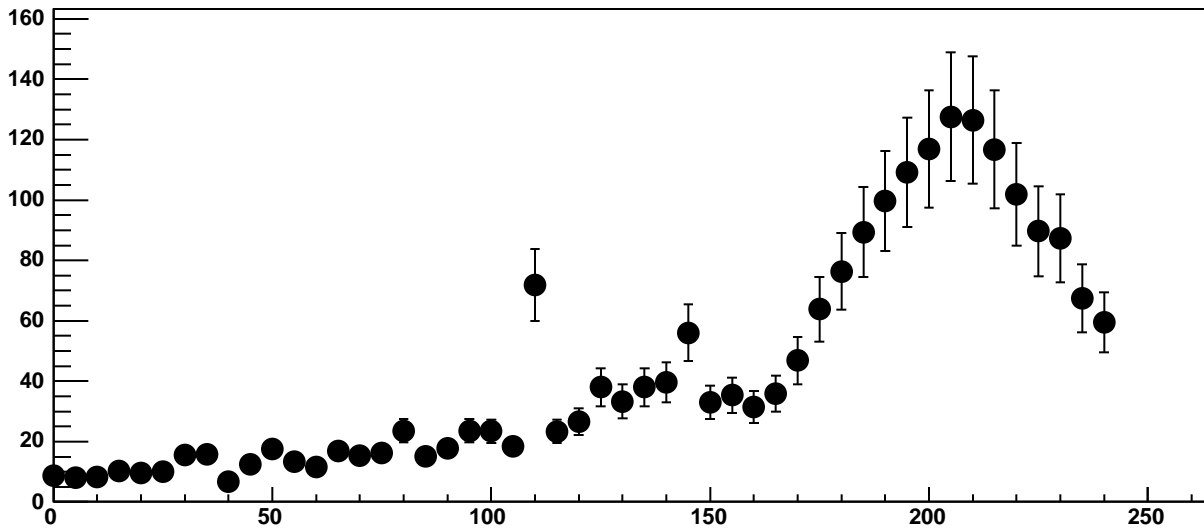


Chip 8, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold

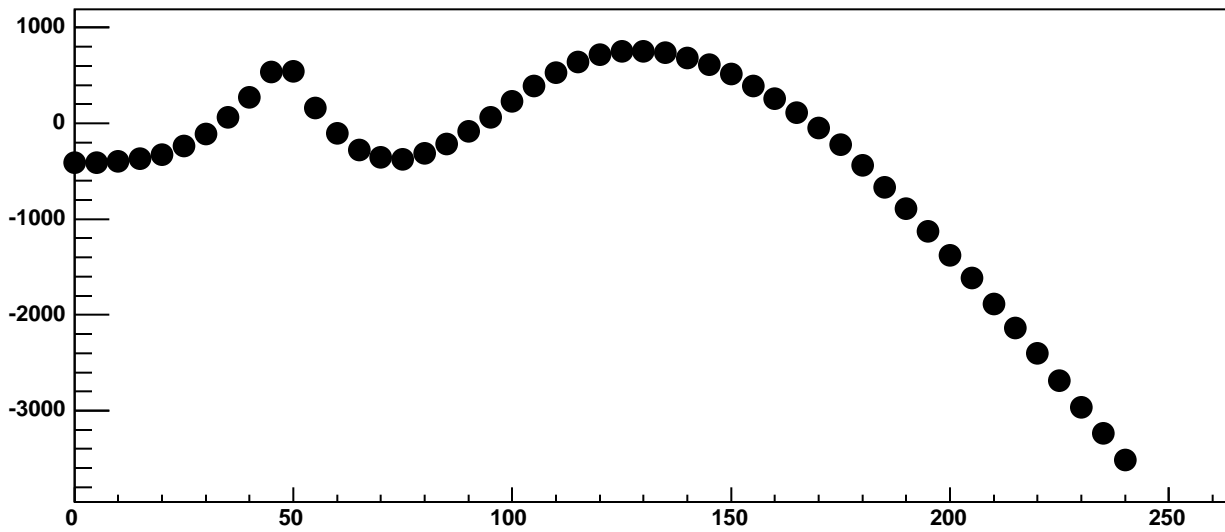


χ^2 / ndf	4.406e+05 / 41
p0	558.5 ± 1.362
p1	48.12 ± 0.01839
p2	6346 ± 9.665
p3	39.45 ± 0.06643
p4	-0.9601 ± 0.05991

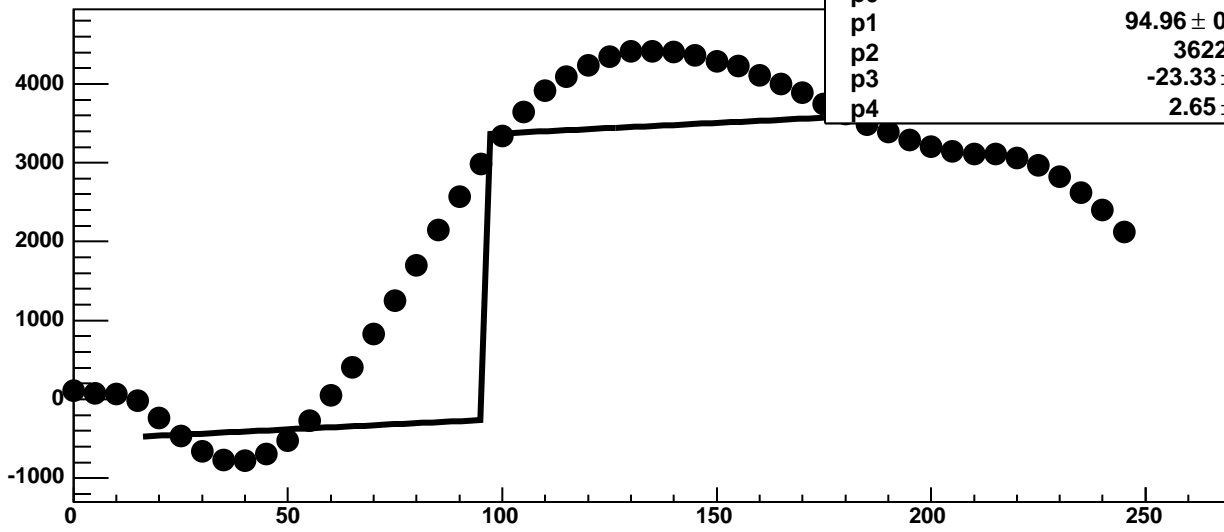
Chip 8, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold

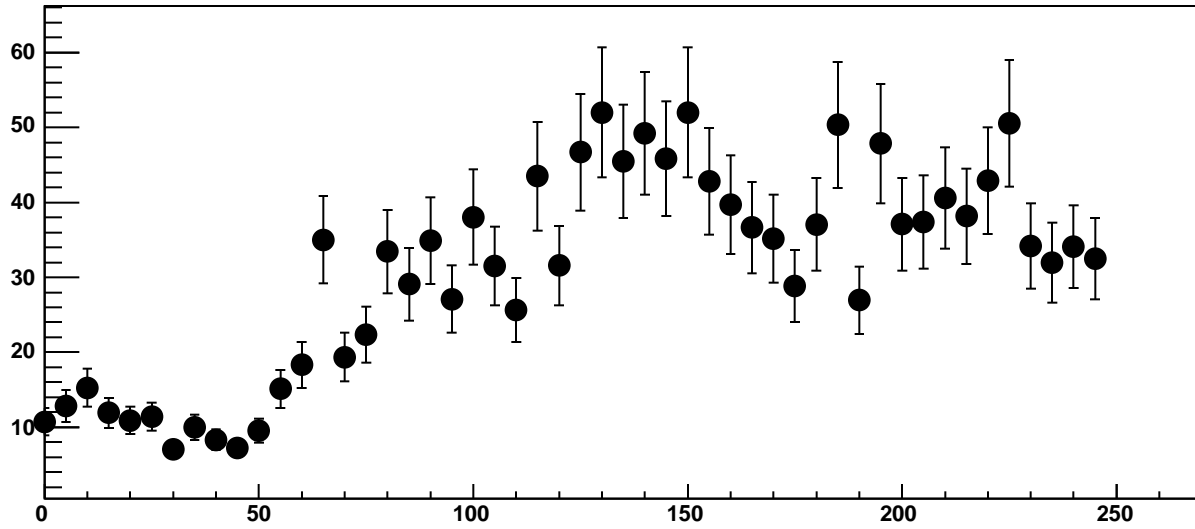


Chip 8, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold

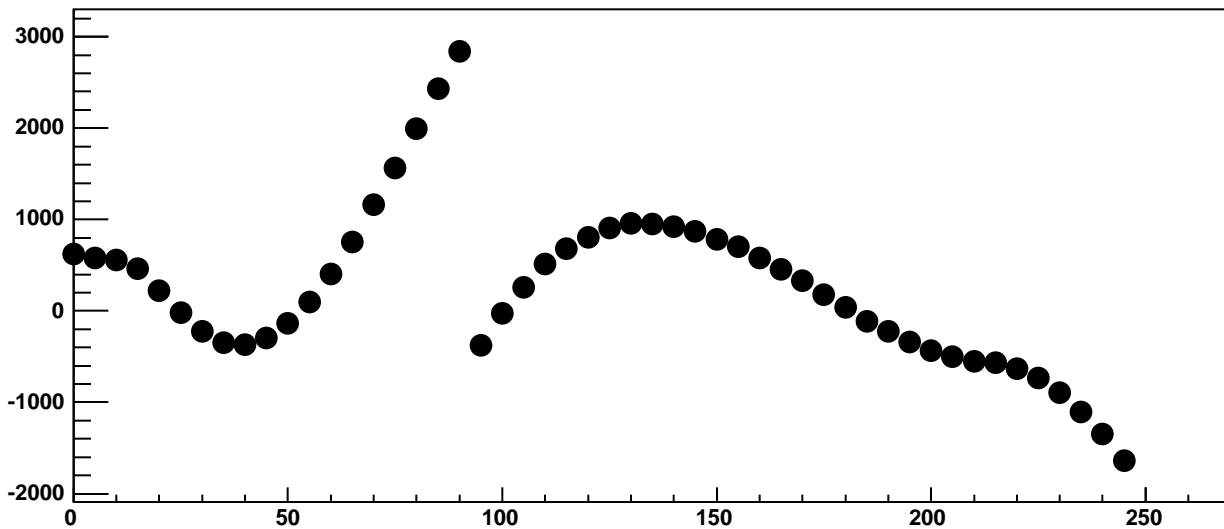


χ^2 / ndf	8.346e+05 / 41
p0	-262.8 ± 8.381
p1	94.96 ± 0.001846
p2	3622 ± 13.34
p3	-23.33 ± 0.6449
p4	2.65 ± 0.1149

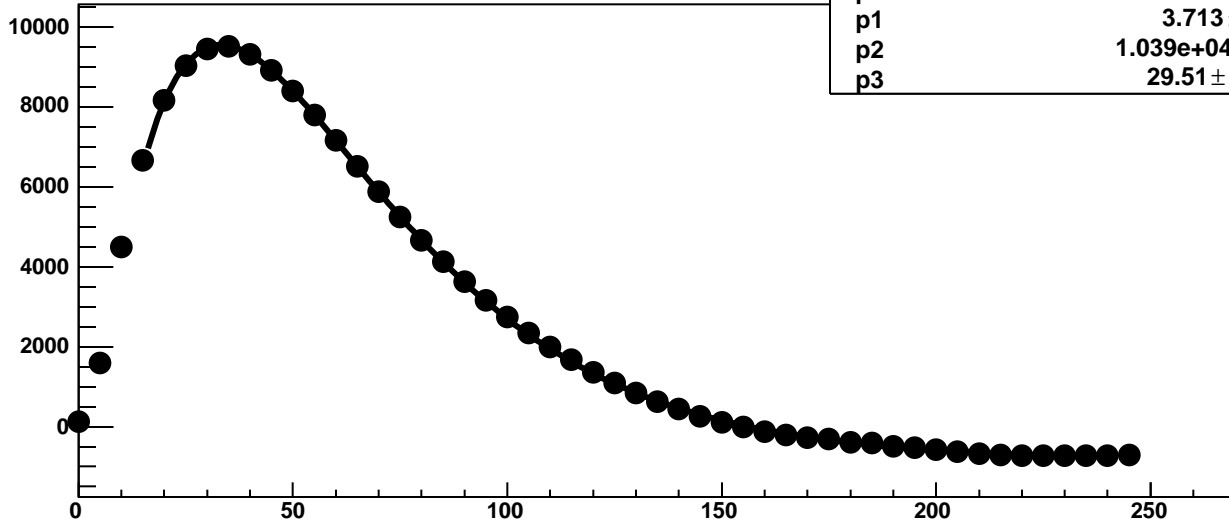
Chip 8, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

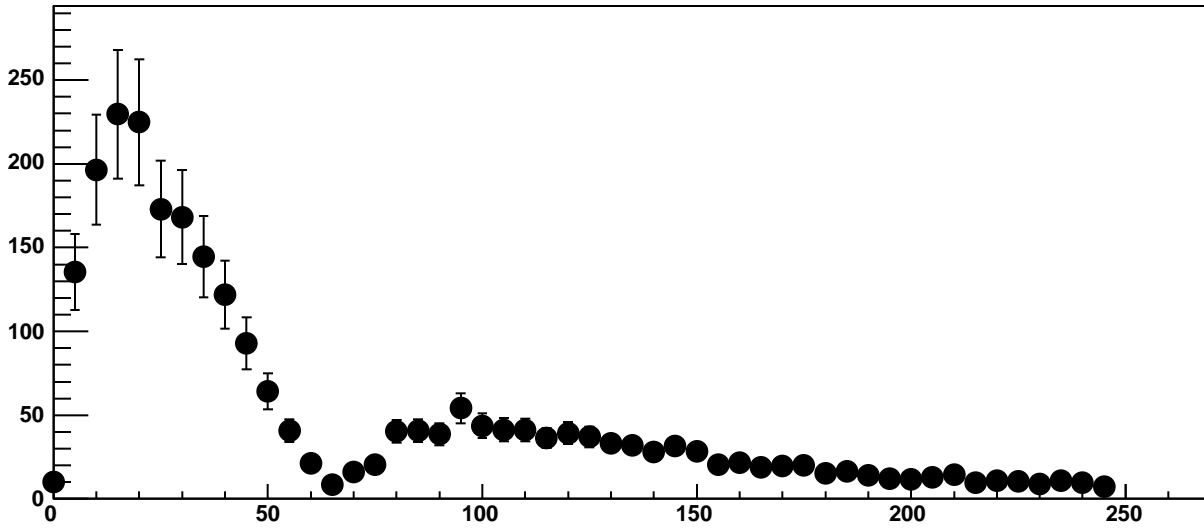


Chip 8, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold

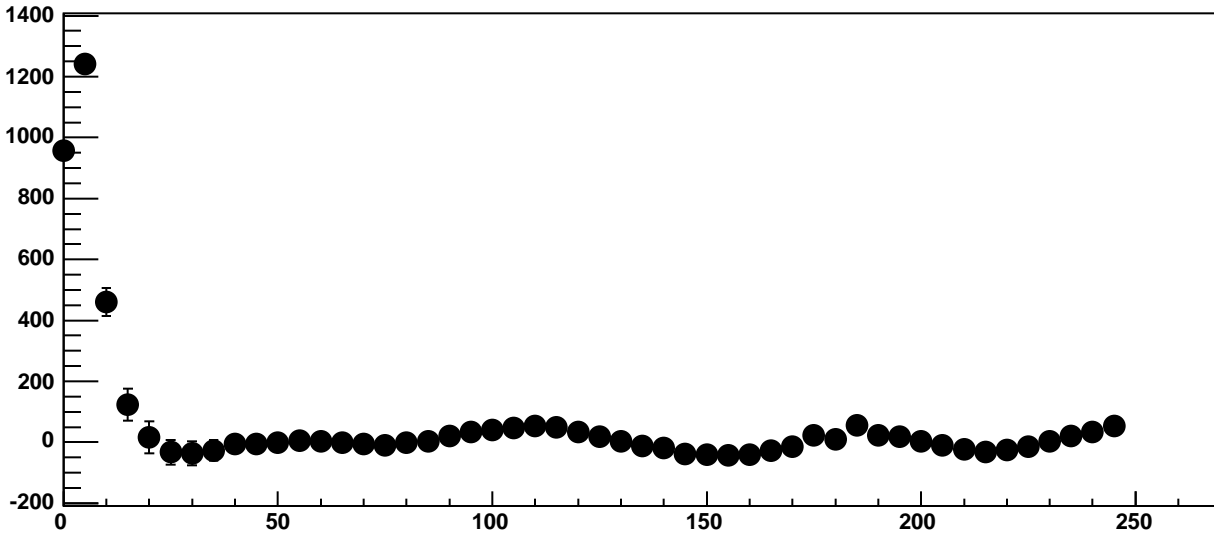


χ^2 / ndf	1469 / 42
p0	-828.6 ± 1.057
p1	3.713 ± 0.0751
p2	$1.039\text{e}+04 \pm 9.346$
p3	29.51 ± 0.02354

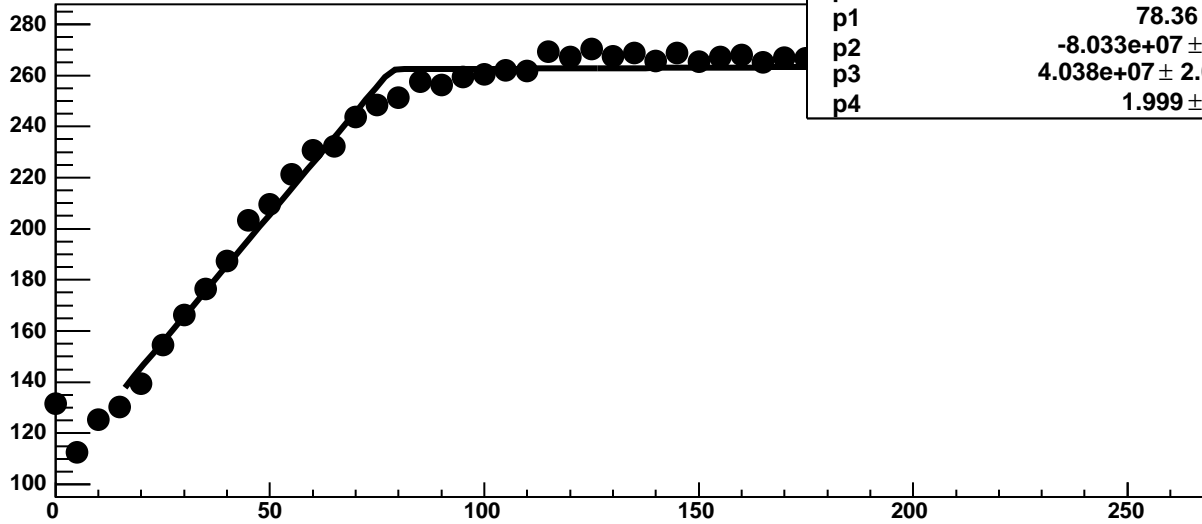
Chip 8, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold

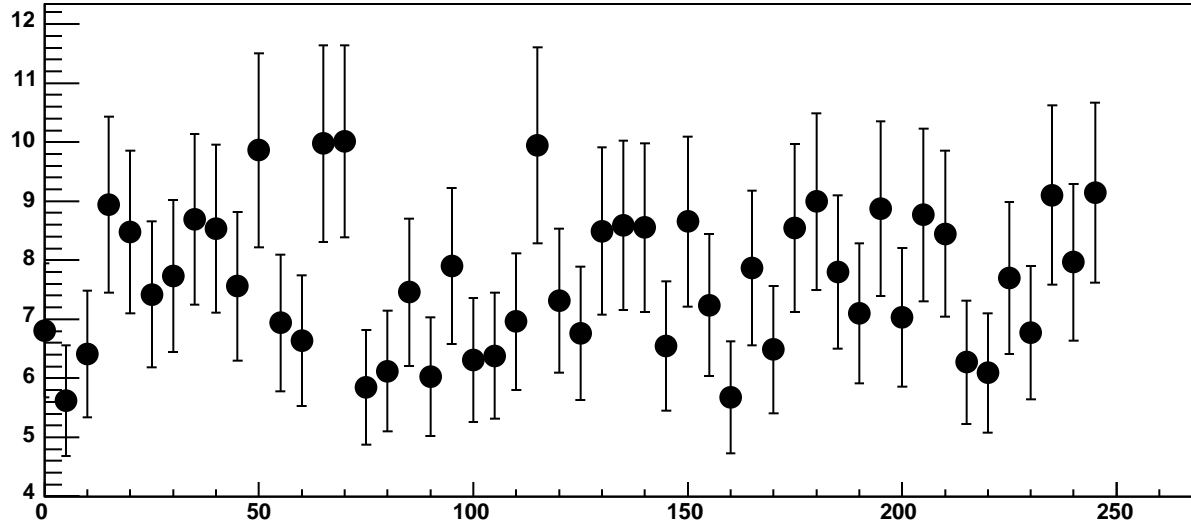


Chip 8, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

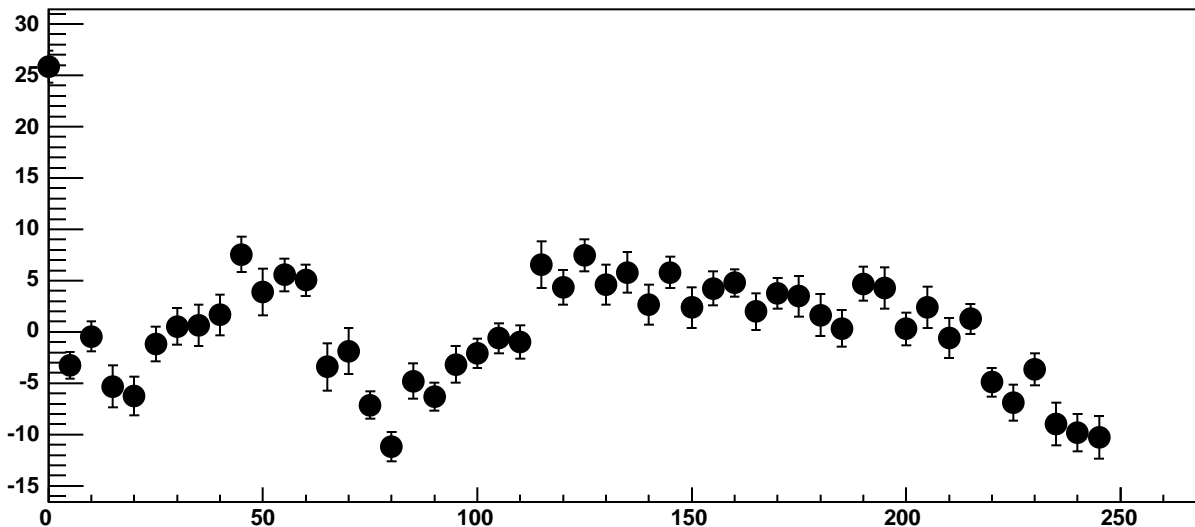


χ^2 / ndf	390.5 / 41
p0	262.3 ± 0.5597
p1	78.36 ± 0.5589
p2	$-8.033\text{e}+07 \pm 1.1\text{e}+06$
p3	$4.038\text{e}+07 \pm 2.061\text{e}+05$
p4	1.999 ± 0.02618

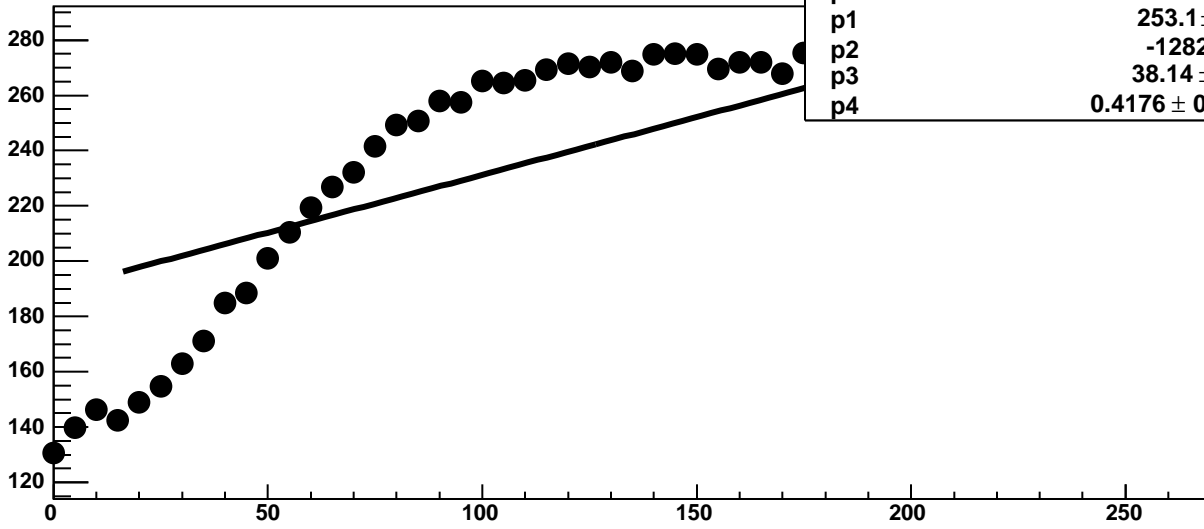
Chip 8, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

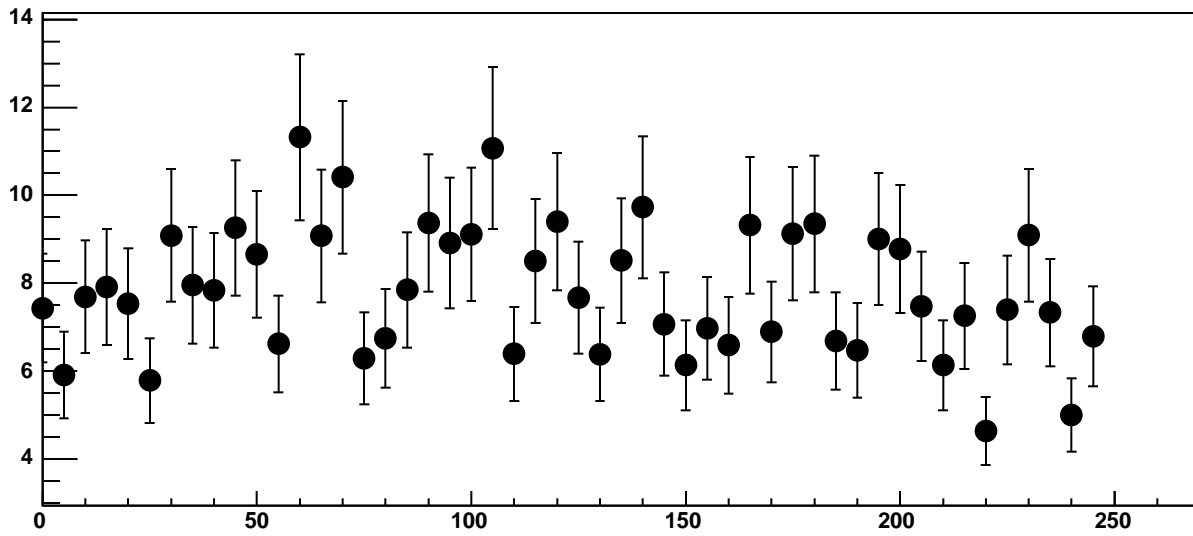


Chip 8, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

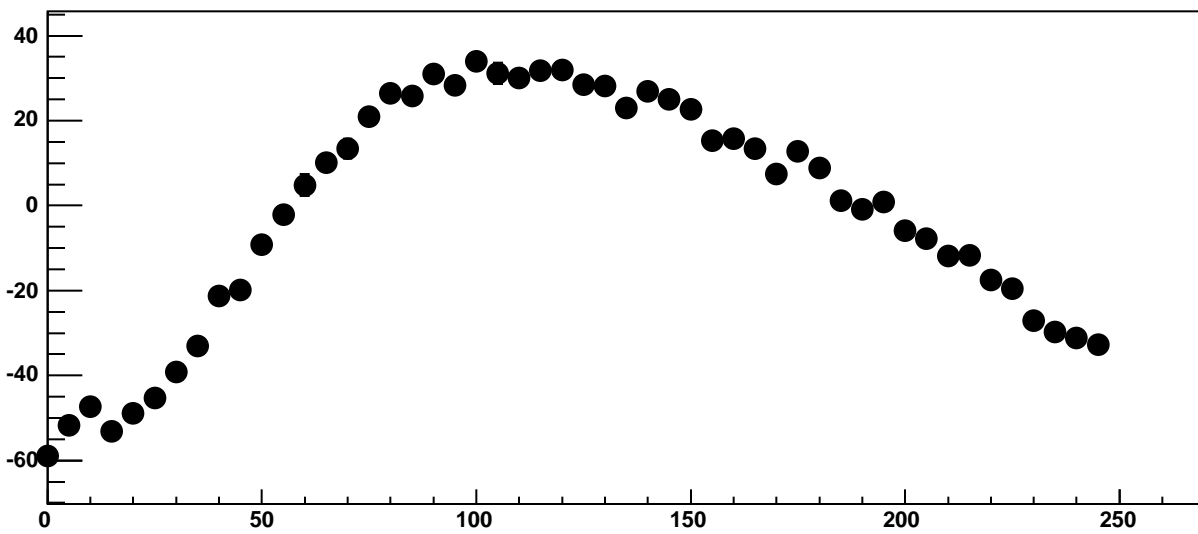


χ^2 / ndf	9738 / 41
p0	295.2 ± 0.3461
p1	253.1 ± 0.5578
p2	-1282 ± 20.37
p3	38.14 ± 0.5954
p4	0.4176 ± 0.003649

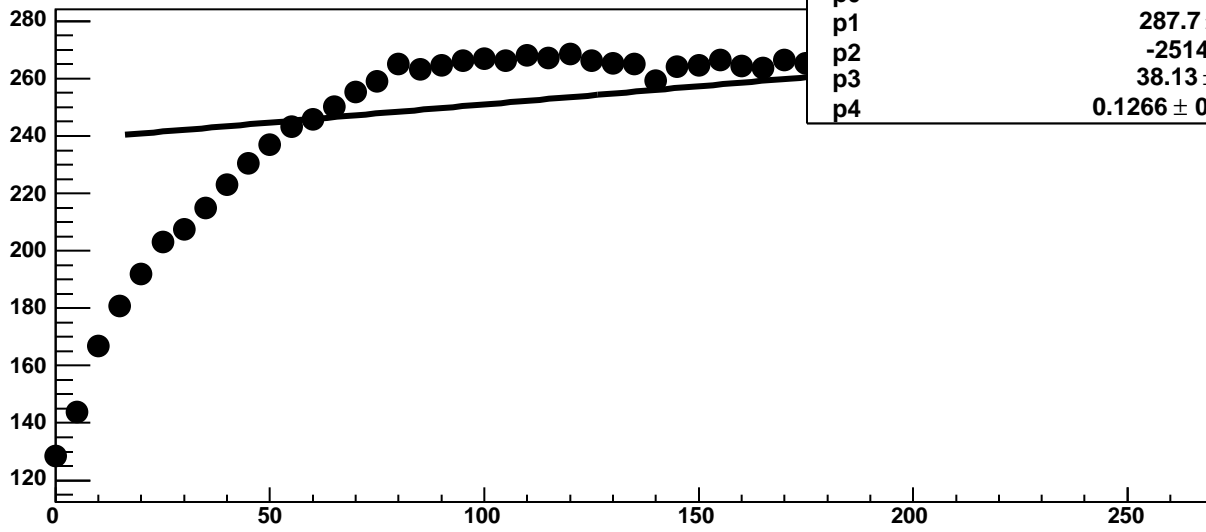
Chip 8, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

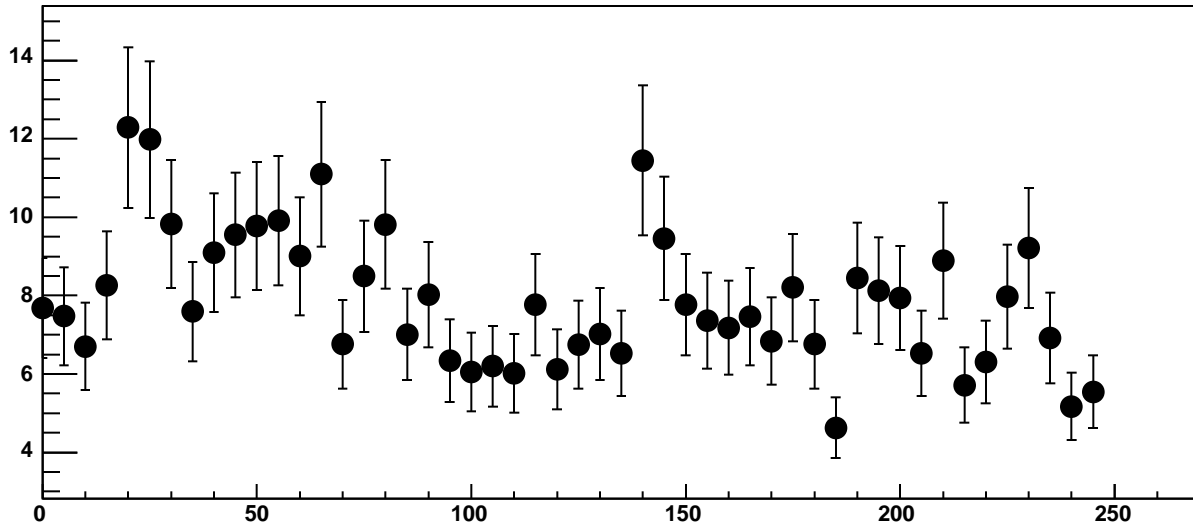


Chip 8, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold

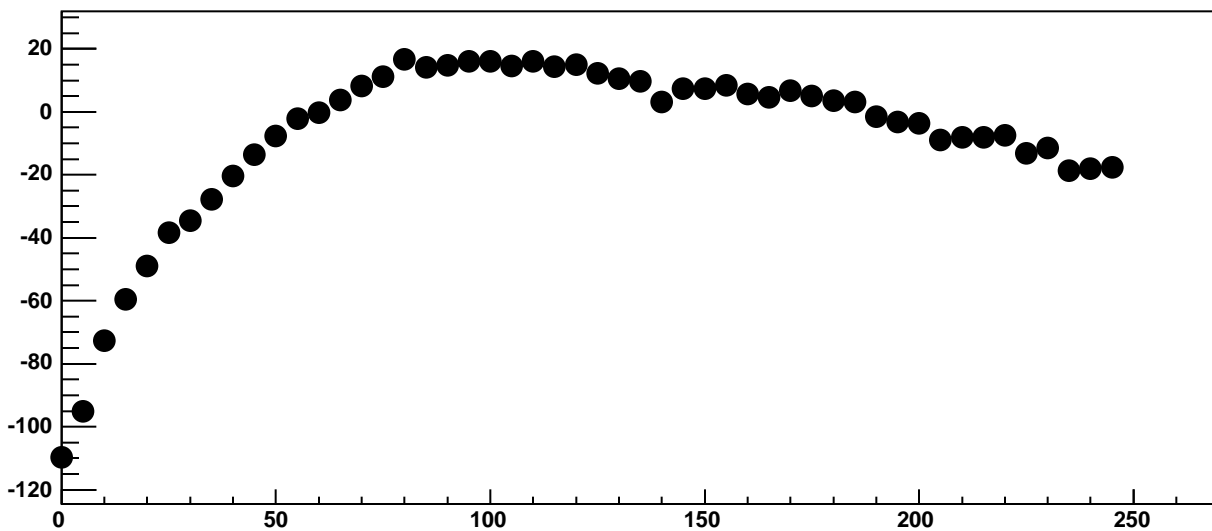


χ^2 / ndf	3883 / 41
p0	274.8 ± 0.5807
p1	287.7 ± 0.6511
p2	-2514 ± 37.93
p3	38.13 ± 0.8147
p4	0.1266 ± 0.003984

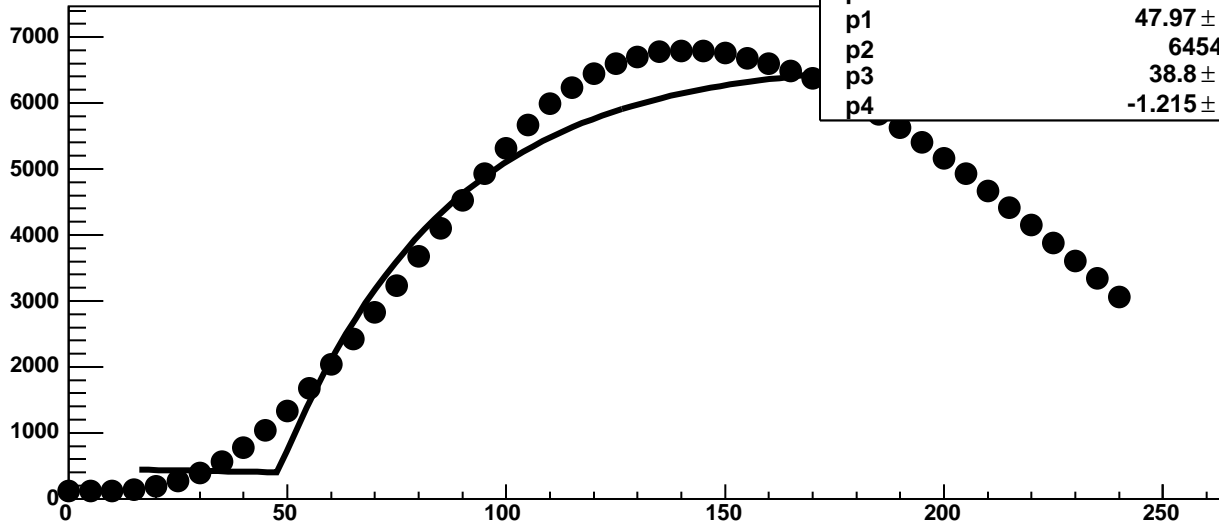
Chip 8, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold

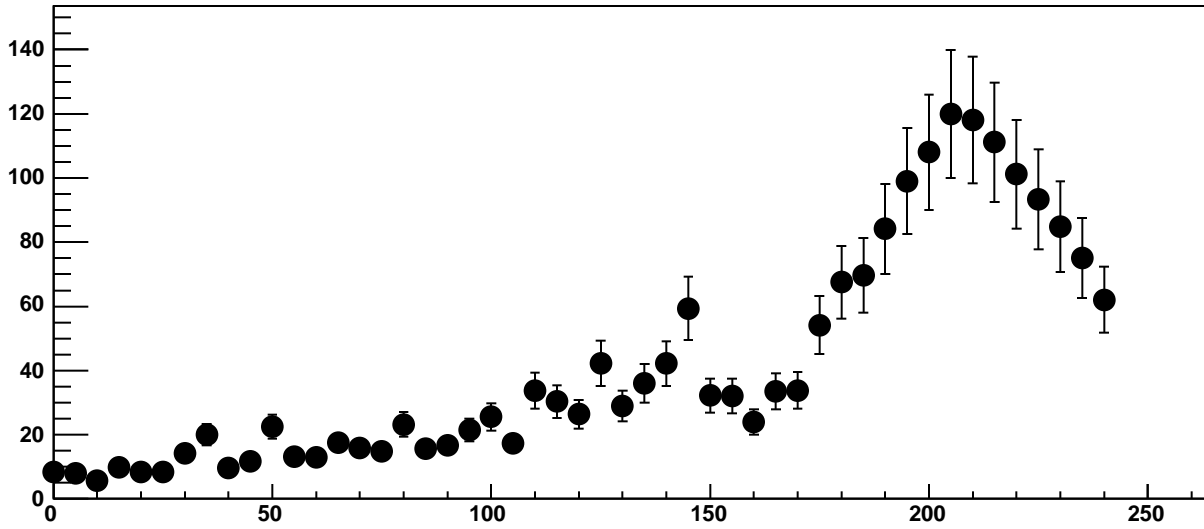


Chip 8, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

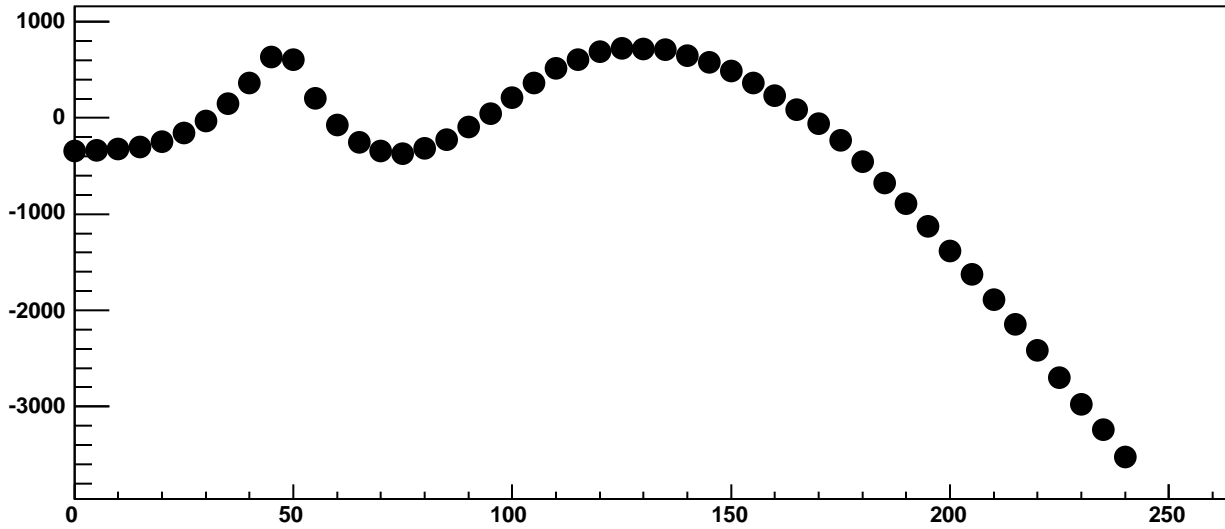


χ^2 / ndf	4.241e+05 / 41
p0	402.7 ± 1.519
p1	47.97 ± 0.01995
p2	6454 ± 10.02
p3	38.8 ± 0.06637
p4	-1.215 ± 0.06139

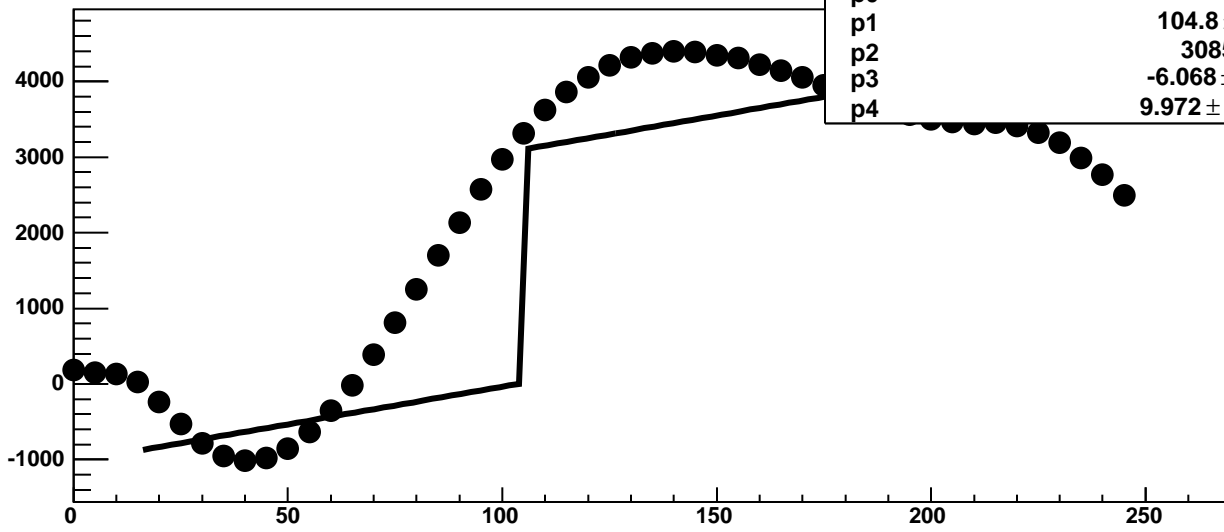
Chip 8, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

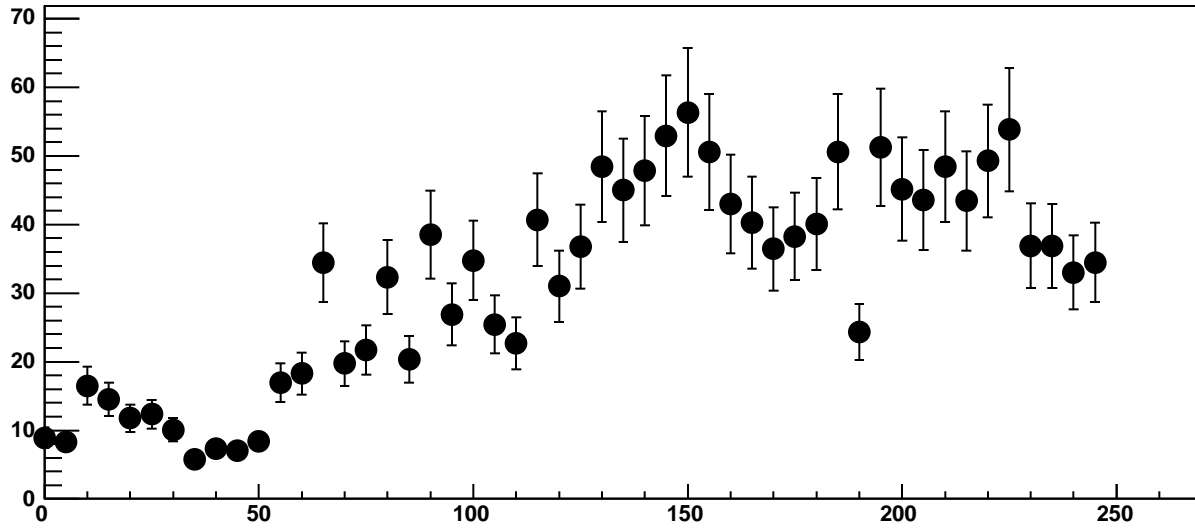


Chip 8, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

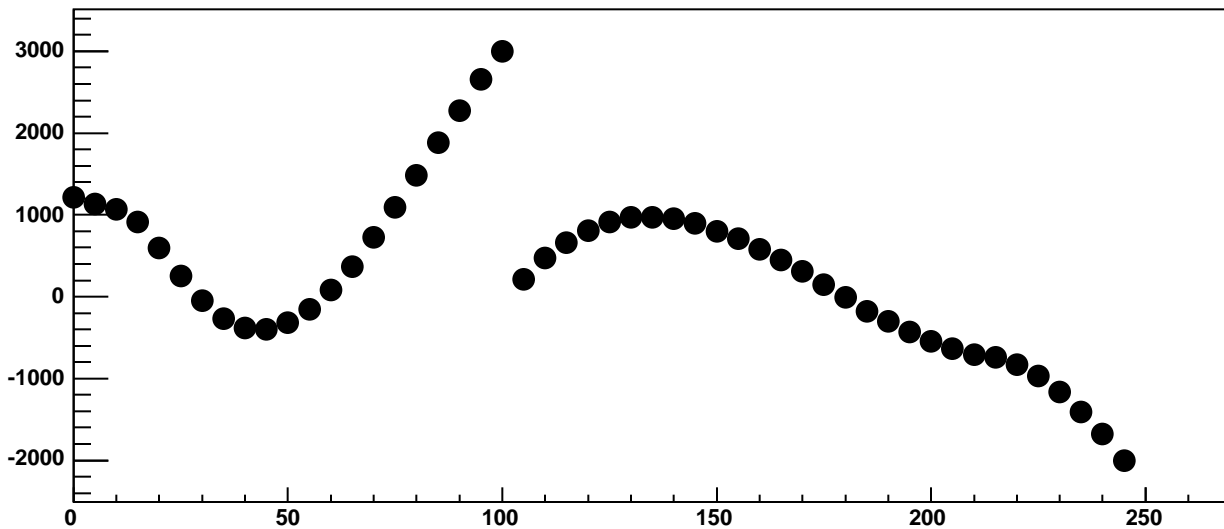


χ^2 / ndf	1.2e+06 / 41
p0	13.09 ± 1.029
p1	104.8 ± 0.6031
p2	3085 ± 1.44
p3	-6.068 ± 0.3763
p4	9.972 ± 0.01635

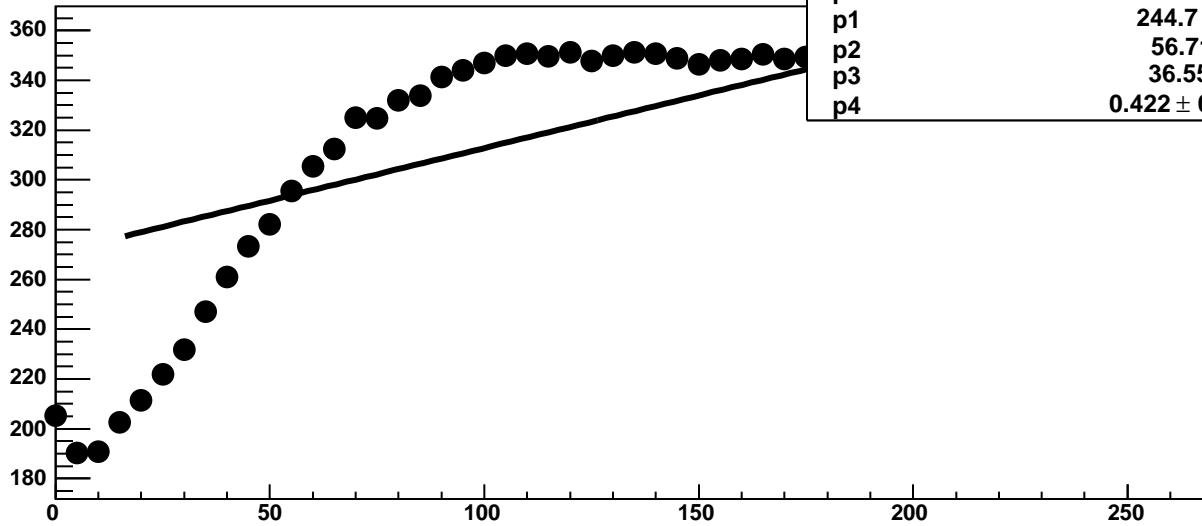
Chip 8, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold

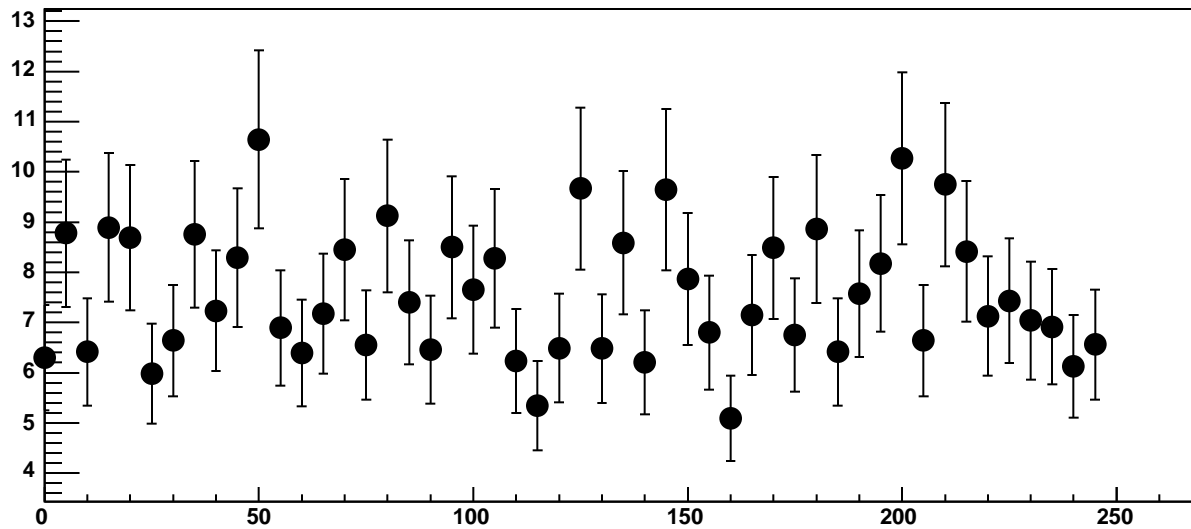


Chip 8, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold

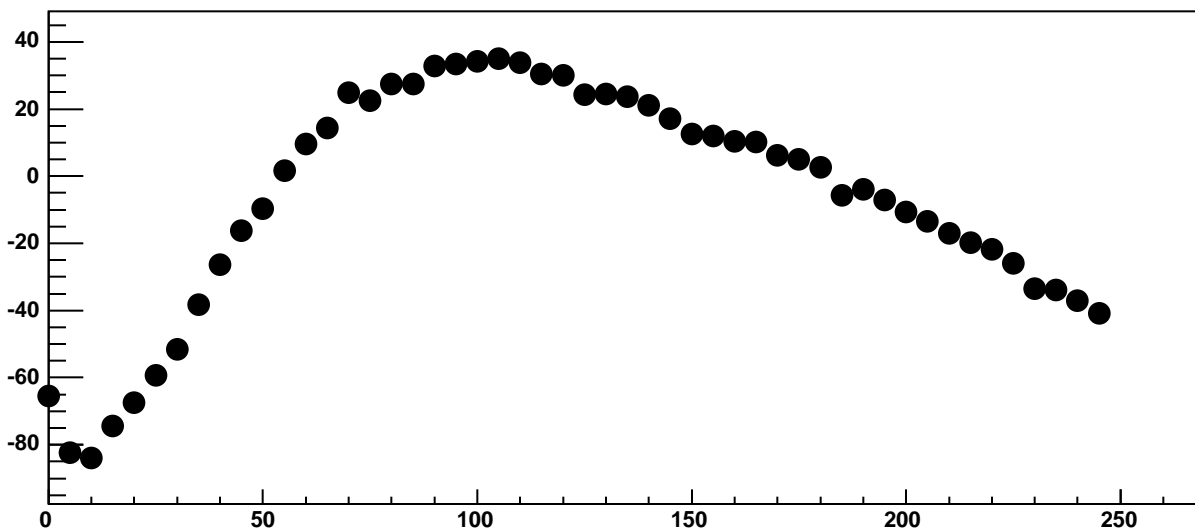


χ^2 / ndf	1.359e+04 / 41
p0	373.8 ± 0.2464
p1	244.7 ± 0.5838
p2	56.71 ± 84.85
p3	36.55 ± 1.414
p4	0.422 ± 0.001852

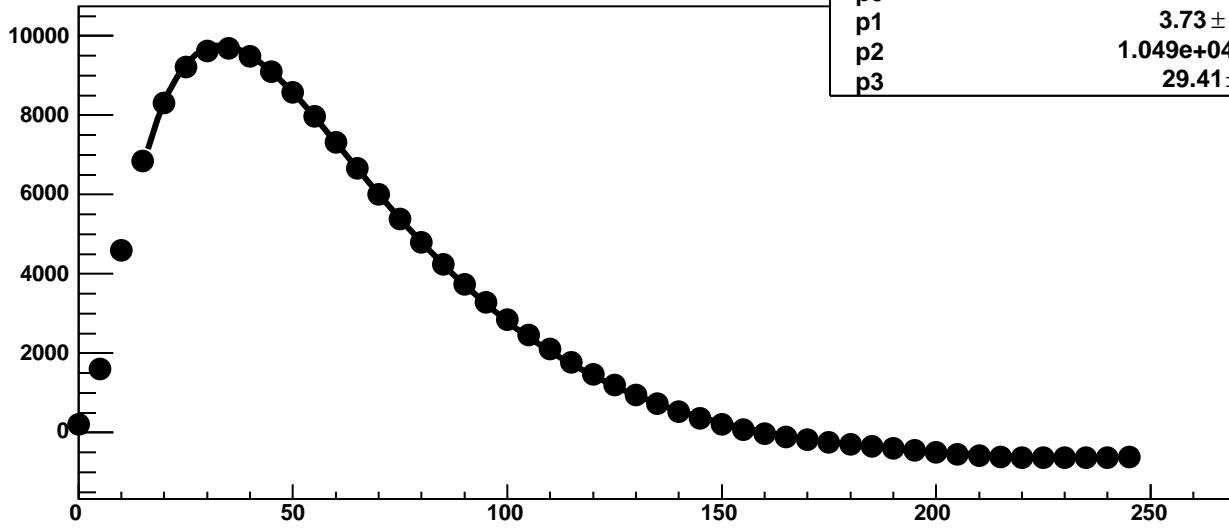
Chip 8, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold

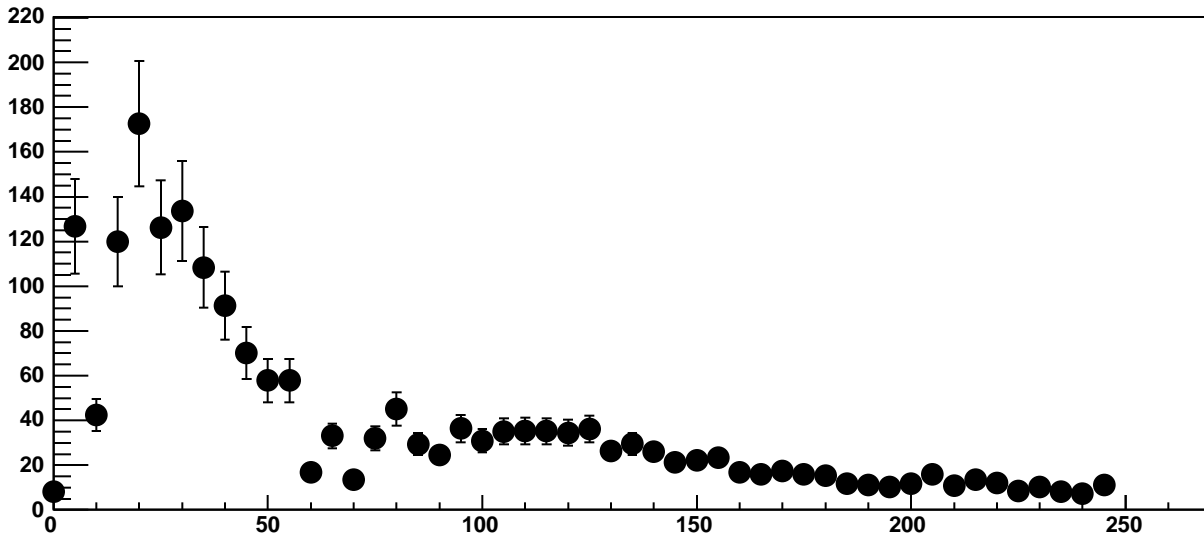


Chip 8, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold

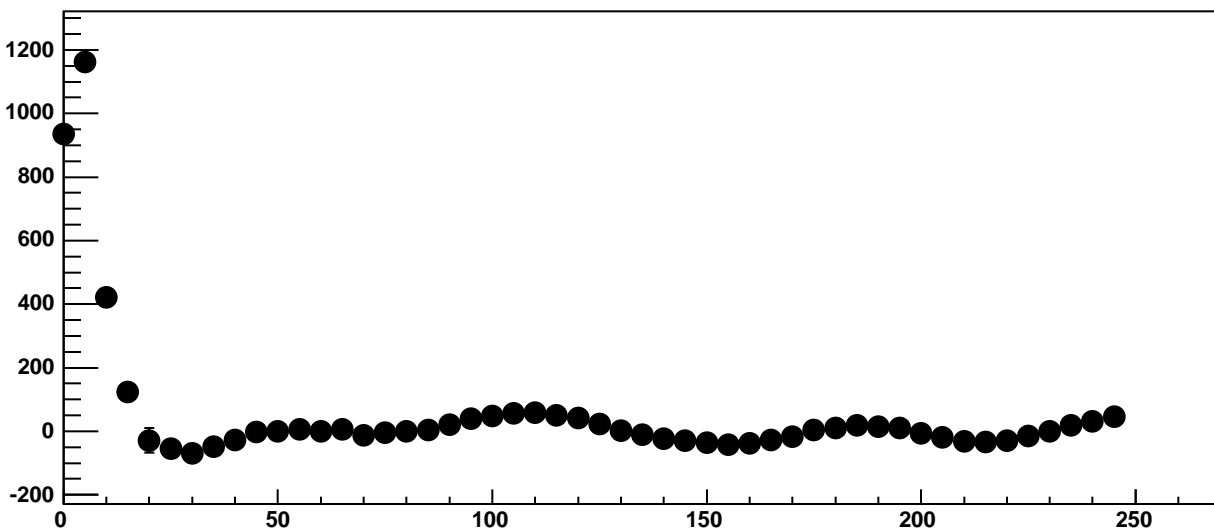


χ^2 / ndf	1728 / 42
p0	-733.8 ± 0.9135
p1	3.73 ± 0.04954
p2	$1.049\text{e}+04 \pm 6.561$
p3	29.41 ± 0.0188

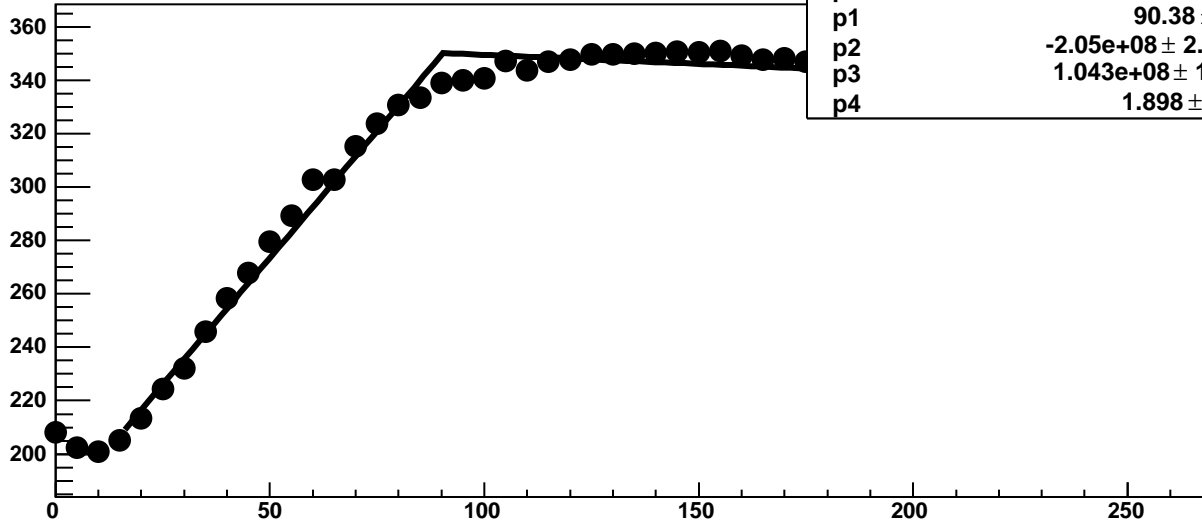
Chip 8, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold

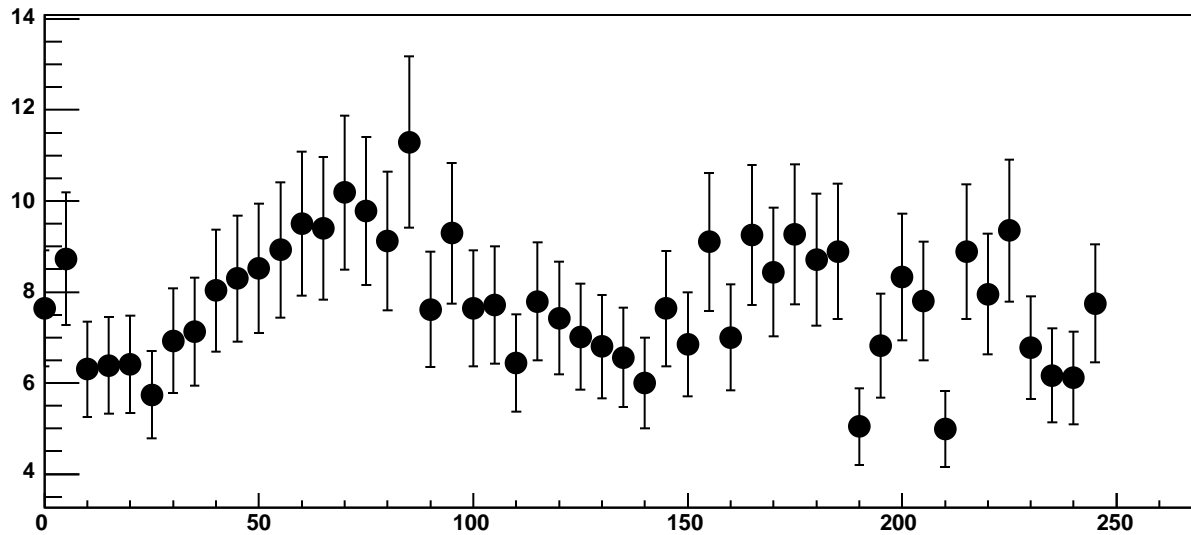


Chip 8, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

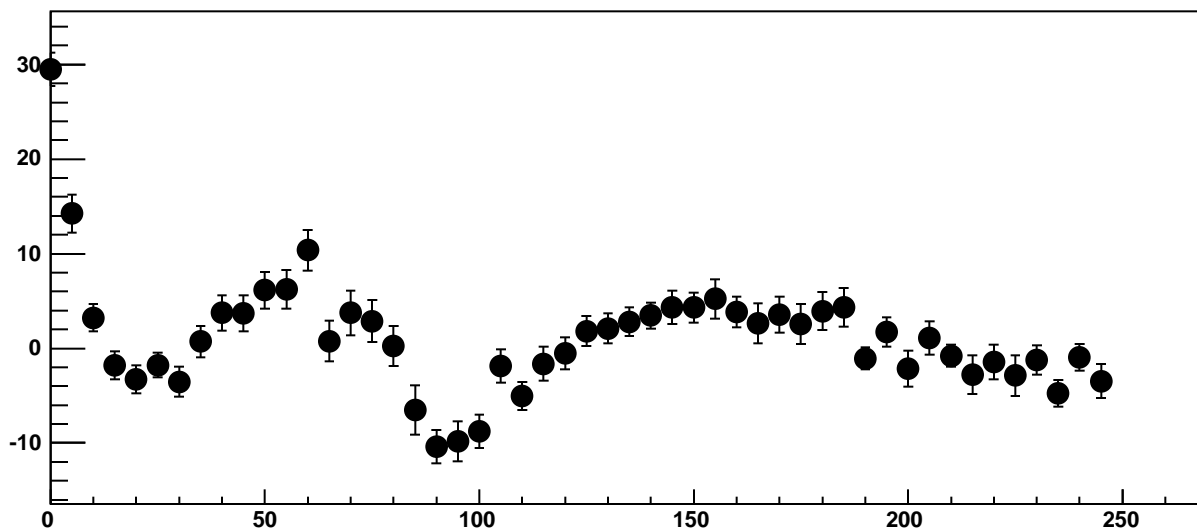


χ^2 / ndf	243.4 / 41
p0	350.2 ± 0.6065
p1	90.38 ± 0.5875
p2	$-2.05\text{e}+08 \pm 2.126\text{e}+06$
p3	$1.043\text{e}+08 \pm 1.57\text{e}+05$
p4	1.898 ± 0.01909

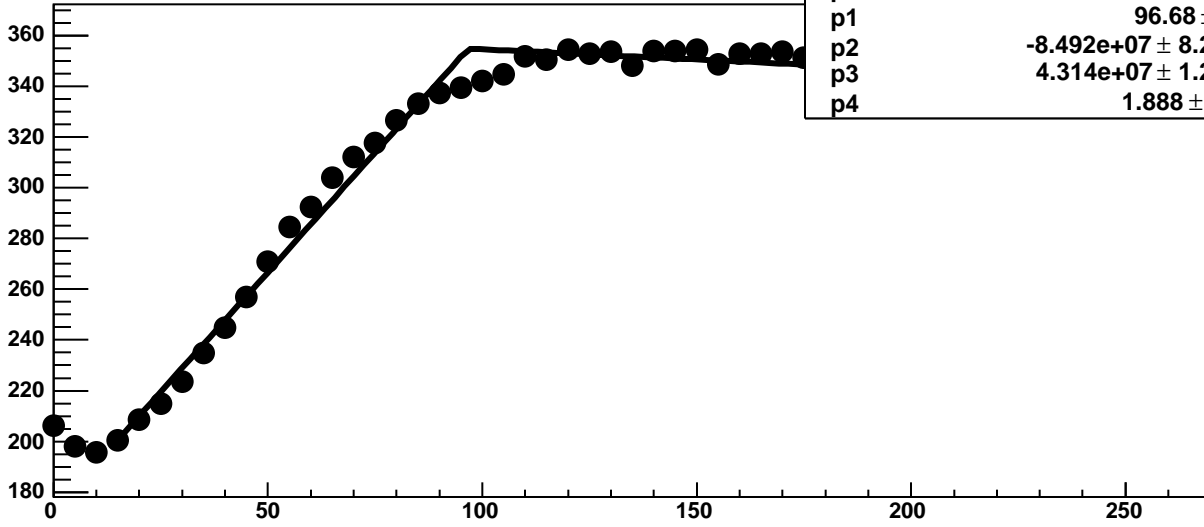
Chip 8, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

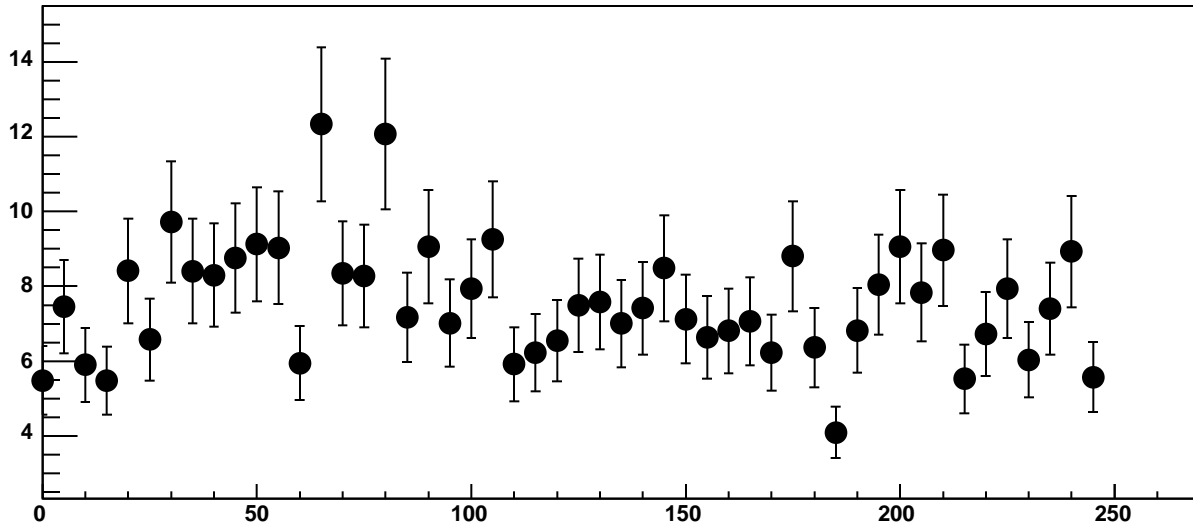


Chip 8, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

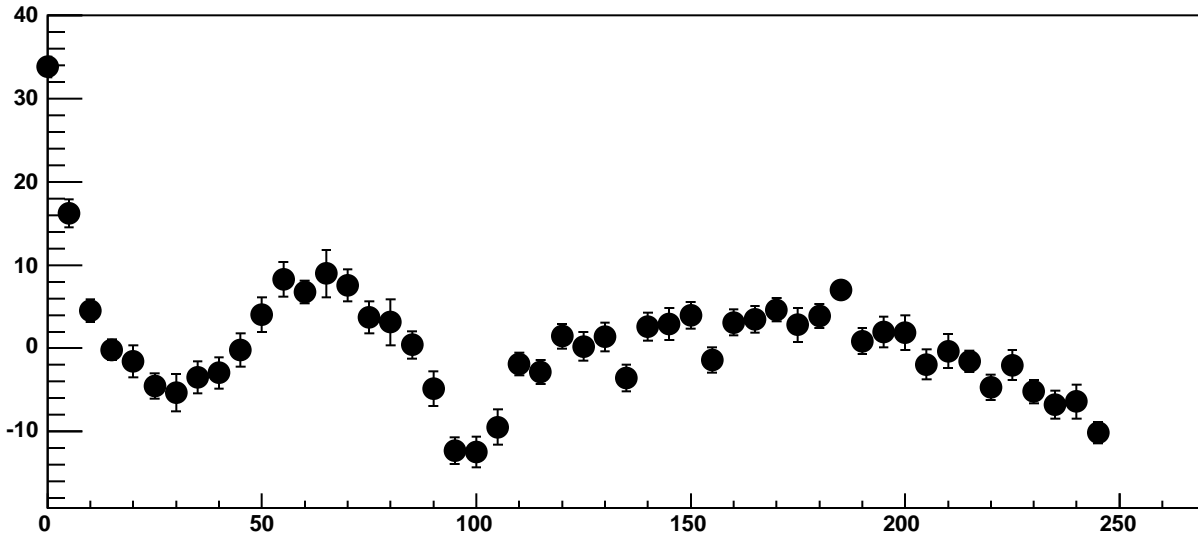


χ^2 / ndf	389.8 / 41
p0	354.8 ± 0.5895
p1	96.68 ± 0.5398
p2	$-8.492e+07 \pm 8.228e+05$
p3	$4.314e+07 \pm 1.262e+05$
p4	1.888 ± 0.01681

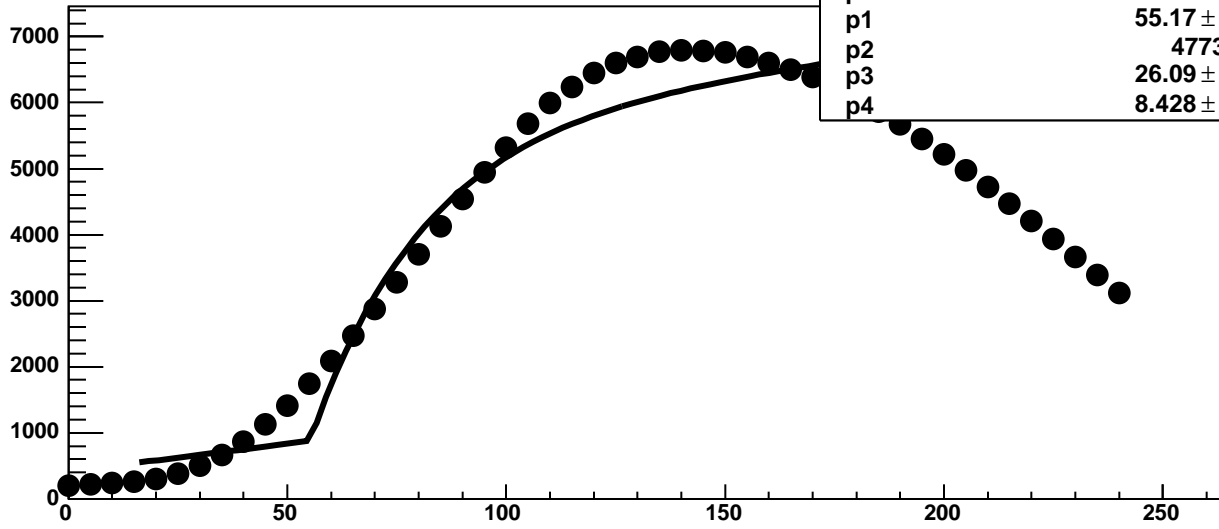
Chip 8, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

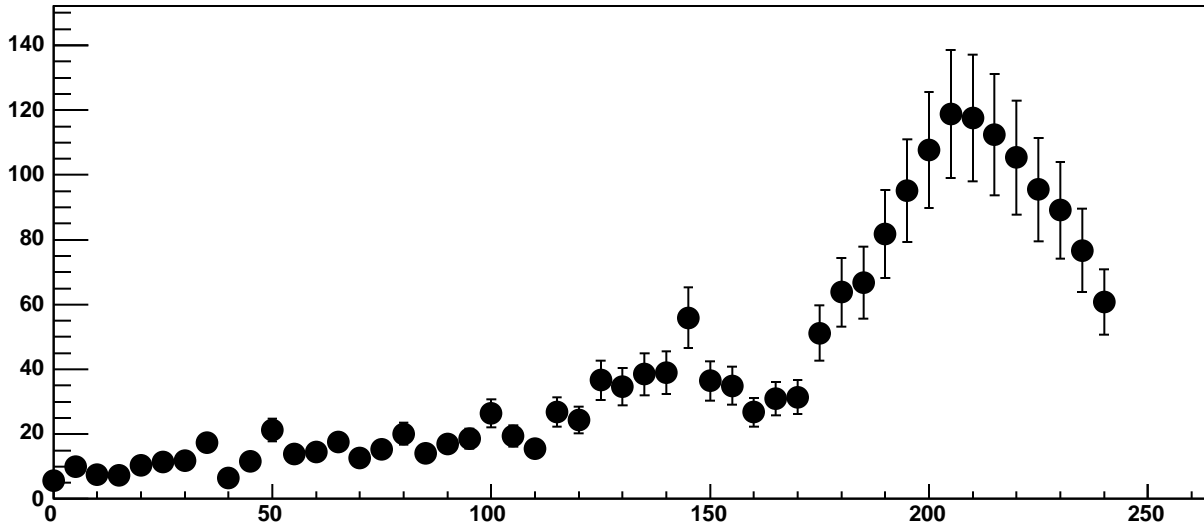


Chip 8, Channel 6, Enable 5, DAC=4000, ADC Mean vs Hold

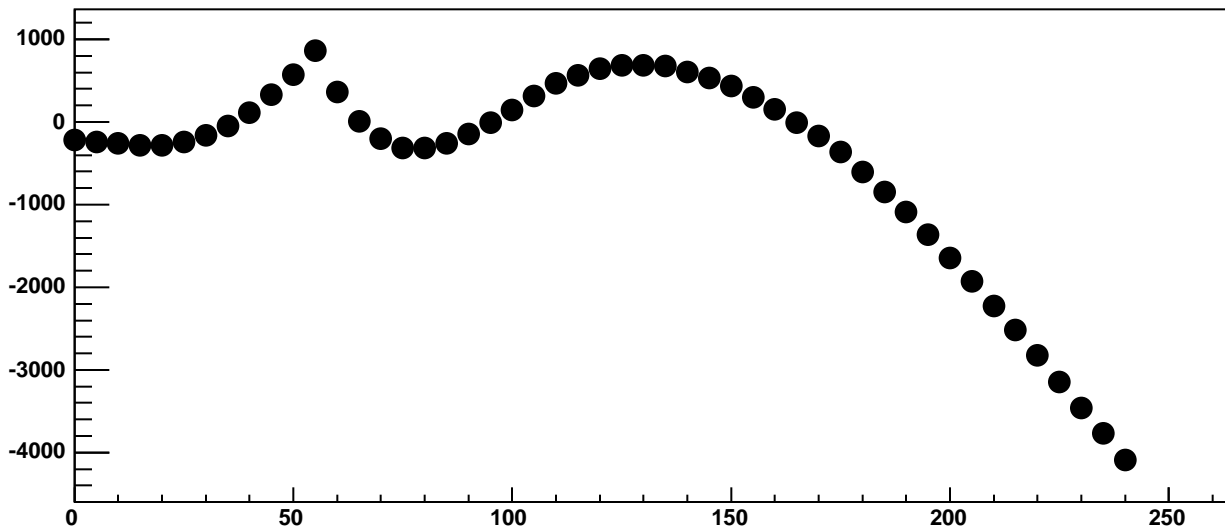


χ^2 / ndf	5.077e+05 / 41
p0	879.1 ± 1.401
p1	55.17 ± 0.02184
p2	4773 ± 6.281
p3	26.09 ± 0.04814
p4	8.428 ± 0.04529

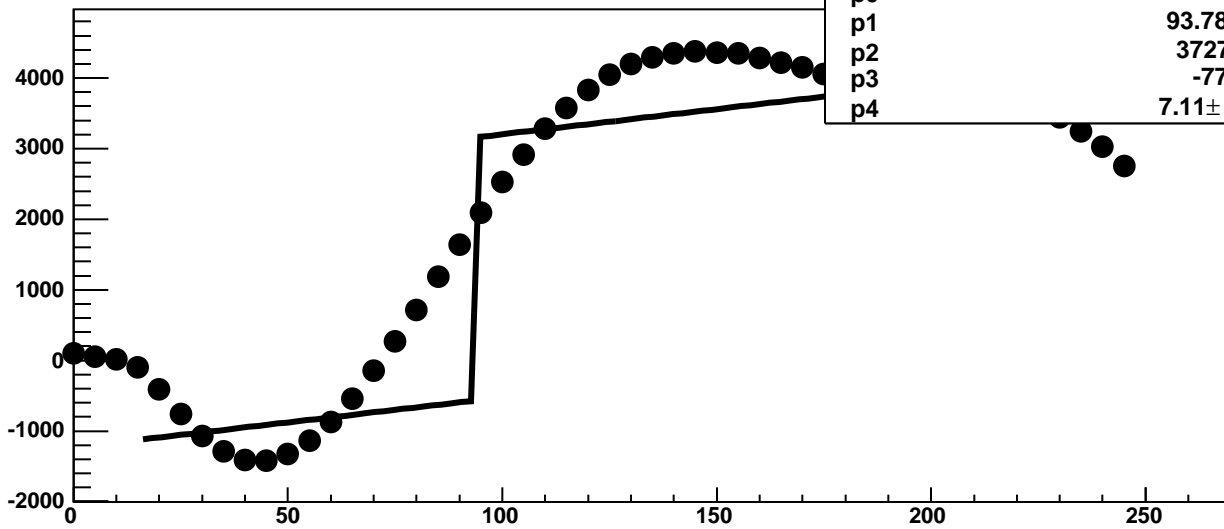
Chip 8, Channel 6, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 6, Enable 5, DAC=4000, ADC Residuals vs Hold

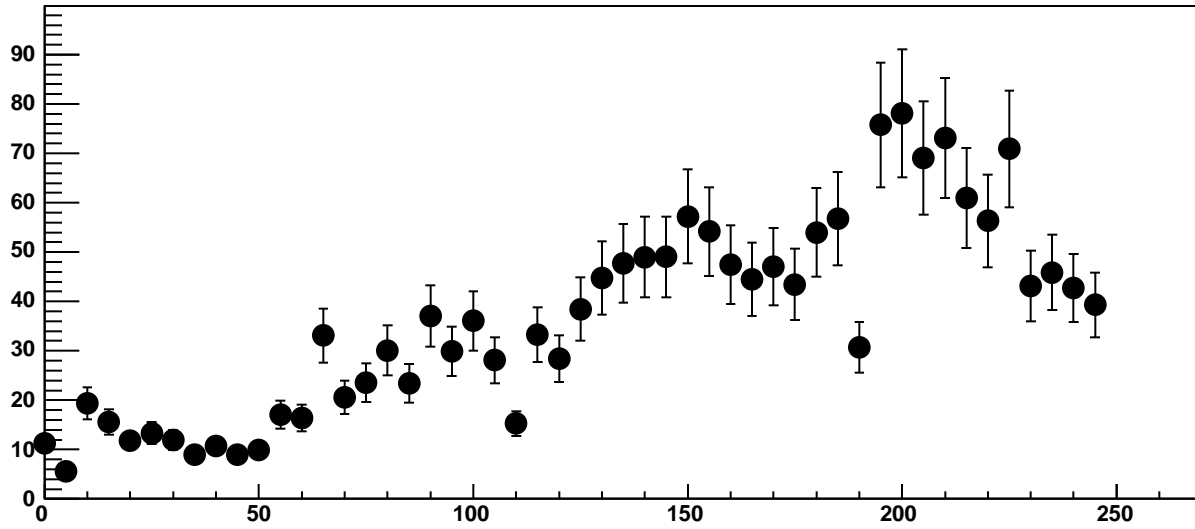


Chip 8, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

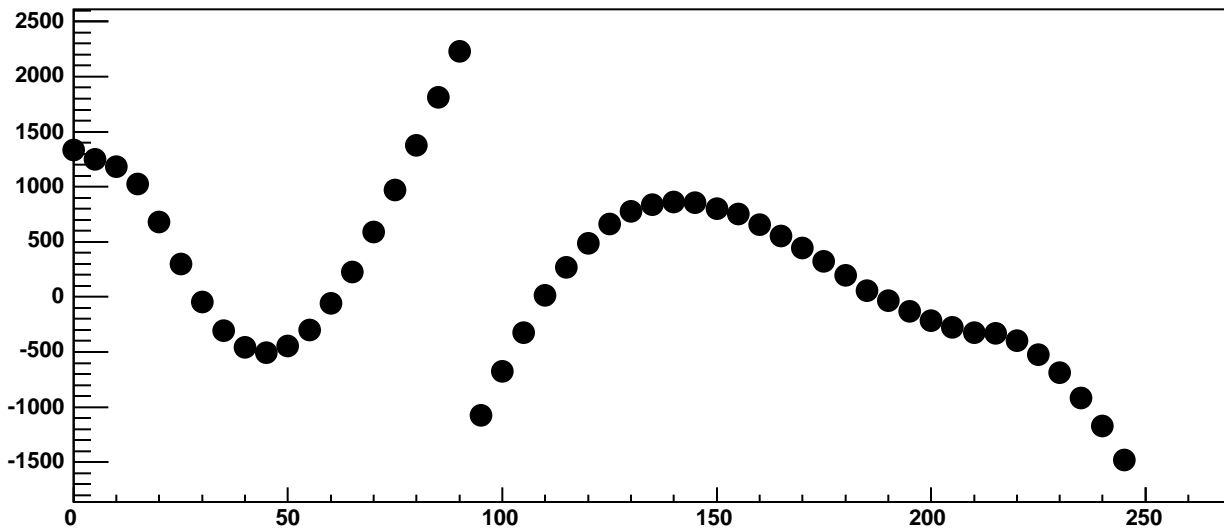


χ^2 / ndf	7.067e+05 / 41
p0	-566.6 ± 0.6323
p1	93.78 ± 1.147
p2	3727 ± 5.051
p3	-77 ± 2.844
p4	7.11 ± 0.08484

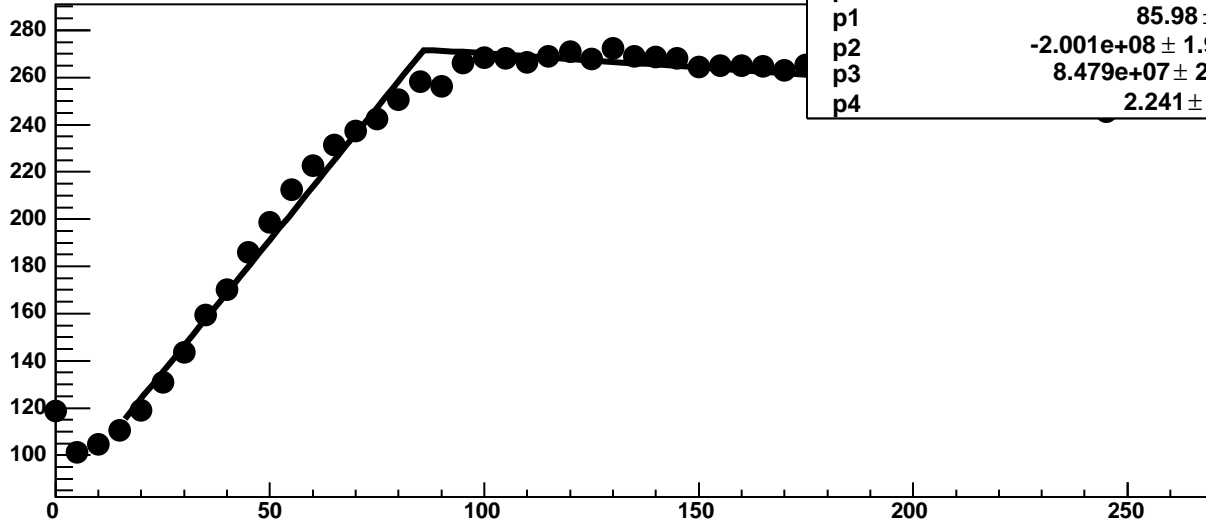
Chip 8, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

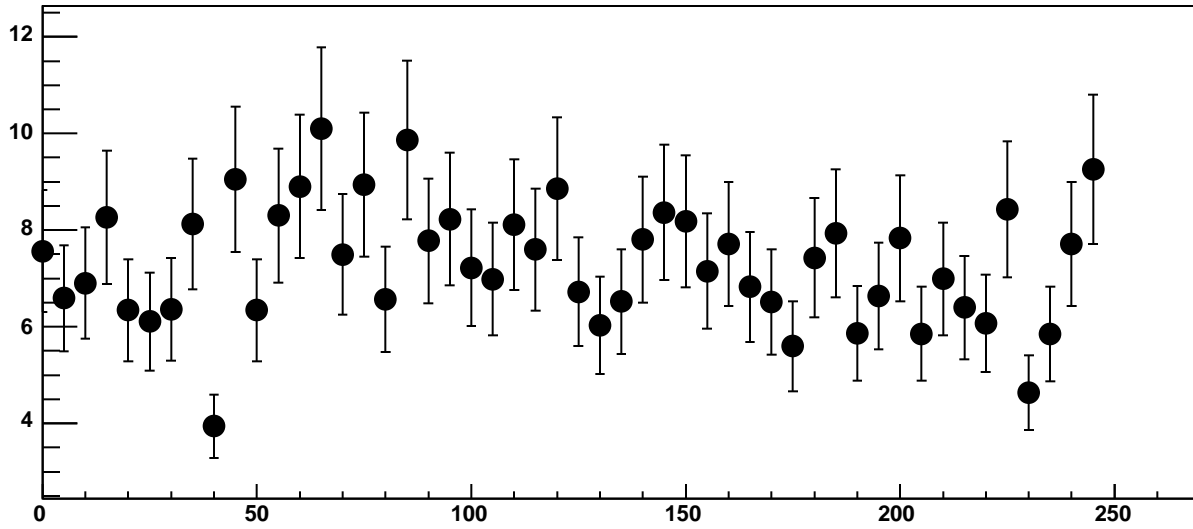


Chip 8, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold

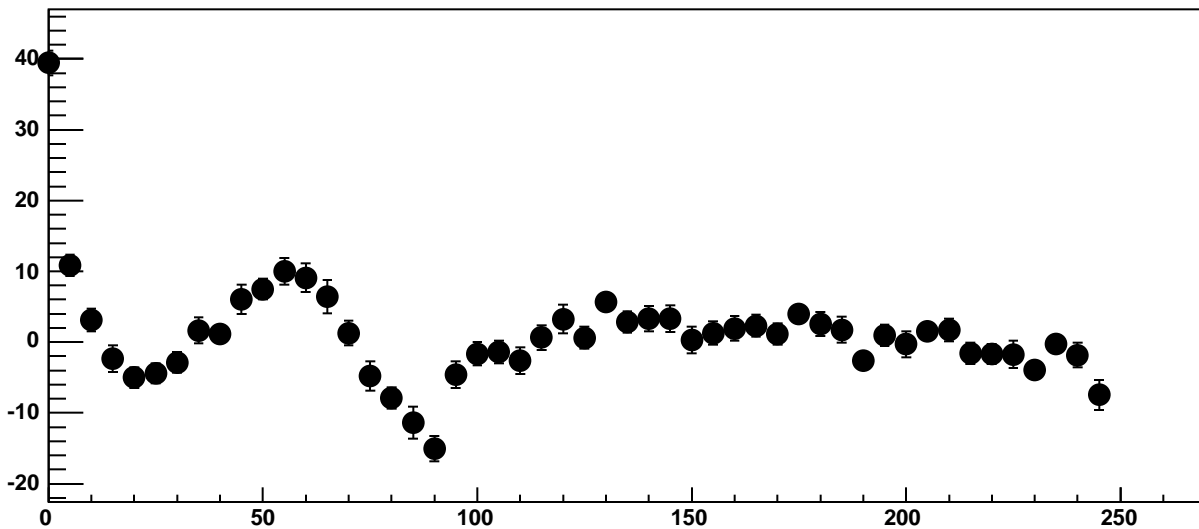


χ^2 / ndf	330.3 / 41
p0	271.9 ± 0.581
p1	85.98 ± 0.4702
p2	$-2.001\text{e}+08 \pm 1.902\text{e}+06$
p3	$8.479\text{e}+07 \pm 2.32\text{e}+05$
p4	2.241 ± 0.02064

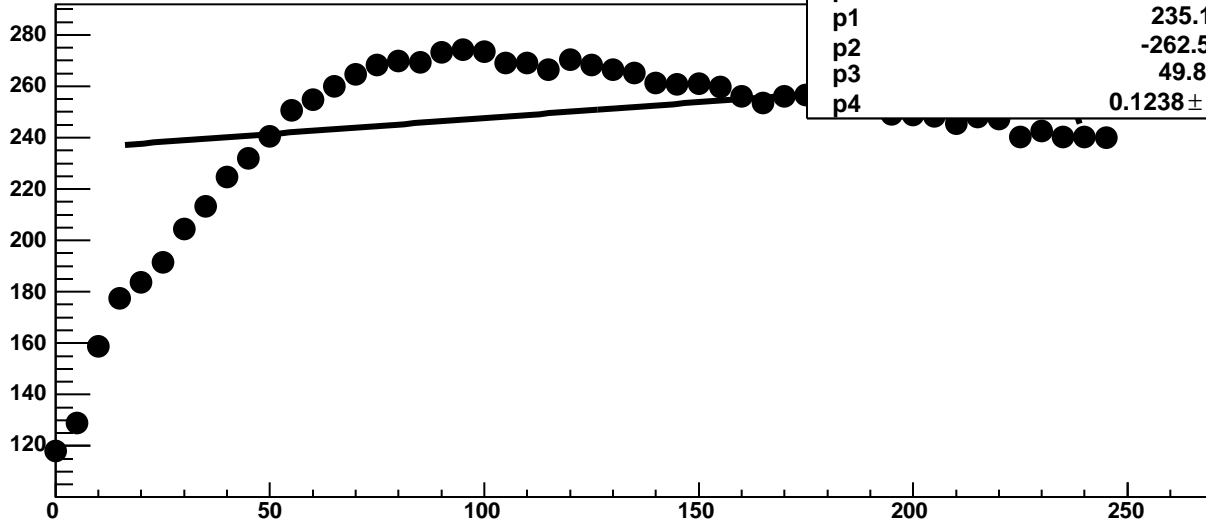
Chip 8, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



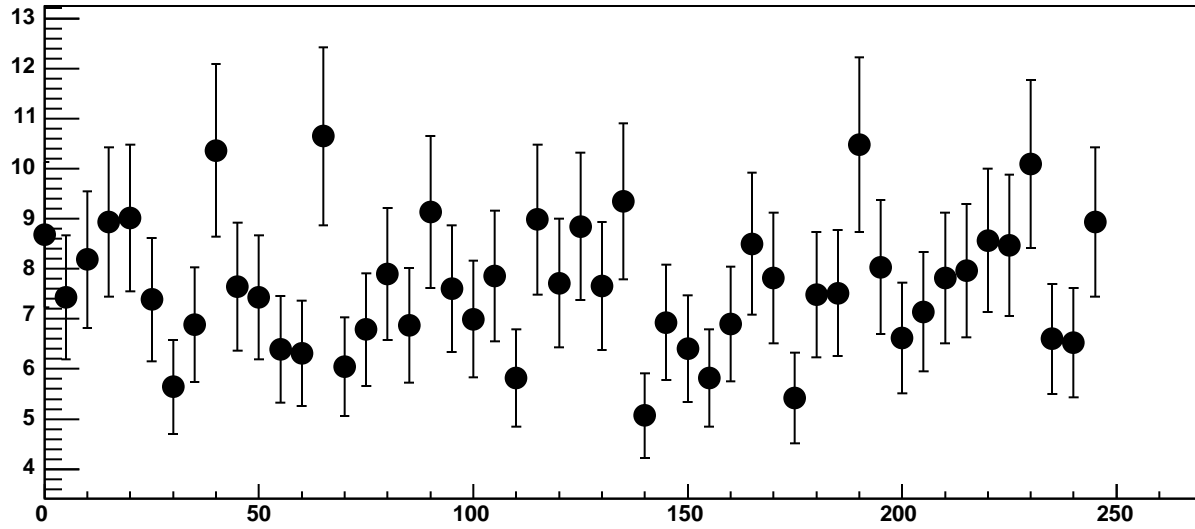
Chip 8, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold



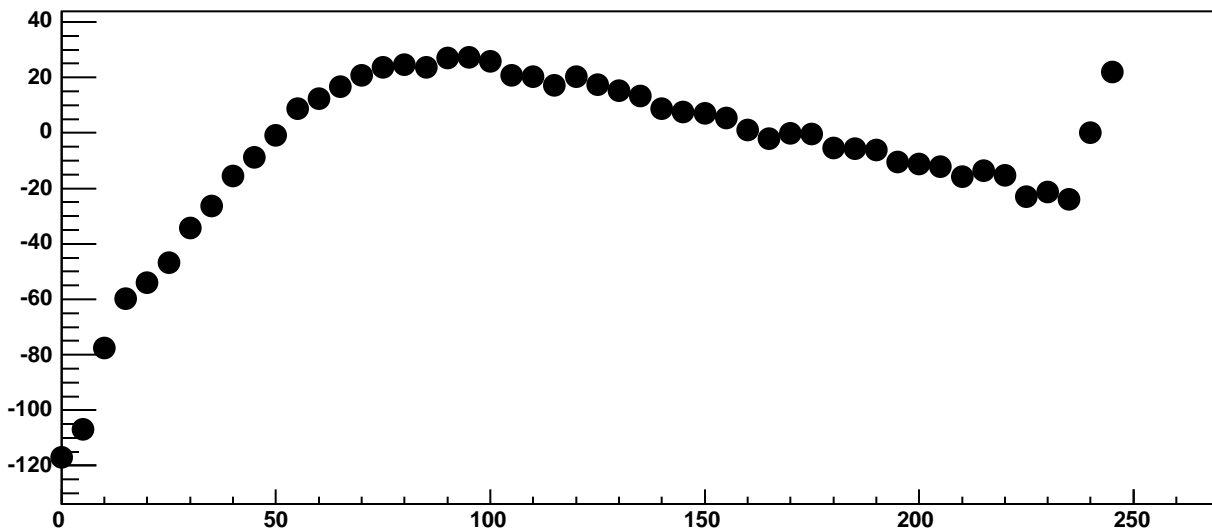
Chip 8, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold



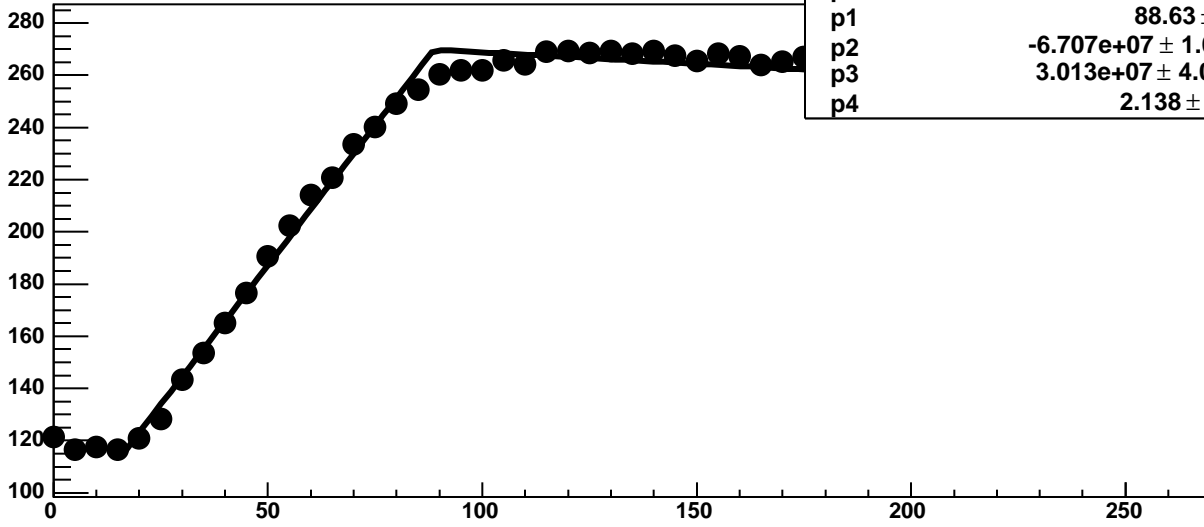
Chip 8, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold

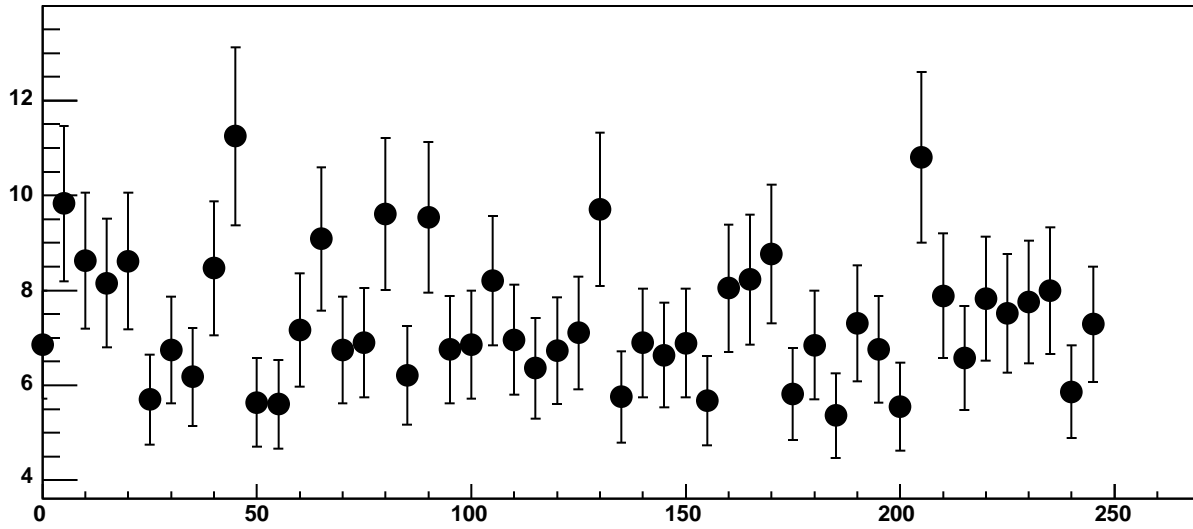


Chip 8, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

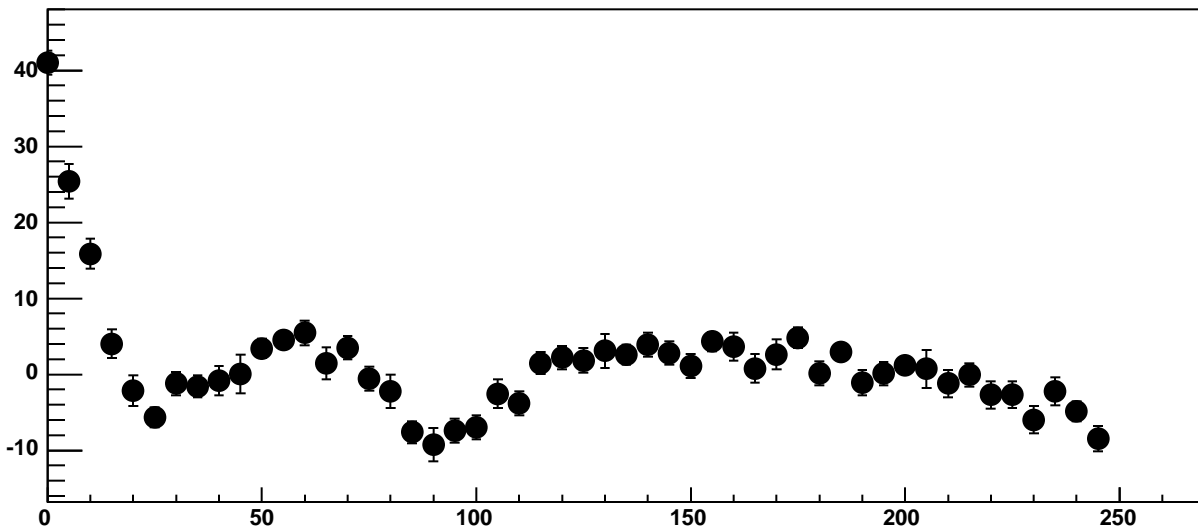


χ^2 / ndf	247.8 / 41
p0	269.8 ± 0.561
p1	88.63 ± 0.4693
p2	$-6.707\text{e}+07 \pm 1.079\text{e}+06$
p3	$3.013\text{e}+07 \pm 4.003\text{e}+05$
p4	2.138 ± 0.01973

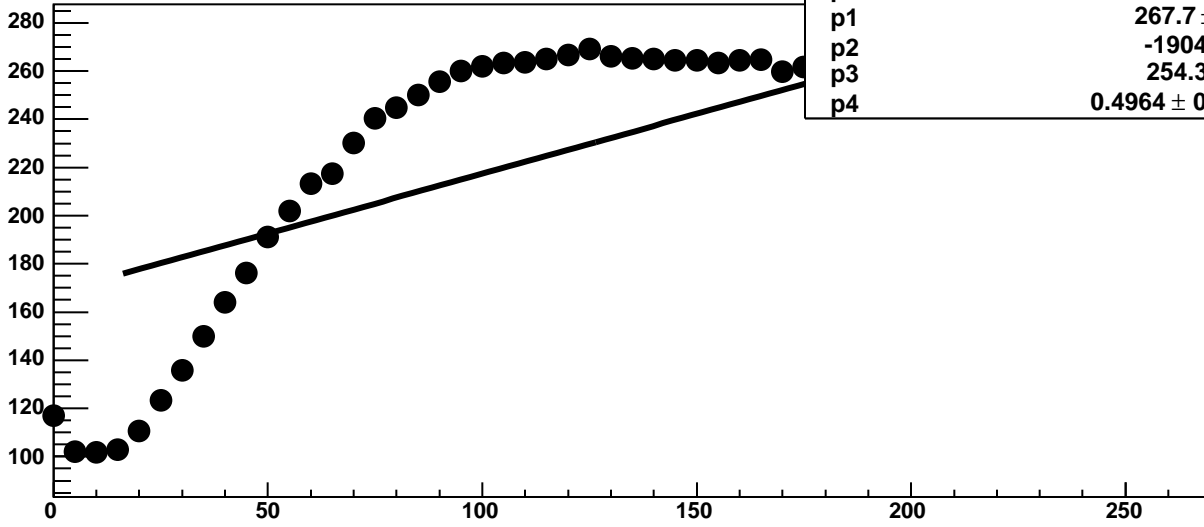
Chip 8, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

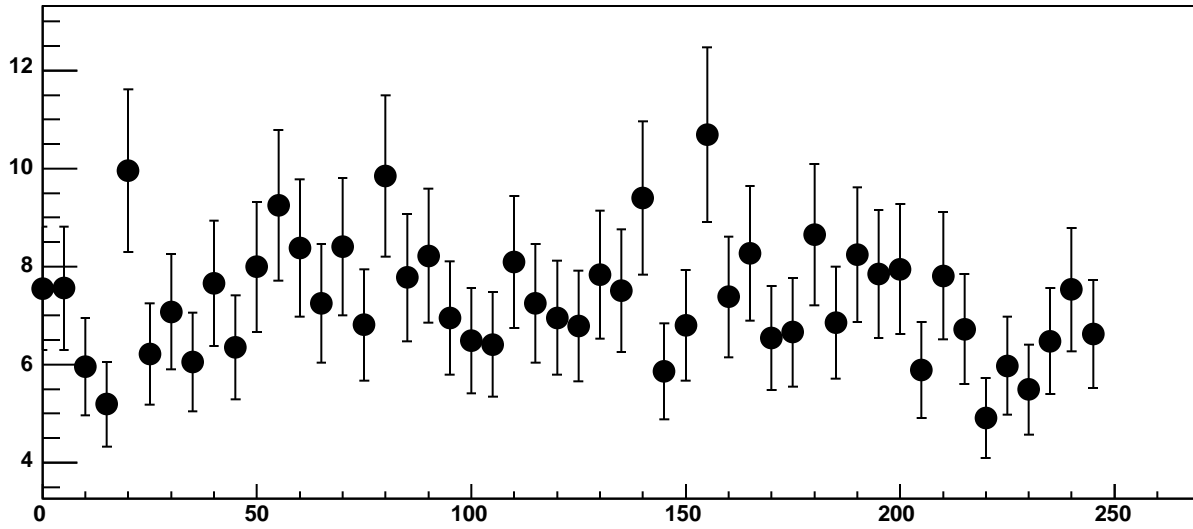


Chip 8, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

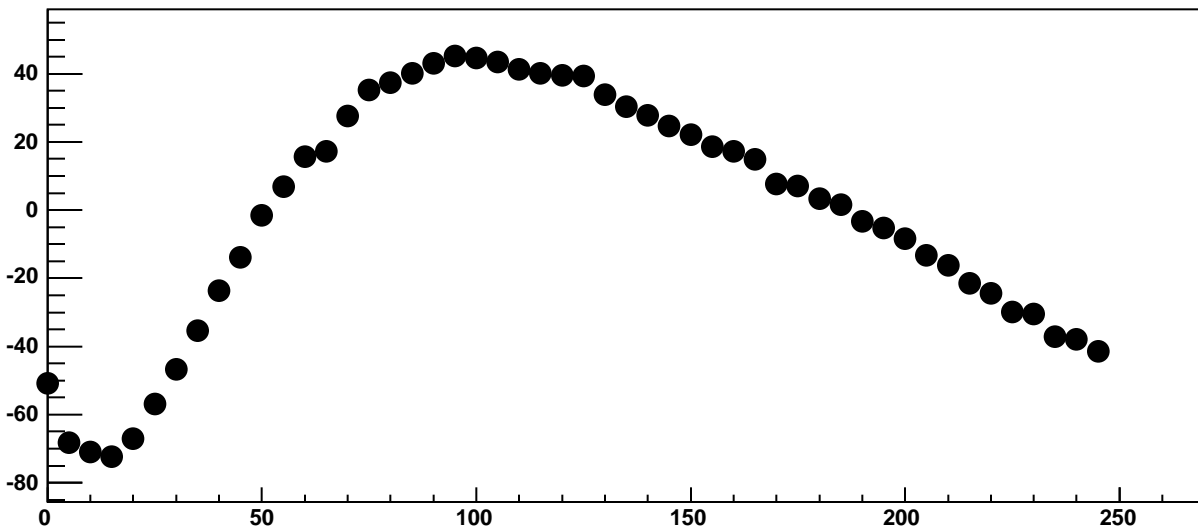


χ^2 / ndf	1.969e+04 / 41
p0	300.7 ± 0.4795
p1	267.7 ± 0.3198
p2	-1904 ± 9.586
p3	254.3 ± 1.315
p4	0.4964 ± 0.003416

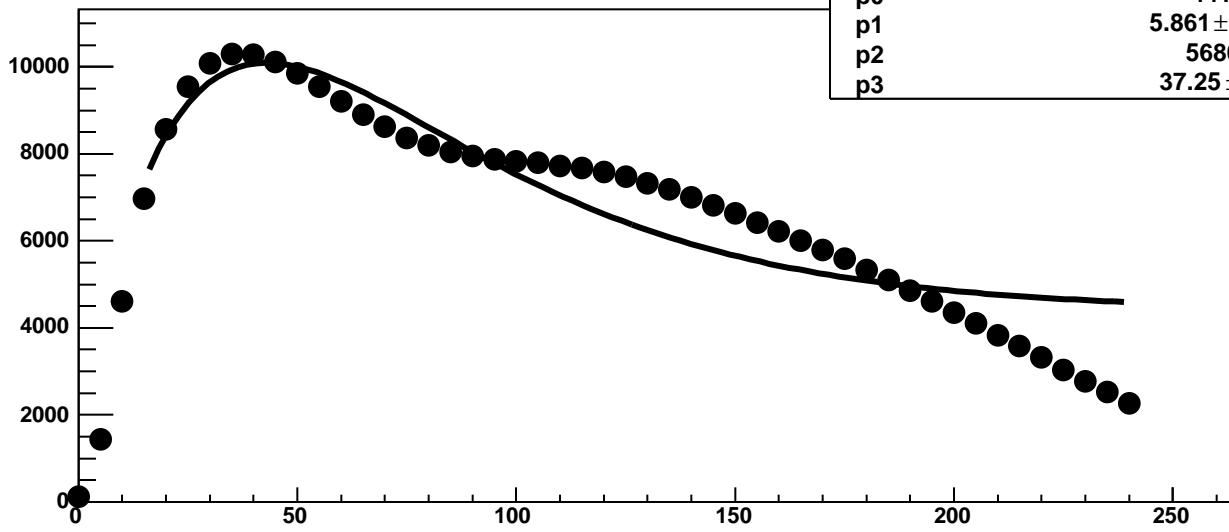
Chip 8, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

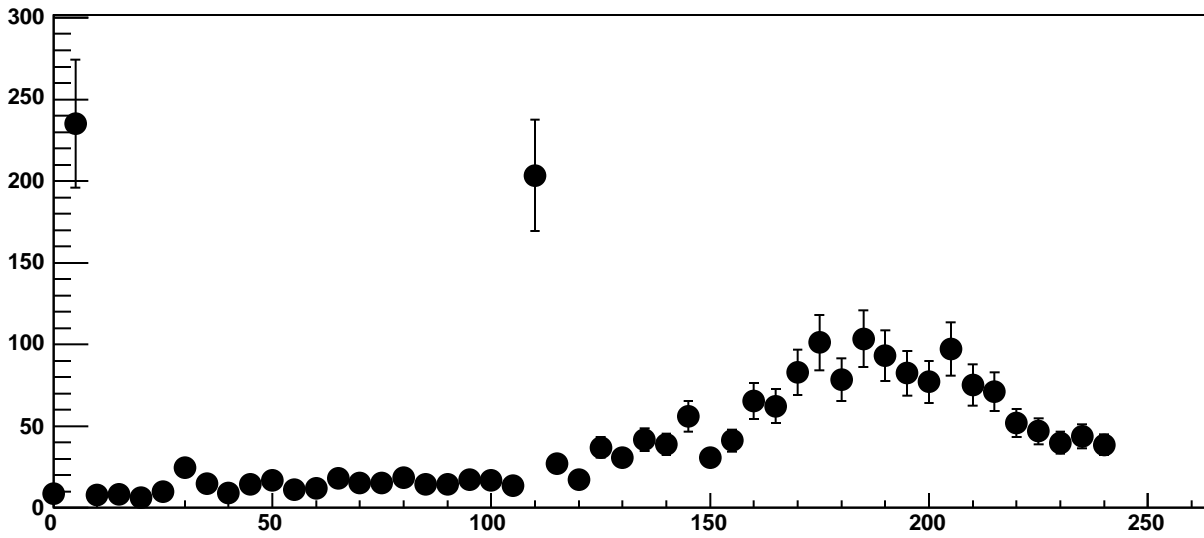


Chip 8, Channel 7, Enable 5!, DAC=4000, ADC Mean vs Hold

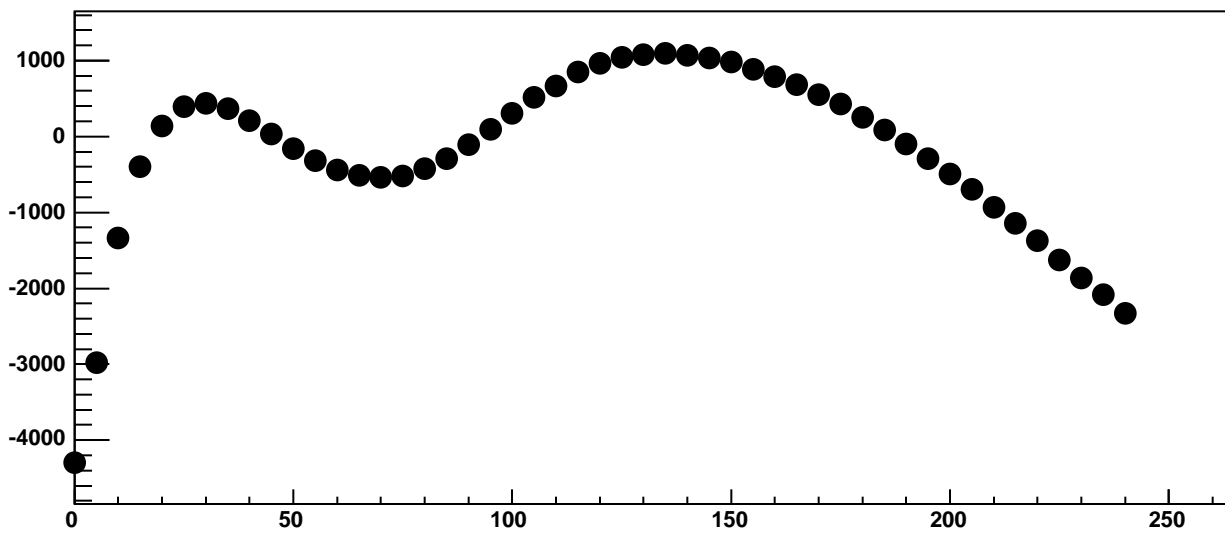


χ^2 / ndf	6.519e+05 / 42
p0	4411 ± 5.12
p1	5.861 ± 0.02381
p2	5680 ± 4.82
p3	37.25 ± 0.0353

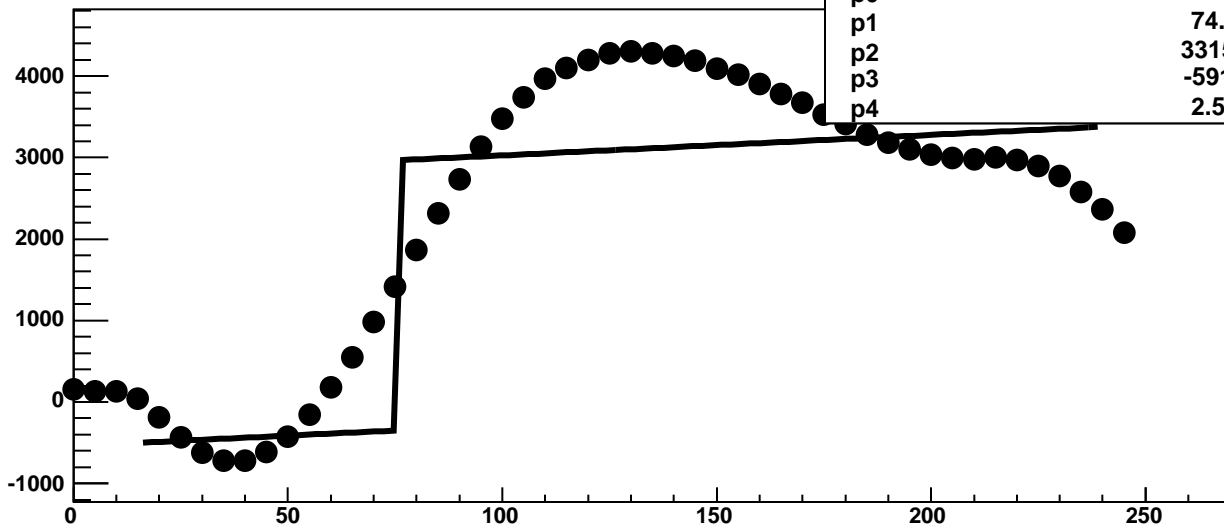
Chip 8, Channel 7, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 7, Enable 5!, DAC=4000, ADC Residuals vs Hold

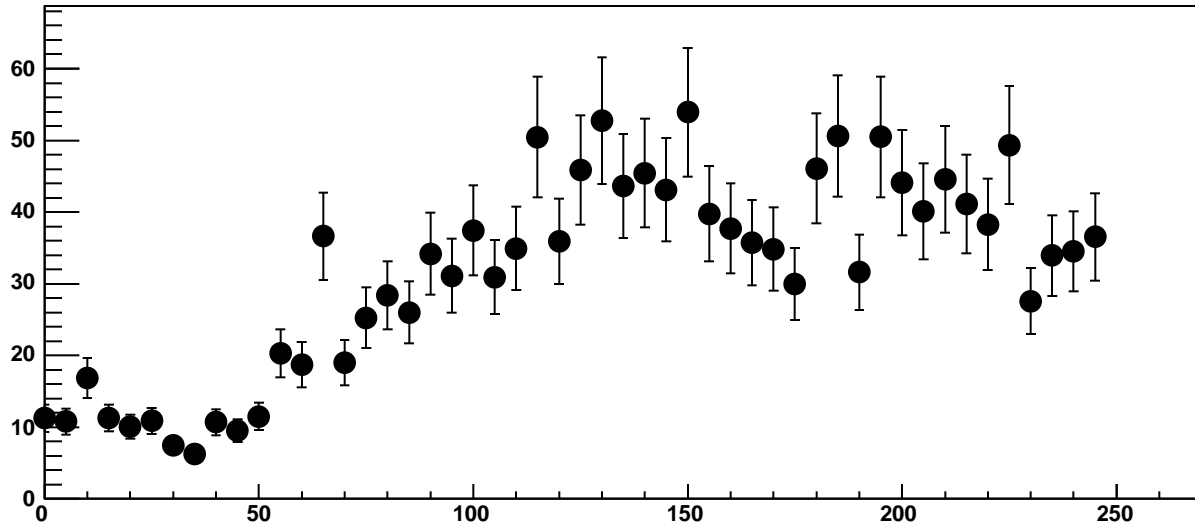


Chip 8, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

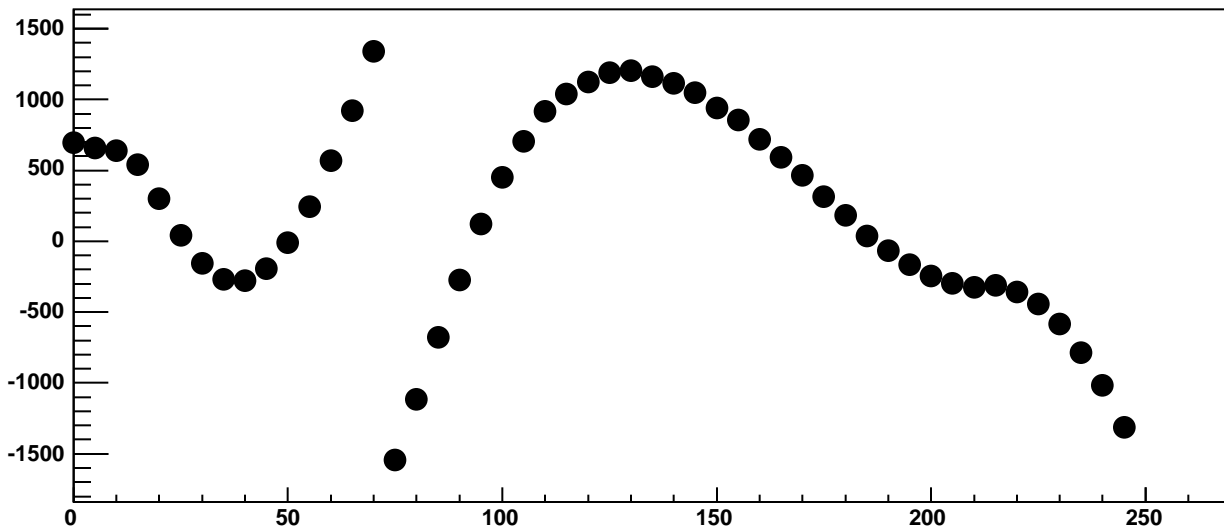


χ^2 / ndf	5.531e+05 / 41
p0	-351 ± 1.454
p1	74.89 ± 1
p2	3315 ± 3.08
p3	-591.5 ± 1
p4	2.512 ± 1

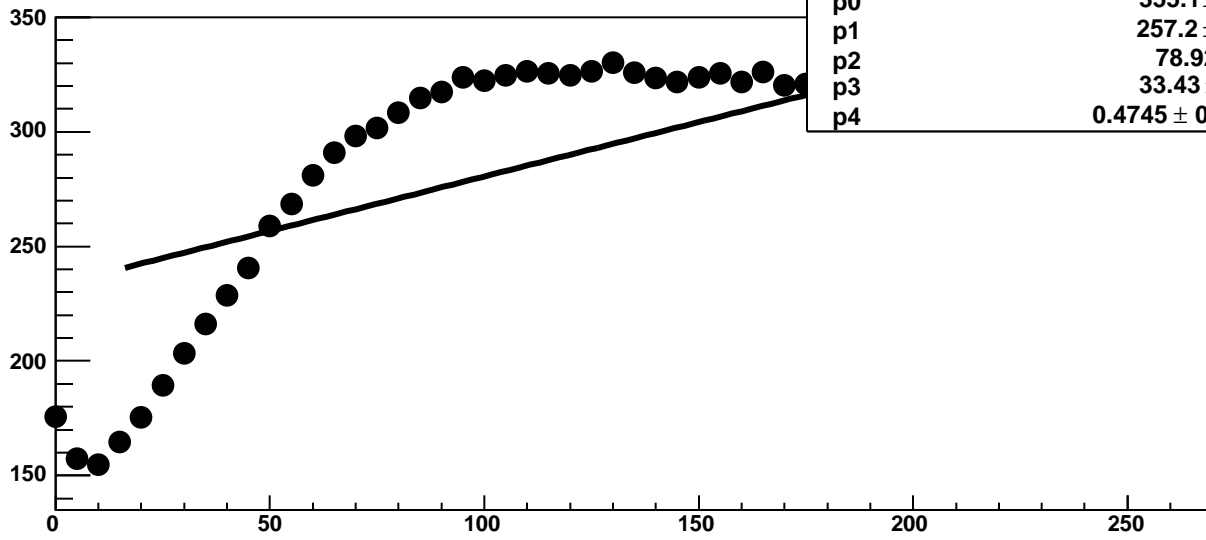
Chip 8, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

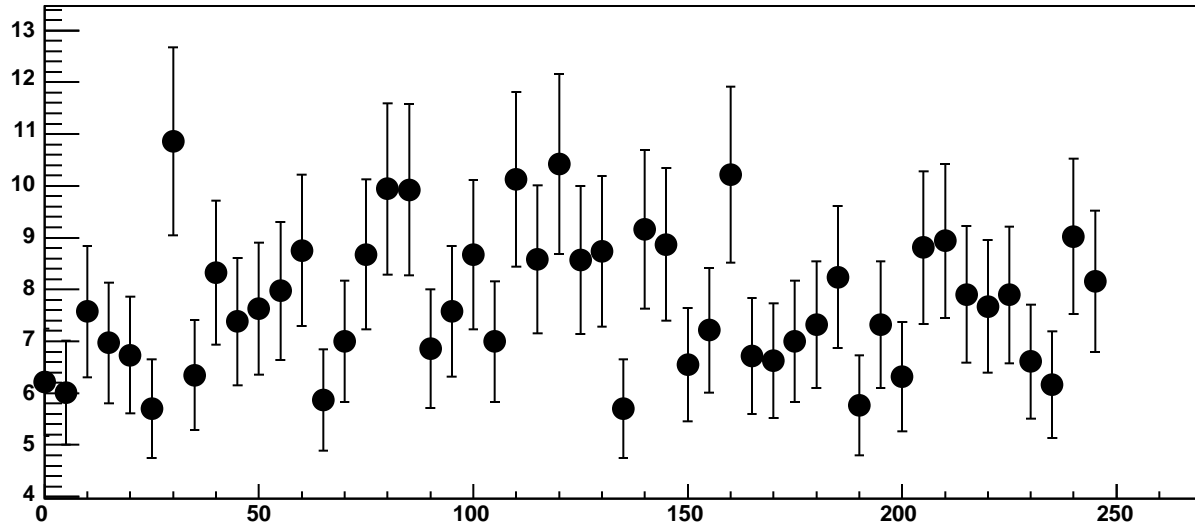


Chip 8, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

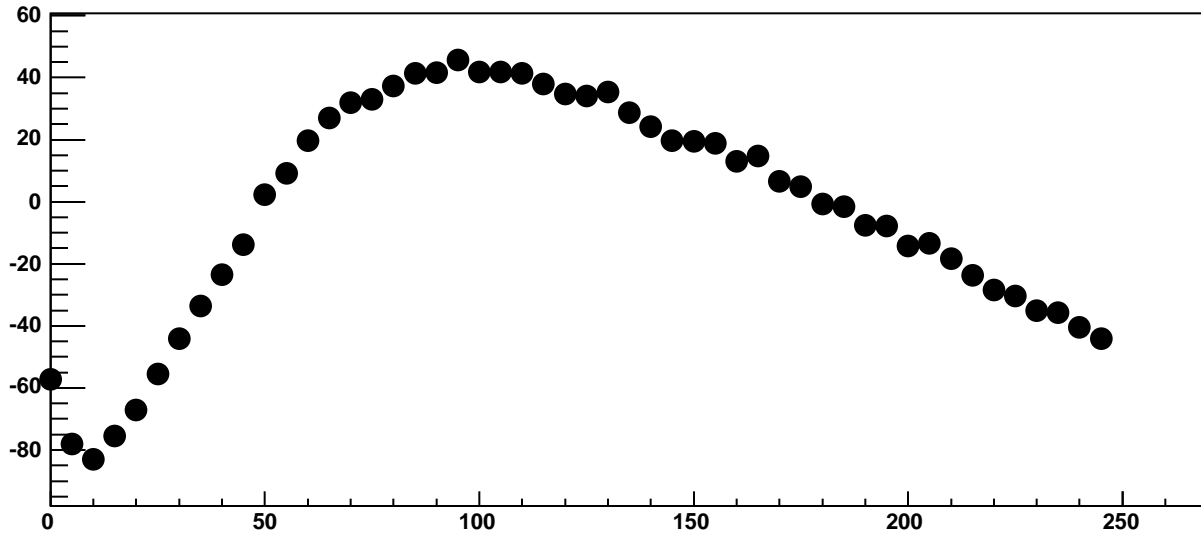


χ^2 / ndf	1.649e+04 / 41
p0	355.1 ± 0.3099
p1	257.2 ± 0.7433
p2	78.92 ± 3.55
p3	33.43 ± 0.2611
p4	0.4745 ± 0.003714

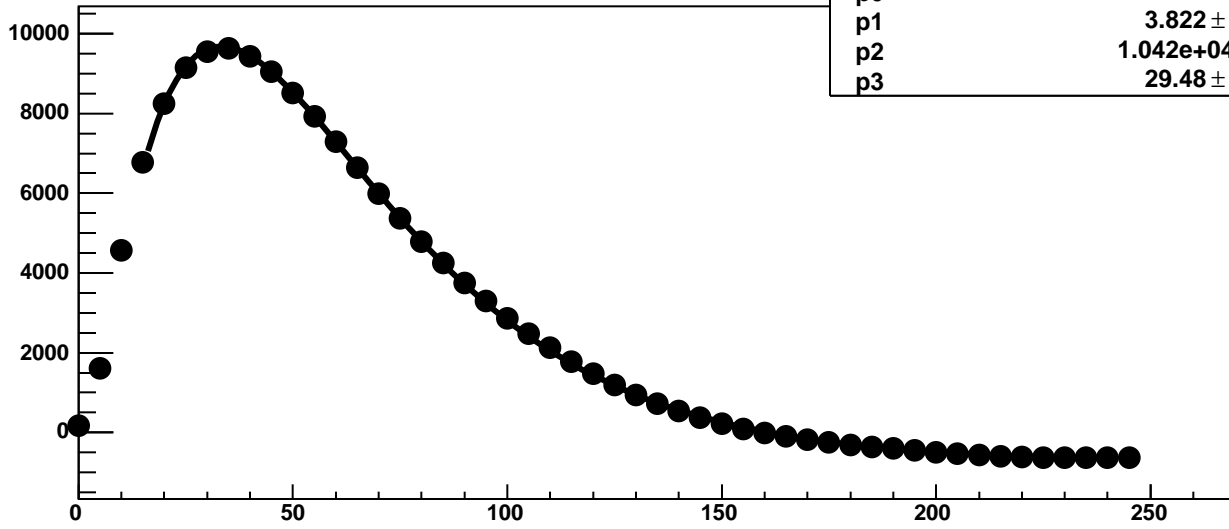
Chip 8, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

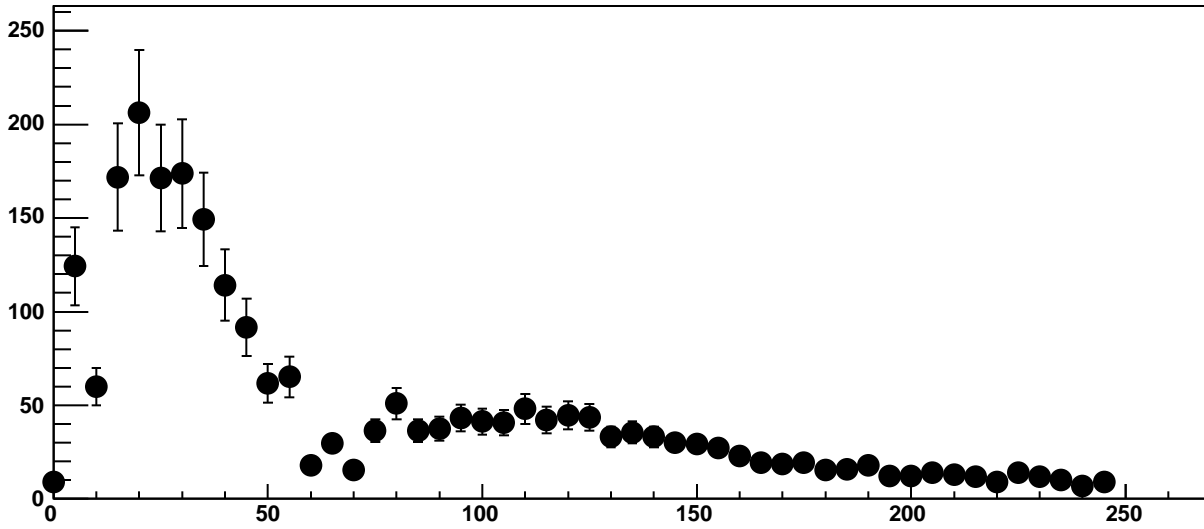


Chip 8, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold

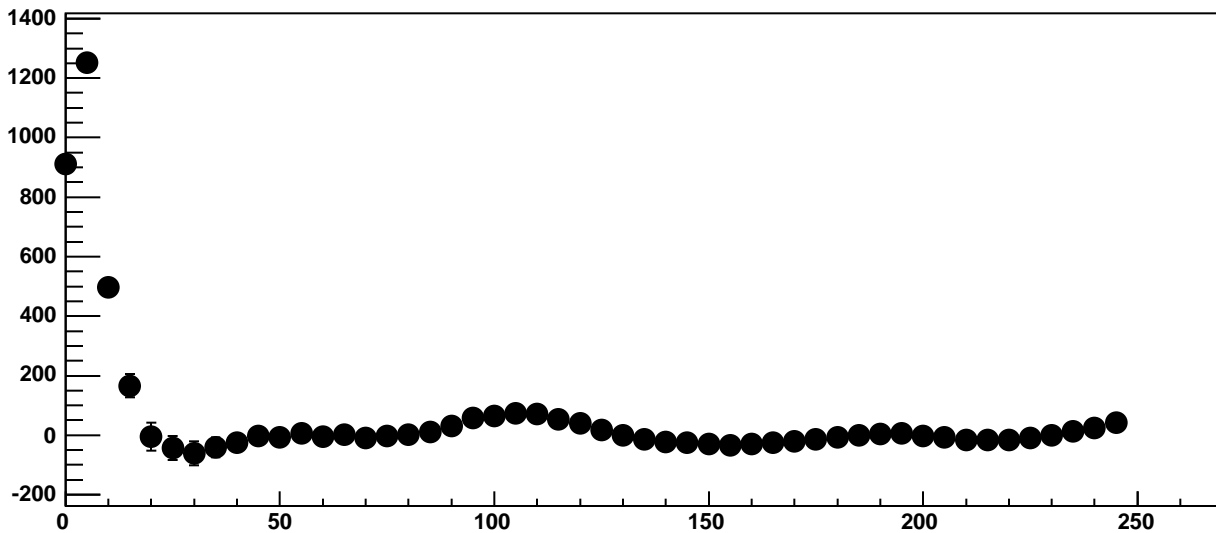


χ^2 / ndf	864 / 42
p0	-732.3 ± 1.02
p1	3.822 ± 0.06568
p2	1.042e+04 ± 8.441
p3	29.48 ± 0.02318

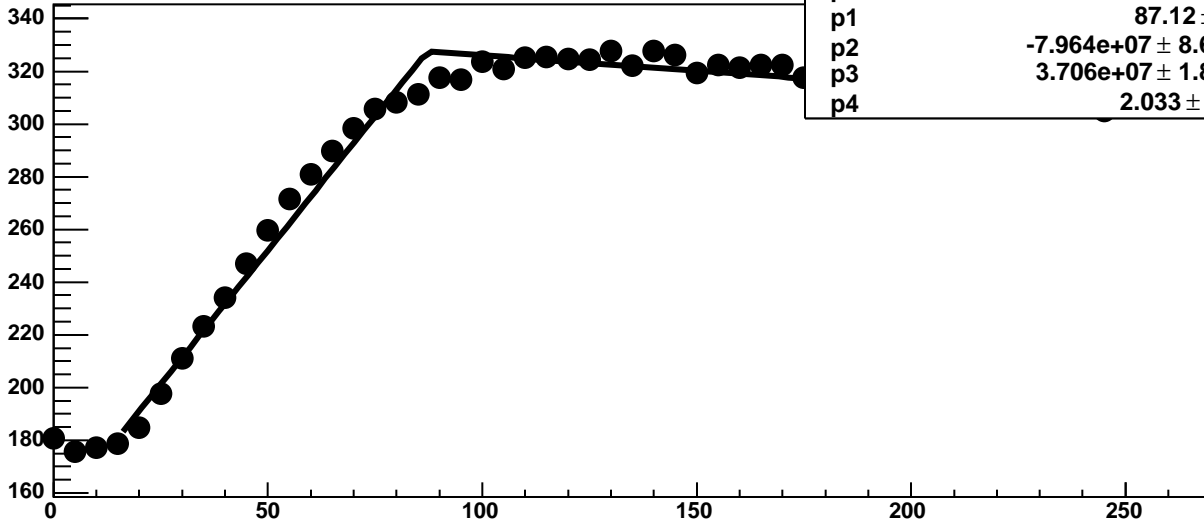
Chip 8, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold

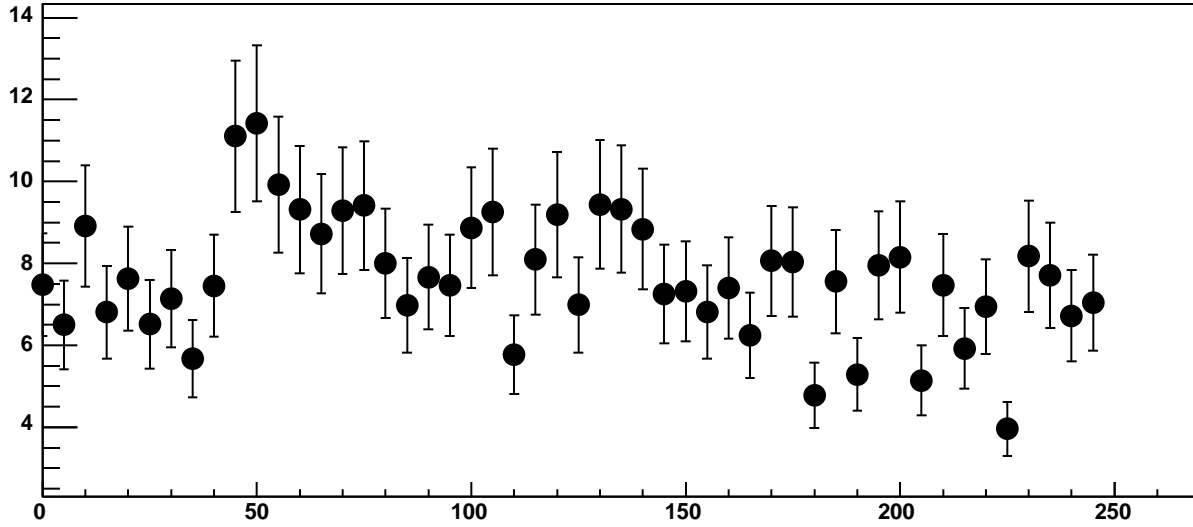


Chip 8, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

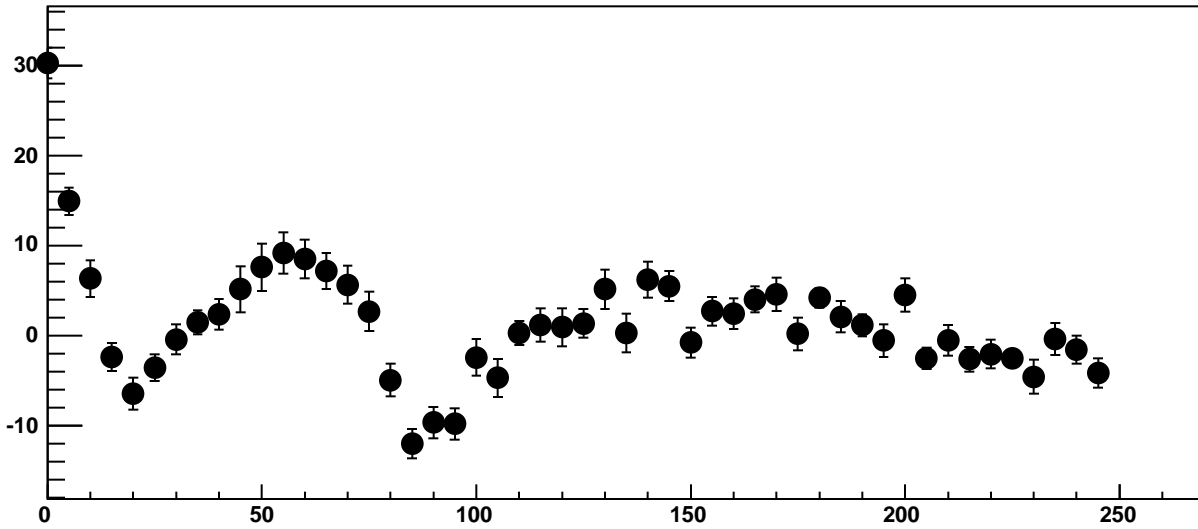


χ^2 / ndf	318.1 / 41
p0	327.5 ± 0.5954
p1	87.12 ± 0.5359
p2	$-7.964\text{e}+07 \pm 8.637\text{e}+05$
p3	$3.706\text{e}+07 \pm 1.816\text{e}+05$
p4	2.033 ± 0.02066

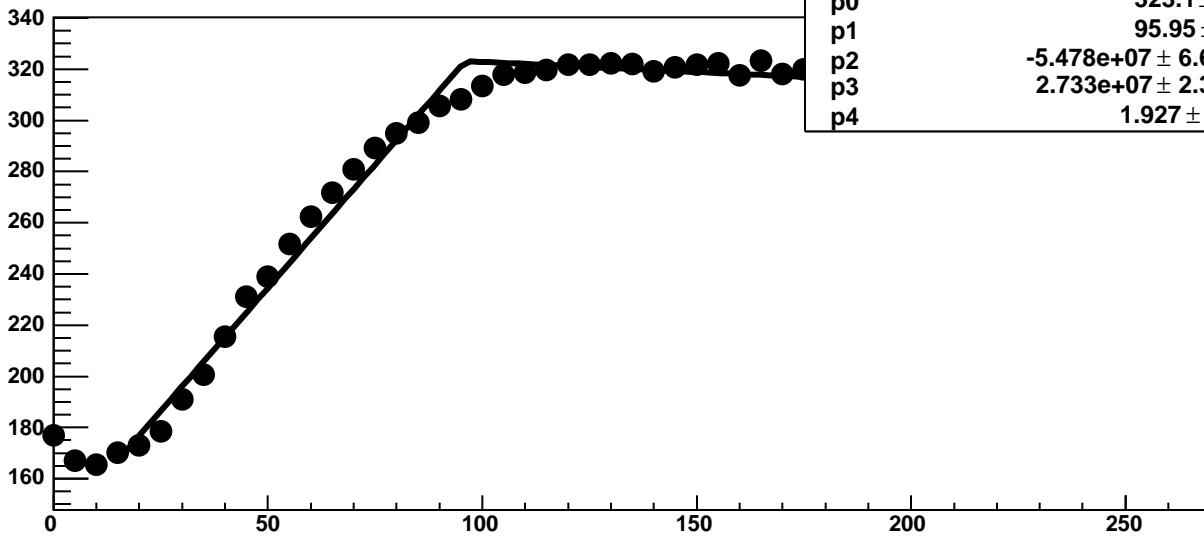
Chip 8, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

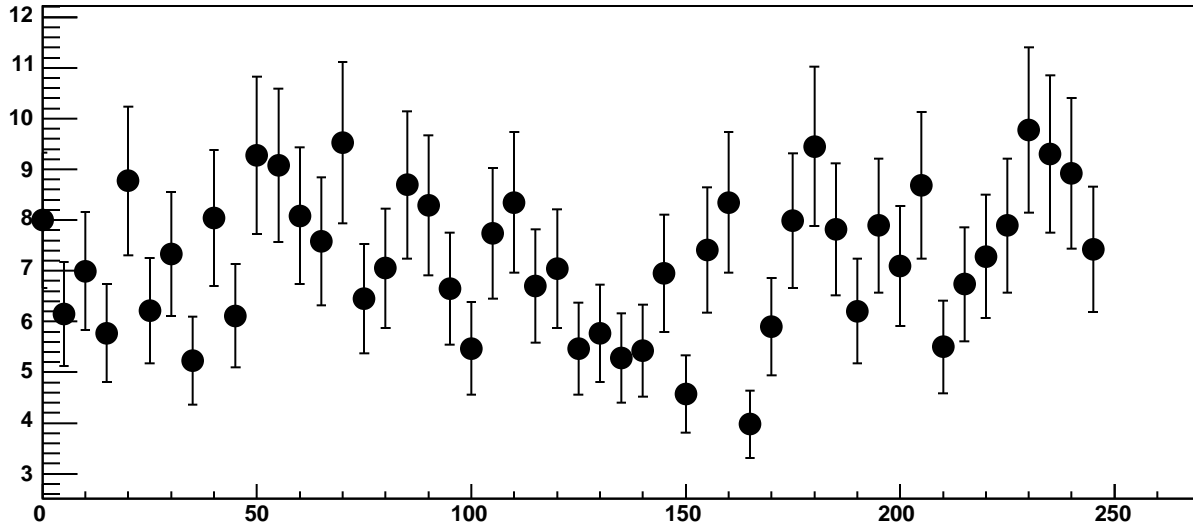


Chip 8, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

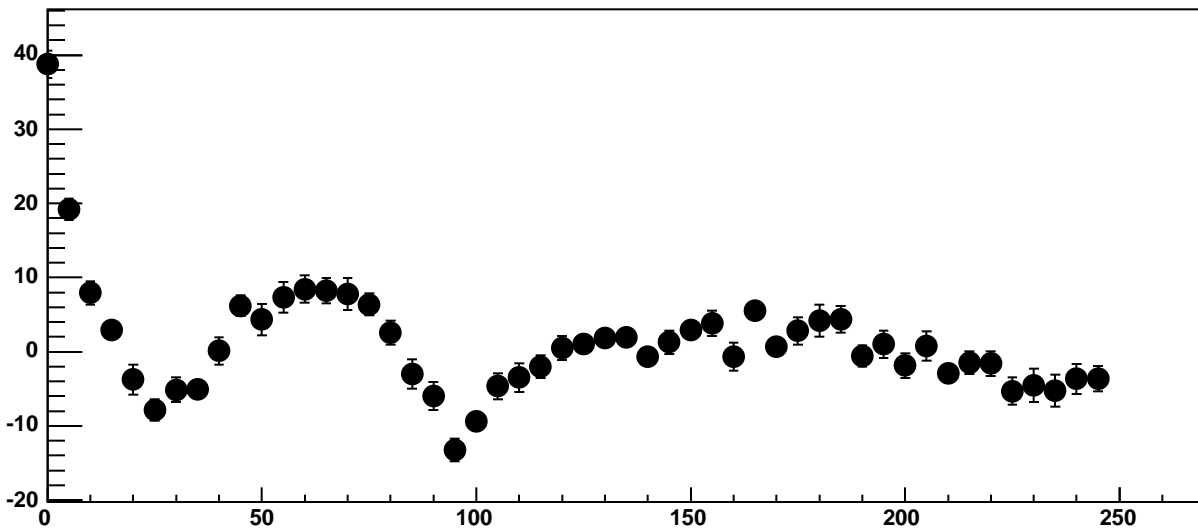


χ^2 / ndf	433.9 / 41
p0	323.1 ± 0.5386
p1	95.95 ± 0.4879
p2	$-5.478e+07 \pm 6.672e+05$
p3	$2.733e+07 \pm 2.394e+05$
p4	1.927 ± 0.01584

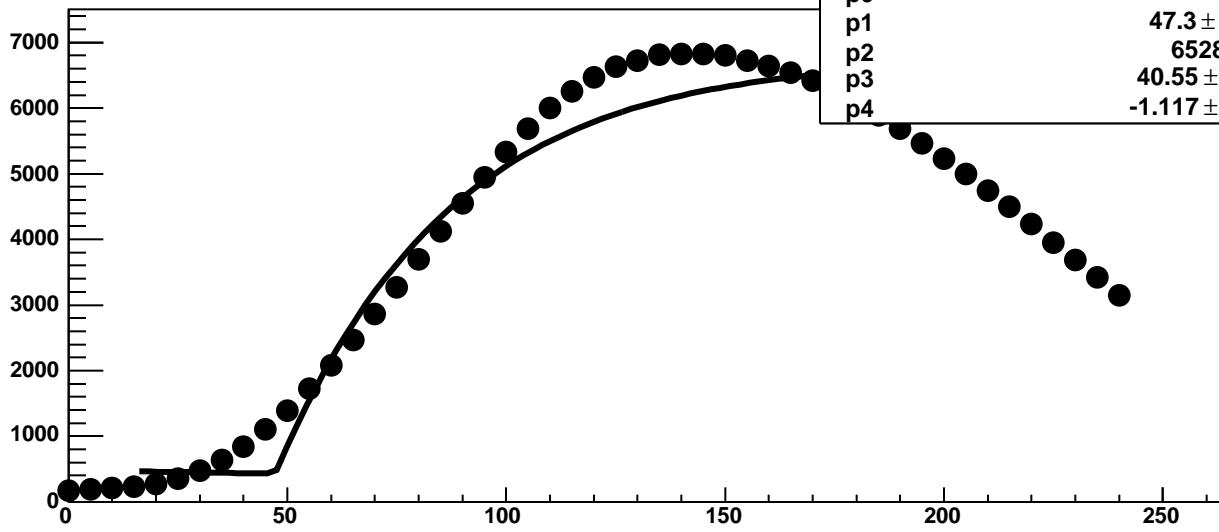
Chip 8, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

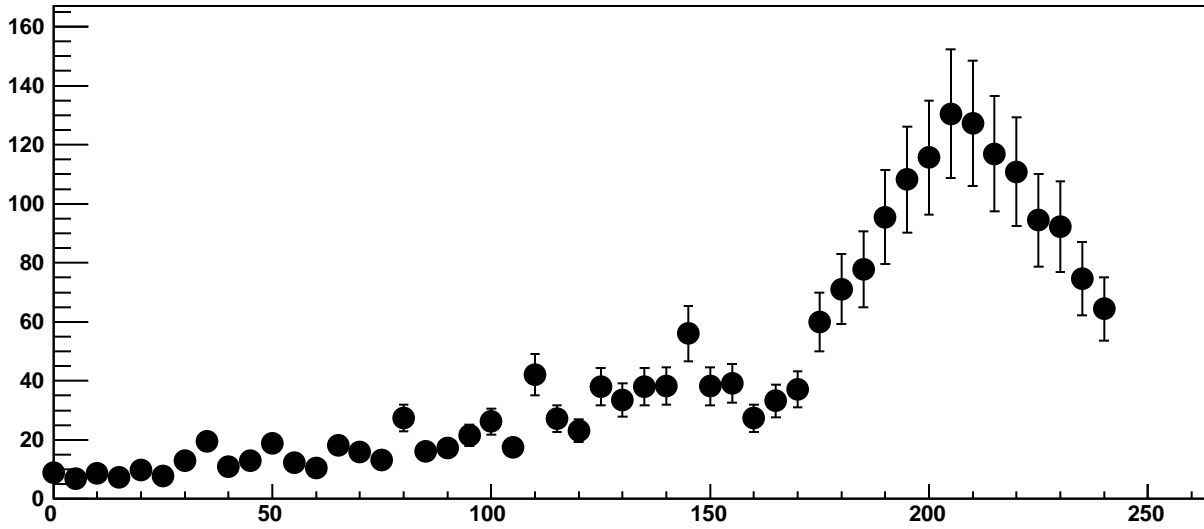


Chip 8, Channel 8, Enable 5, DAC=4000, ADC Mean vs Hold

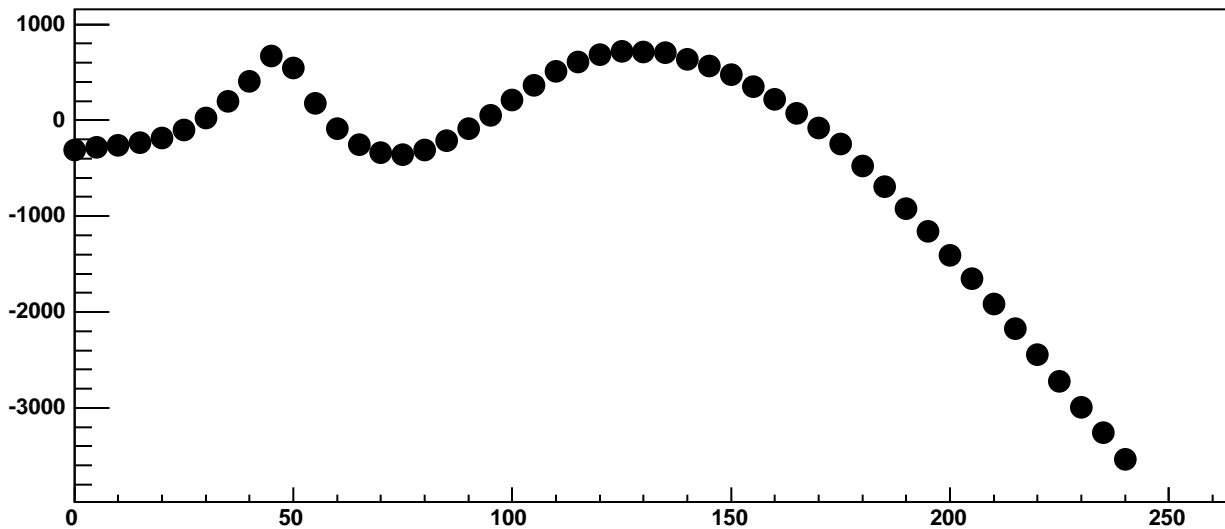


χ^2 / ndf	3.959e+05 / 41
p0	429.7 ± 1.594
p1	47.3 ± 0.01973
p2	6528 ± 10.51
p3	40.55 ± 0.07031
p4	-1.117 ± 0.06281

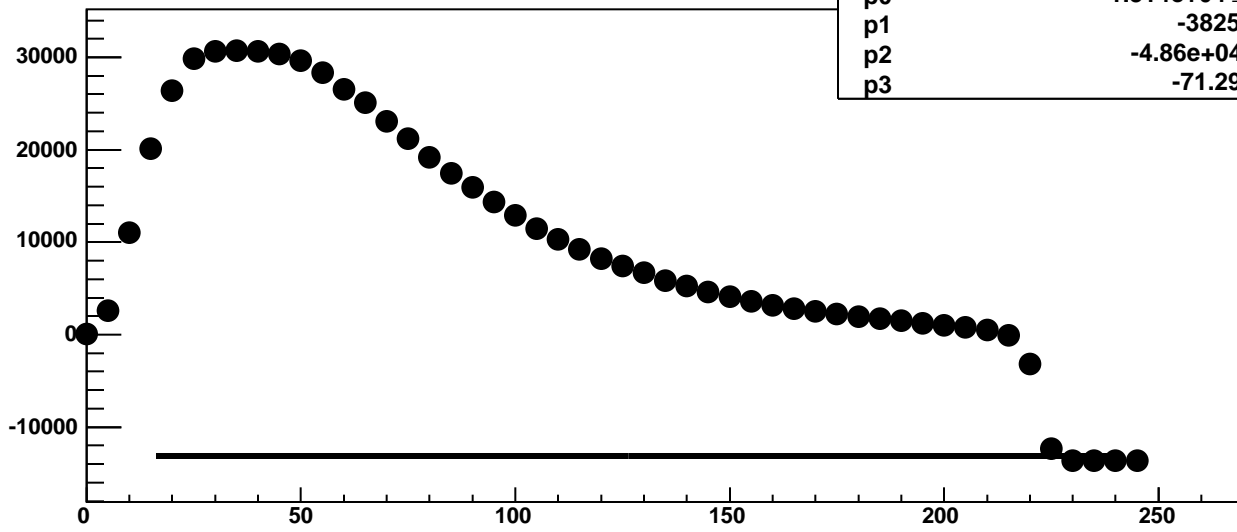
Chip 8, Channel 8, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 8, Enable 5, DAC=4000, ADC Residuals vs Hold

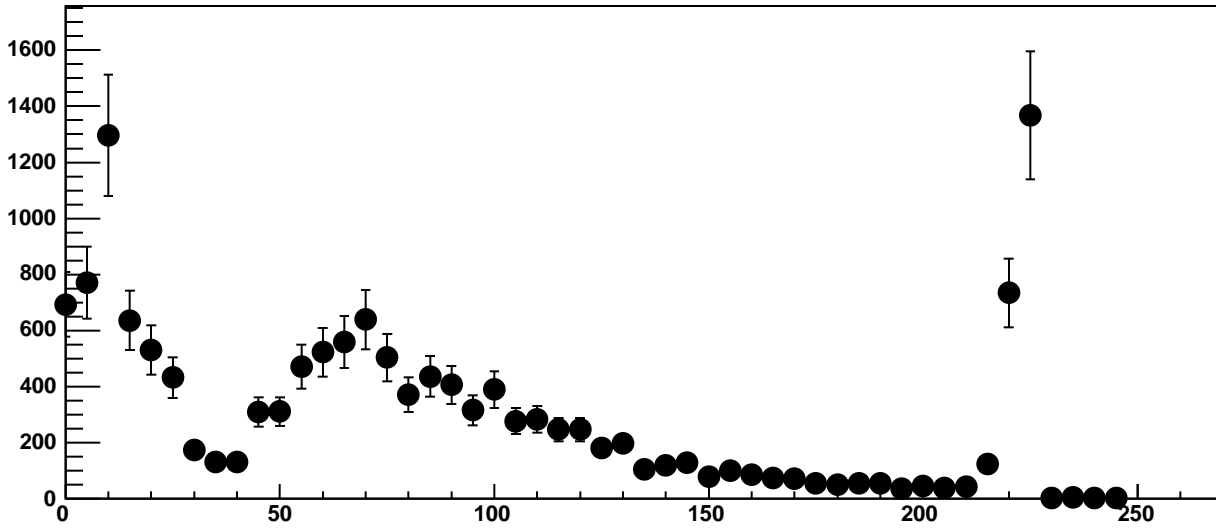


Chip 8, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold

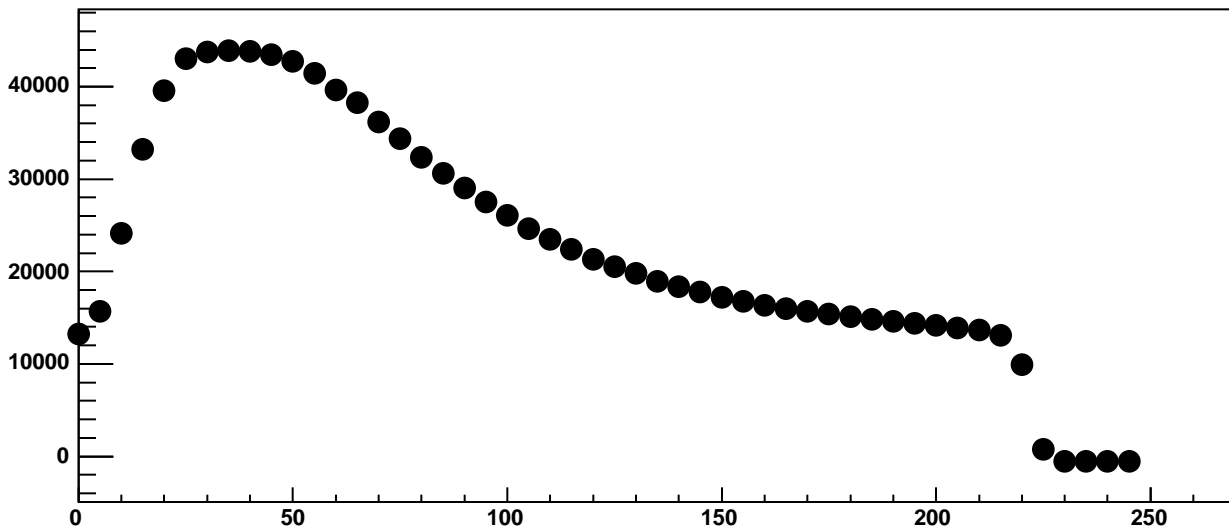


χ^2 / ndf	2.944e+07 / 42
p0	-1.314e+04 \pm 0.5575
p1	-3825 \pm 1.909
p2	-4.86e+04 \pm 1.414
p3	-71.29 \pm 1.414

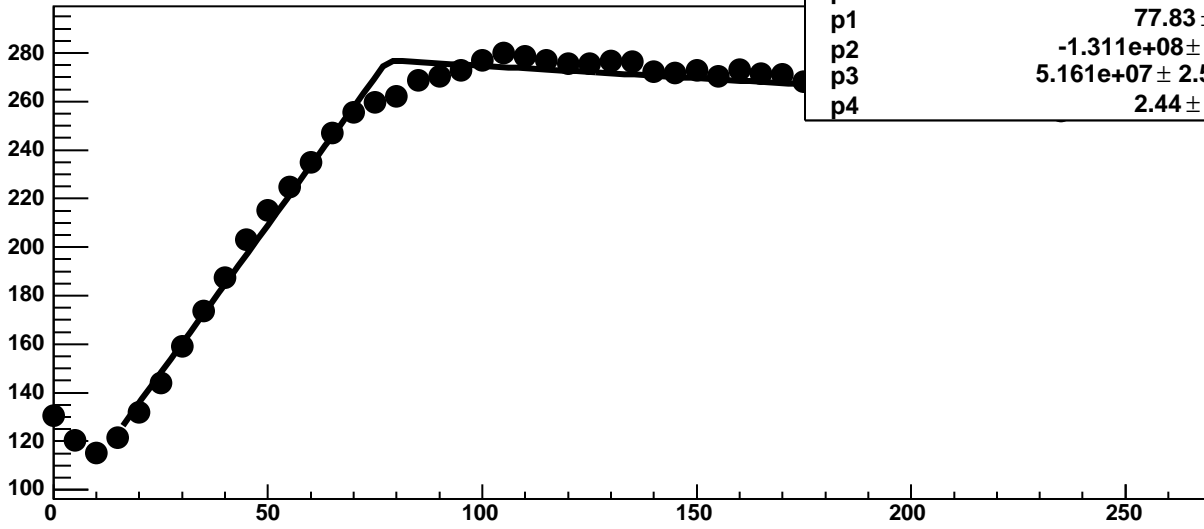
Chip 8, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold

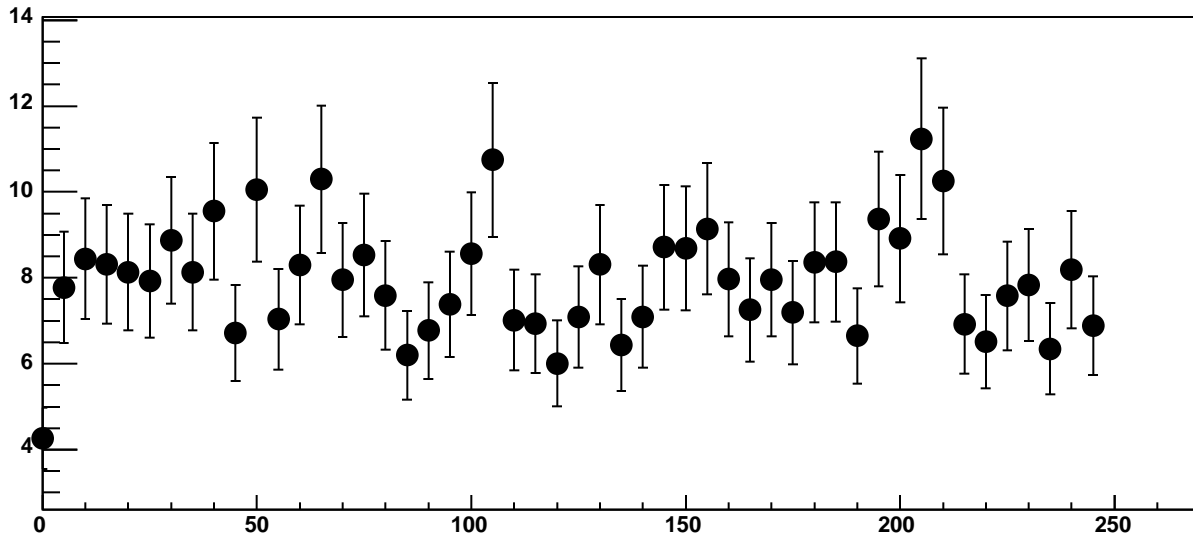


Chip 8, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

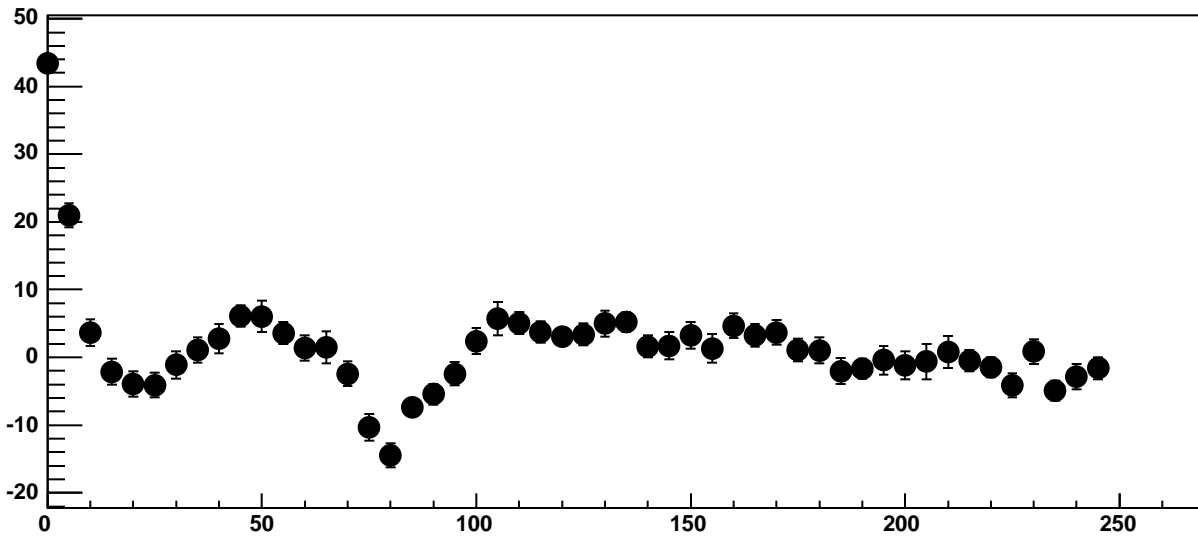


χ^2 / ndf	276.1 / 41
p0	276.9 ± 0.5589
p1	77.83 ± 0.4823
p2	$-1.311\text{e}+08 \pm 1.6\text{e}+06$
p3	$5.161\text{e}+07 \pm 2.592\text{e}+05$
p4	2.44 ± 0.02844

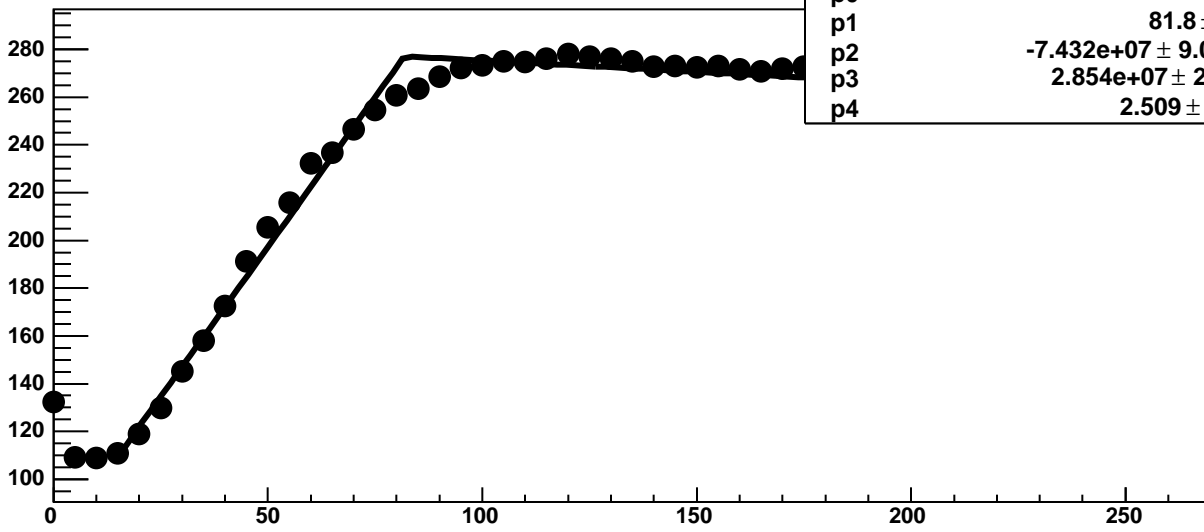
Chip 8, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

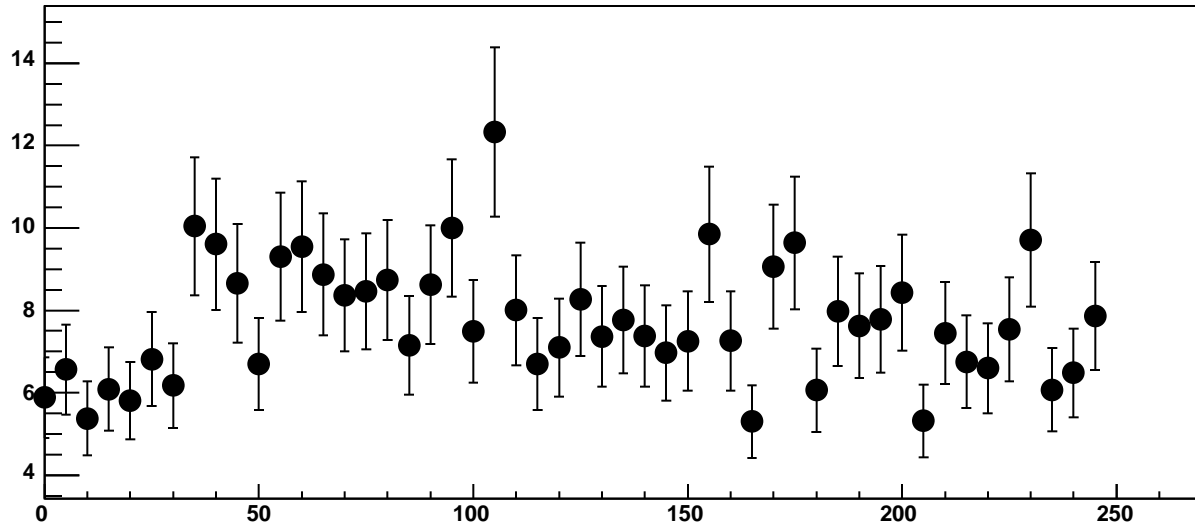


Chip 8, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

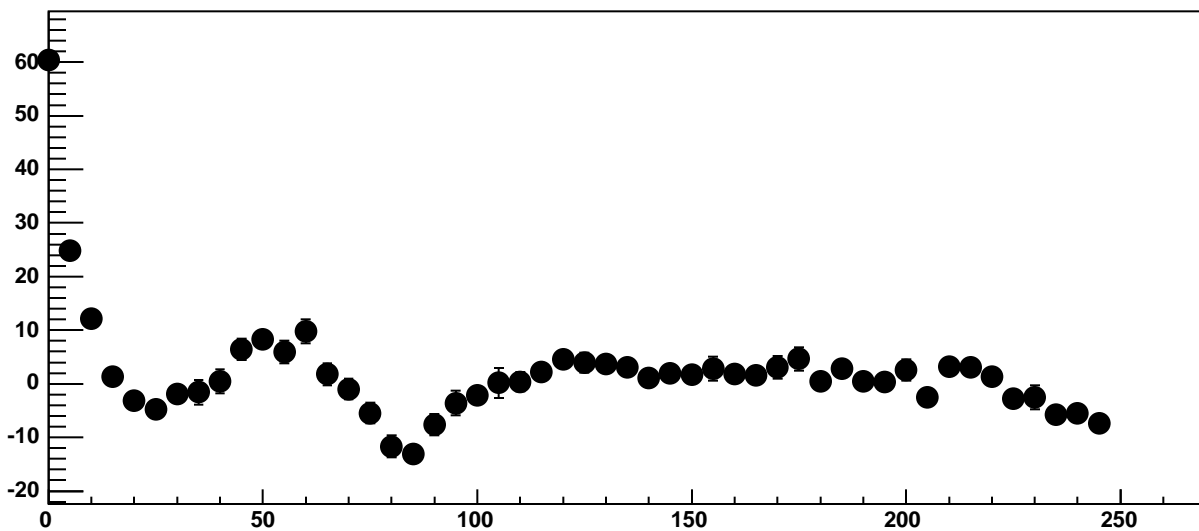


χ^2 / ndf	298.7 / 41
p0	277.1 ± 0.6146
p1	81.8 ± 0.4572
p2	$-7.432e+07 \pm 9.067e+05$
p3	$2.854e+07 \pm 2.33e+05$
p4	2.509 ± 0.02243

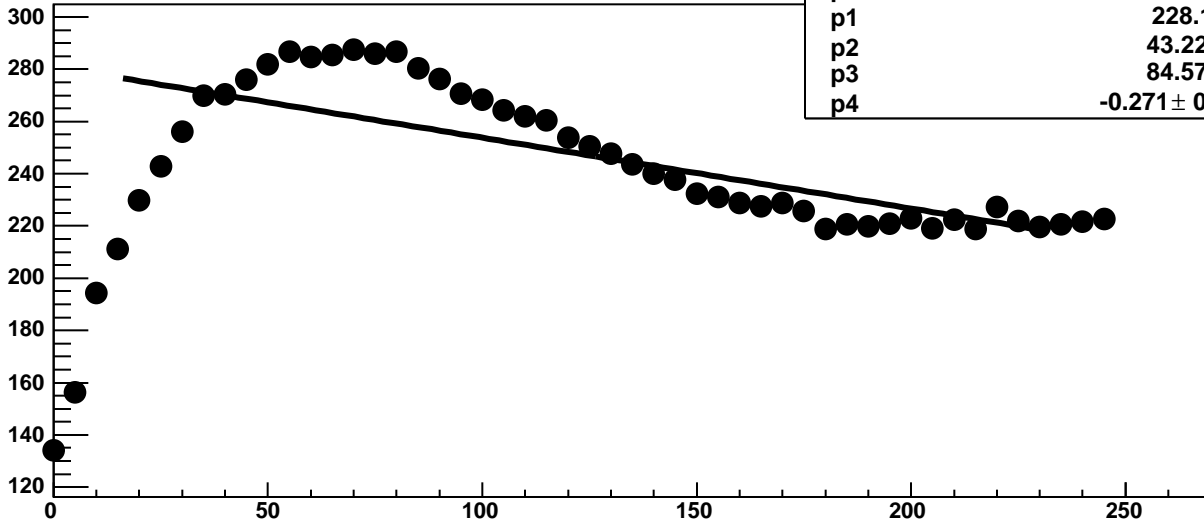
Chip 8, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold

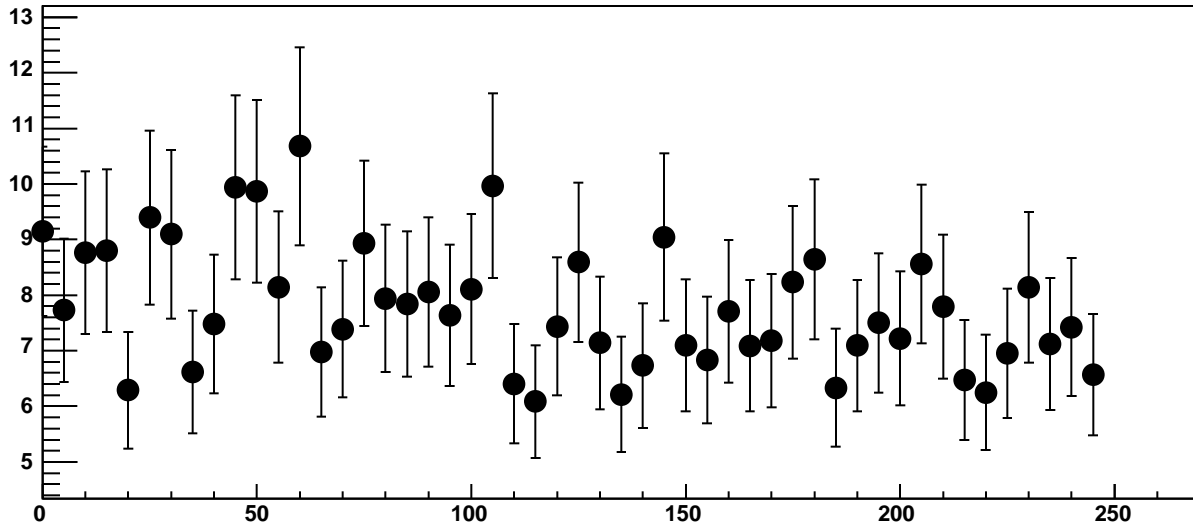


Chip 8, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold

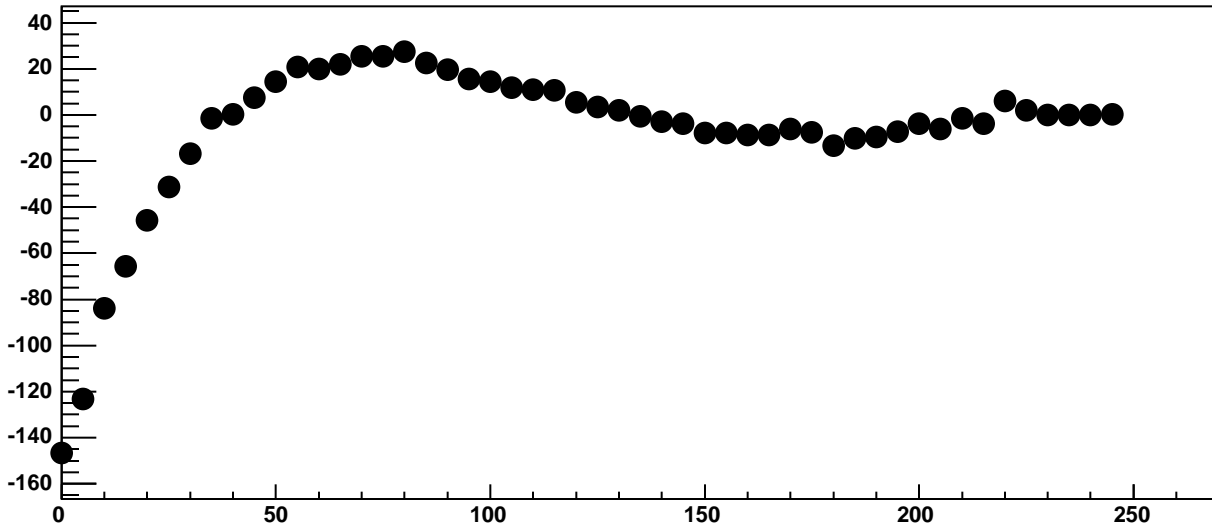


χ^2 / ndf	4260 / 41
p0	219.1 ± 1.171
p1	228.1 ± 4.361
p2	43.22 ± 90.05
p3	84.57 ± 178.9
p4	-0.271 ± 0.004264

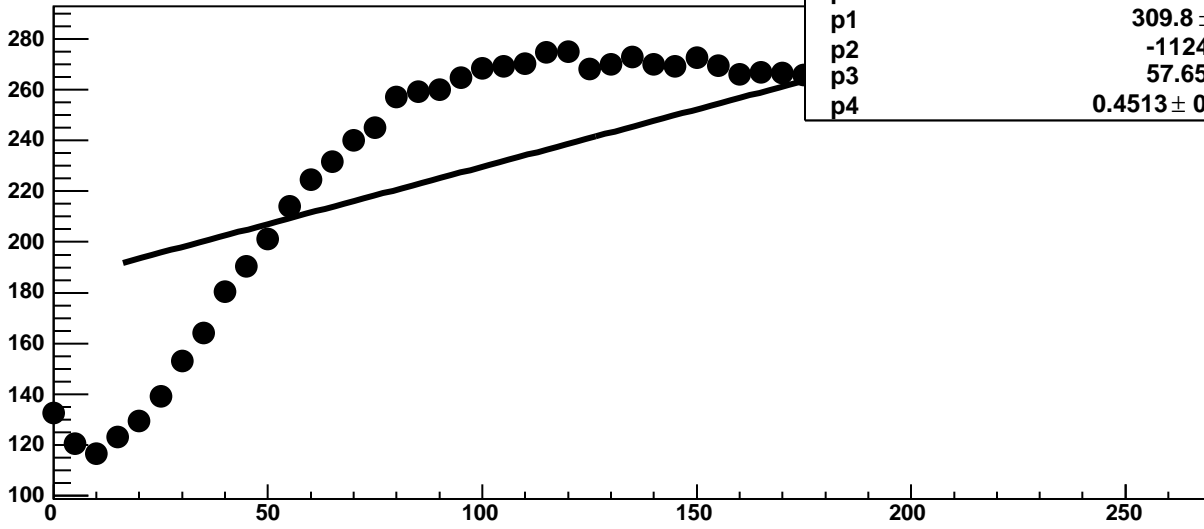
Chip 8, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold

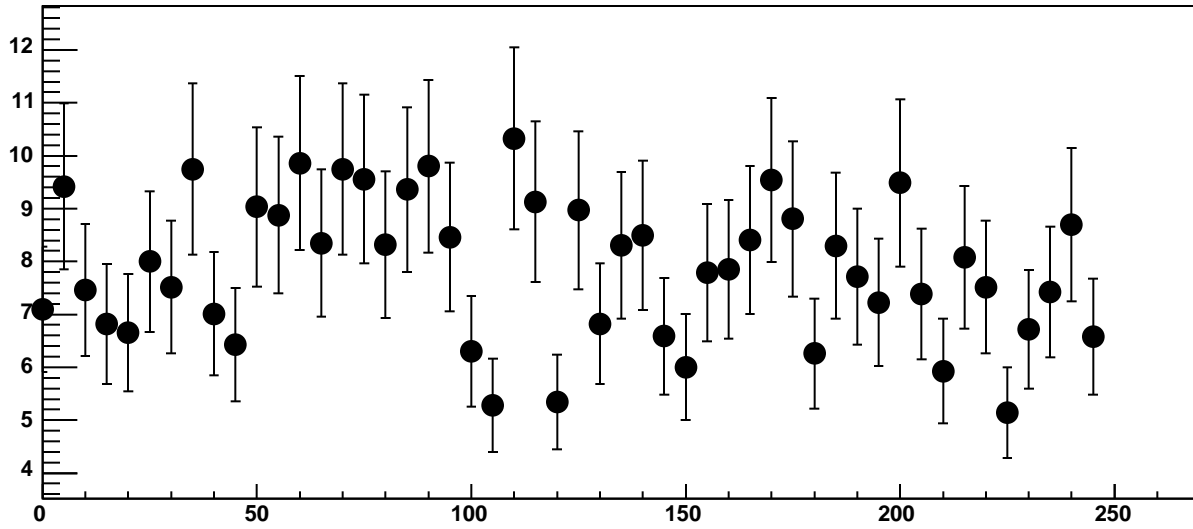


Chip 8, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

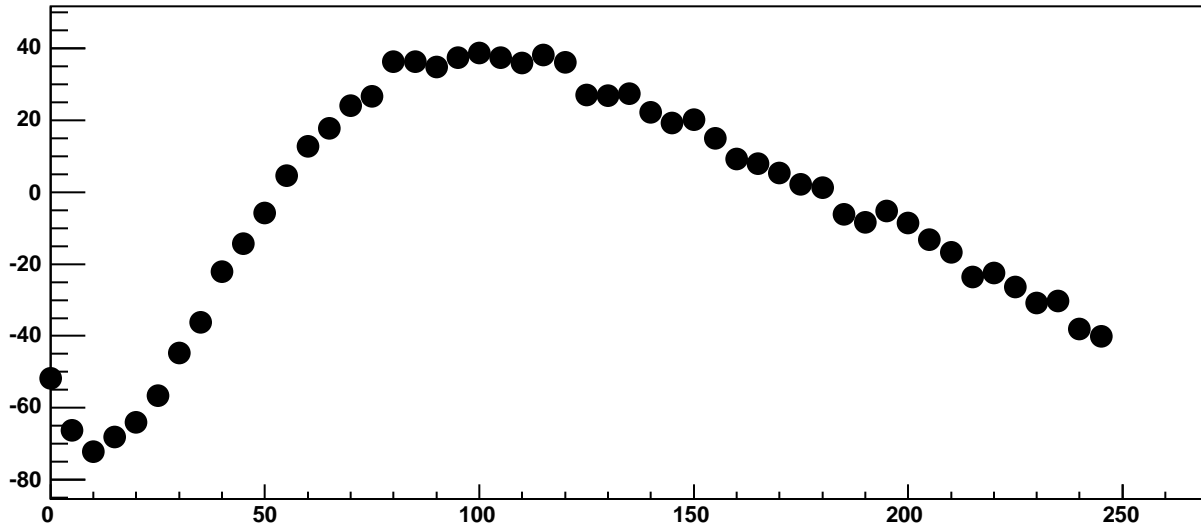


χ^2 / ndf	1.408e+04 / 41
p0	324.3 ± 0.2543
p1	309.8 ± 0.5635
p2	-1124 ± 84.85
p3	57.65 ± 1.414
p4	0.4513 ± 0.001338

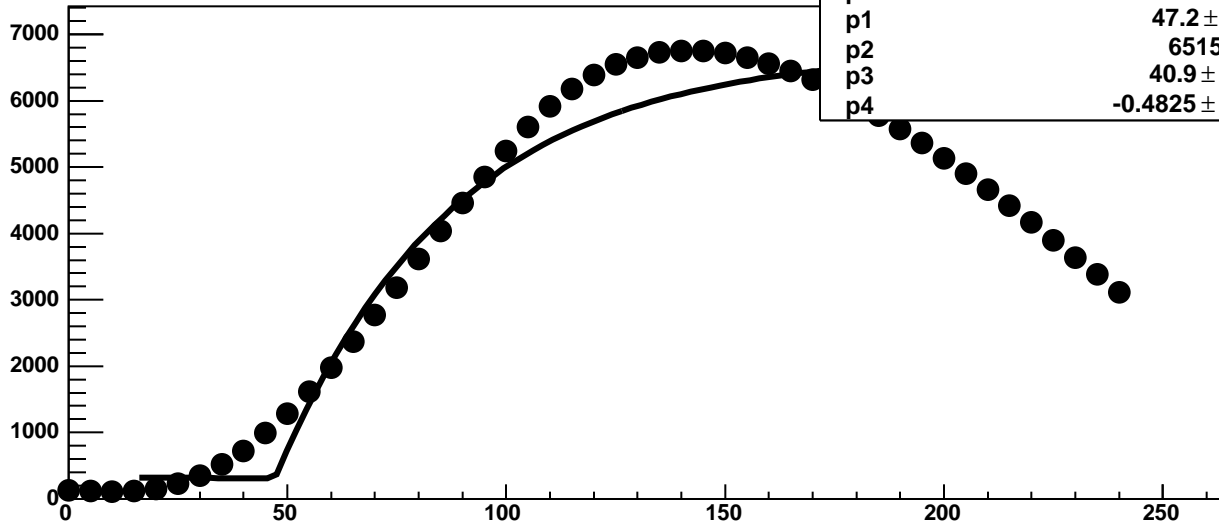
Chip 8, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

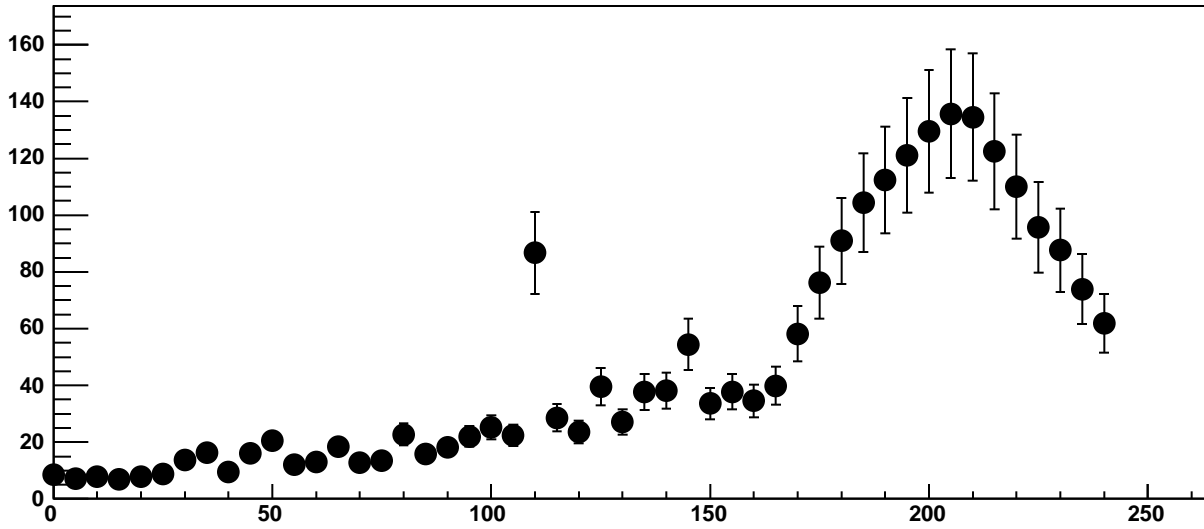


Chip 8, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold

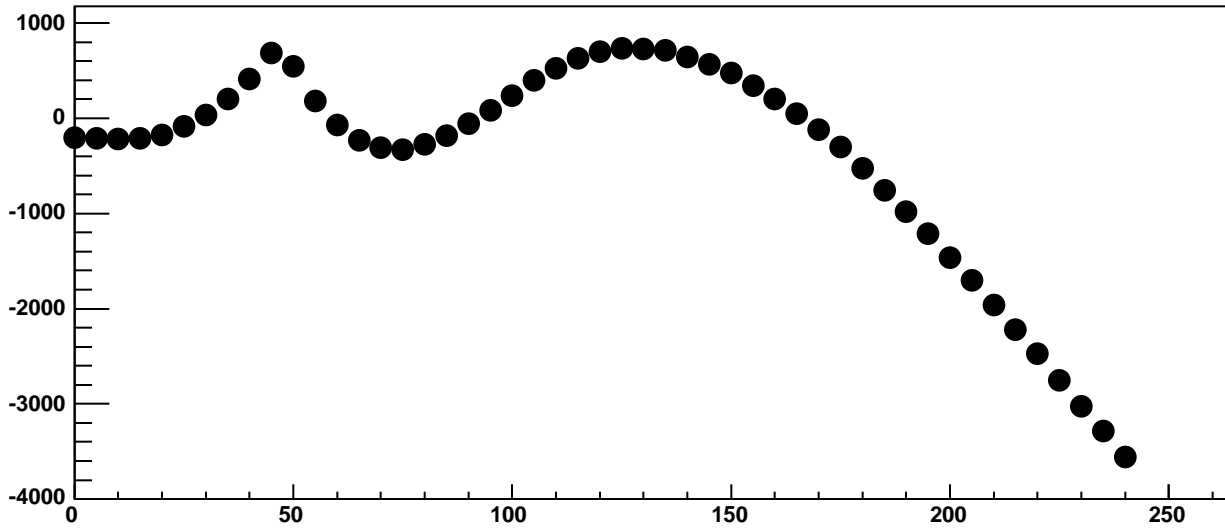


χ^2 / ndf	3.954e+05 / 41
p0	307.5 ± 1.633
p1	47.2 ± 0.02071
p2	6515 ± 10.73
p3	40.9 ± 0.07243
p4	-0.4825 ± 0.06317

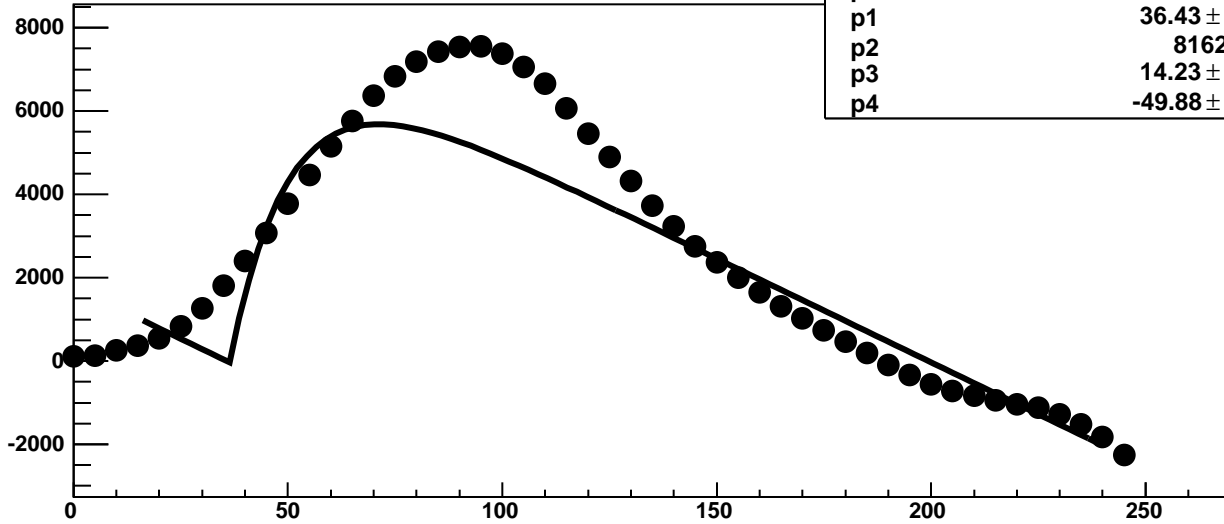
Chip 8, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold

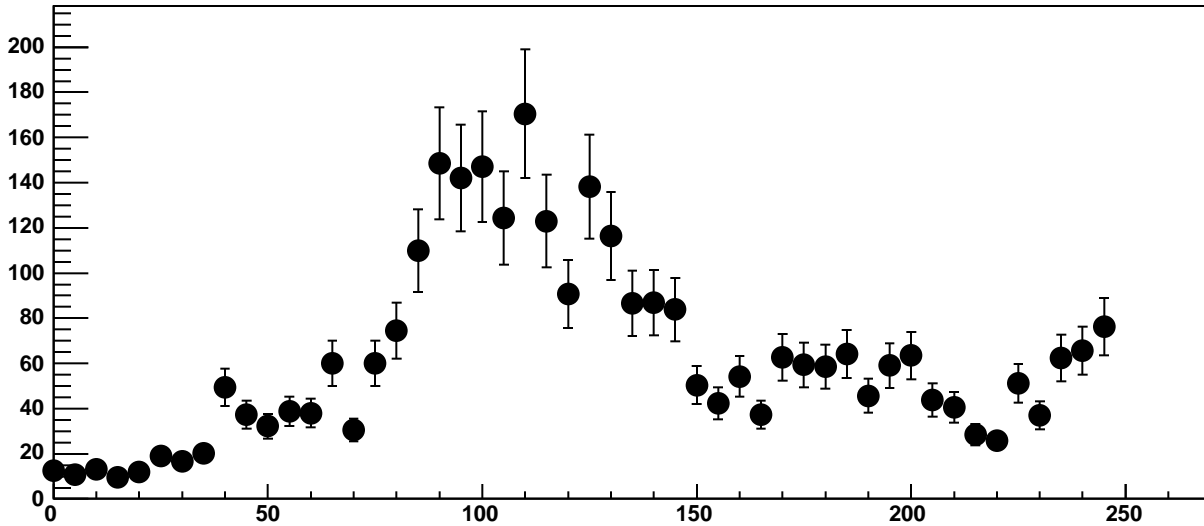


Chip 8, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold

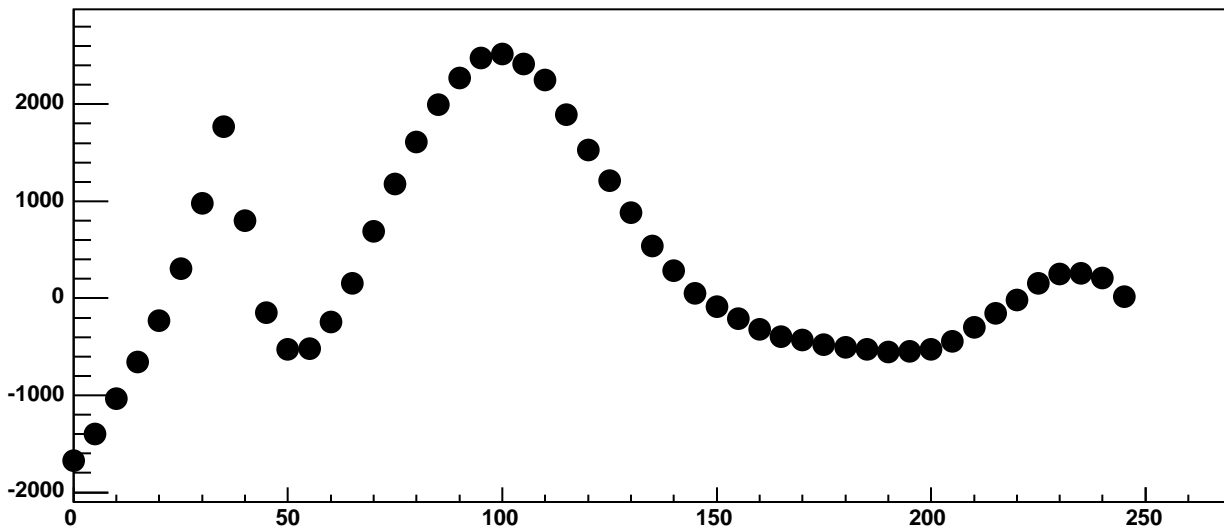


χ^2 / ndf	4.199e+05 / 41
p0	-36.82 ± 1.847
p1	36.43 ± 0.02458
p2	8162 ± 8.429
p3	14.23 ± 0.03704
p4	-49.88 ± 0.04892

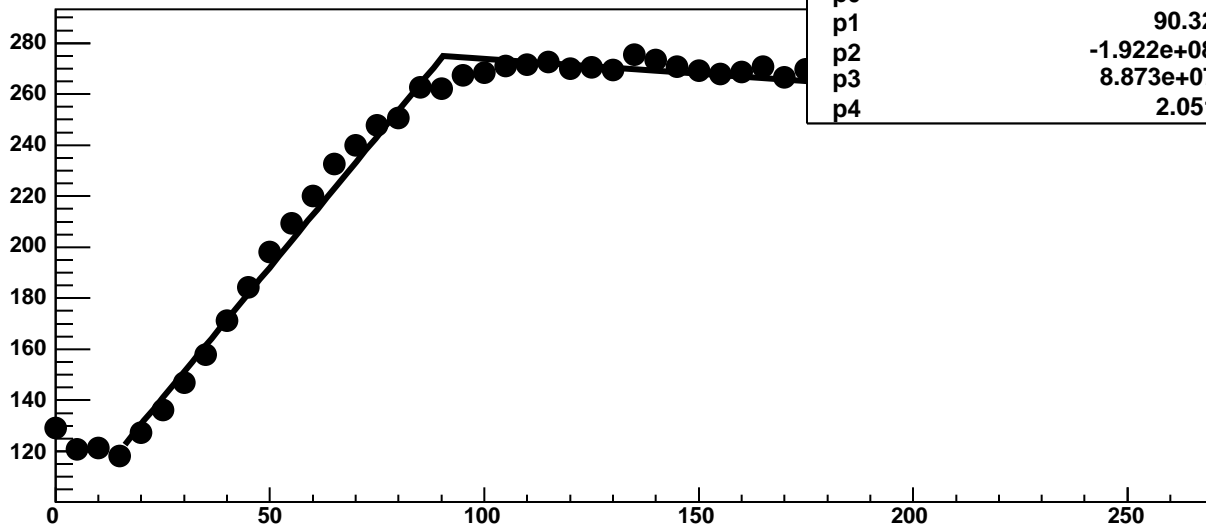
Chip 8, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold

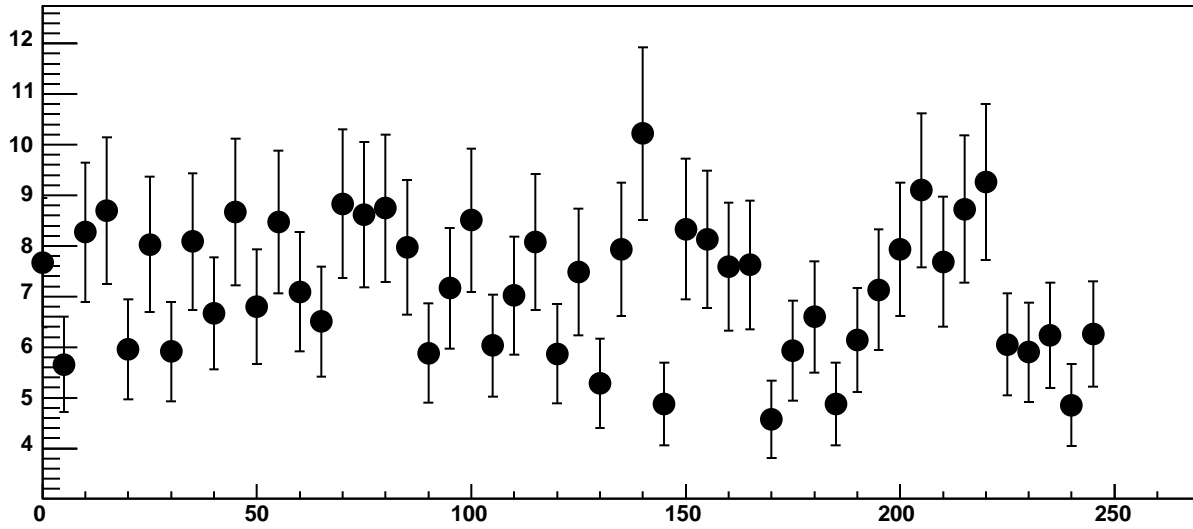


Chip 8, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

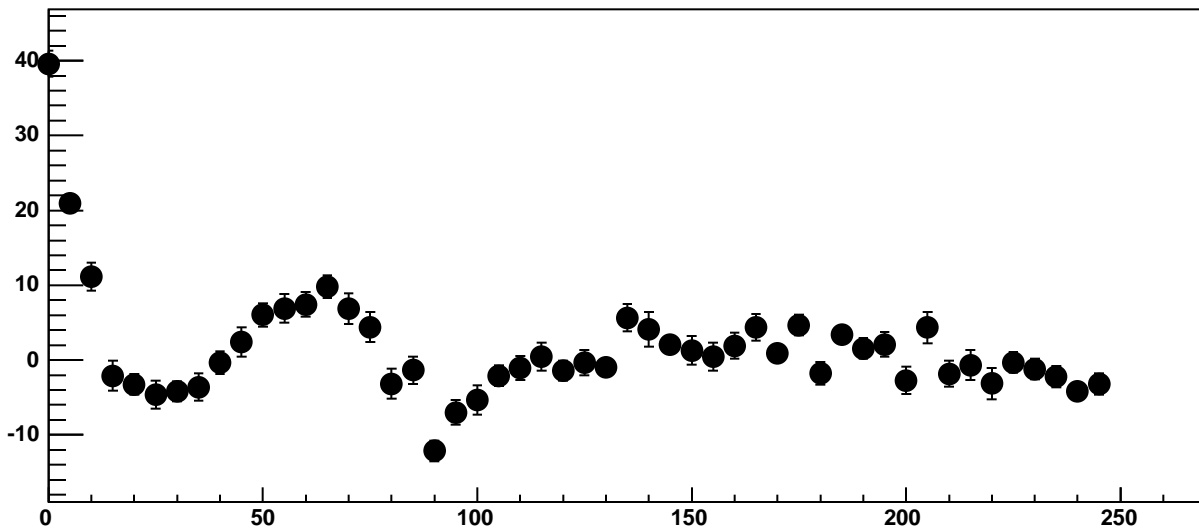


χ^2 / ndf	327.6 / 41
p0	$274.9 \pm \text{nan}$
p1	$90.32 \pm \text{nan}$
p2	$-1.922\text{e}+08 \pm \text{nan}$
p3	$8.873\text{e}+07 \pm \text{nan}$
p4	$2.051 \pm \text{nan}$

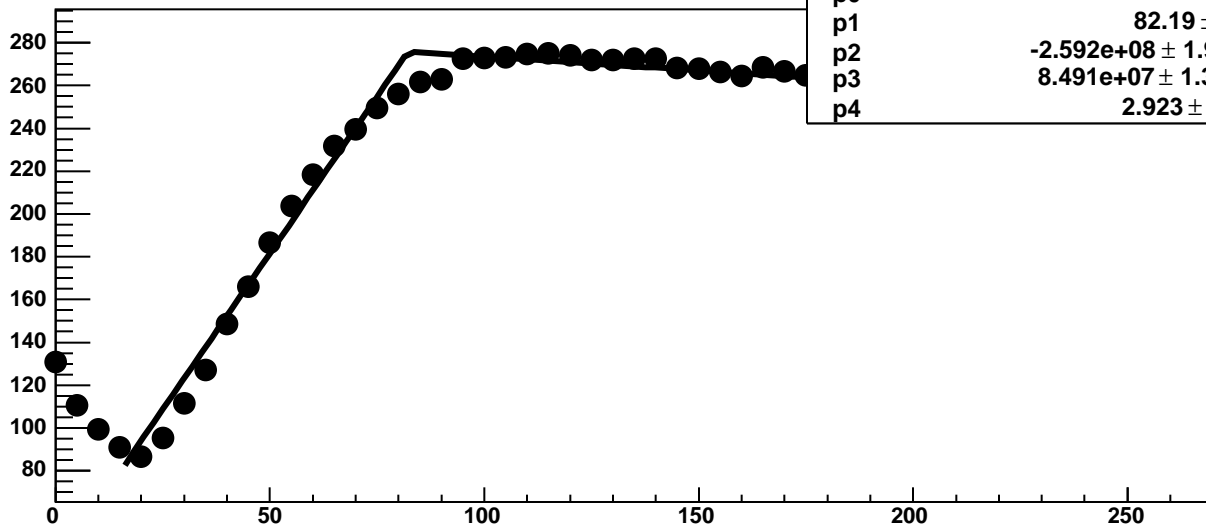
Chip 8, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

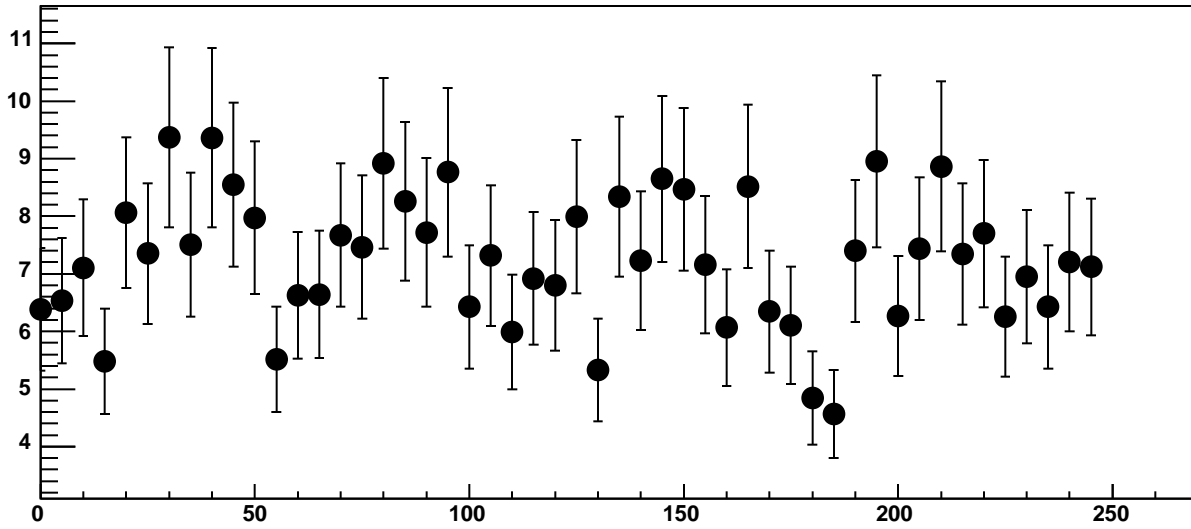


Chip 8, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

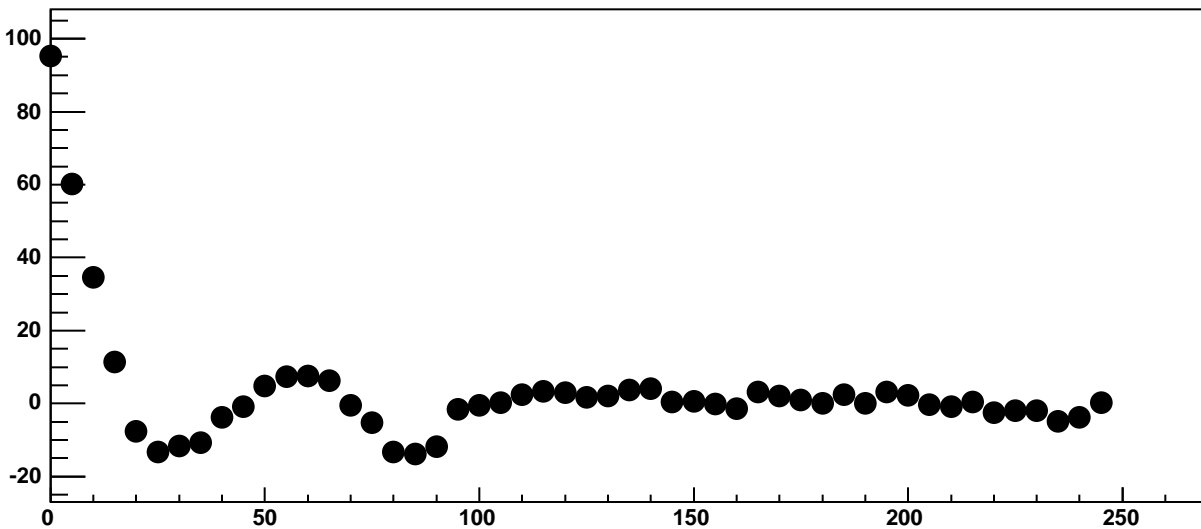


χ^2 / ndf	528.7 / 41
p0	275.8 ± 0.5607
p1	82.19 ± 0.3495
p2	$-2.592\text{e}+08 \pm 1.954\text{e}+06$
p3	$8.491\text{e}+07 \pm 1.355\text{e}+05$
p4	2.923 ± 0.02175

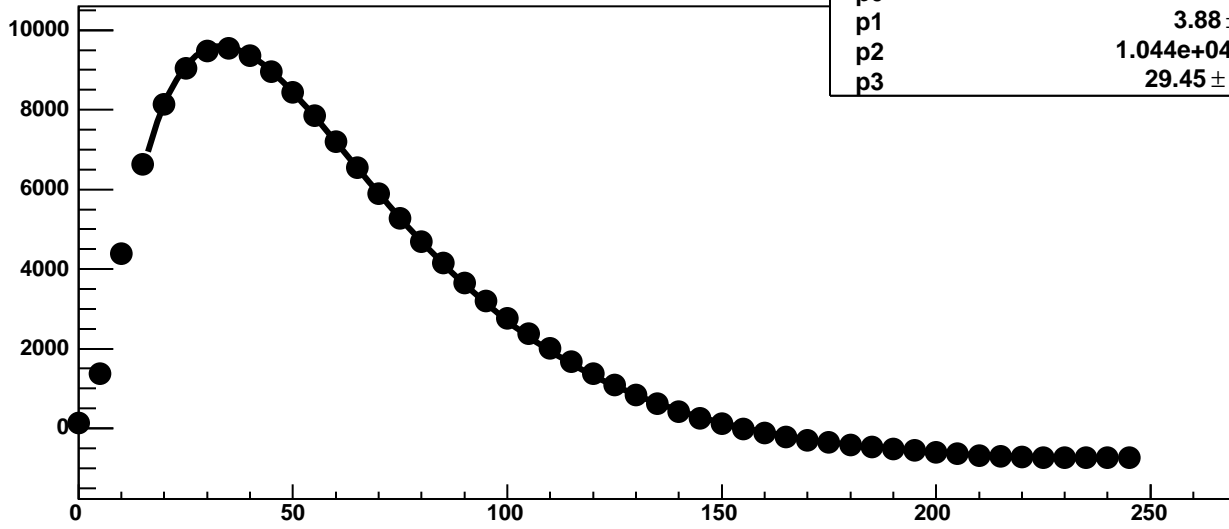
Chip 8, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

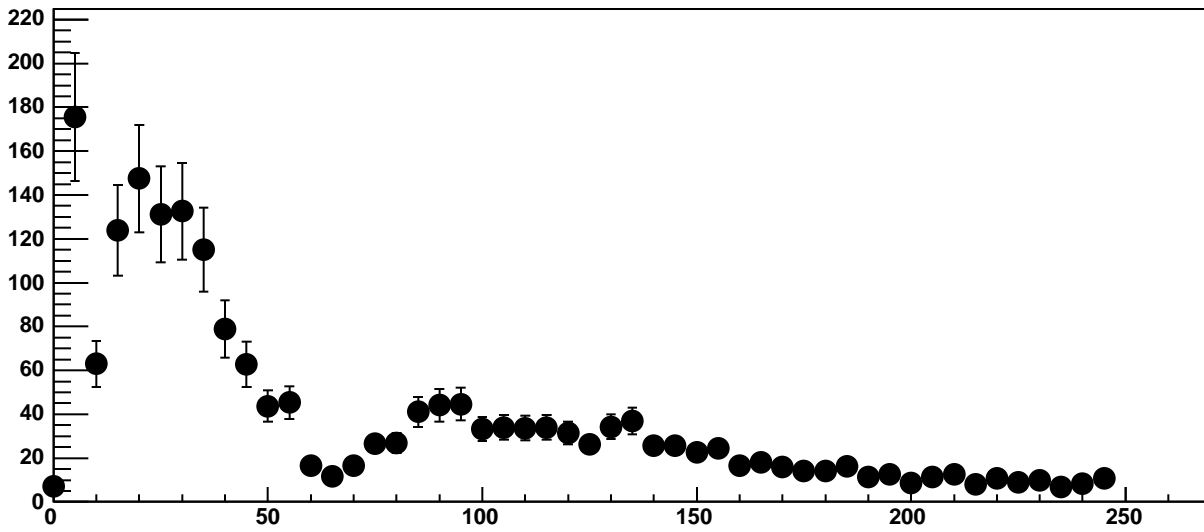


Chip 8, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold

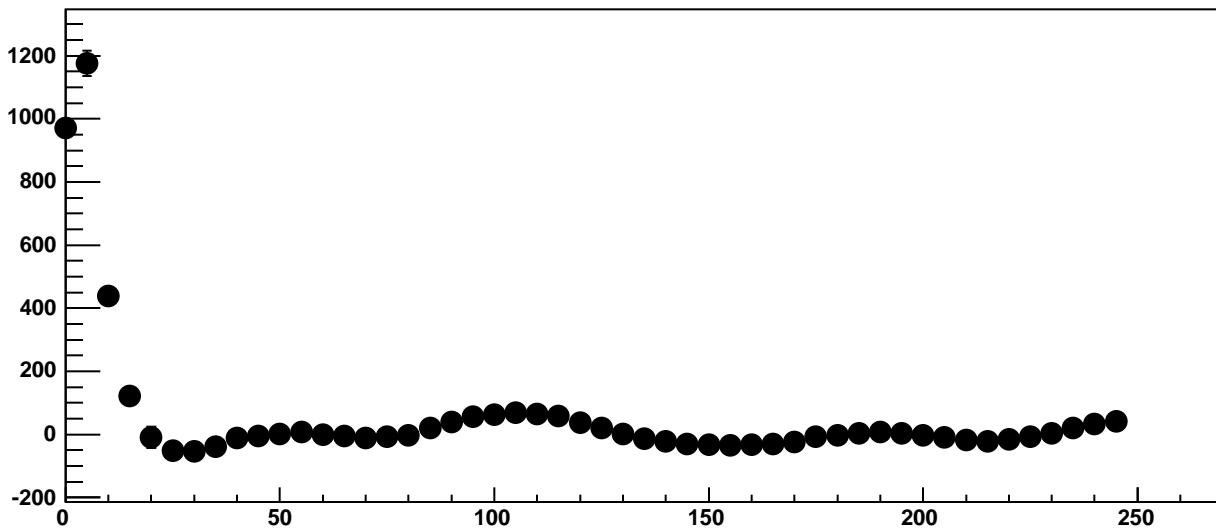


χ^2 / ndf	1411 / 42
p0	-841.2 ± 0.8774
p1	3.88 ± 0.0488
p2	1.044e+04 ± 6.234
p3	29.45 ± 0.01868

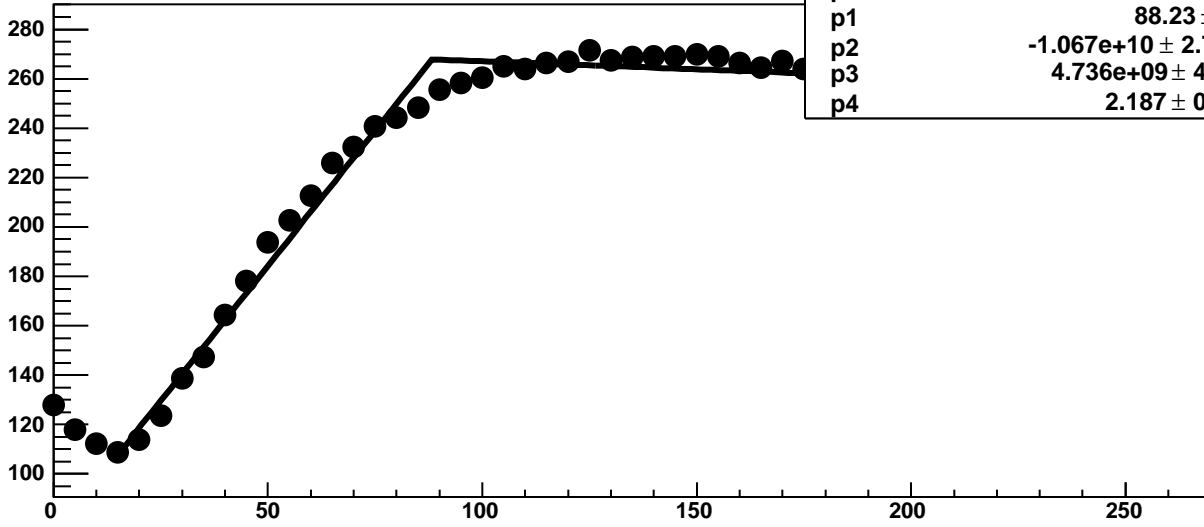
Chip 8, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold

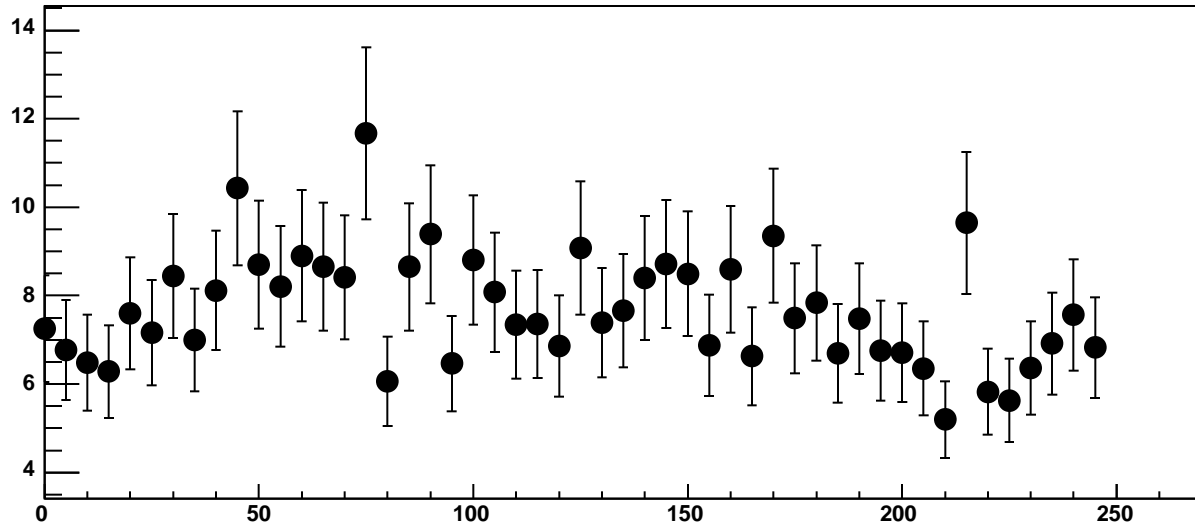


Chip 8, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

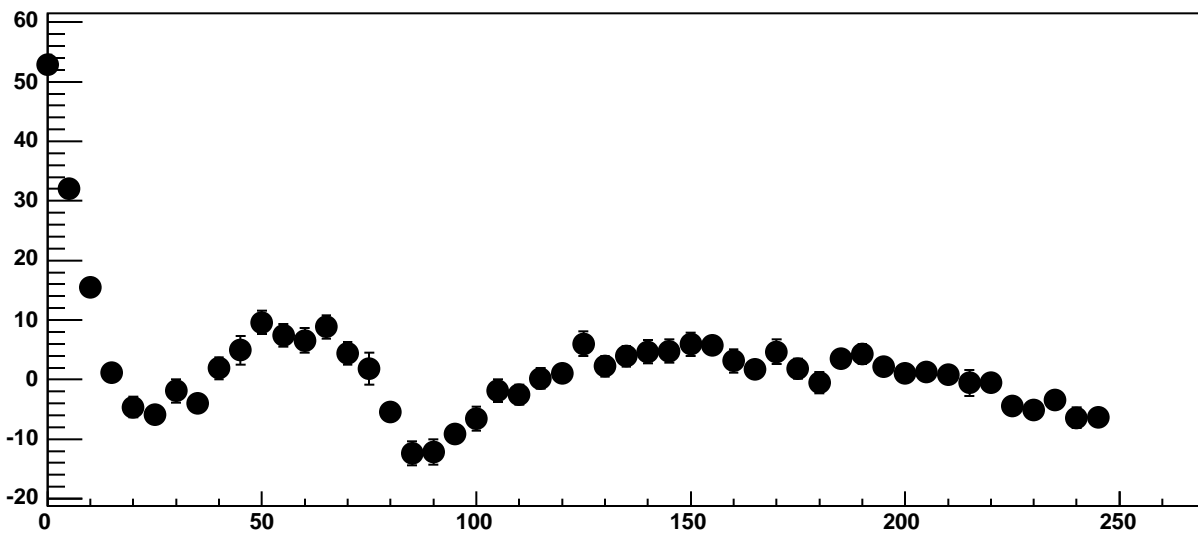


χ^2 / ndf	361.6 / 41
p0	267.8 ± 0.6425
p1	88.23 ± 0.3548
p2	$-1.067\text{e}+10 \pm 2.728\text{e}+07$
p3	$4.736\text{e}+09 \pm 4.32\text{e}+05$
p4	2.187 ± 0.003656

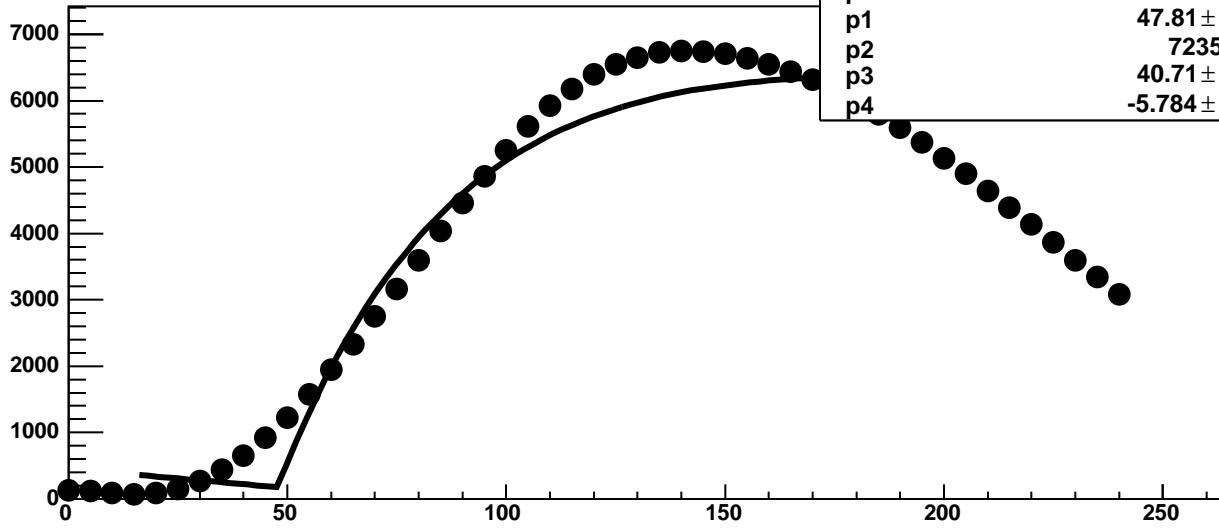
Chip 8, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

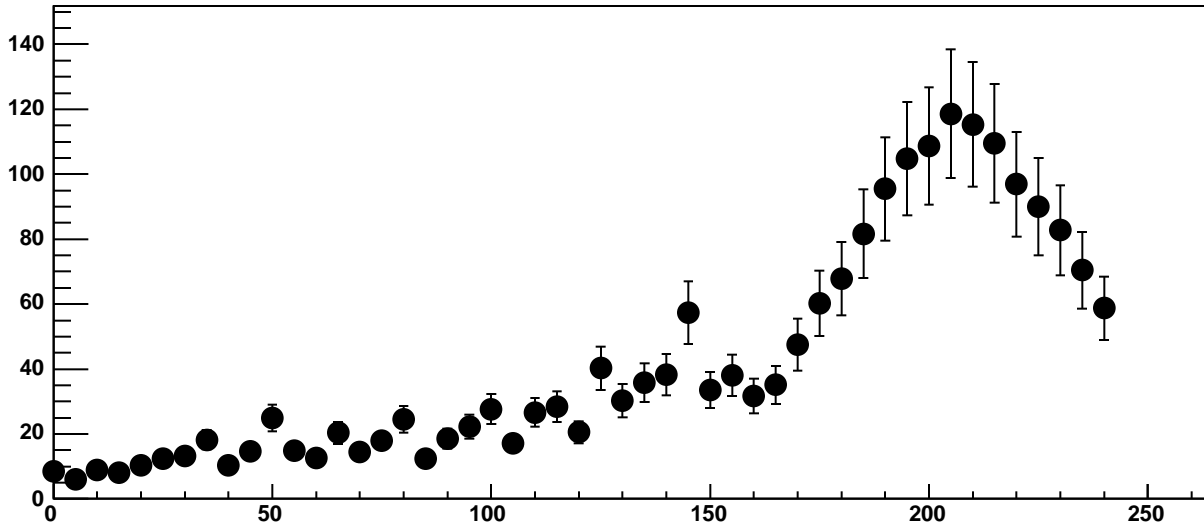


Chip 8, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold

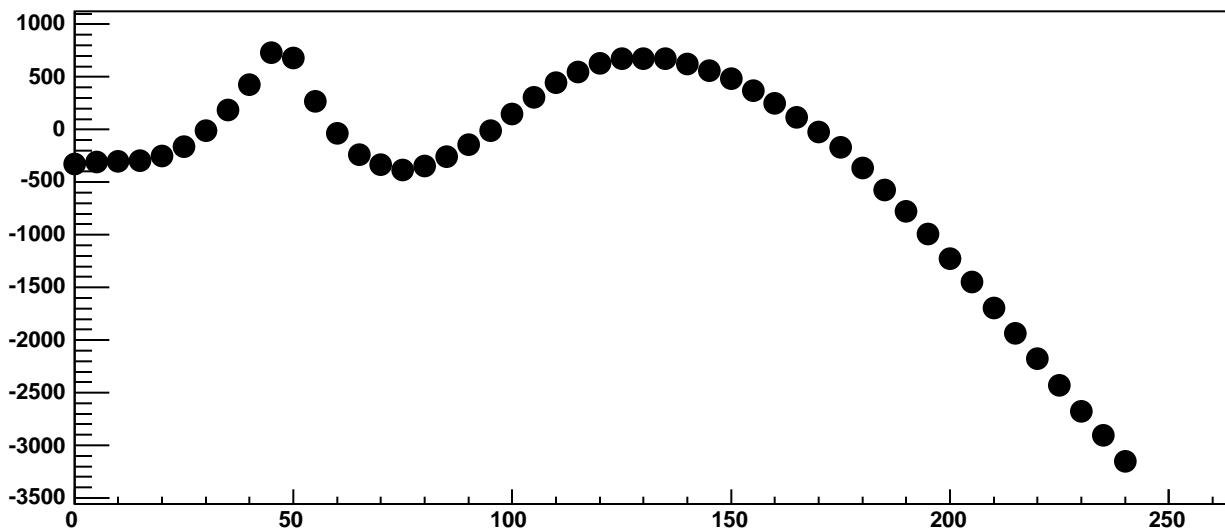


χ^2 / ndf	3.979e+05 / 41
p0	175.5 ± 1.624
p1	47.81 ± 0.02007
p2	7235 ± 10.89
p3	40.71 ± 0.06743
p4	-5.784 ± 0.06428

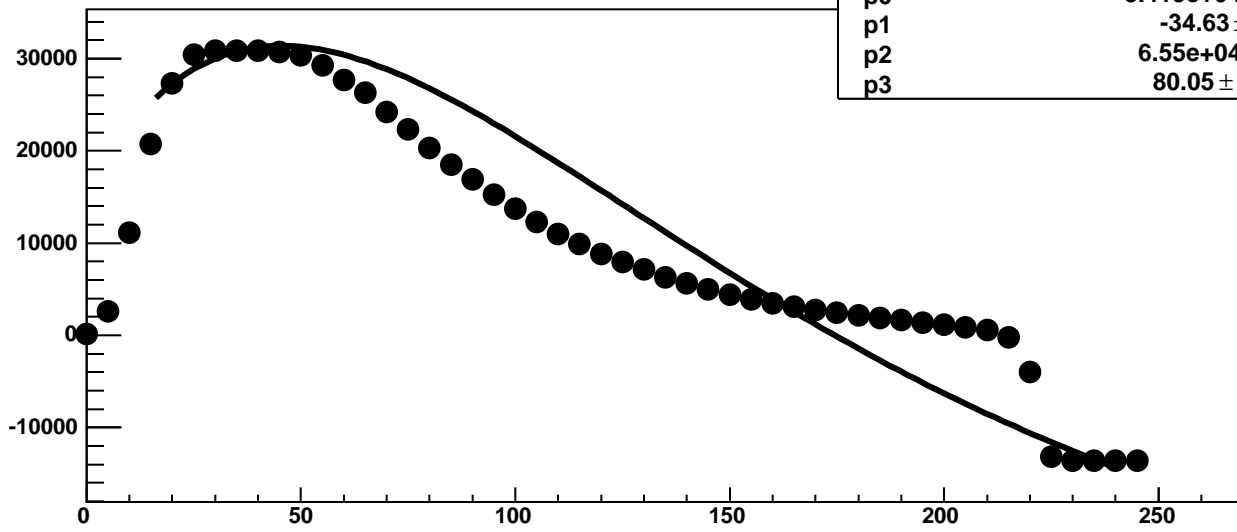
Chip 8, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

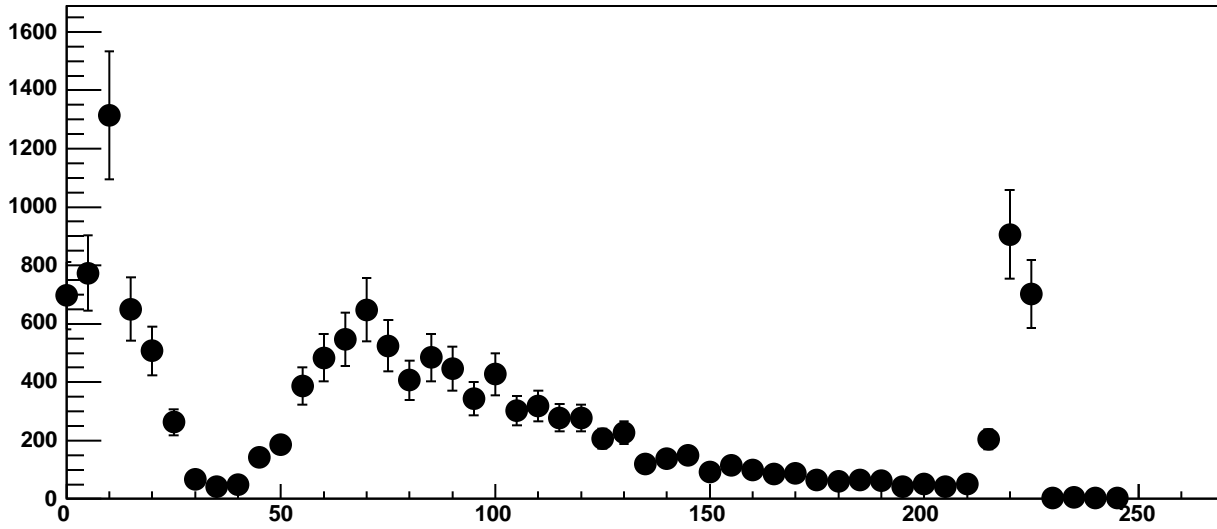


Chip 8, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold

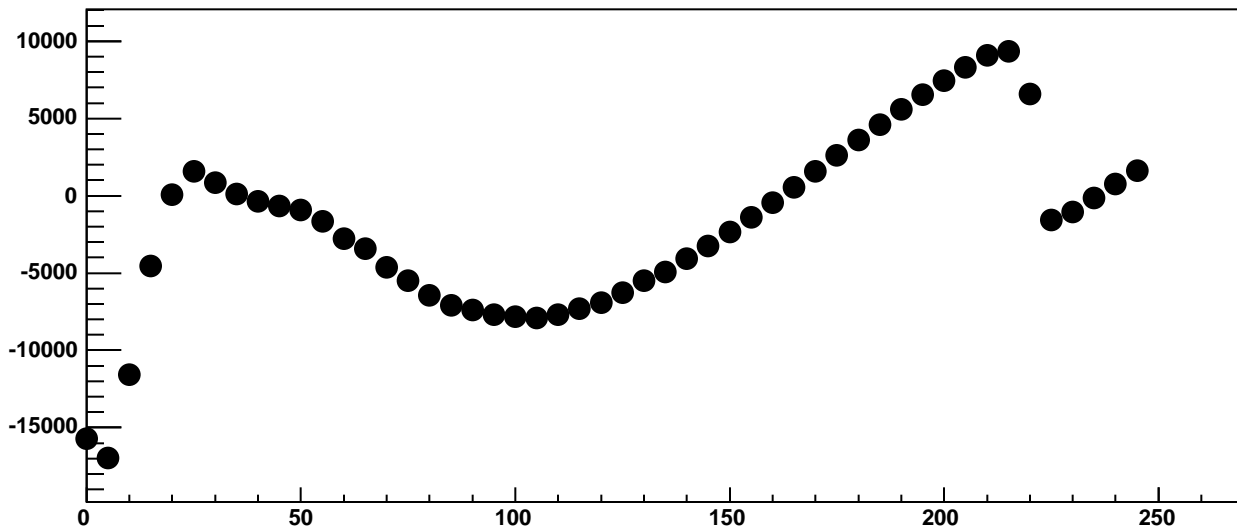


χ^2 / ndf	4.545e+06 / 42
p0	-3.413e+04 ± 28.55
p1	-34.63 ± 0.1145
p2	6.55e+04 ± 24.48
p3	80.05 ± 0.06707

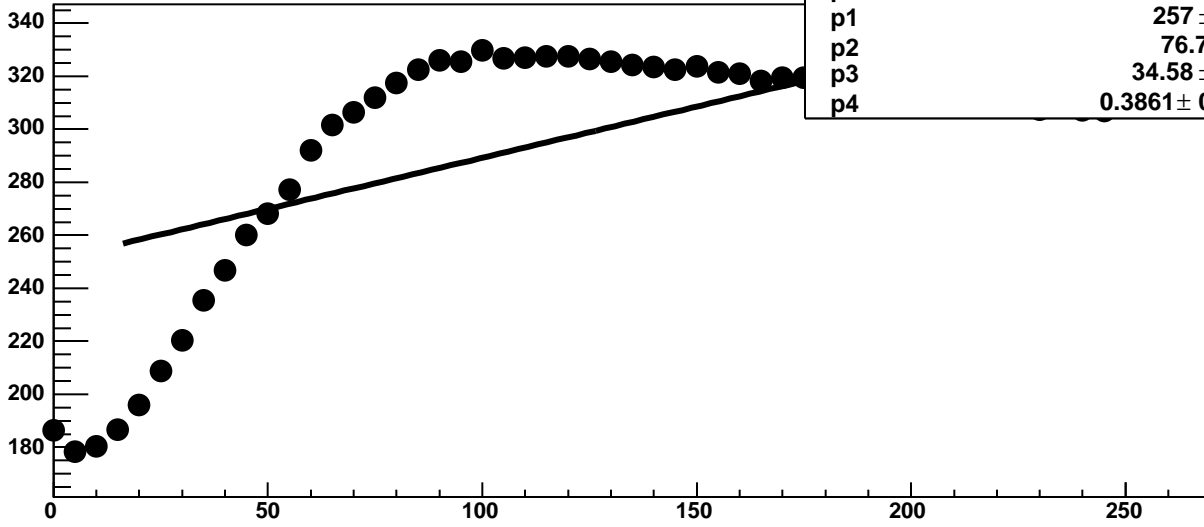
Chip 8, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold

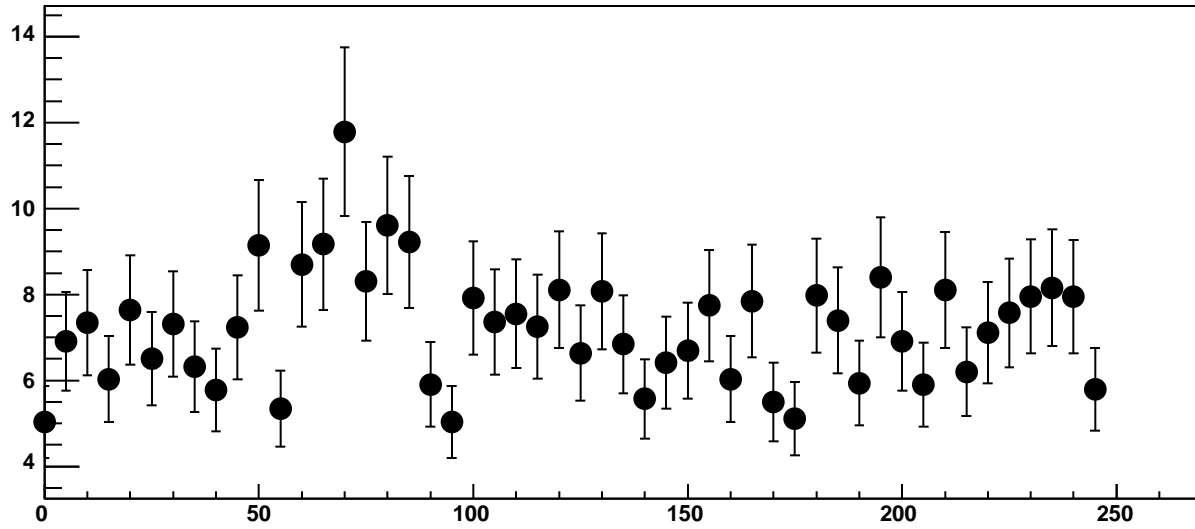


Chip 8, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

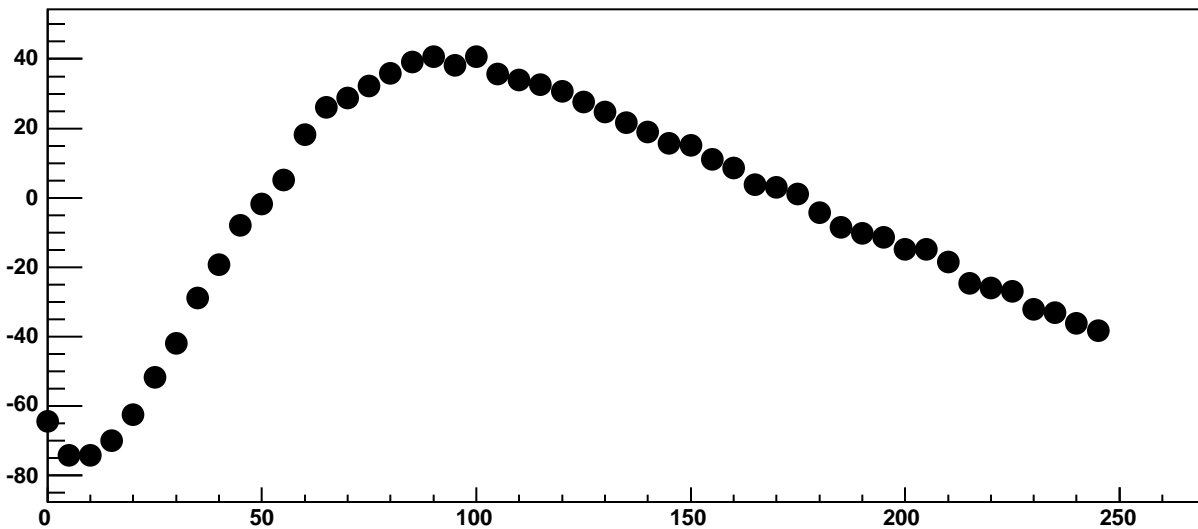


χ^2 / ndf	1.496e+04 / 41
p0	349.9 ± 0.3549
p1	257 ± 0.6274
p2	76.76 ± 2.5
p3	34.58 ± 0.2184
p4	0.3861 ± 0.003571

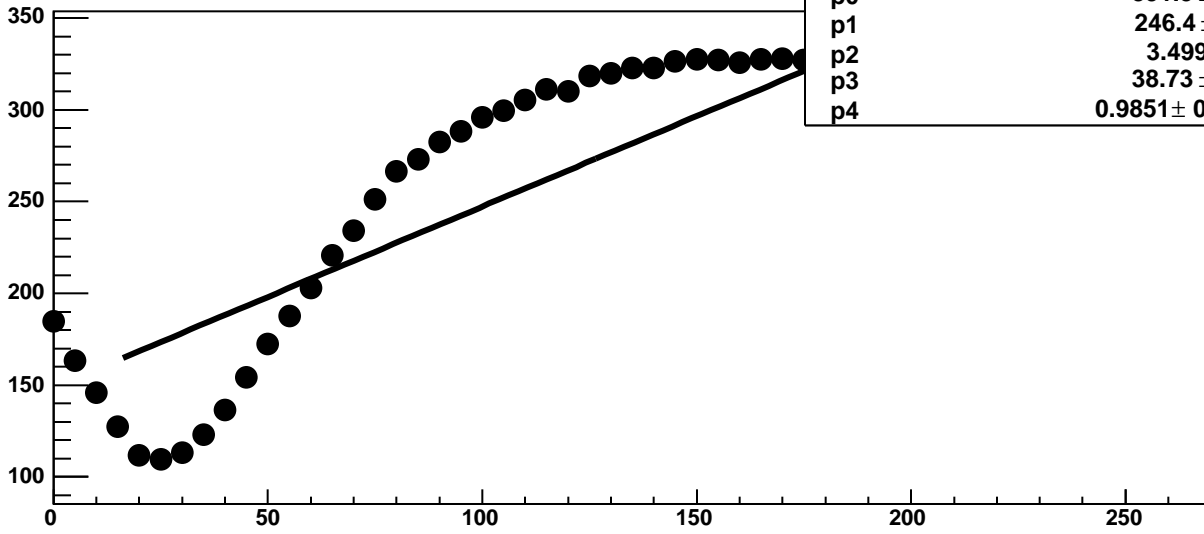
Chip 8, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold

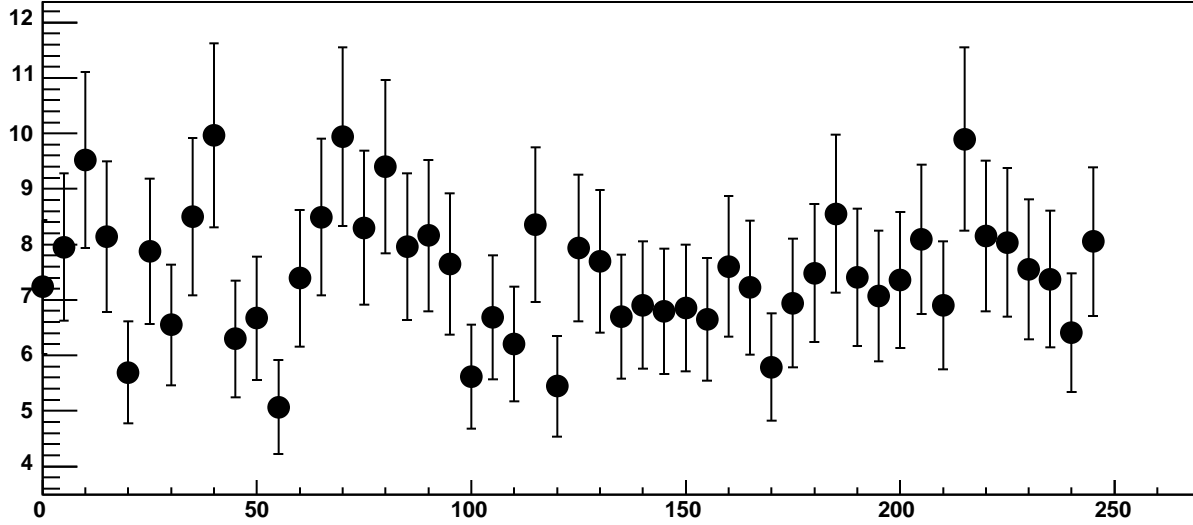


Chip 8, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

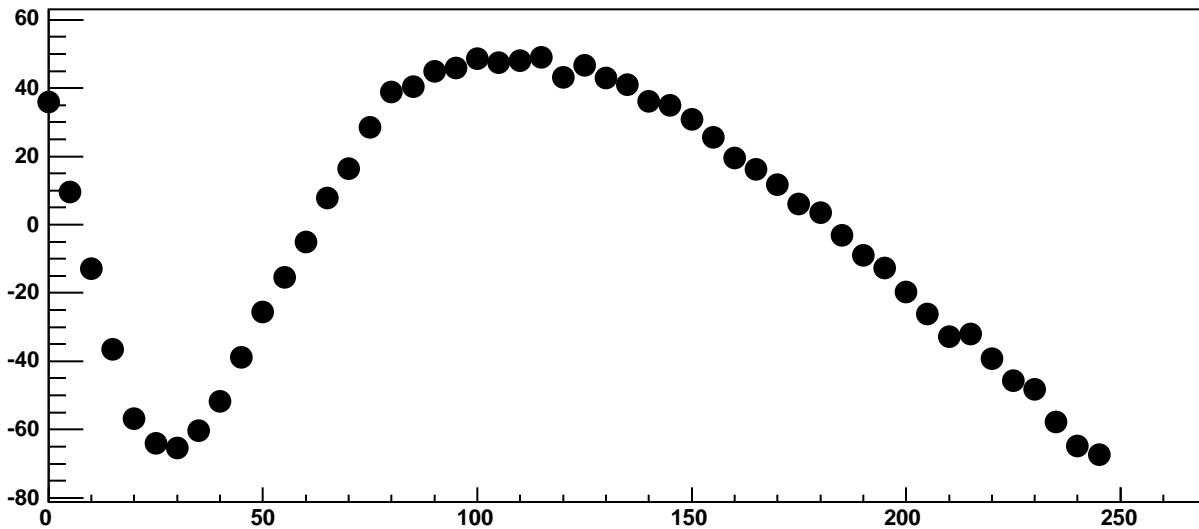


χ^2 / ndf	2.589e+04 / 41
p0	391.5 ± 0.5405
p1	246.4 ± 0.3479
p2	3.499 ± 2.642
p3	38.73 ± 0.2096
p4	0.9851 ± 0.003696

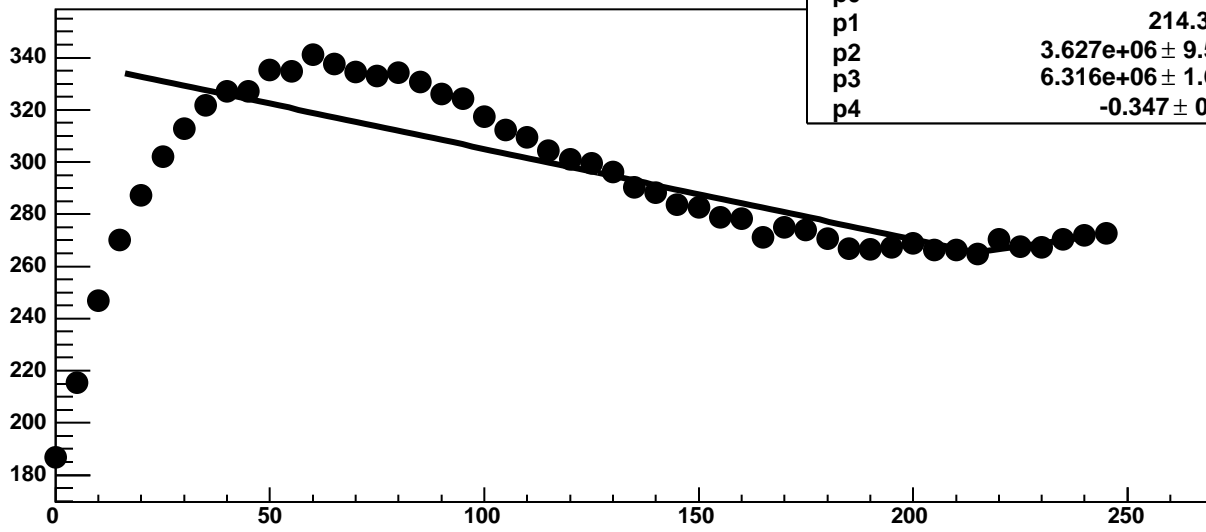
Chip 8, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

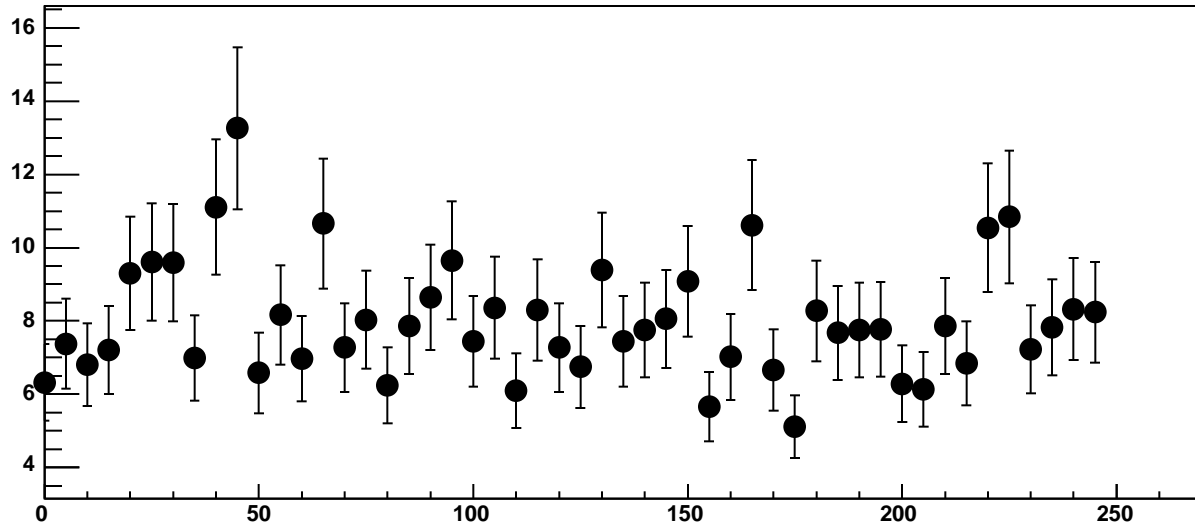


Chip 8, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold

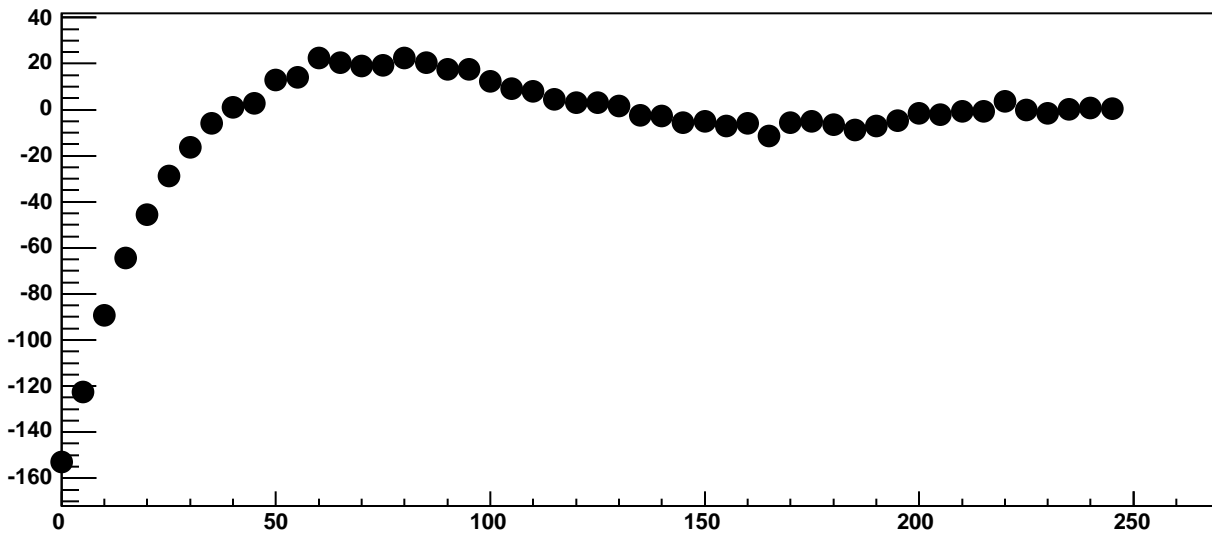


χ^2 / ndf	3663 / 41
p0	265.4 ± 0.844
p1	214.3 ± 2.526
p2	$3.627\text{e}+06 \pm 9.548\text{e}+06$
p3	$6.316\text{e}+06 \pm 1.641\text{e}+07$
p4	-0.347 ± 0.004813

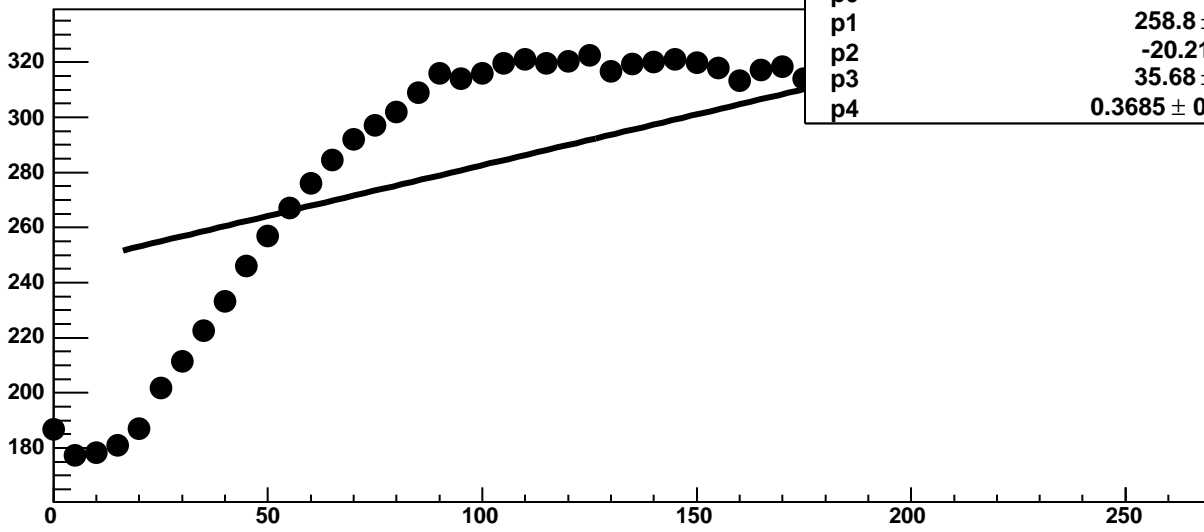
Chip 8, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold

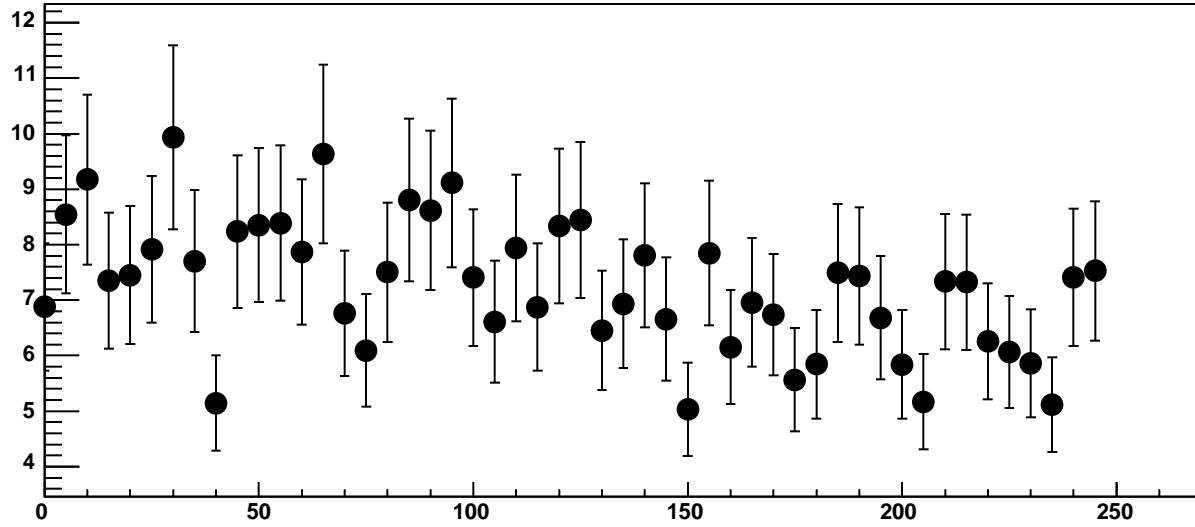


Chip 8, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

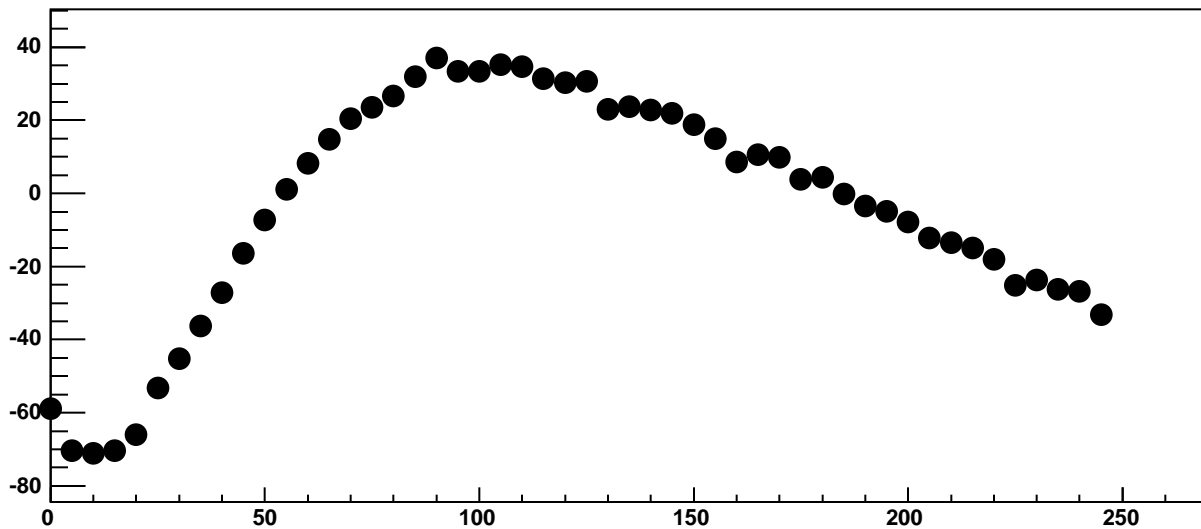


χ^2 / ndf	1.249e+04 / 41
p0	341.2 ± 0.38
p1	258.8 ± 0.4488
p2	-20.21 ± 3.618
p3	35.68 ± 0.2463
p4	0.3685 ± 0.003496

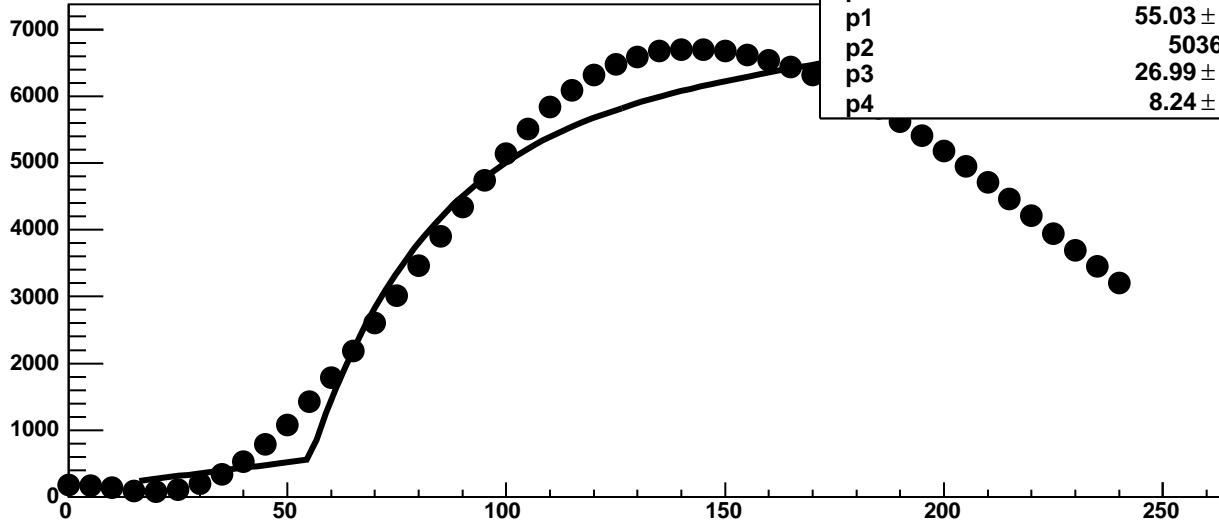
Chip 8, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

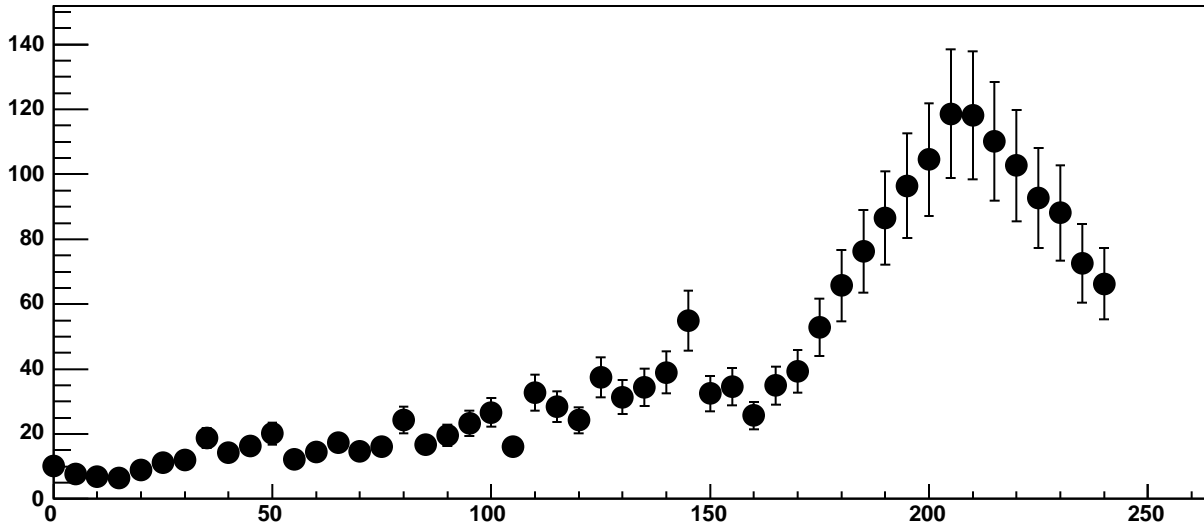


Chip 8, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold

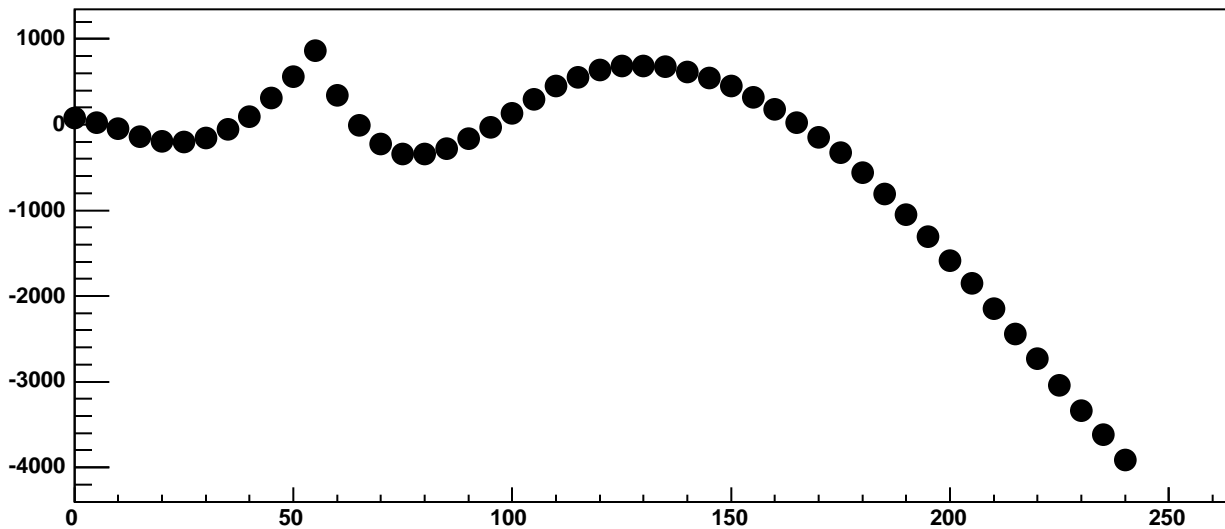


χ^2 / ndf	4.548e+05 / 41
p0	559.1 ± 1.637
p1	55.03 ± 0.02225
p2	5036 ± 6.857
p3	26.99 ± 0.05088
p4	8.24 ± 0.04695

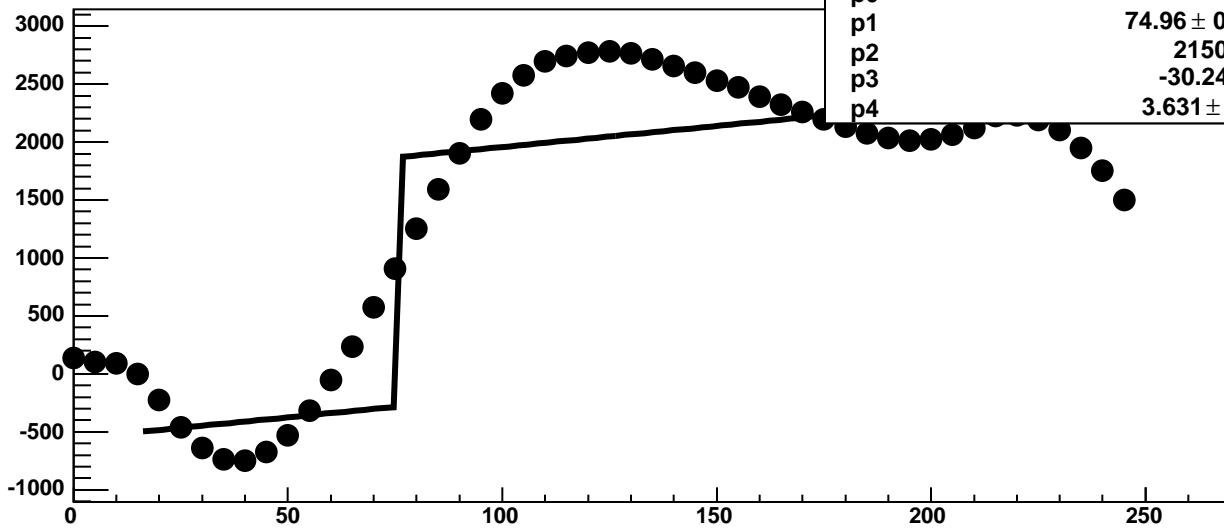
Chip 8, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

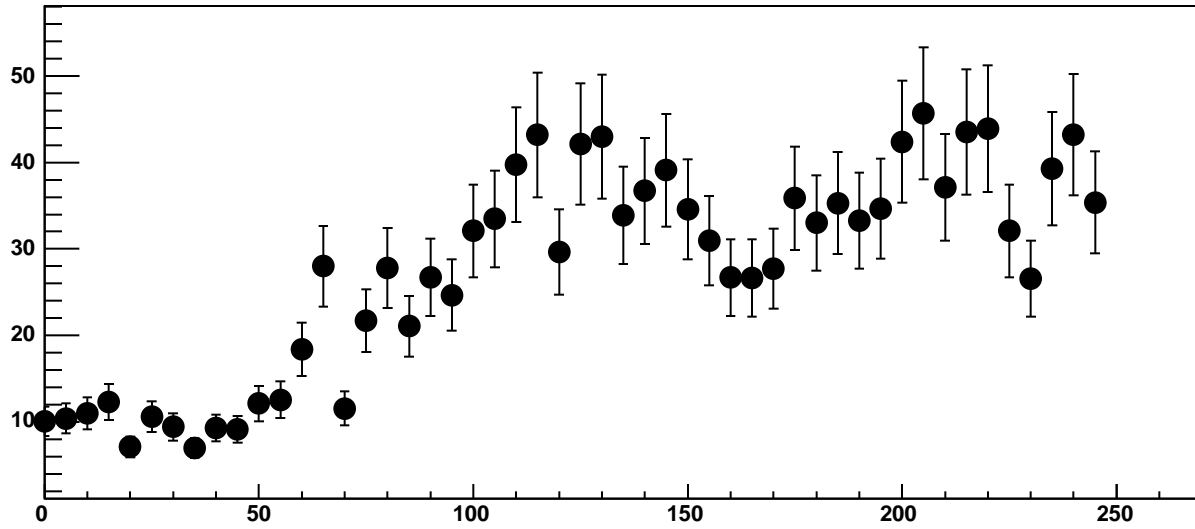


Chip 8, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

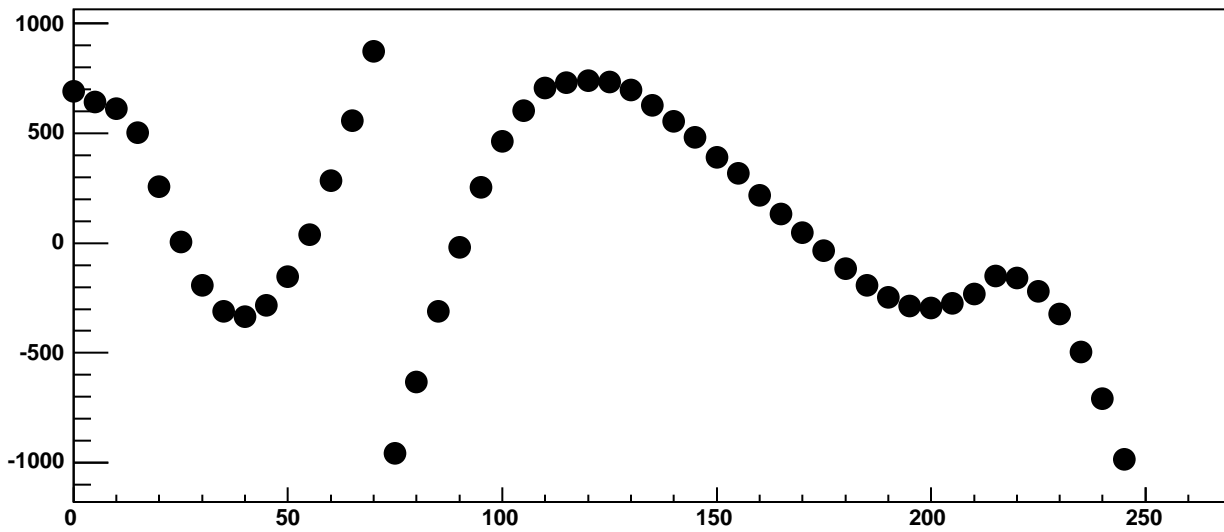


χ^2 / ndf	4.032e+05 / 41
p0	-283.8 ± 3.386
p1	74.96 ± 0.004398
p2	2150 ± 13.75
p3	-30.24 ± 2.436
p4	3.631 ± 0.03826

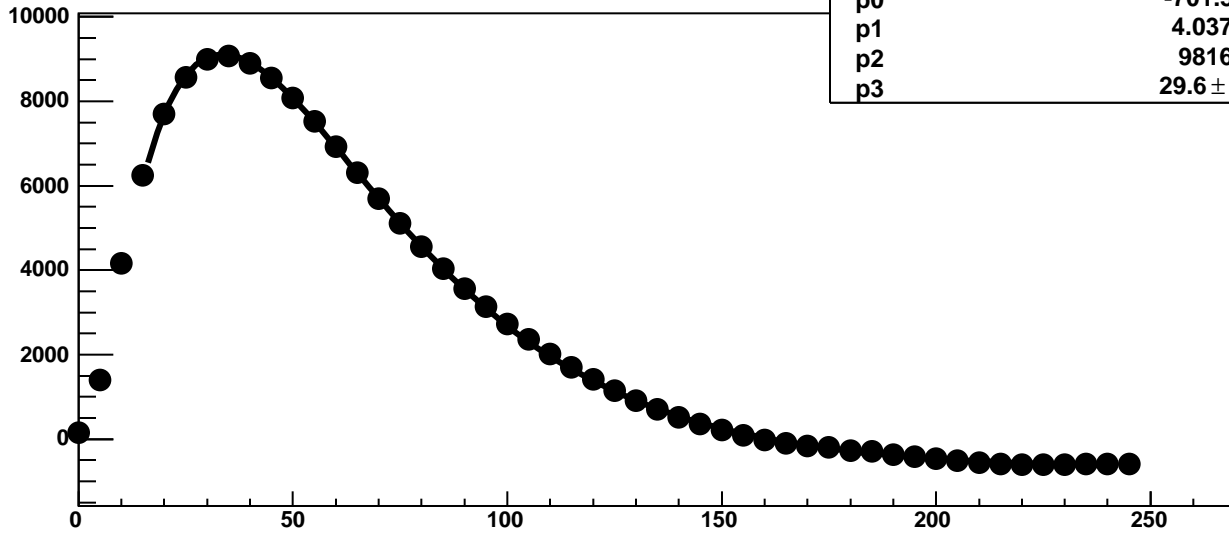
Chip 8, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

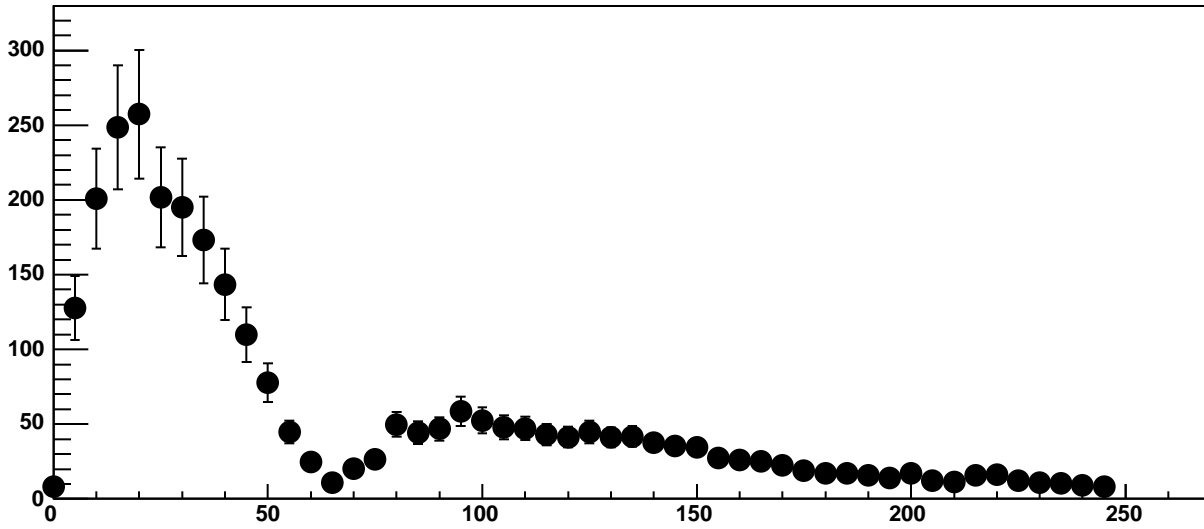


Chip 8, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold

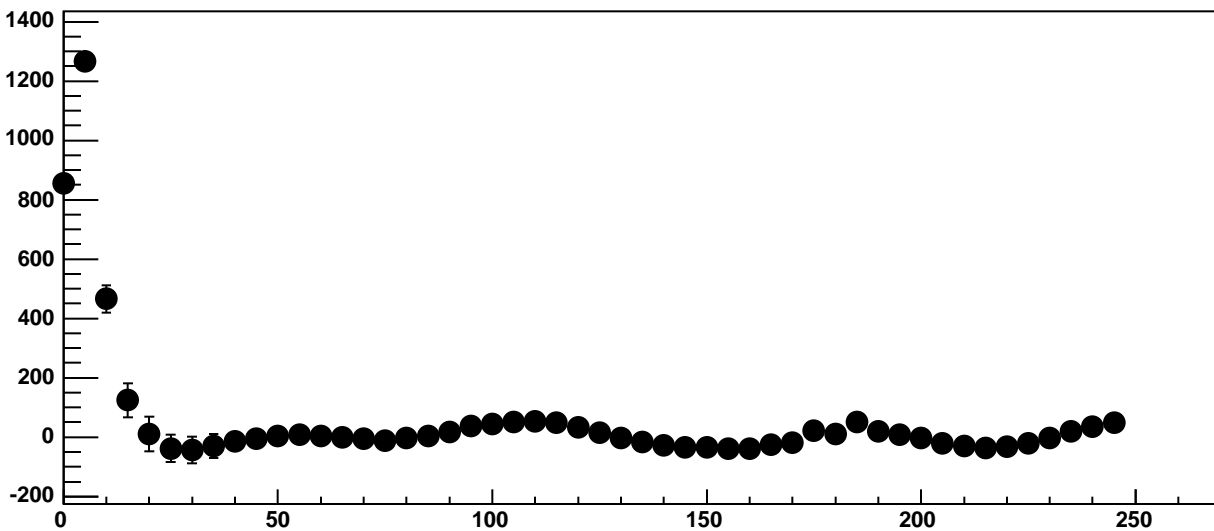


χ^2 / ndf	1281 / 42
p0	-701.3 \pm 1.189
p1	4.037 \pm 0.088
p2	9816 \pm 10.36
p3	29.6 \pm 0.02846

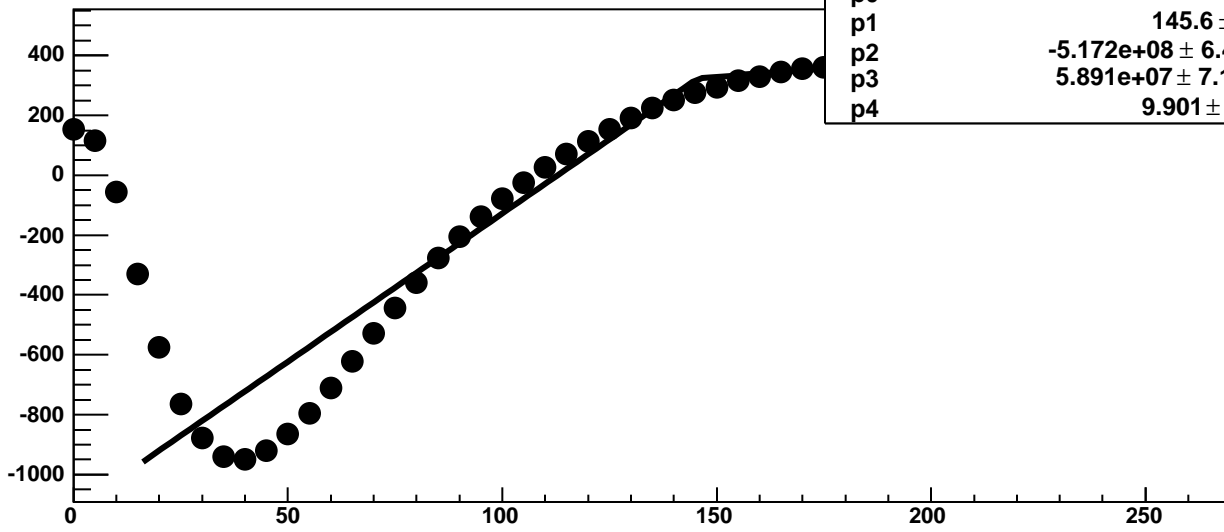
Chip 8, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold

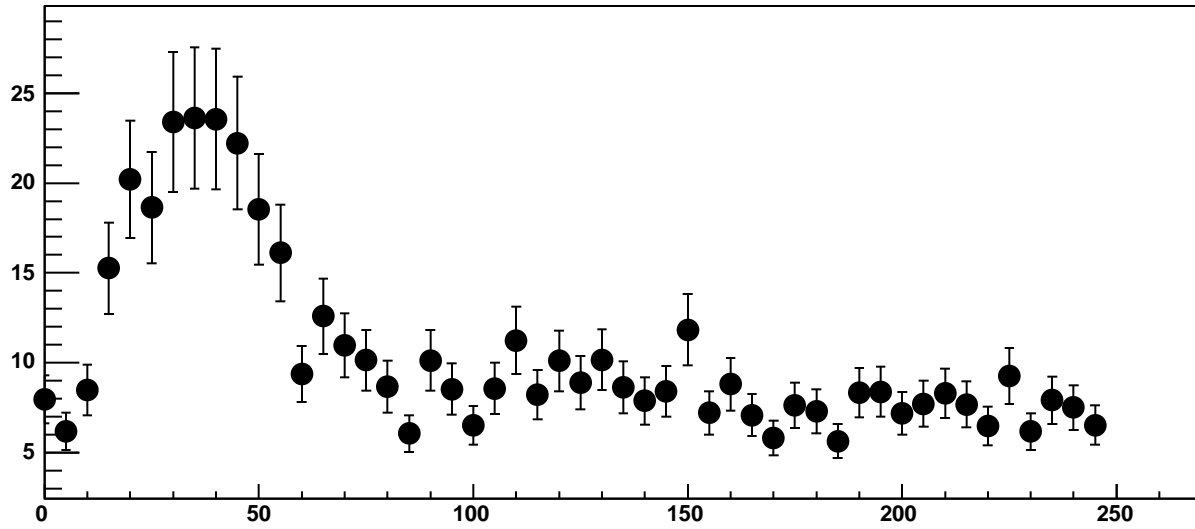


Chip 8, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

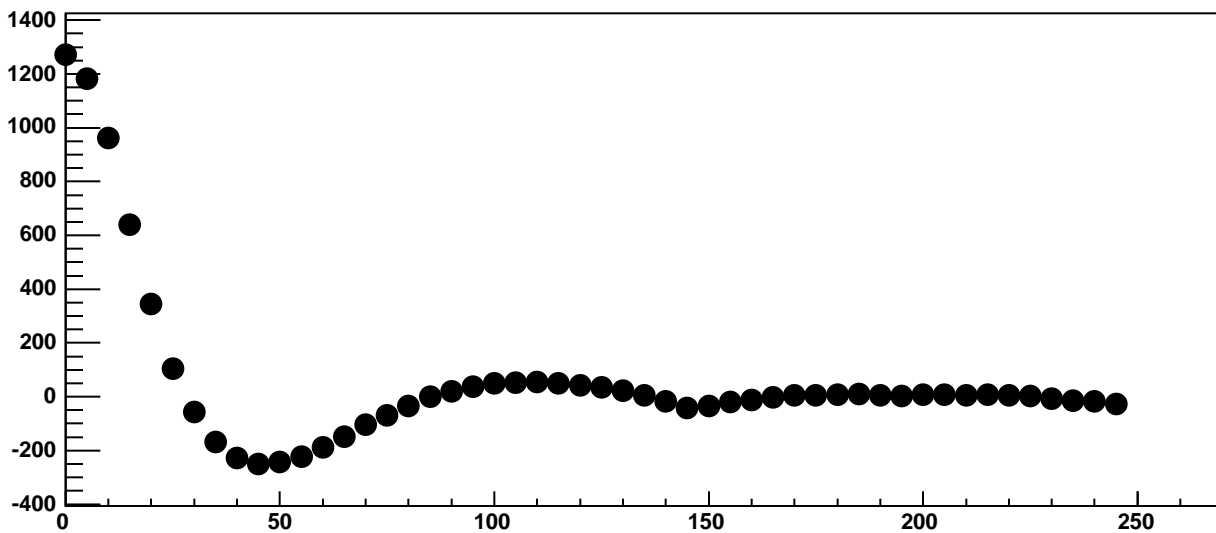


χ^2 / ndf	7.038e+04 / 41
p0	323.8 ± 0.9456
p1	145.6 ± 0.1336
p2	-5.172e+08 ± 6.414e+06
p3	5.891e+07 ± 7.174e+05
p4	9.901 ± 0.01438

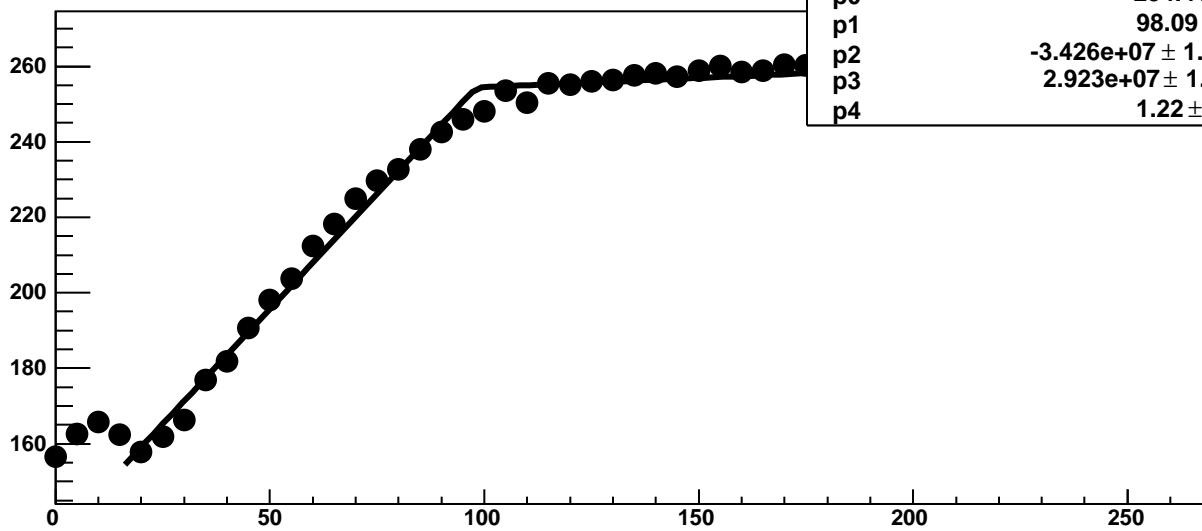
Chip 8, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

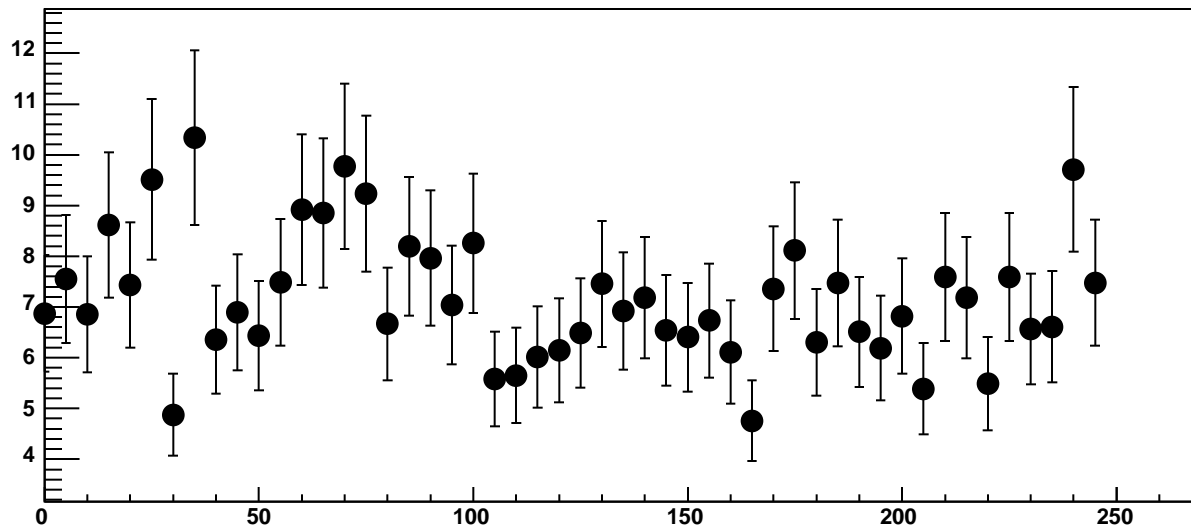


Chip 8, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

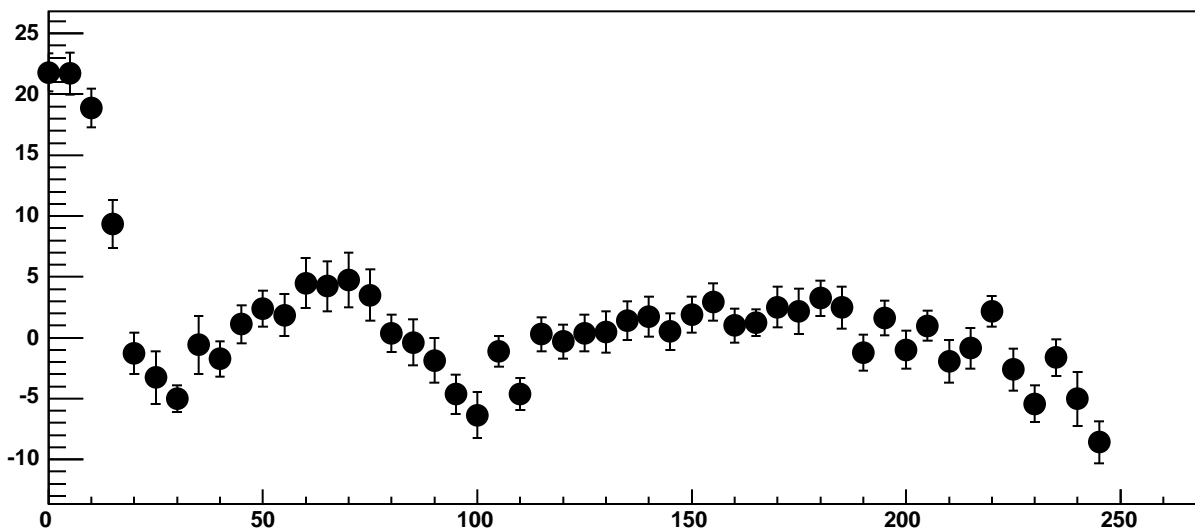


χ^2 / ndf	150.5 / 41
p0	254.4 ± 0.5644
p1	98.09 ± 0.7742
p2	$-3.426\text{e}+07 \pm 1.165\text{e}+06$
p3	$2.923\text{e}+07 \pm 1.111\text{e}+06$
p4	1.22 ± 0.01459

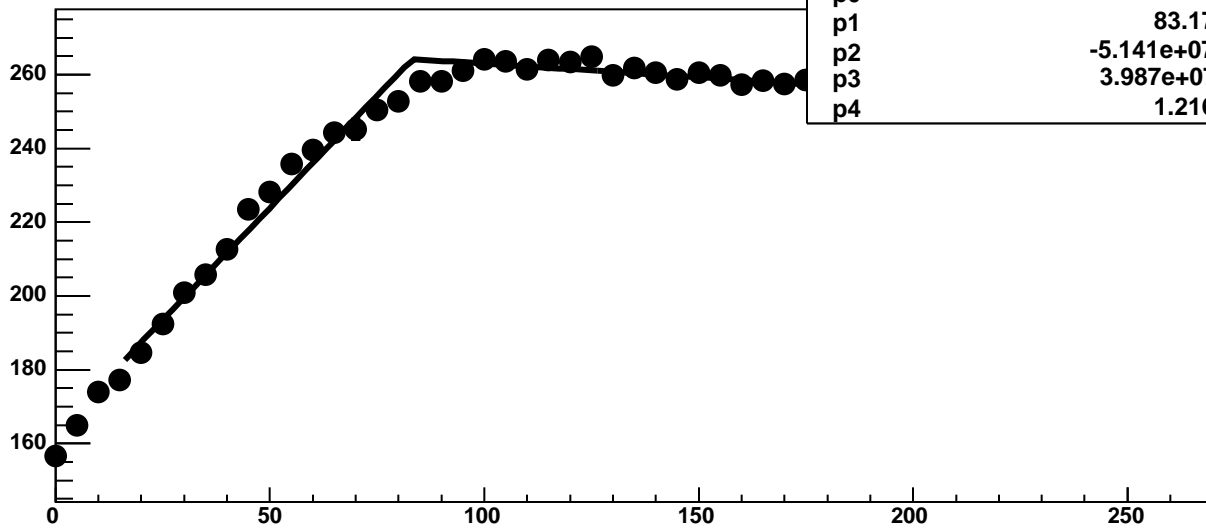
Chip 8, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

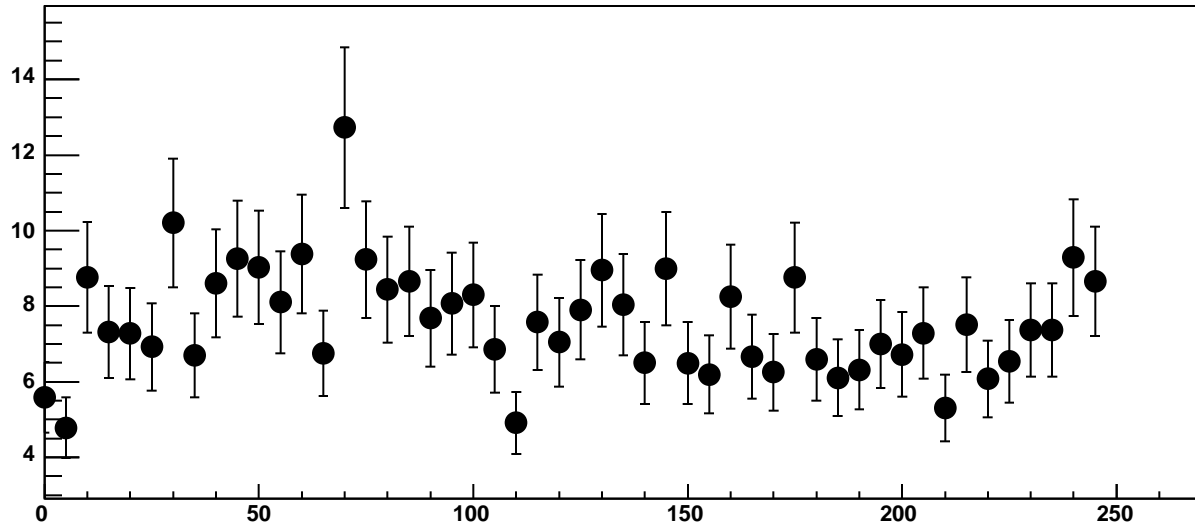


Chip 8, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold

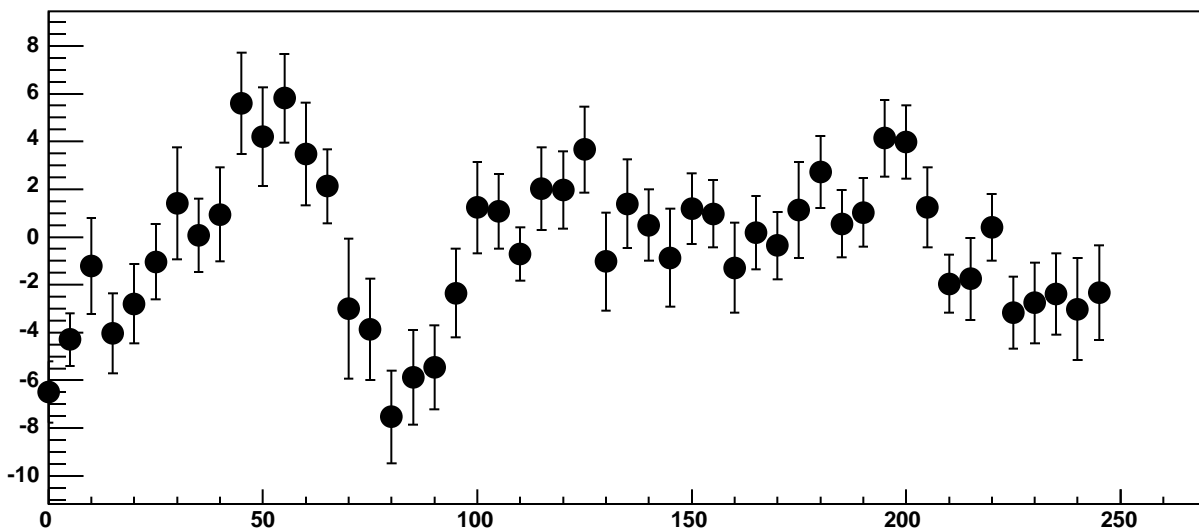


χ^2 / ndf	118.2 / 41
p0	264.2 ± nan
p1	83.17 ± nan
p2	-5.141e+07 ± nan
p3	3.987e+07 ± nan
p4	1.216 ± nan

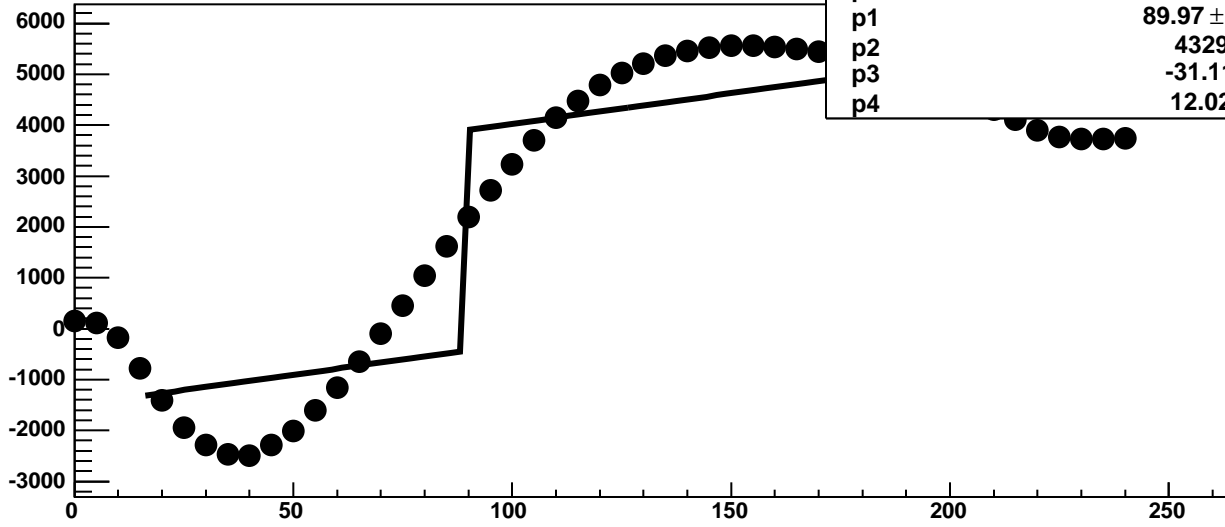
Chip 8, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold

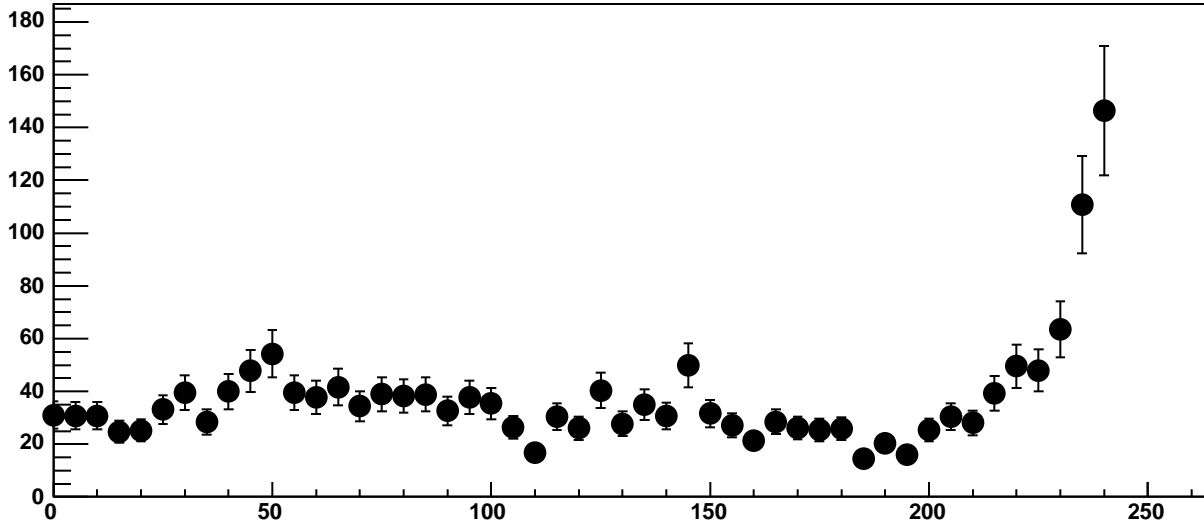


Chip 8, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

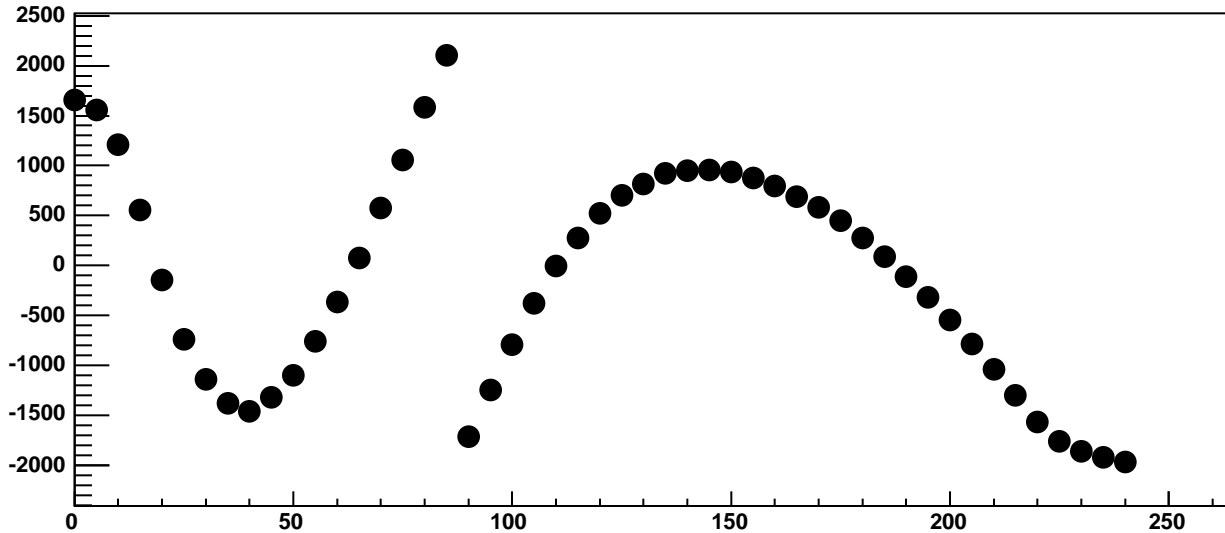


χ^2 / ndf	6.384e+05 / 41
p0	-425 ± 6.116
p1	89.97 ± 0.09481
p2	4329 ± 1.536
p3	-31.11 ± 6.693
p4	12.02 ± 1.371

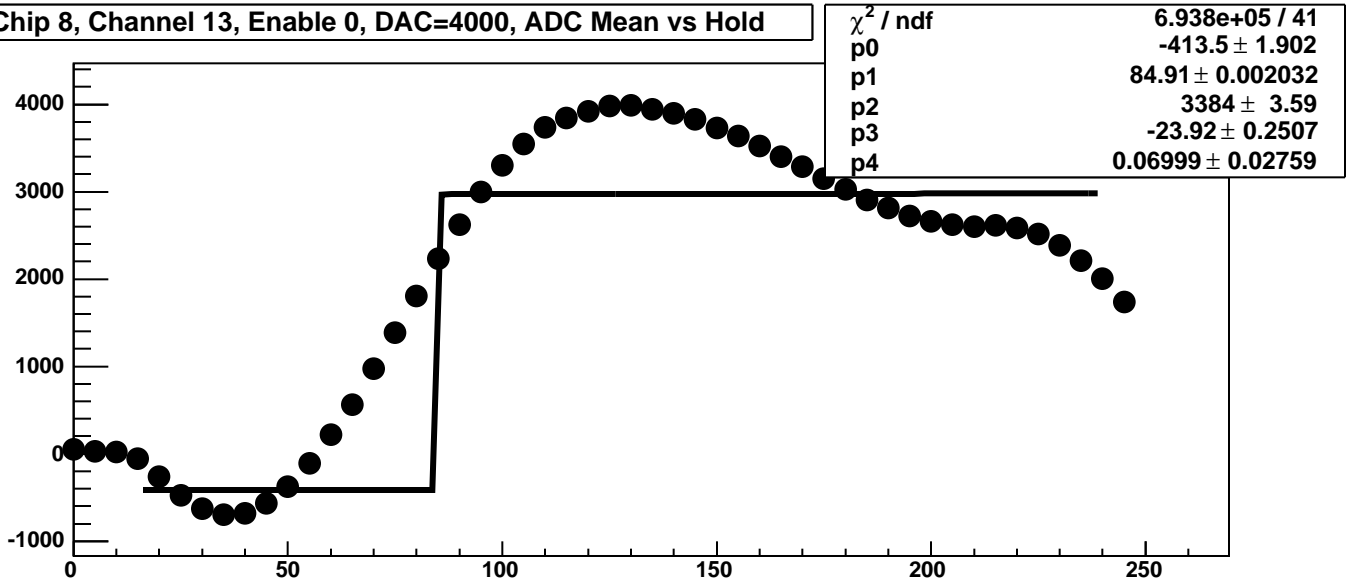
Chip 8, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold



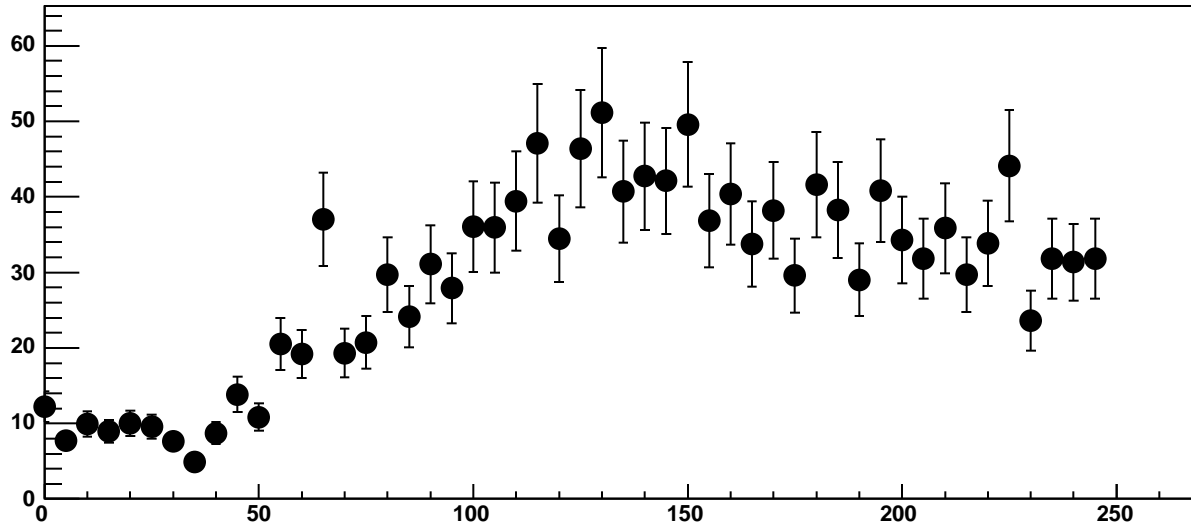
Chip 8, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold



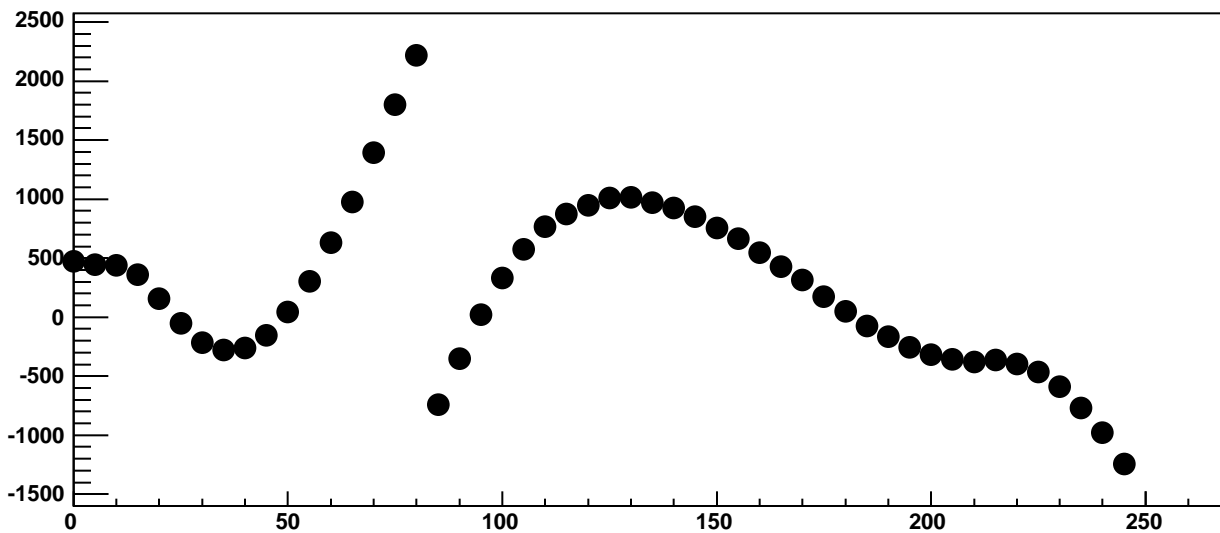
Chip 8, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold



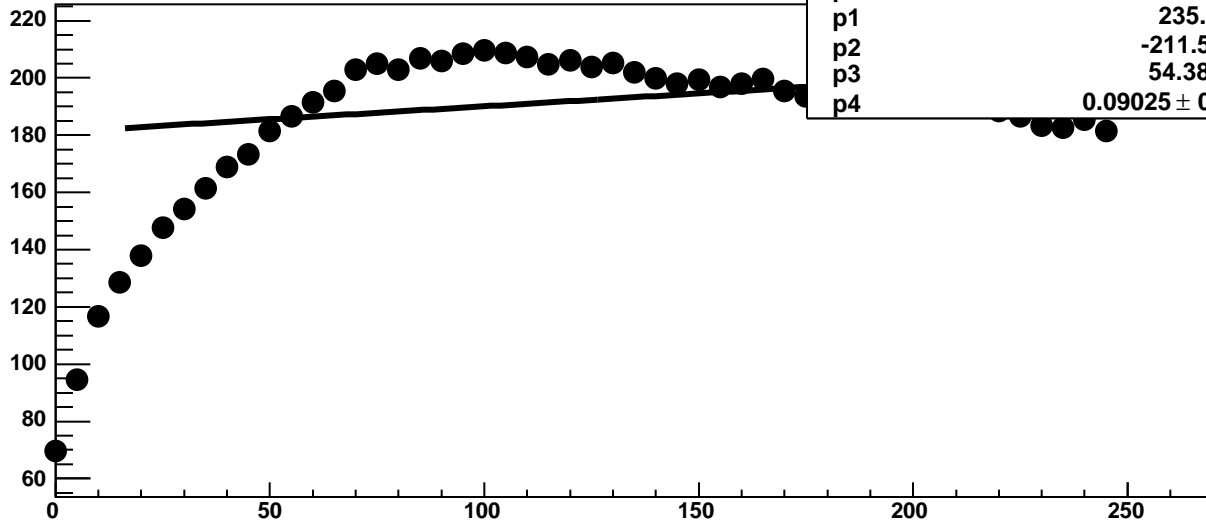
Chip 8, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold

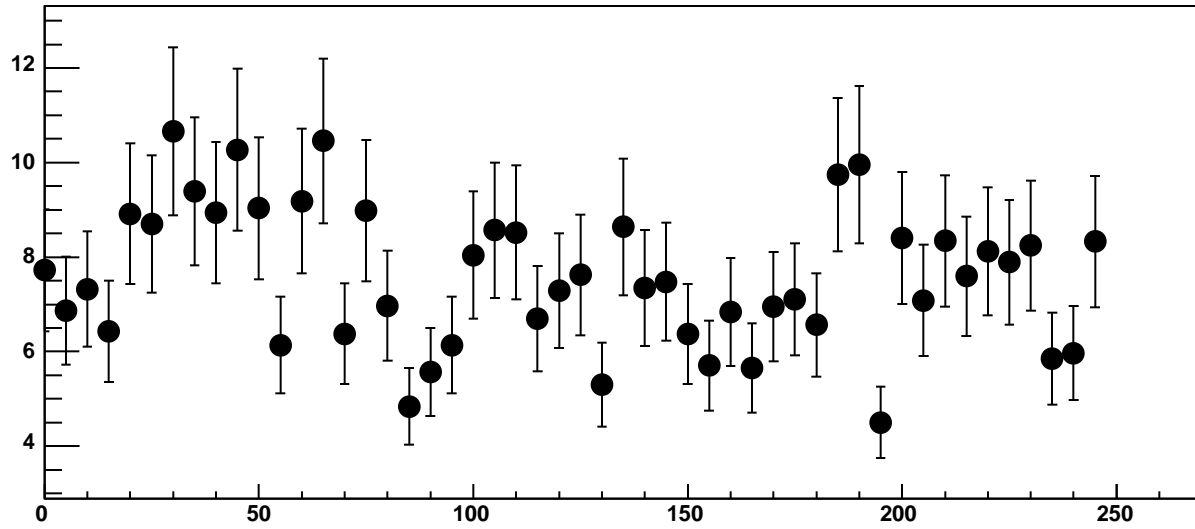


Chip 8, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold

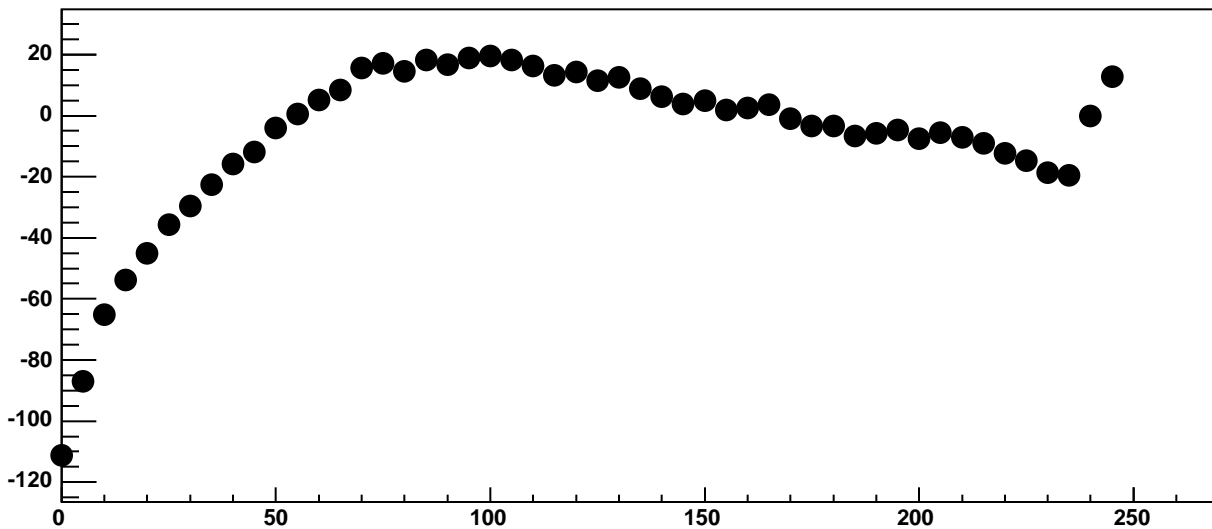


χ^2 / ndf	4539 / 41
p0	202.3 ± 0.6107
p1	235.4 ± 4.21
p2	-211.5 ± 180.7
p3	54.38 ± 40.08
p4	0.09025 ± 0.003982

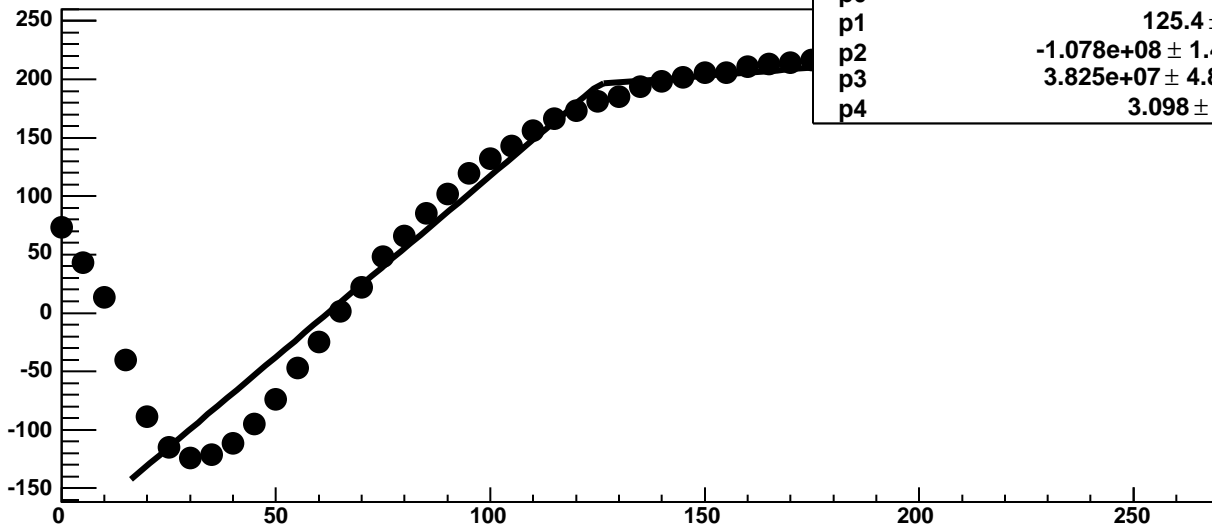
Chip 8, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold

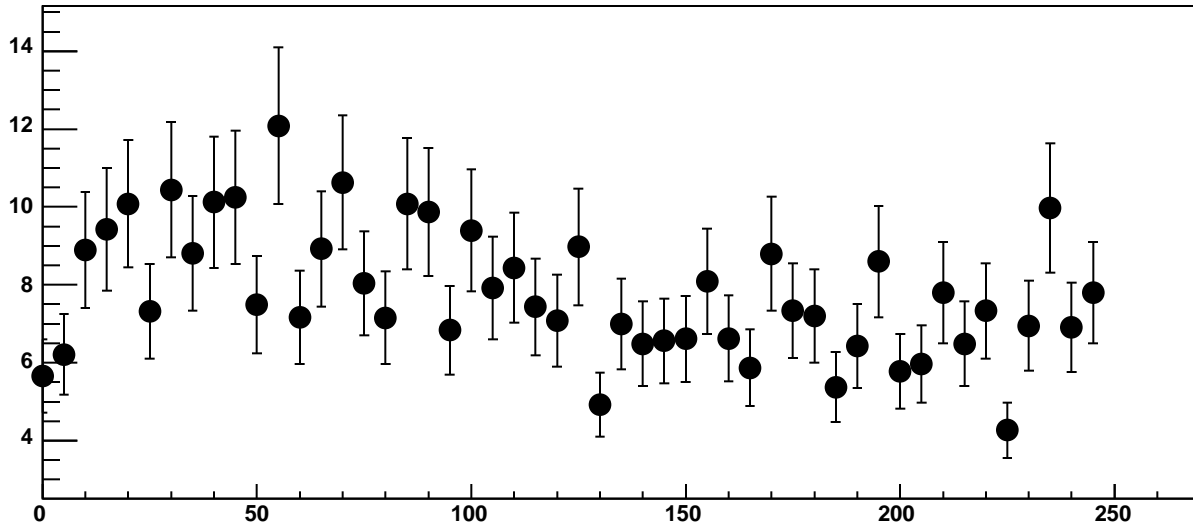


Chip 8, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

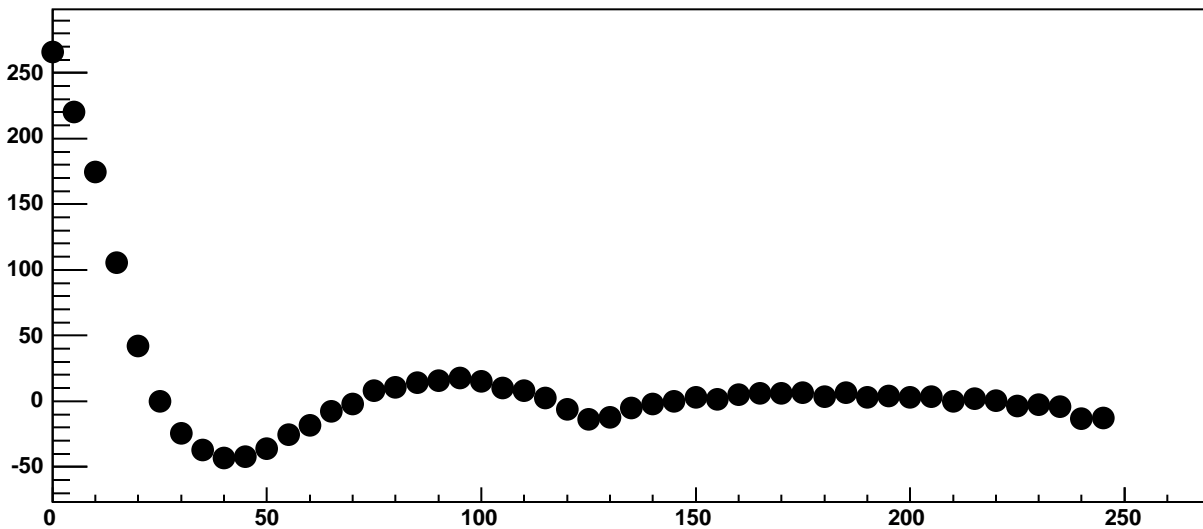


χ^2 / ndf	5271 / 41
p0	196.1 ± 0.6895
p1	125.4 ± 0.3483
p2	-1.078e+08 ± 1.481e+06
p3	3.825e+07 ± 4.831e+05
p4	3.098 ± 0.01229

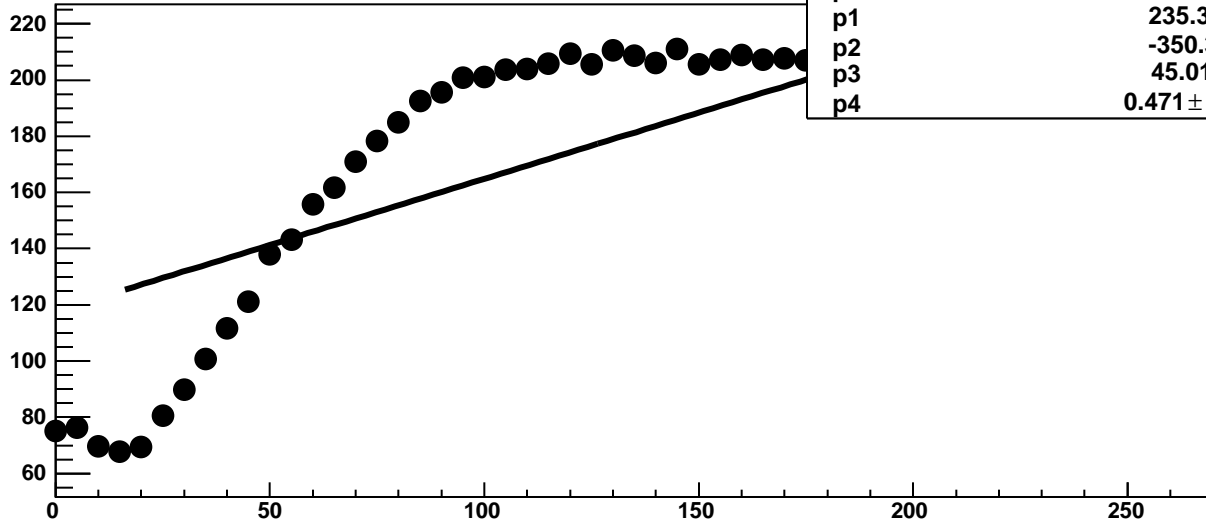
Chip 8, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

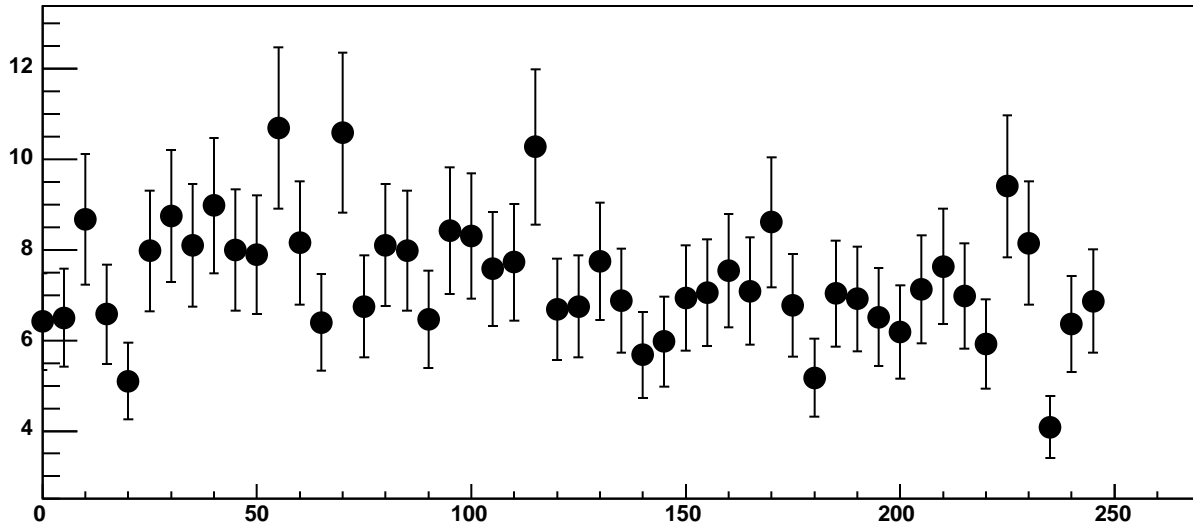


Chip 8, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

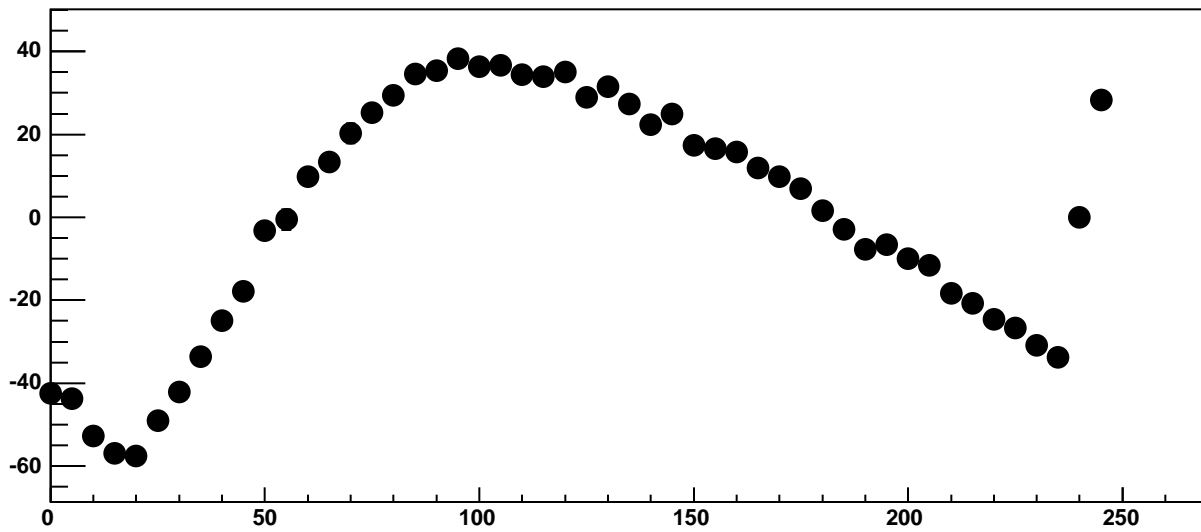


χ^2 / ndf	1.399e+04 / 41
p0	228.6 ± 1.226
p1	235.3 ± 2.444
p2	-350.3 ± 166
p3	45.01 ± 18.52
p4	0.471 ± 0.00358

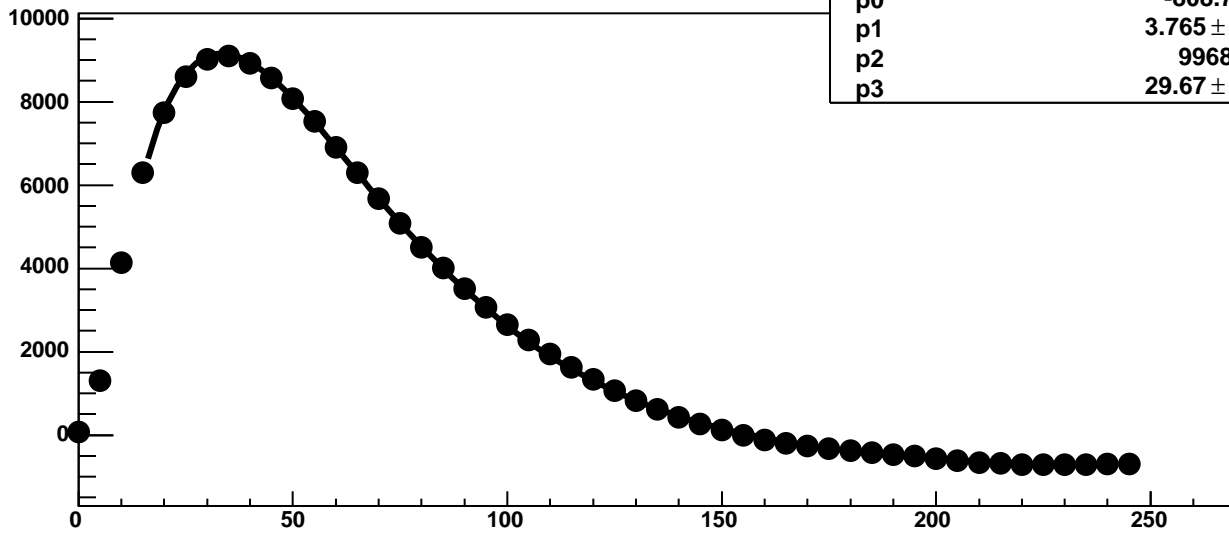
Chip 8, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

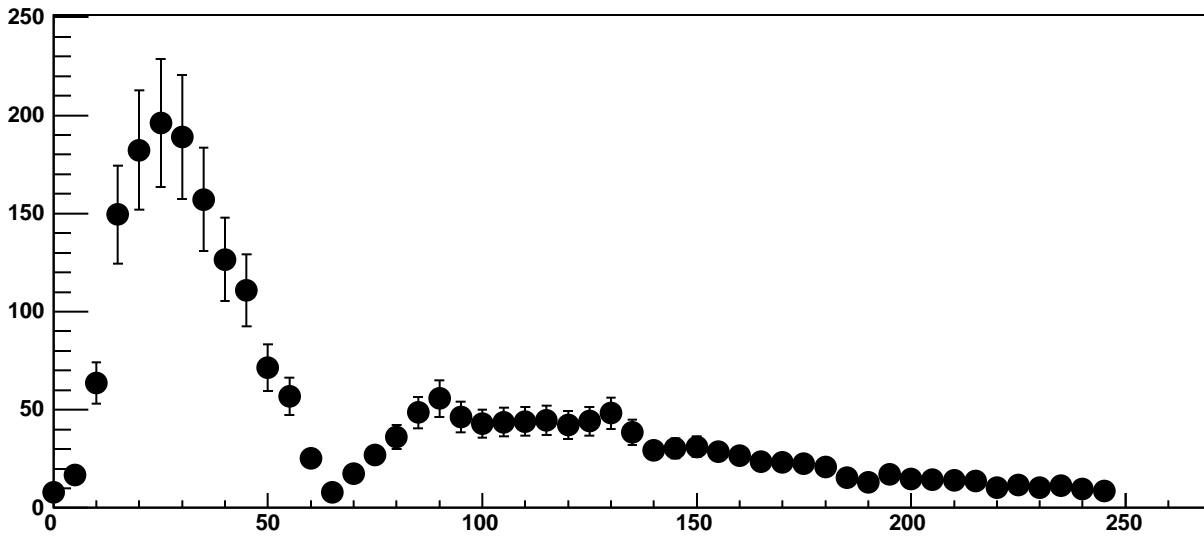


Chip 8, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold

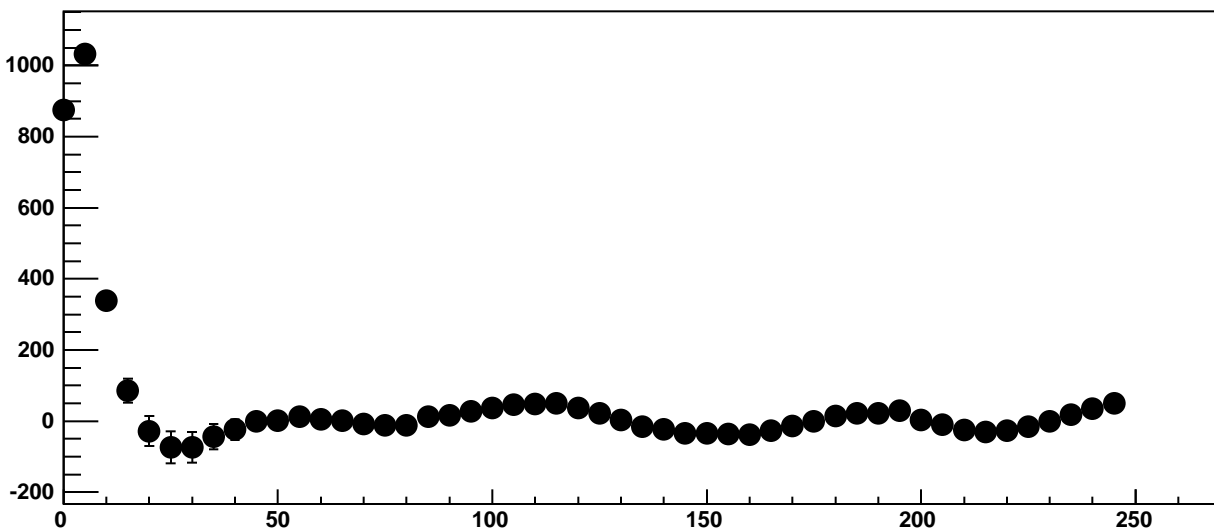


χ^2 / ndf	1096 / 42
p0	-808.7 ± 1.145
p1	3.765 ± 0.06388
p2	9968 ± 8.012
p3	29.67 ± 0.02517

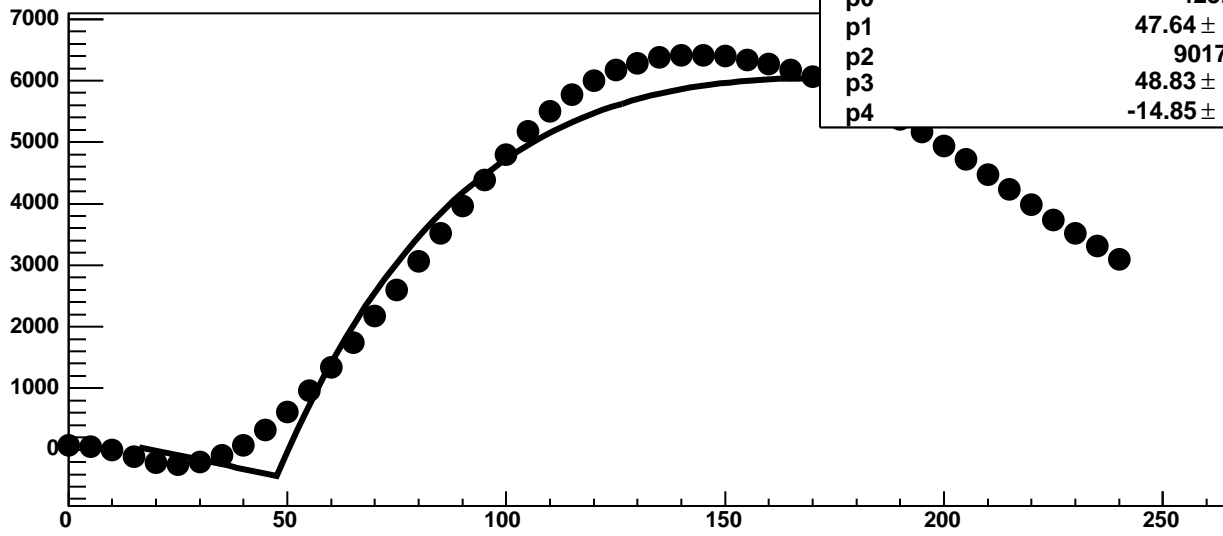
Chip 8, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold

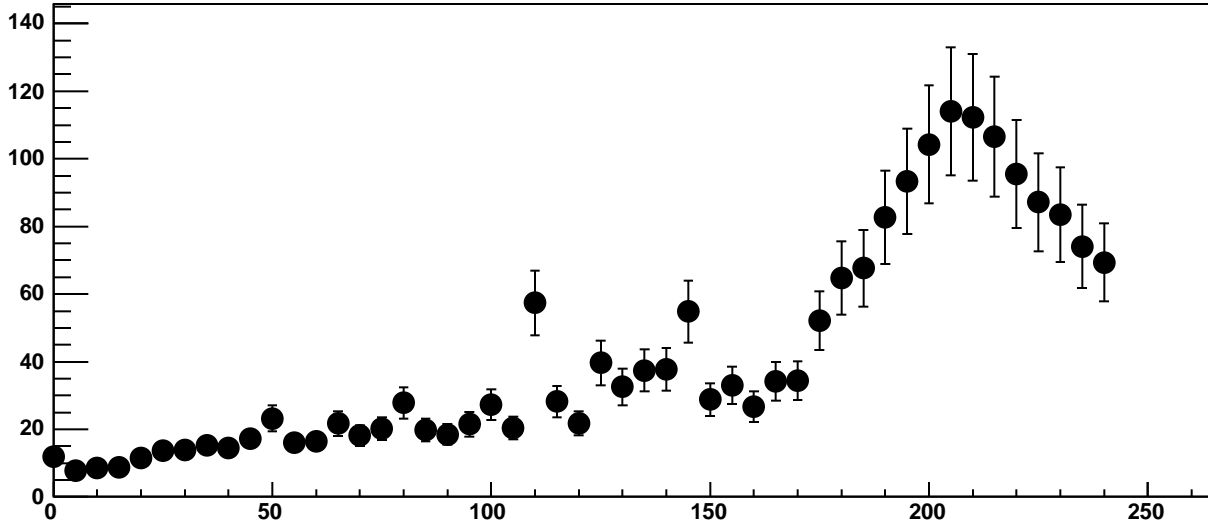


Chip 8, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

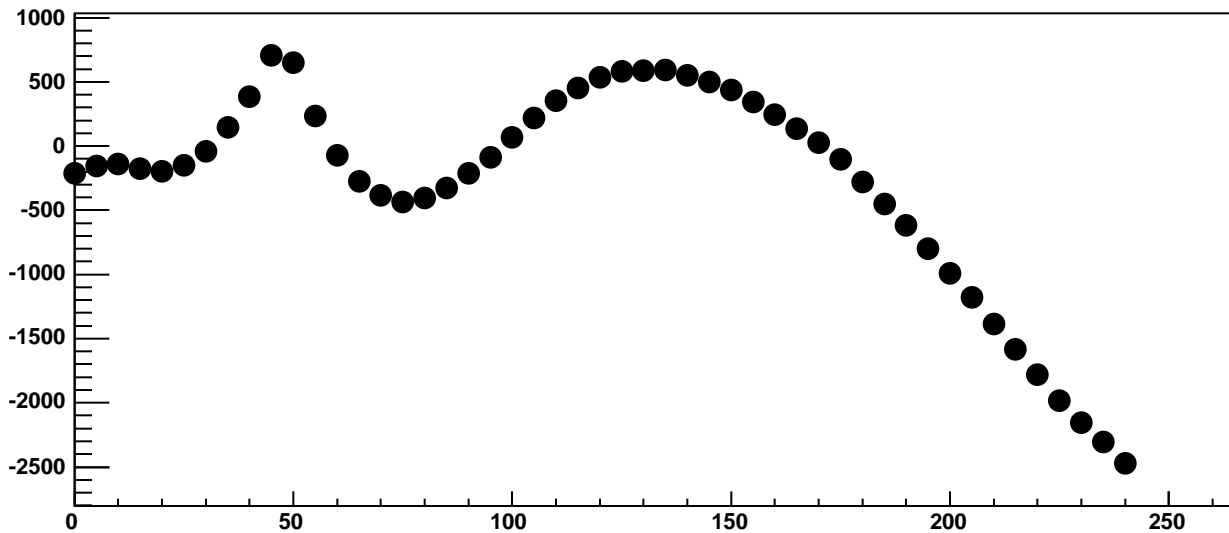


χ^2 / ndf	2.491e+05 / 41
p0	-425.1 ± 1.9
p1	47.64 ± 0.02109
p2	9017 ± 15.11
p3	48.83 ± 0.08366
p4	-14.85 ± 0.07974

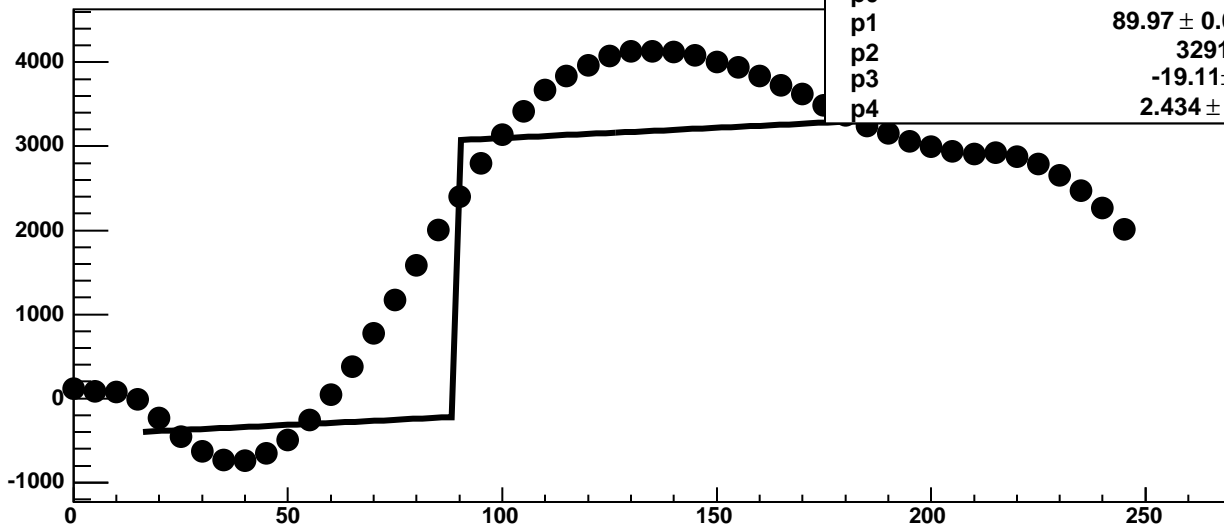
Chip 8, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold

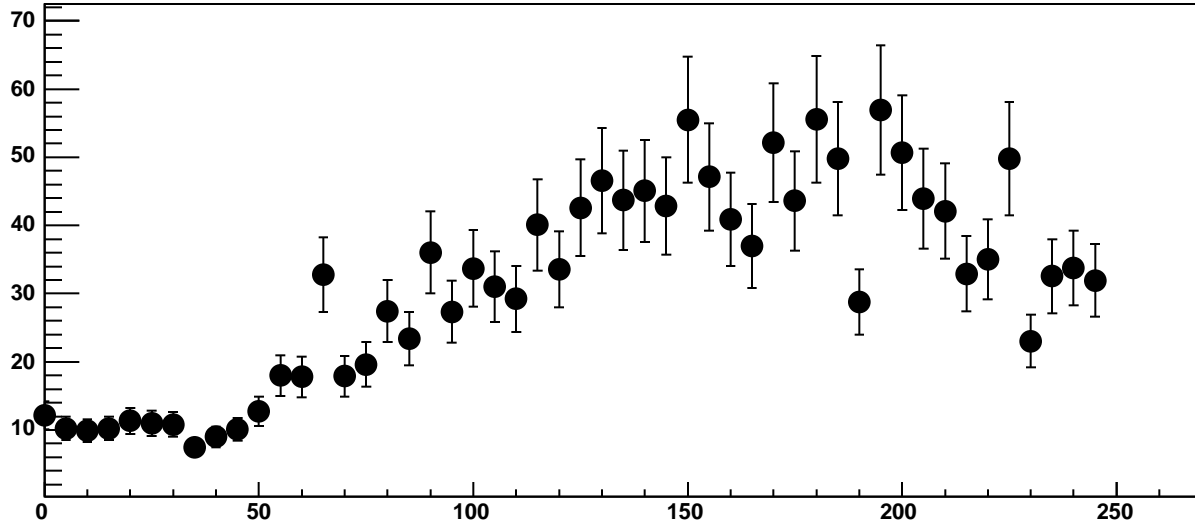


Chip 8, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

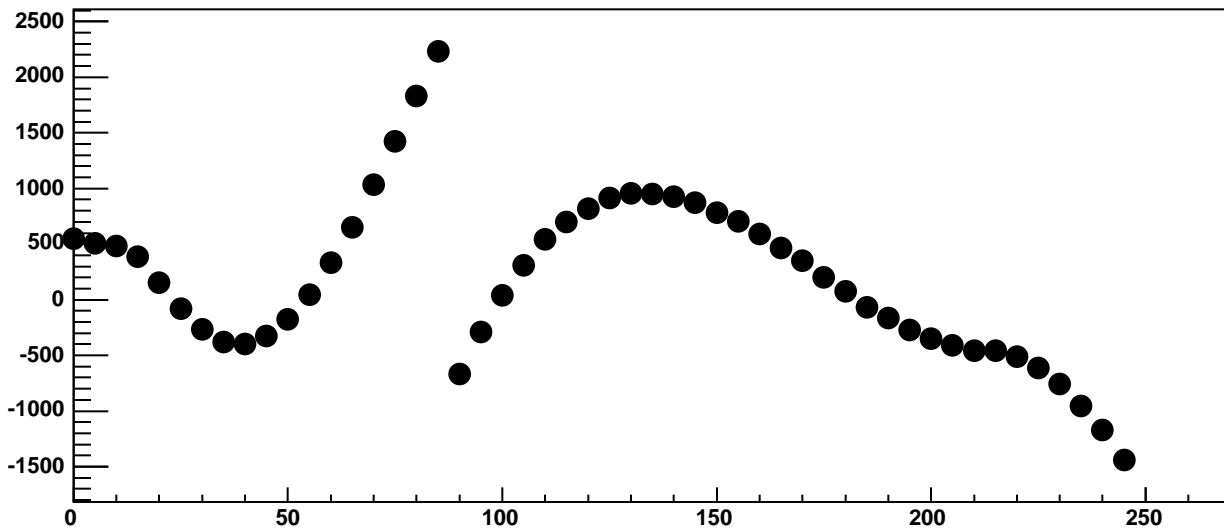


χ^2 / ndf	7.609e+05 / 41
p0	-217.7 ± 1.628
p1	89.97 ± 0.0002824
p2	3291 ± 3.654
p3	-19.11 ± 0.1087
p4	2.434 ± 0.02618

Chip 8, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold

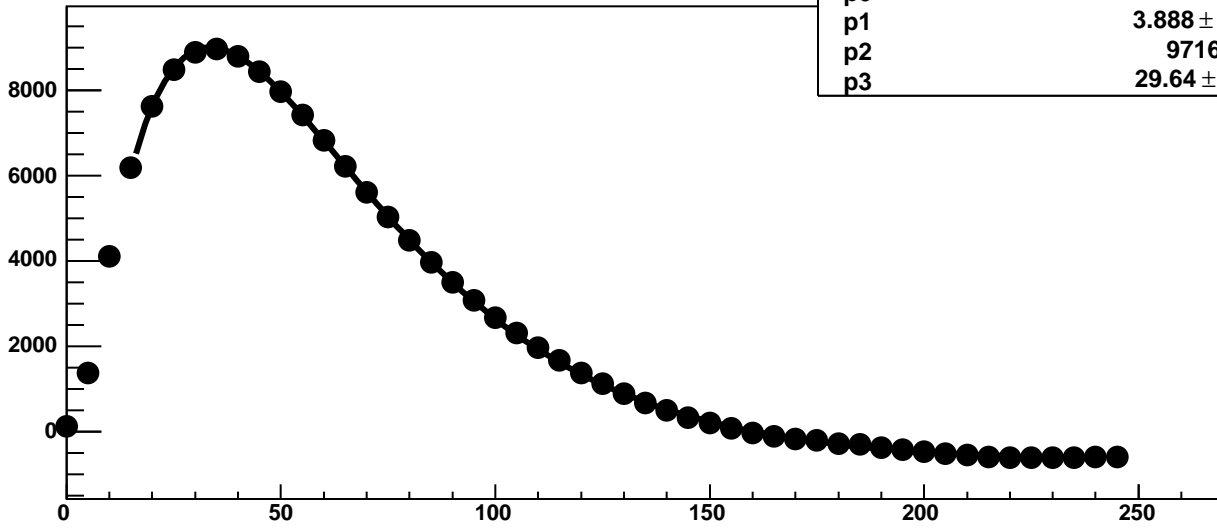


Chip 8, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

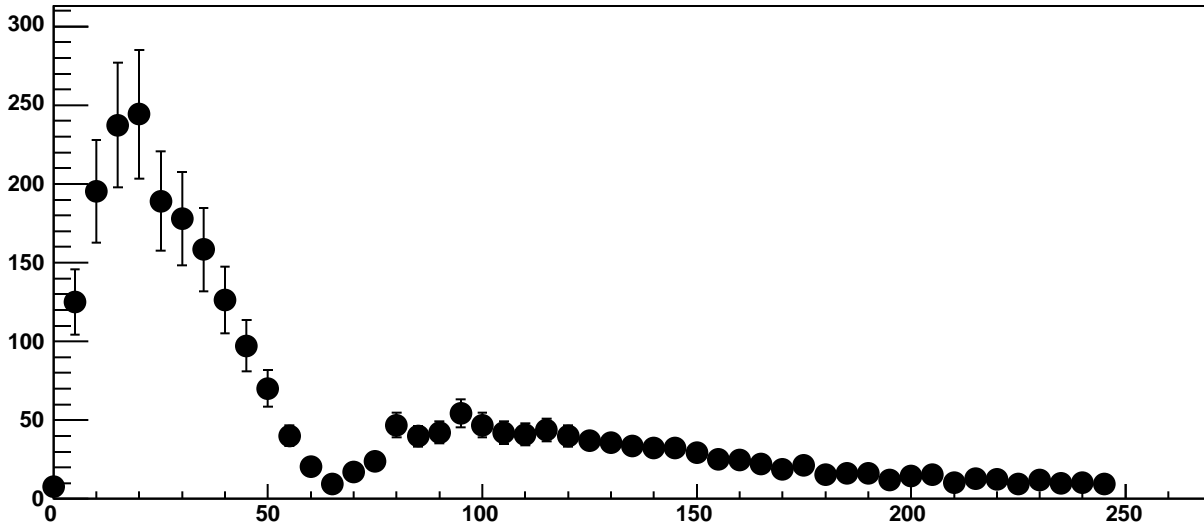


Chip 8, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold

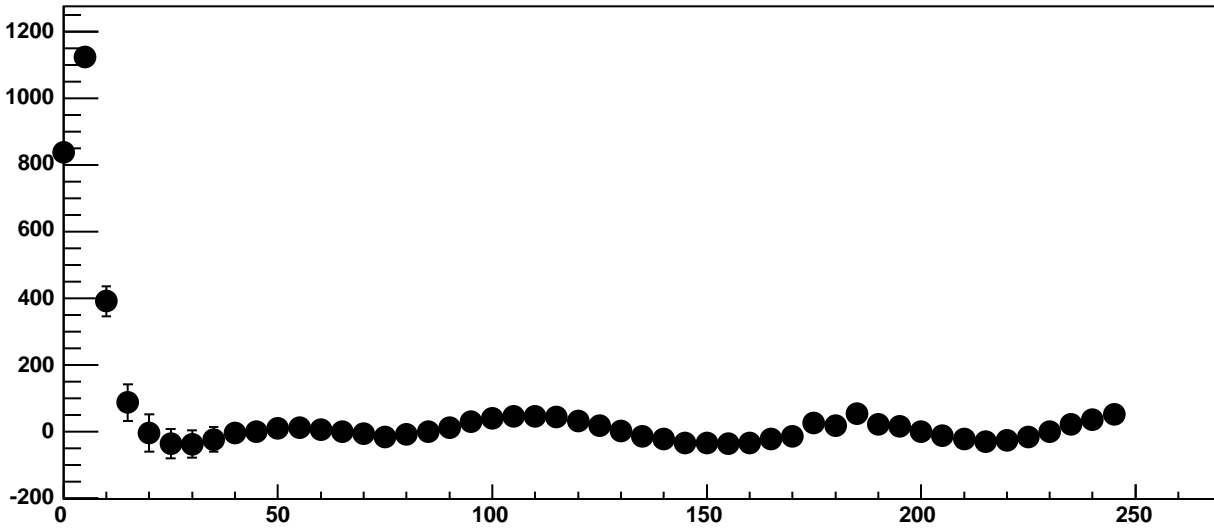
χ^2 / ndf	1275 / 42
p0	-708.3 \pm 1.124
p1	3.888 \pm 0.08433
p2	9716 \pm 9.712
p3	29.64 \pm 0.02661



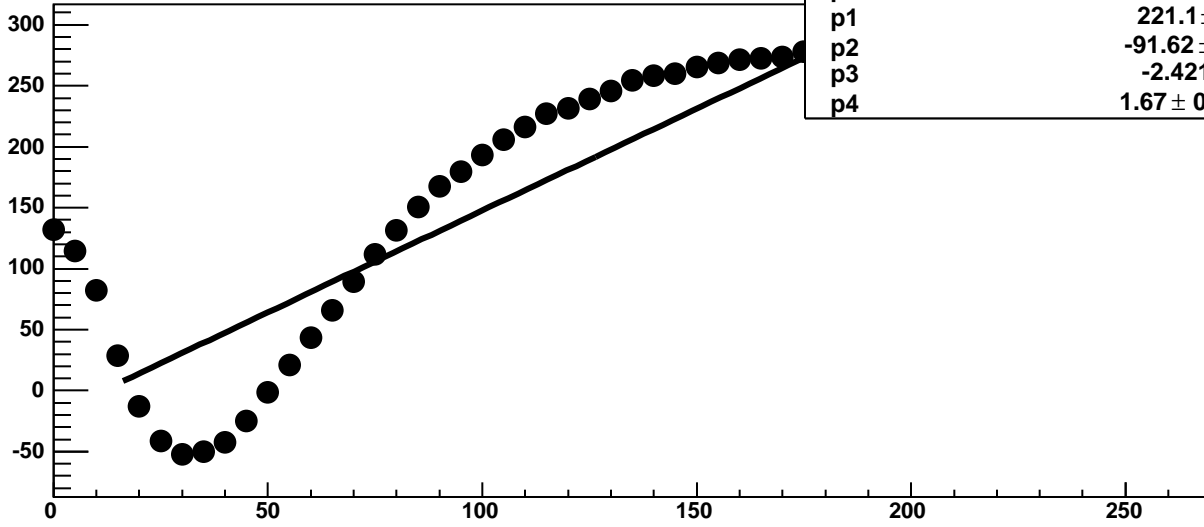
Chip 8, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold



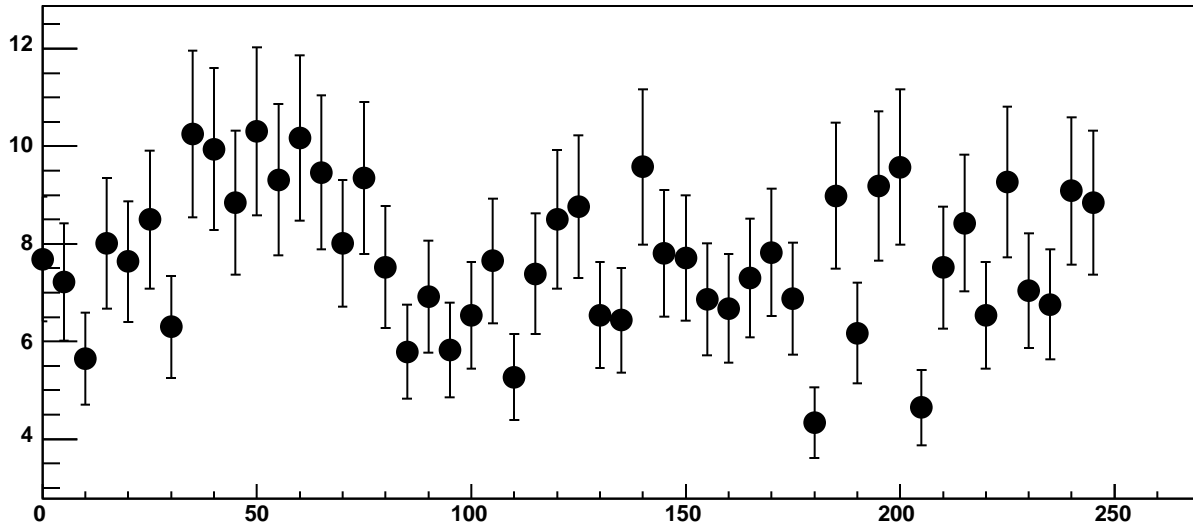
Chip 8, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold



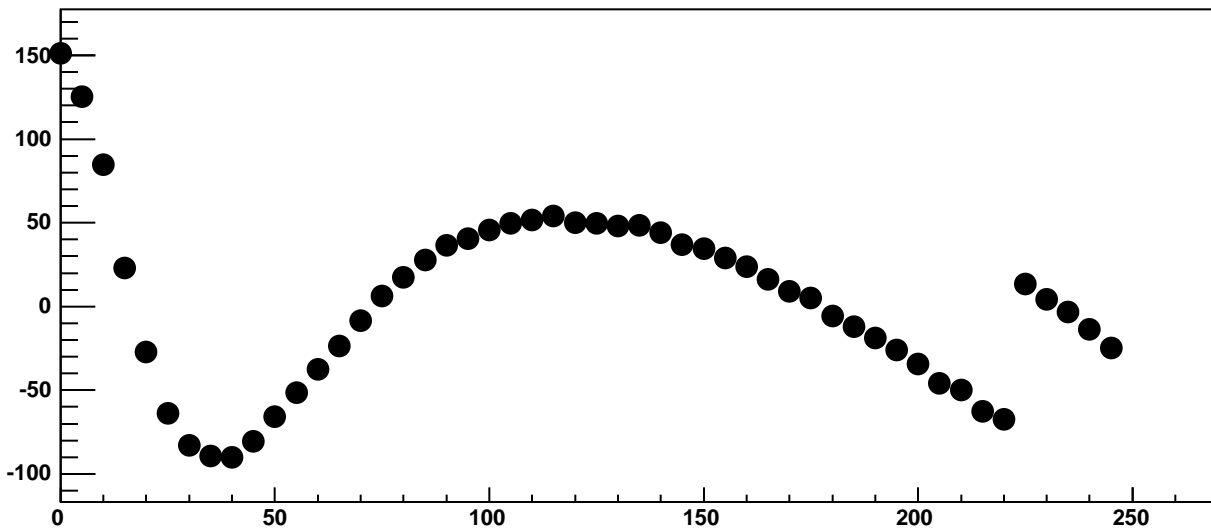
Chip 8, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold



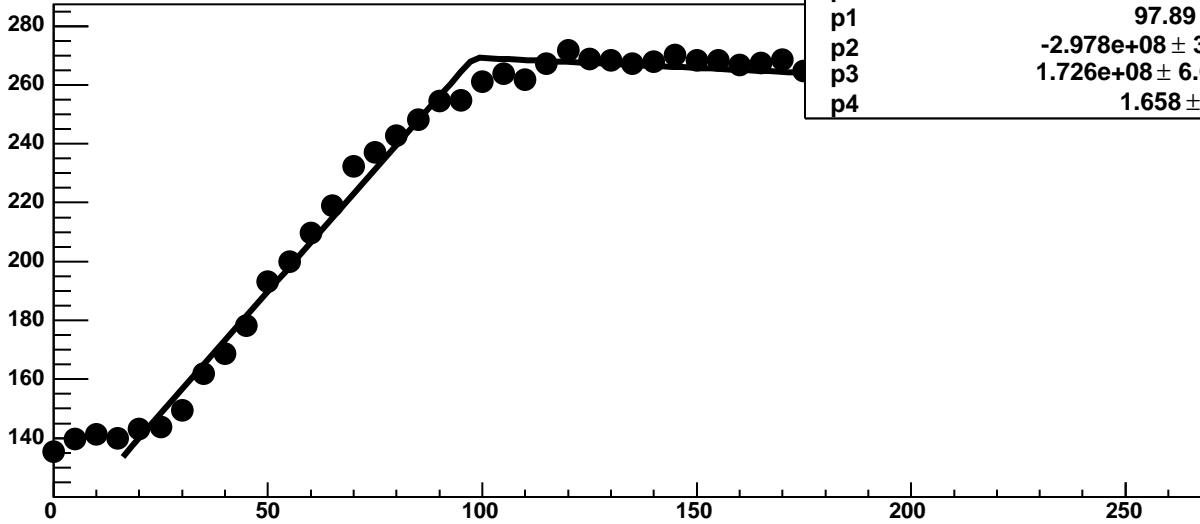
Chip 8, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

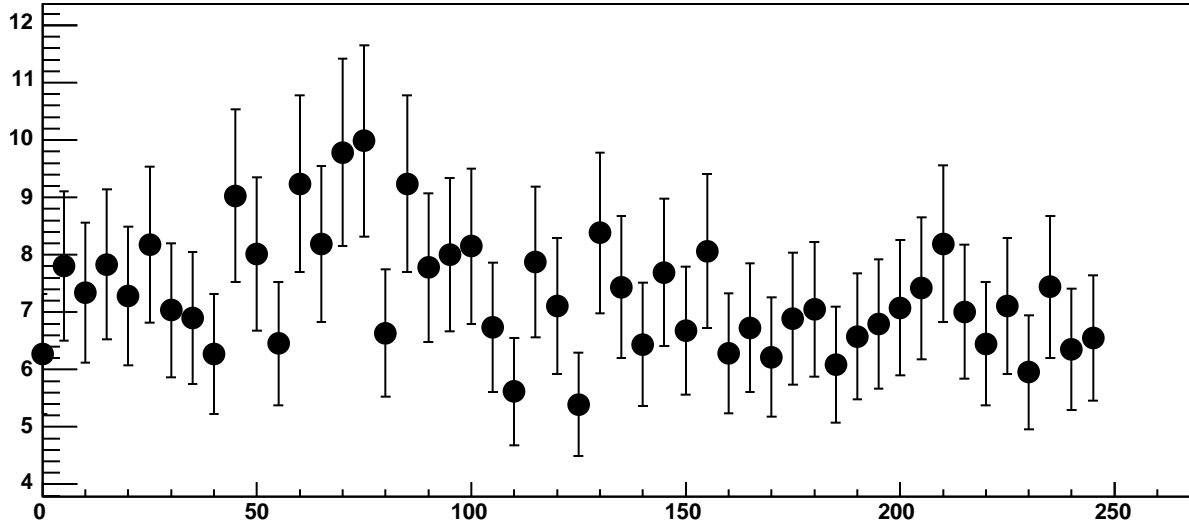


Chip 8, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

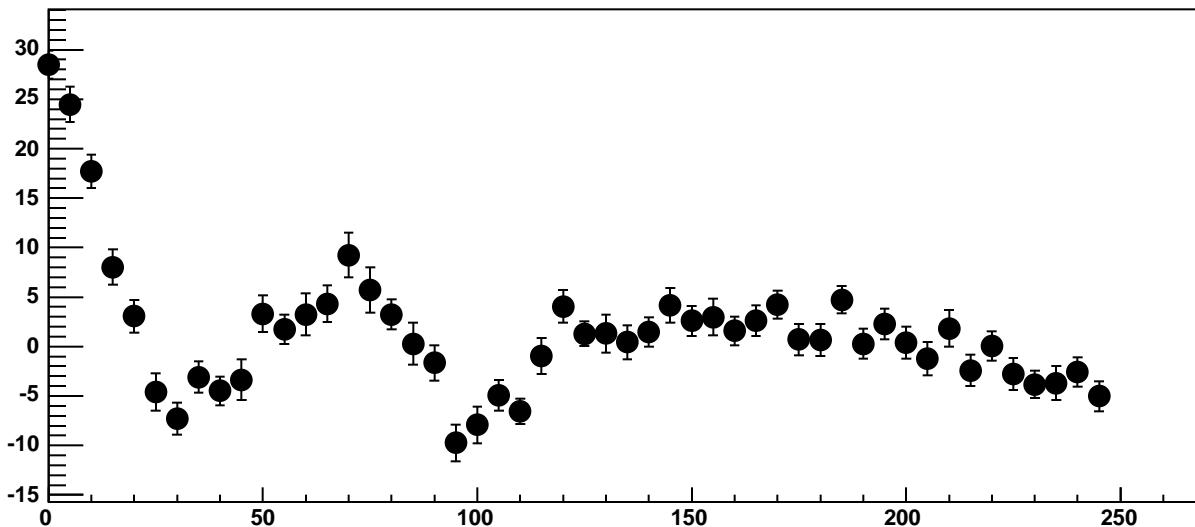


χ^2 / ndf	258.6 / 41
p0	269.3 ± 0.5639
p1	97.89 ± 0.6452
p2	$-2.978e+08 \pm 3.35e+06$
p3	$1.726e+08 \pm 6.652e+04$
p4	1.658 ± 0.01842

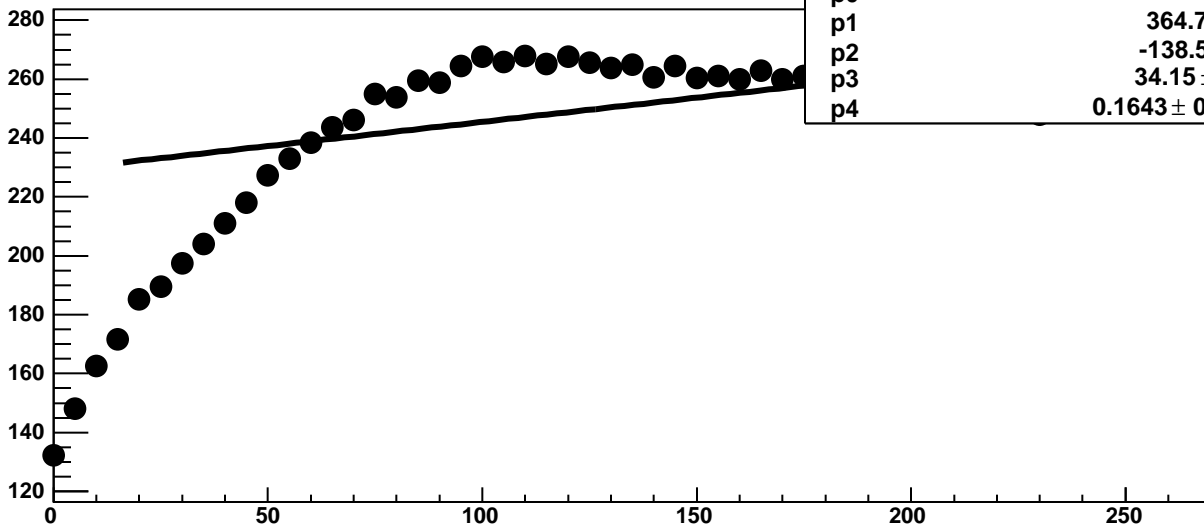
Chip 8, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



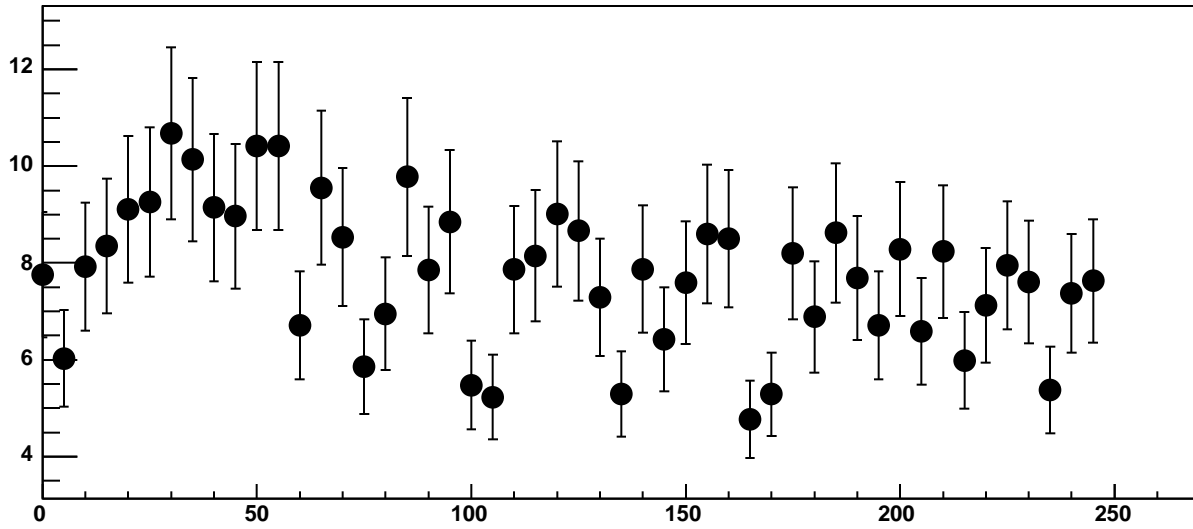
Chip 8, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold



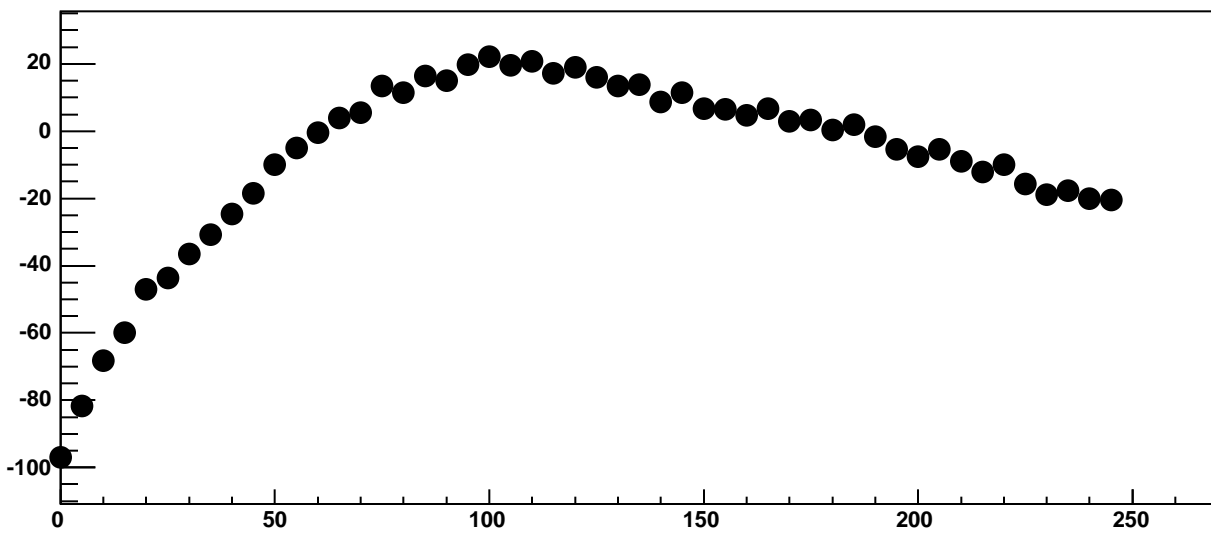
Chip 8, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold



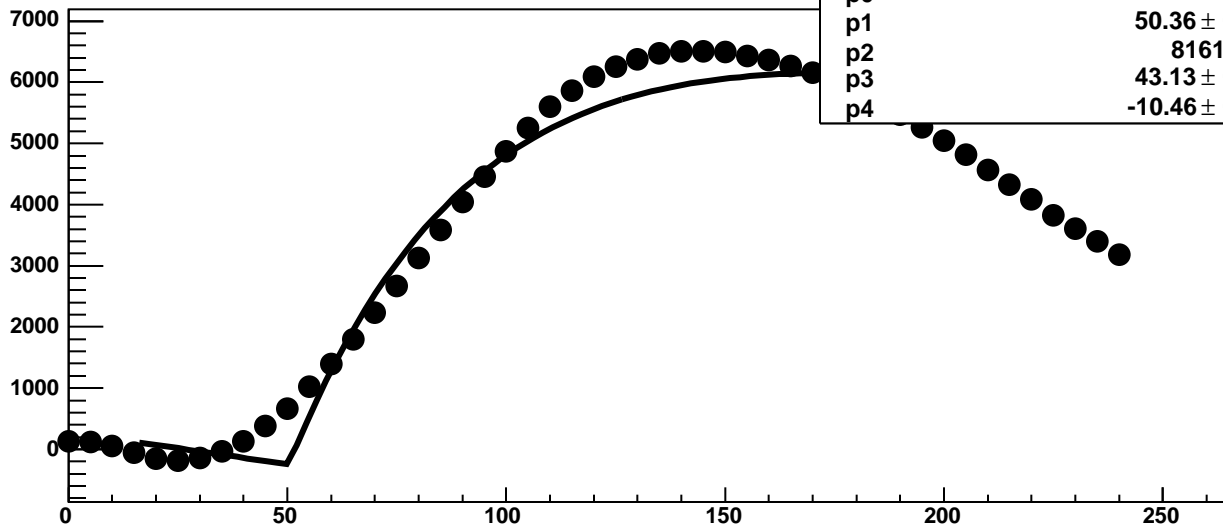
Chip 8, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold

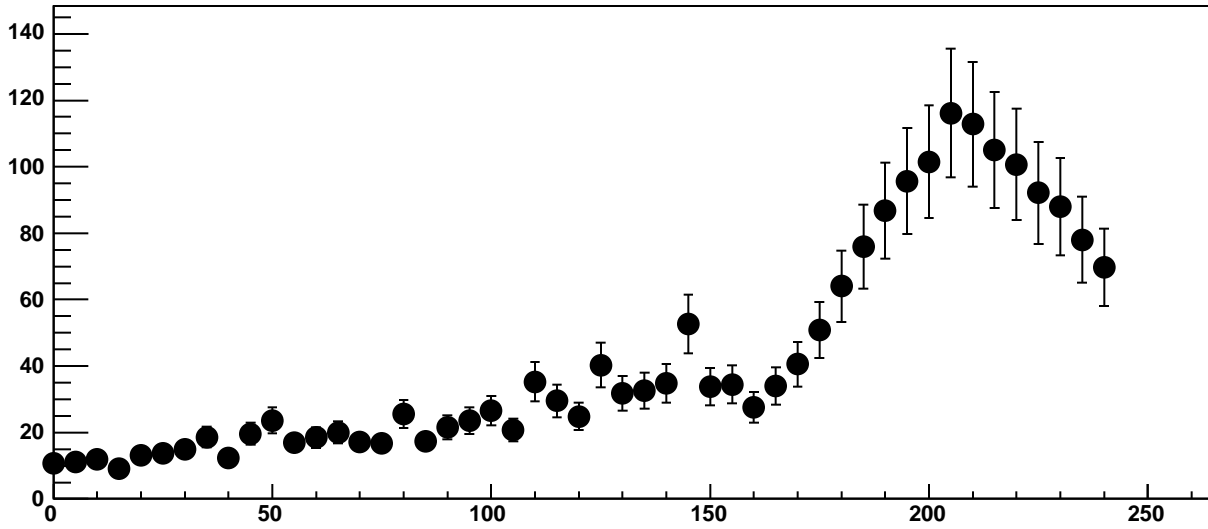


Chip 8, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

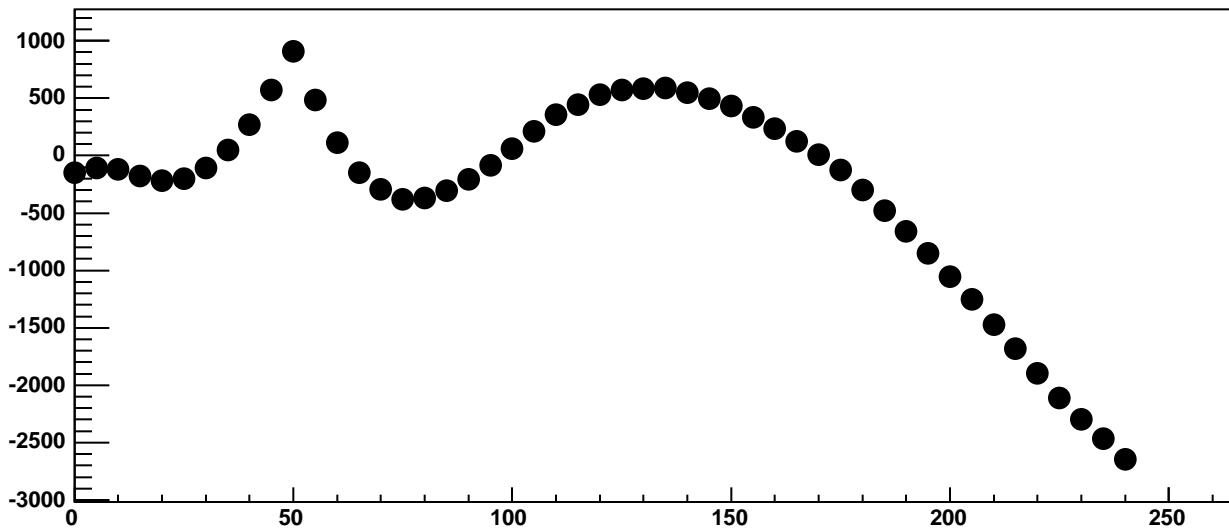


χ^2 / ndf	2.54e+05 / 41
p0	-247.9 ± 1.942
p1	50.36 ± 0.02223
p2	8161 ± 13.18
p3	43.13 ± 0.07574
p4	-10.46 ± 0.07413

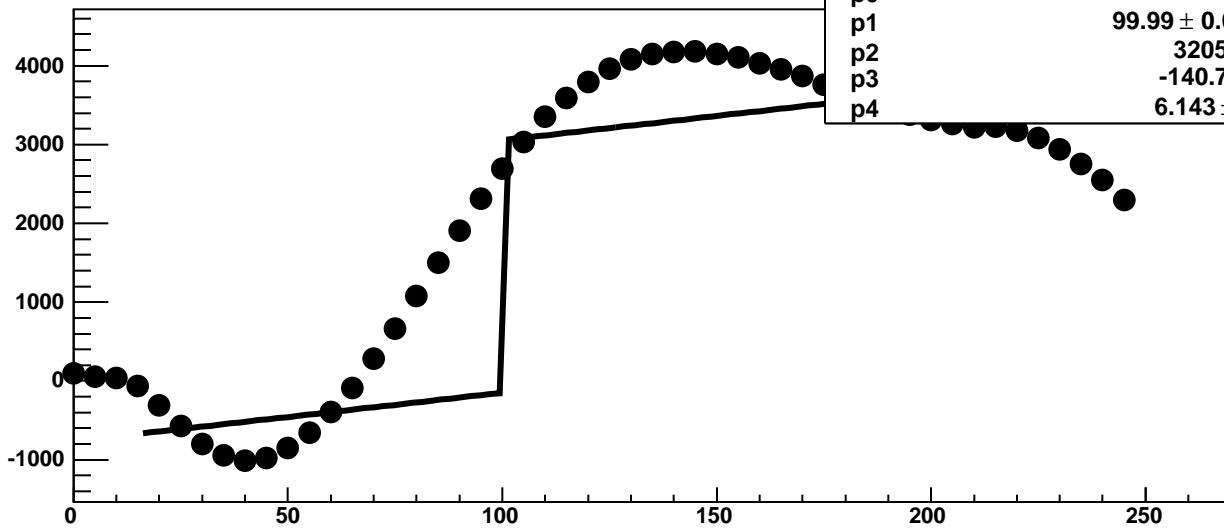
Chip 8, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

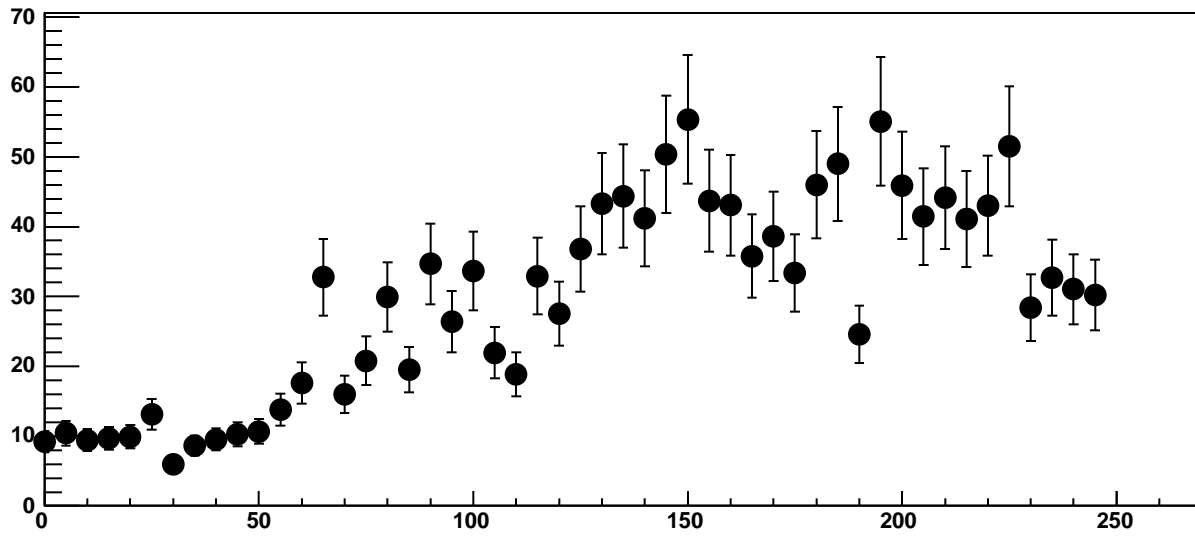


Chip 8, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

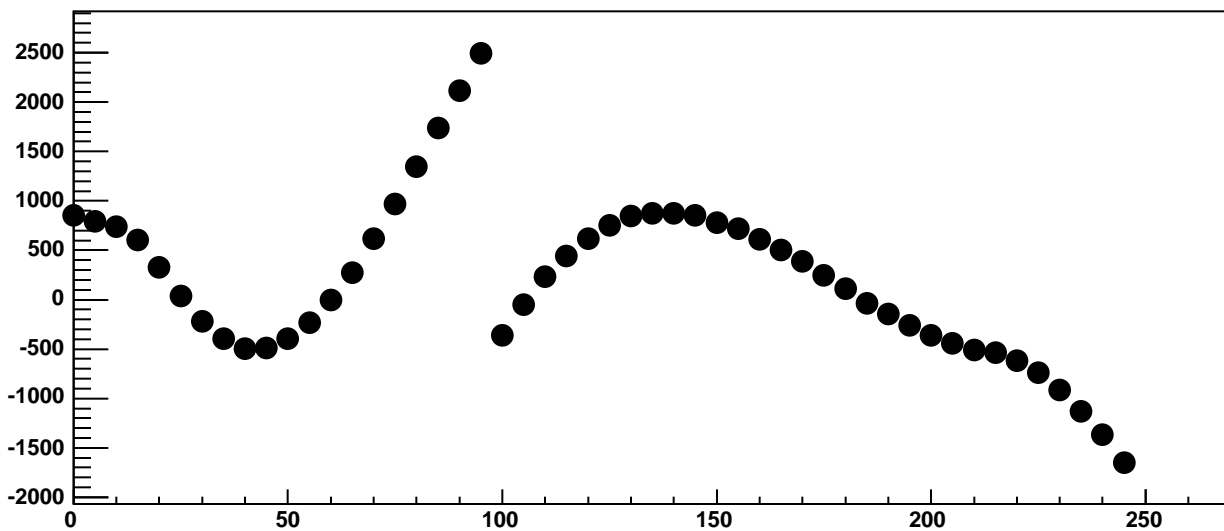


χ^2 / ndf	9.618e+05 / 41
p0	-149.5 ± 8.645
p1	99.99 ± 0.0003605
p2	3205 ± 10.73
p3	-140.7 ± 3.935
p4	6.143 ± 0.1228

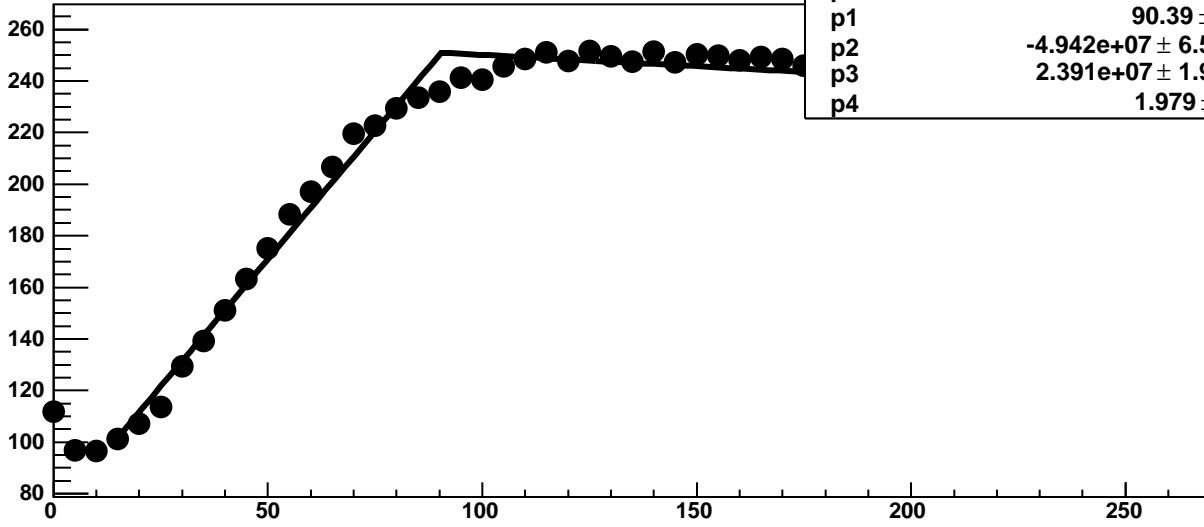
Chip 8, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold

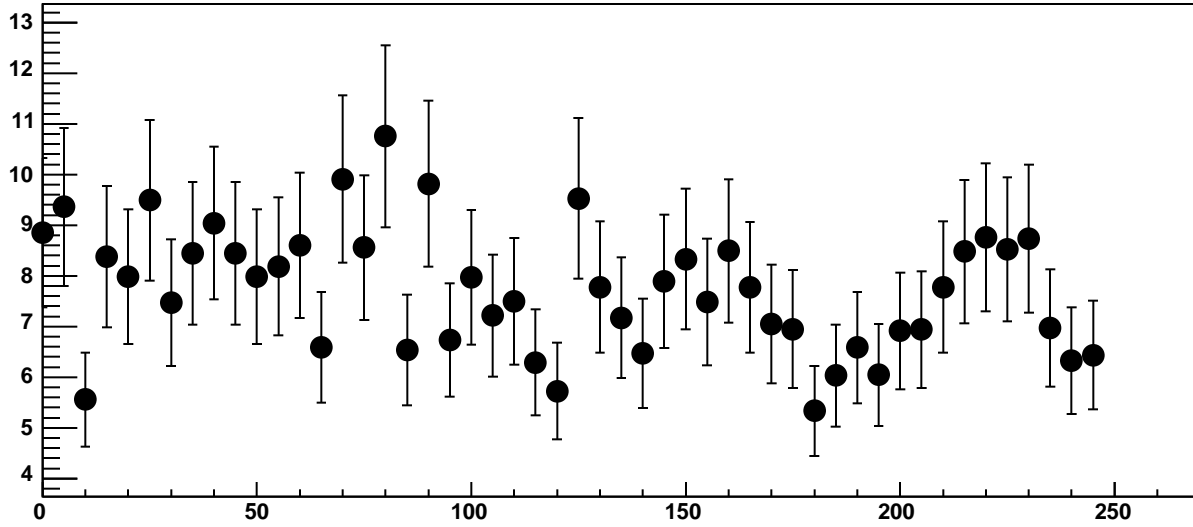


Chip 8, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

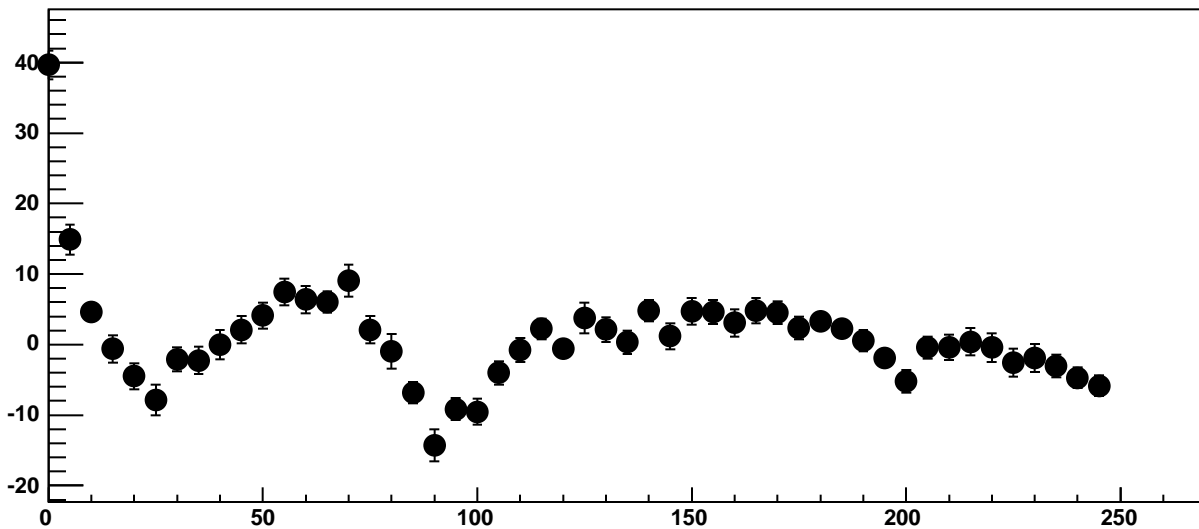


χ^2 / ndf	307 / 41
p0	251 ± 0.5884
p1	90.39 ± 0.5346
p2	$-4.942\text{e}+07 \pm 6.555\text{e}+05$
p3	$2.391\text{e}+07 \pm 1.945\text{e}+05$
p4	1.979 ± 0.0208

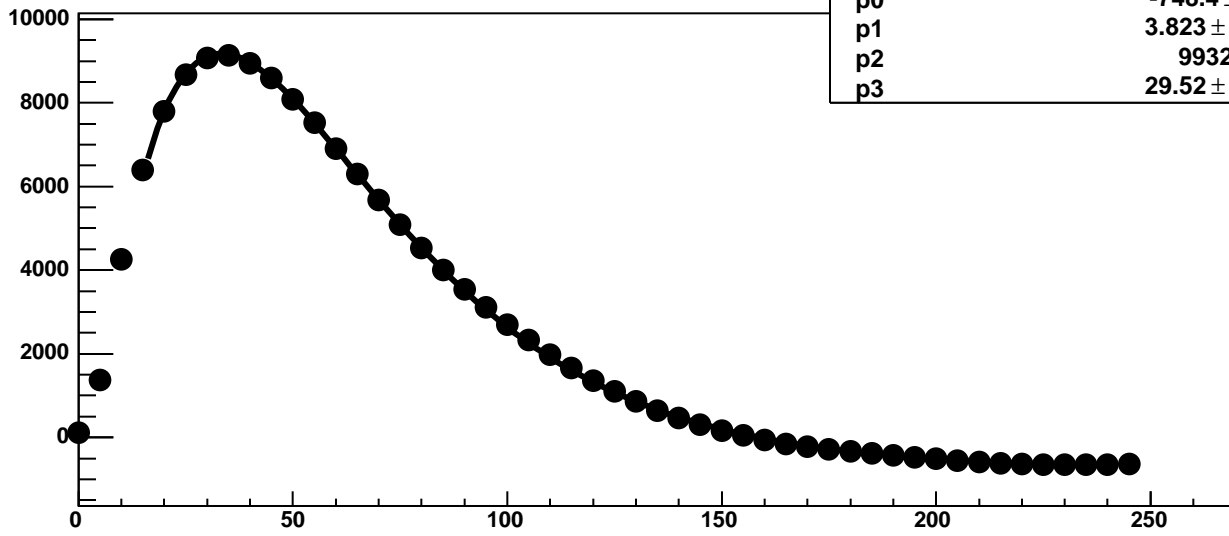
Chip 8, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

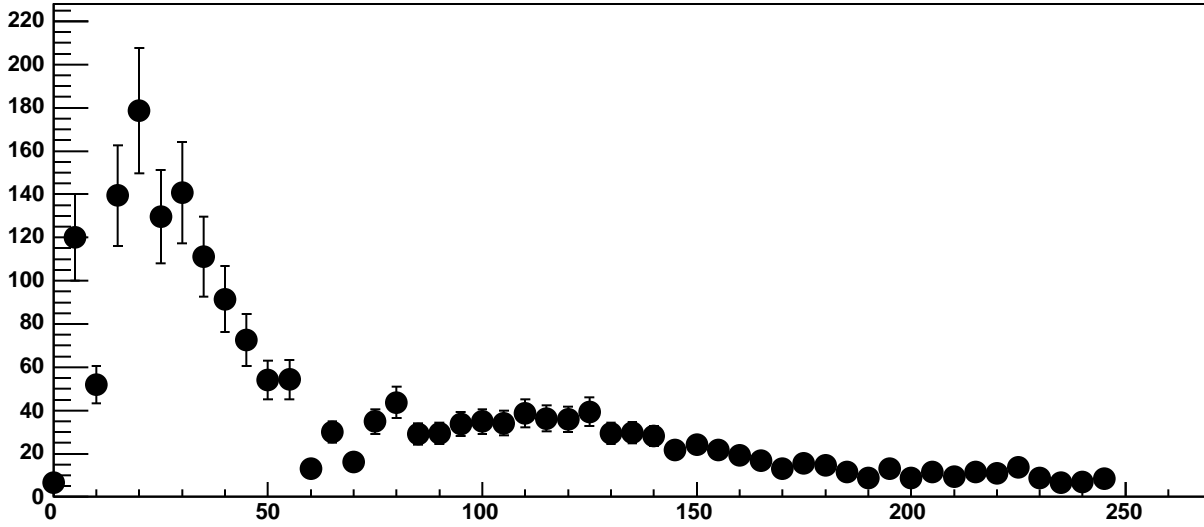


Chip 8, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold

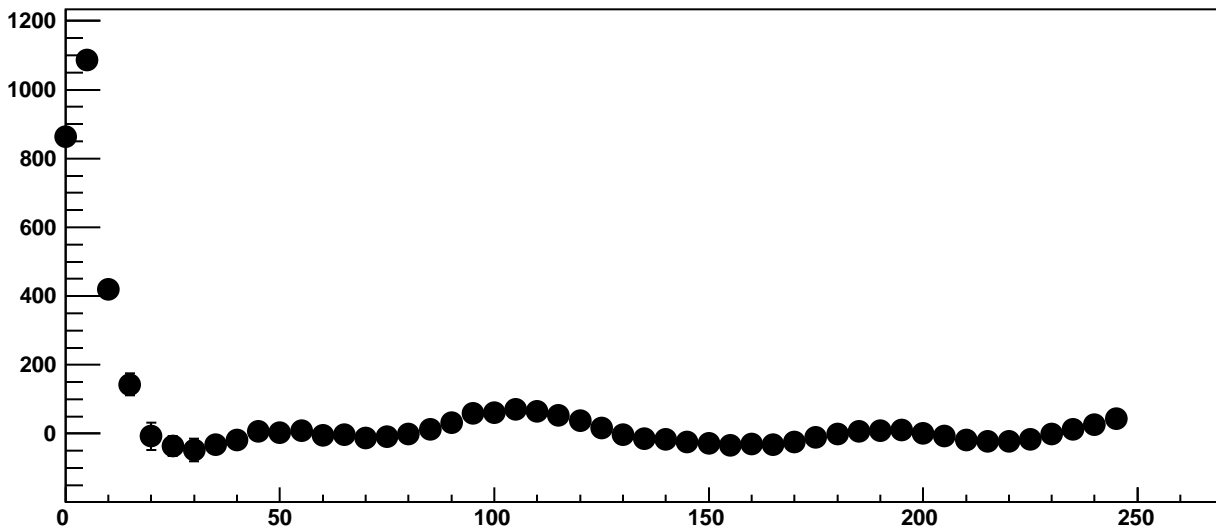


χ^2 / ndf	1358 / 42
p0	-748.4 ± 0.8674
p1	3.823 ± 0.05634
p2	9932 ± 6.774
p3	29.52 ± 0.01996

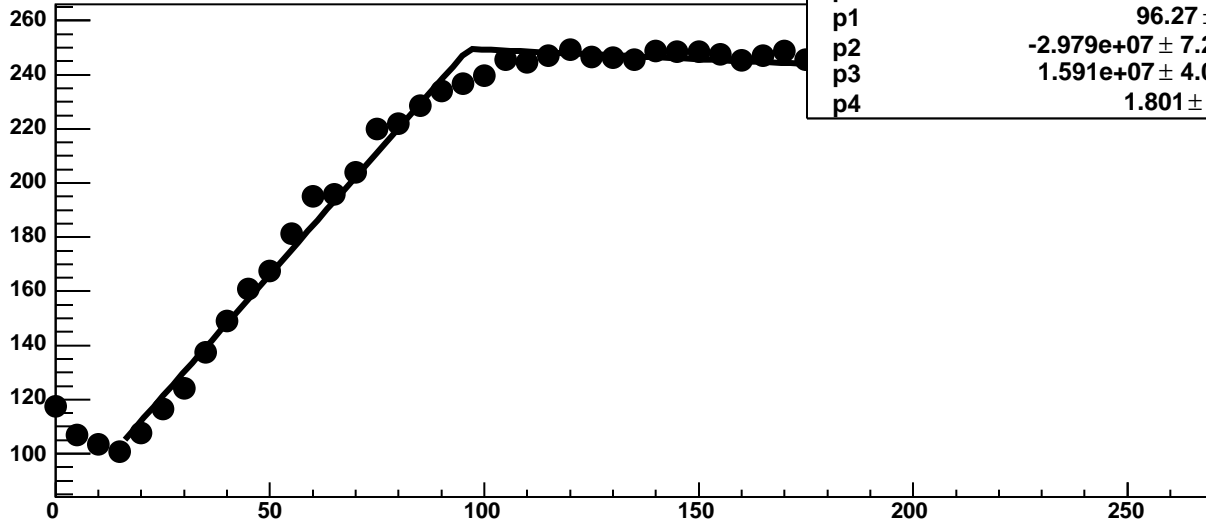
Chip 8, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold

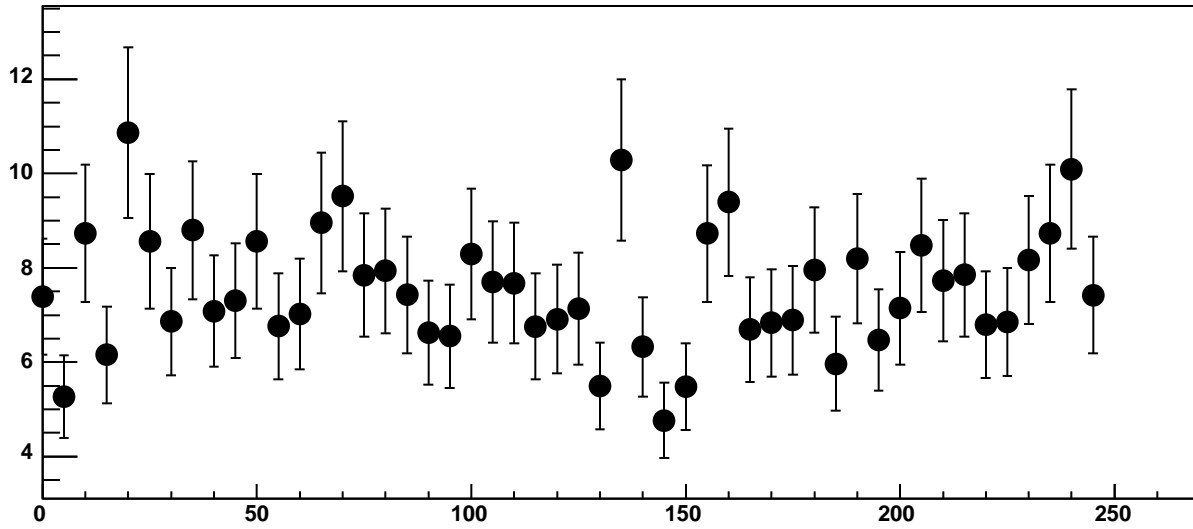


Chip 8, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

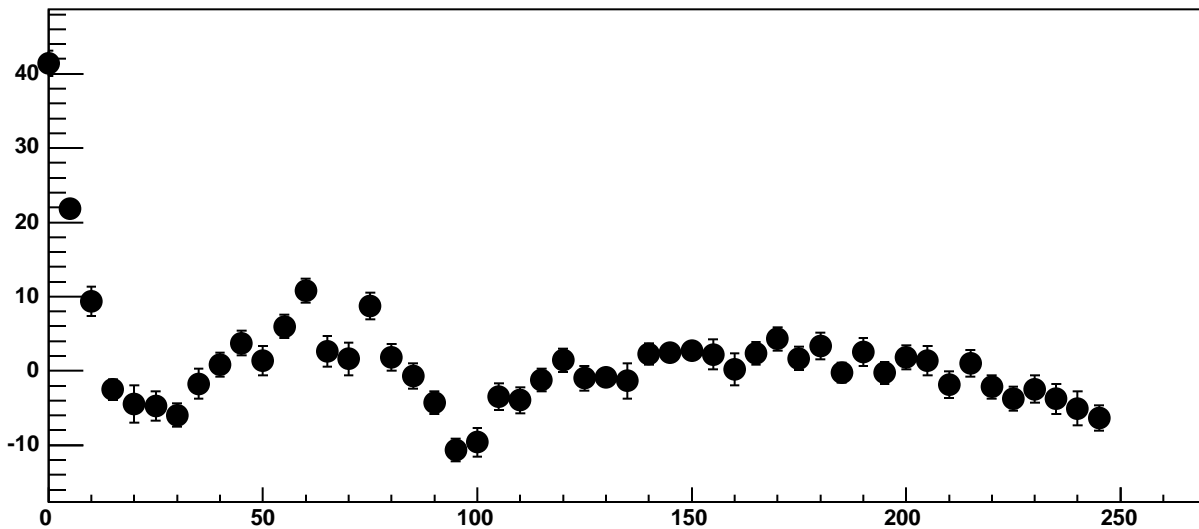


χ^2 / ndf	265.9 / 41
p0	249.6 ± 0.5837
p1	96.27 ± 0.5082
p2	$-2.979\text{e}+07 \pm 7.219\text{e}+05$
p3	$1.591\text{e}+07 \pm 4.054\text{e}+05$
p4	1.801 ± 0.01523

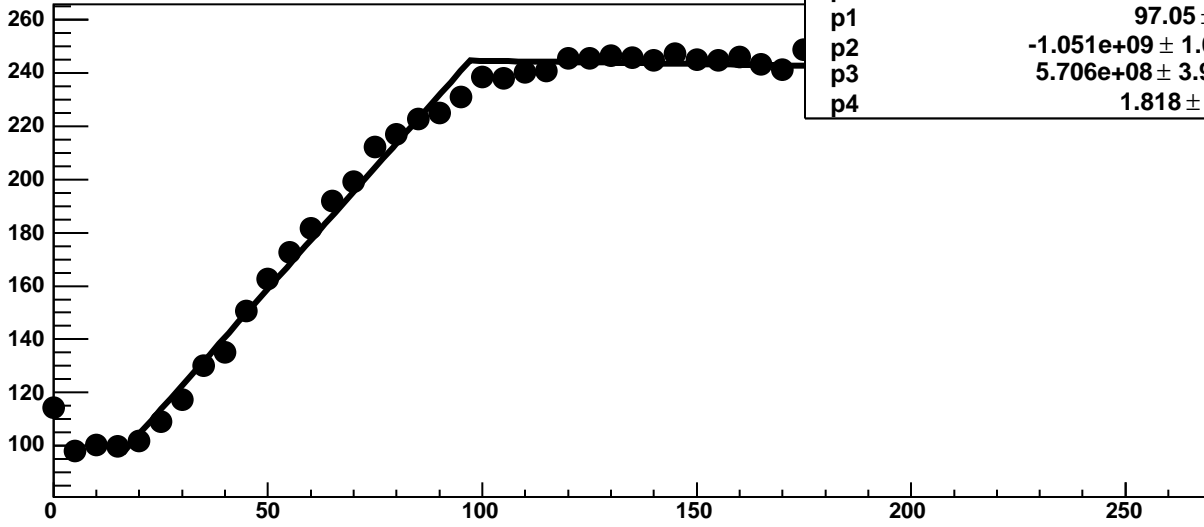
Chip 8, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

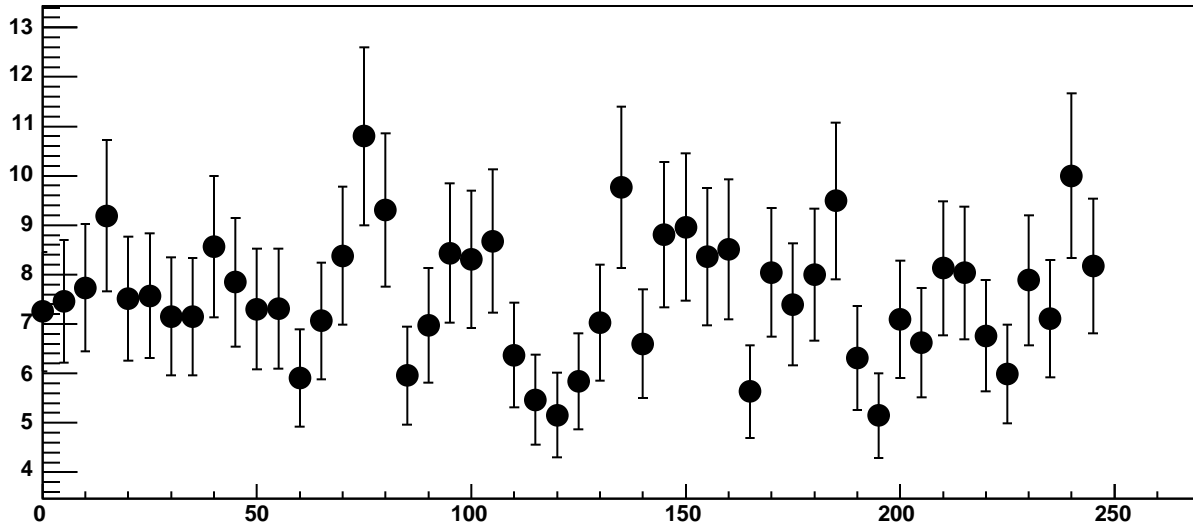


Chip 8, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

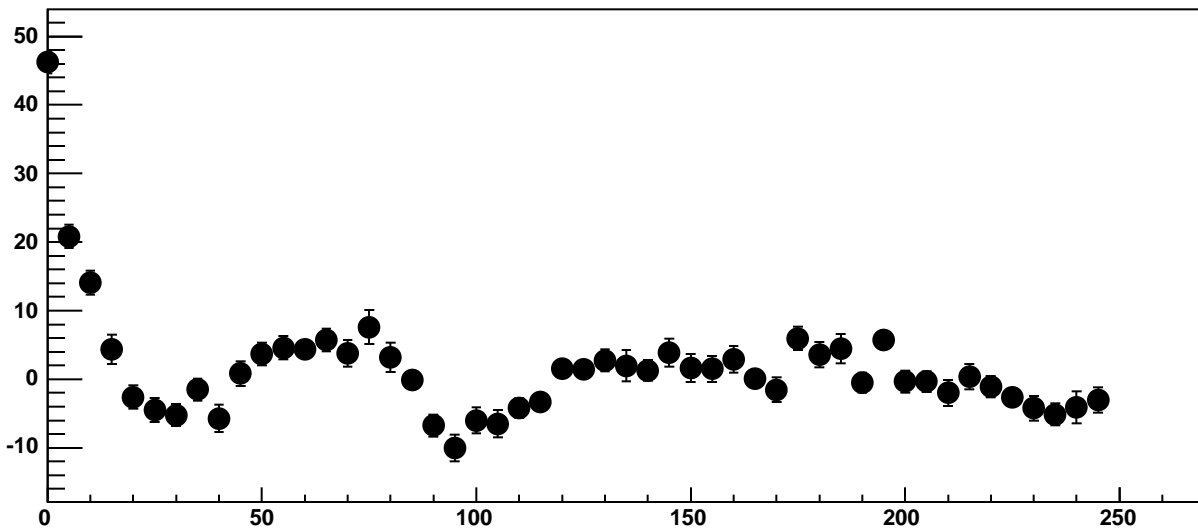


χ^2 / ndf	247.4 / 41
p0	244.6 ± 0.5568
p1	97.05 ± 0.4812
p2	$-1.051\text{e}+09 \pm 1.049\text{e}+07$
p3	$5.706\text{e}+08 \pm 3.956\text{e}+05$
p4	1.818 ± 0.01639

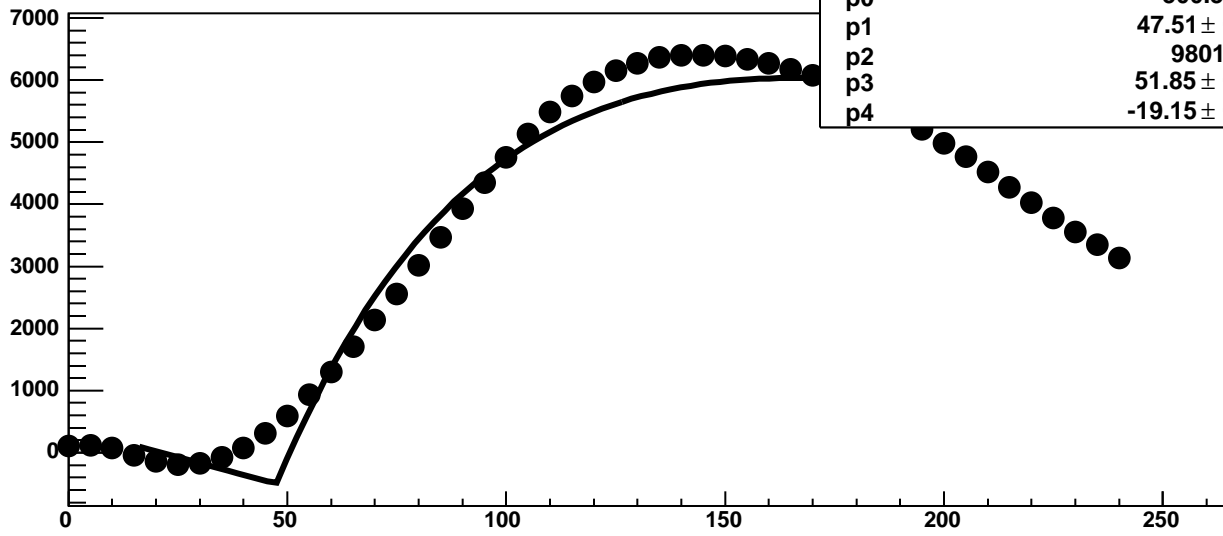
Chip 8, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

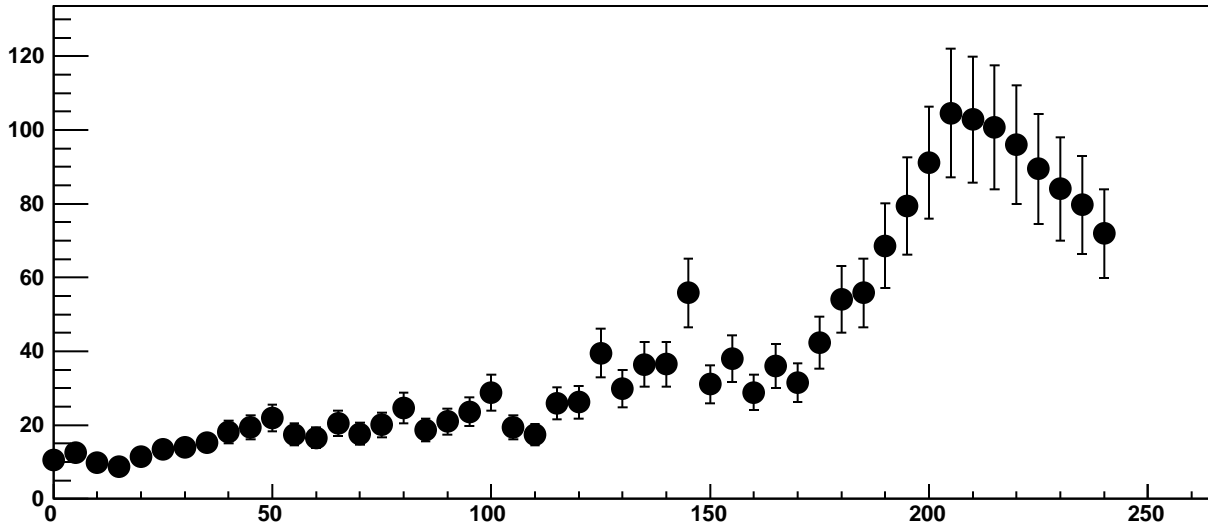


Chip 8, Channel 15, Enable 5, DAC=4000, ADC Mean vs Hold

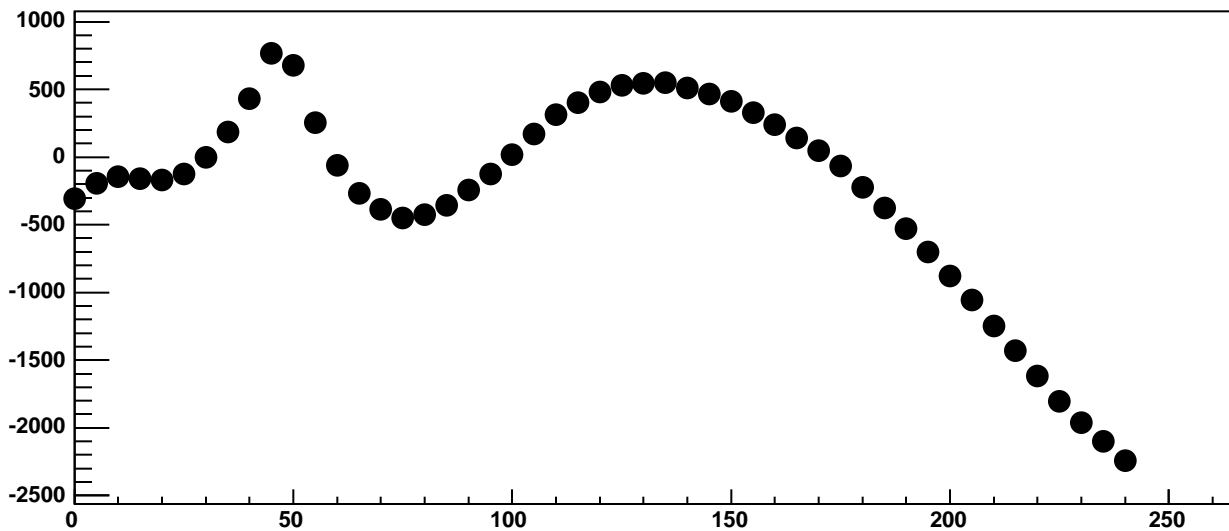


χ^2 / ndf	2.286e+05 / 41
p0	-500.5 ± 2.027
p1	47.51 ± 0.02123
p2	9801 ± 16.87
p3	51.85 ± 0.08792
p4	-19.15 ± 0.08552

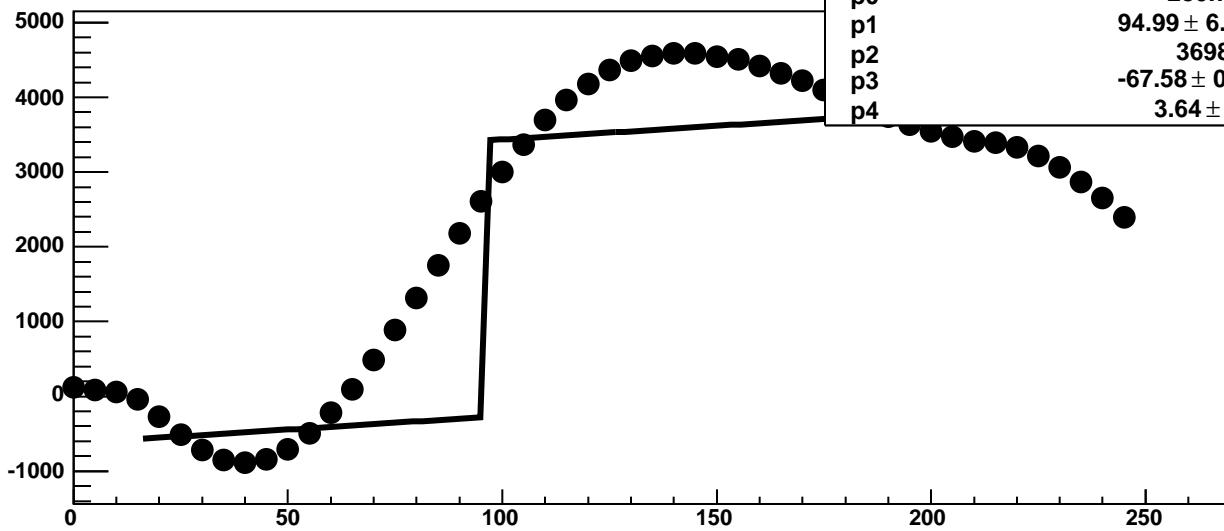
Chip 8, Channel 15, Enable 5, DAC=4000, ADC Noise vs Hold



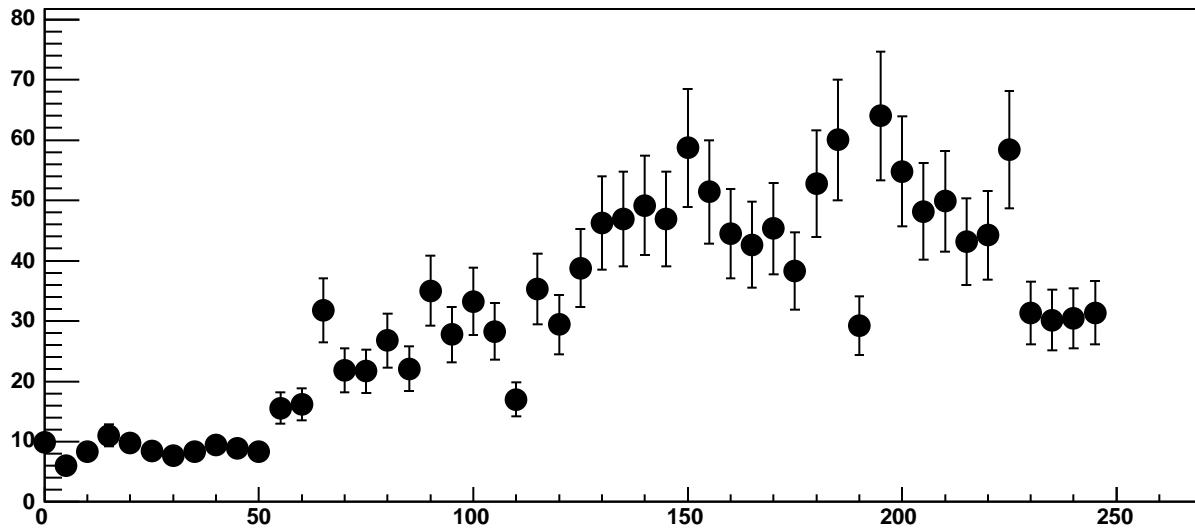
Chip 8, Channel 15, Enable 5, DAC=4000, ADC Residuals vs Hold



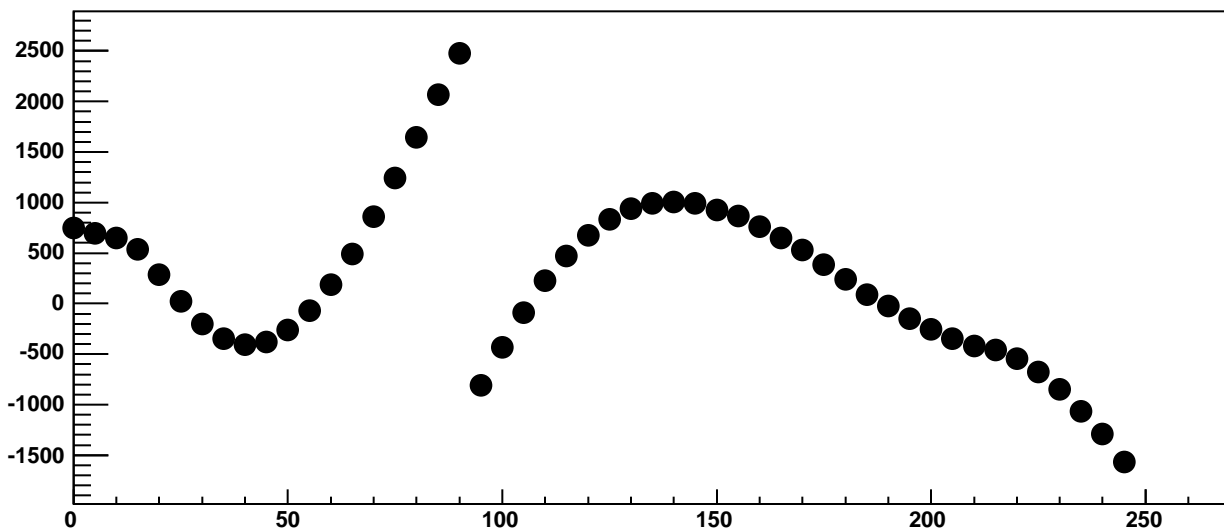
Chip 8, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold



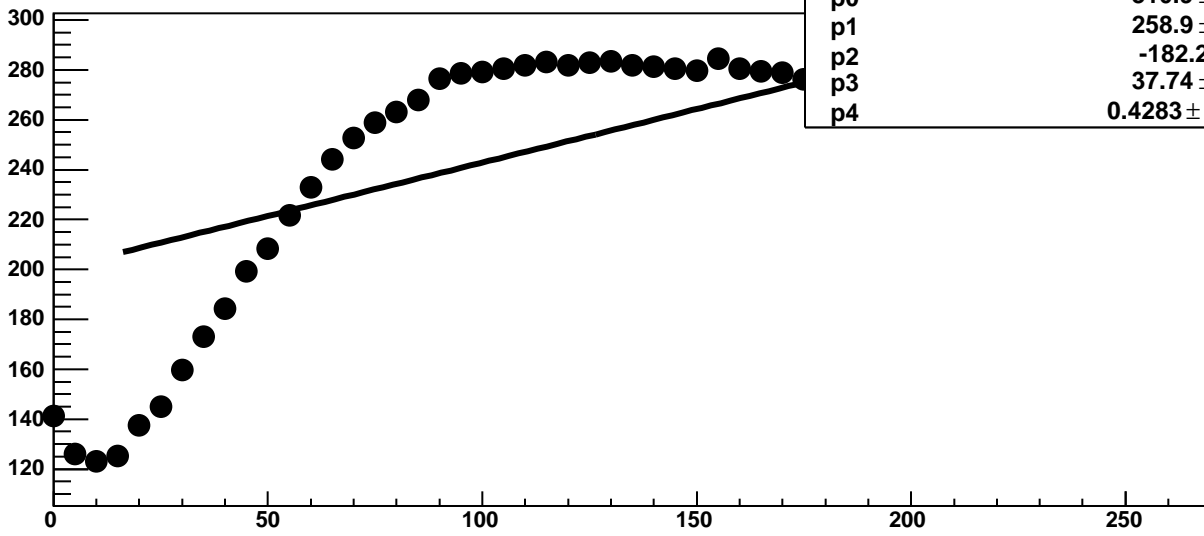
Chip 8, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

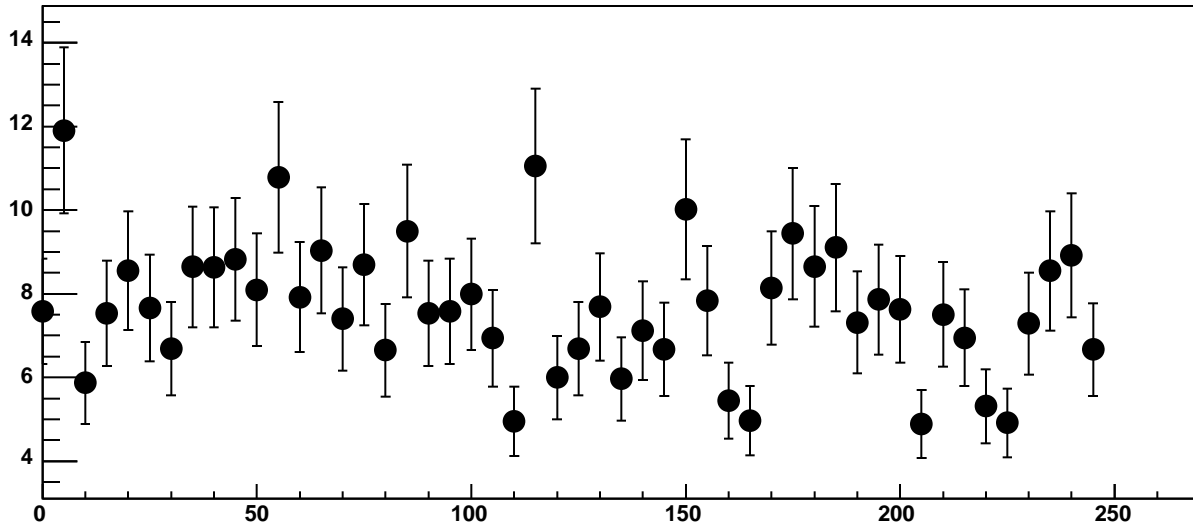


Chip 8, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold

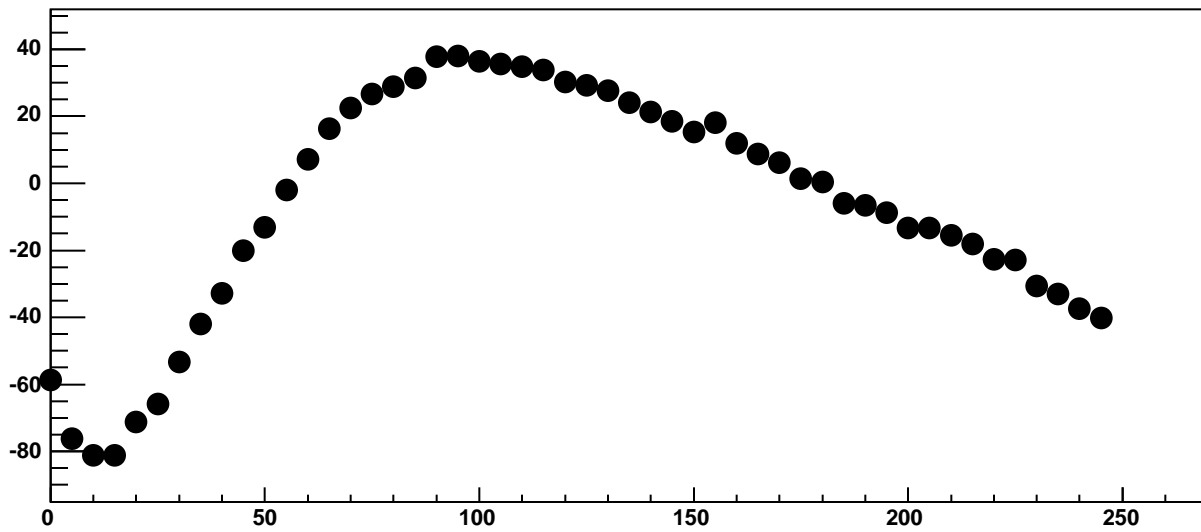


χ^2 / ndf	1.515e+04 / 41
p0	310.9 ± 0.3086
p1	258.9 ± 0.7106
p2	-182.2 ± 6.259
p3	37.74 ± 0.3715
p4	0.4283 ± 0.00375

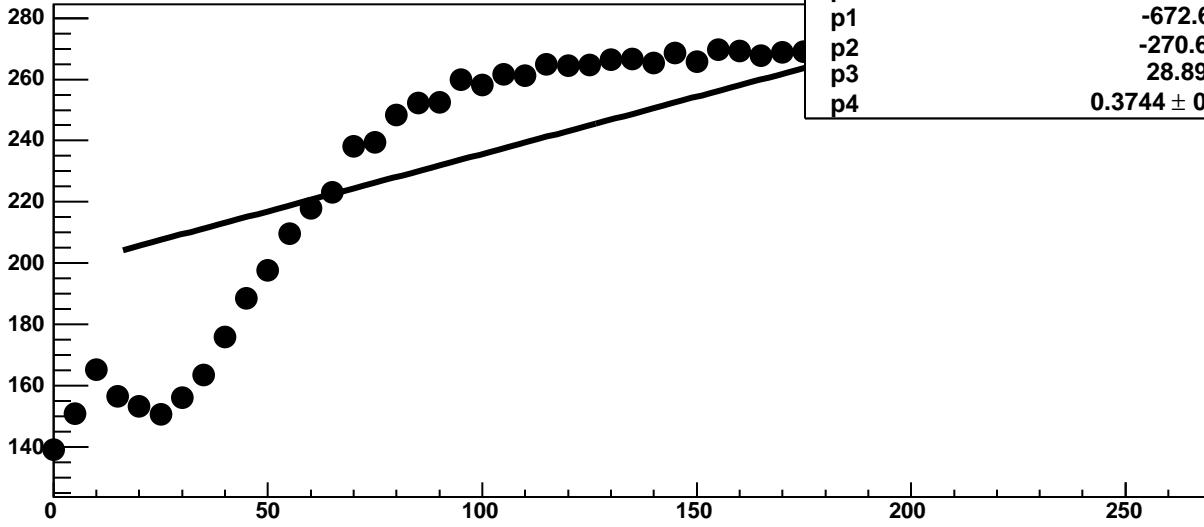
Chip 8, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold

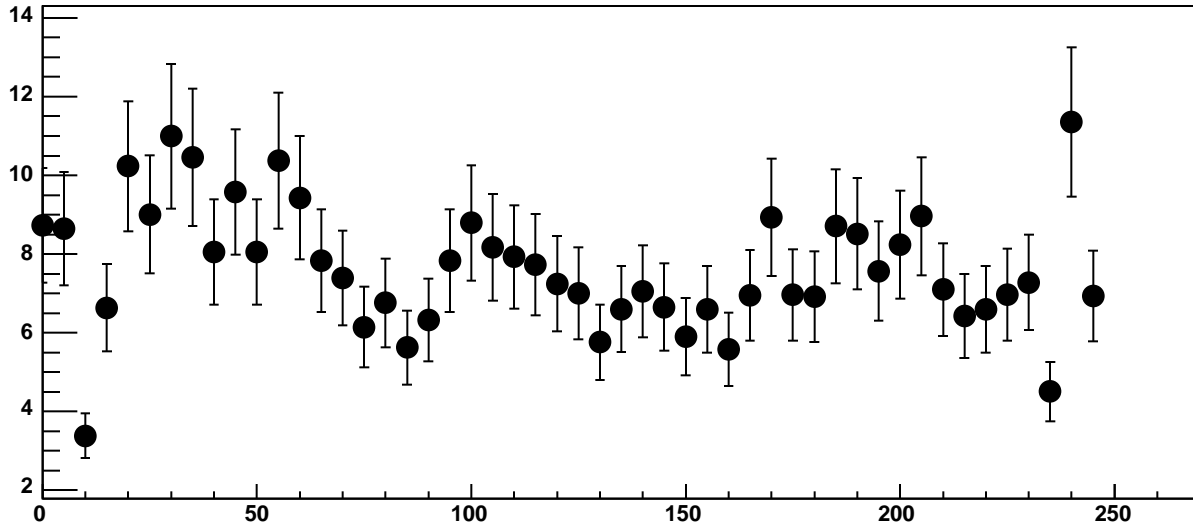


Chip 8, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold

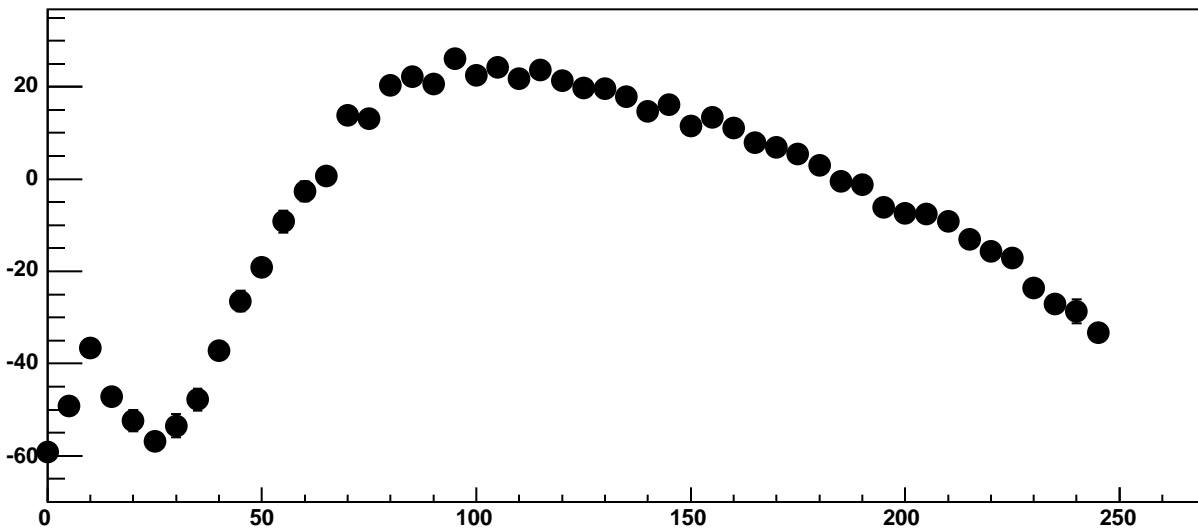


χ^2 / ndf	7954 / 41
p0	217 ± 3.265
p1	-672.6 ± 8.721
p2	-270.6 ± 3.265
p3	28.89 ± 1298
p4	0.3744 ± 0.002826

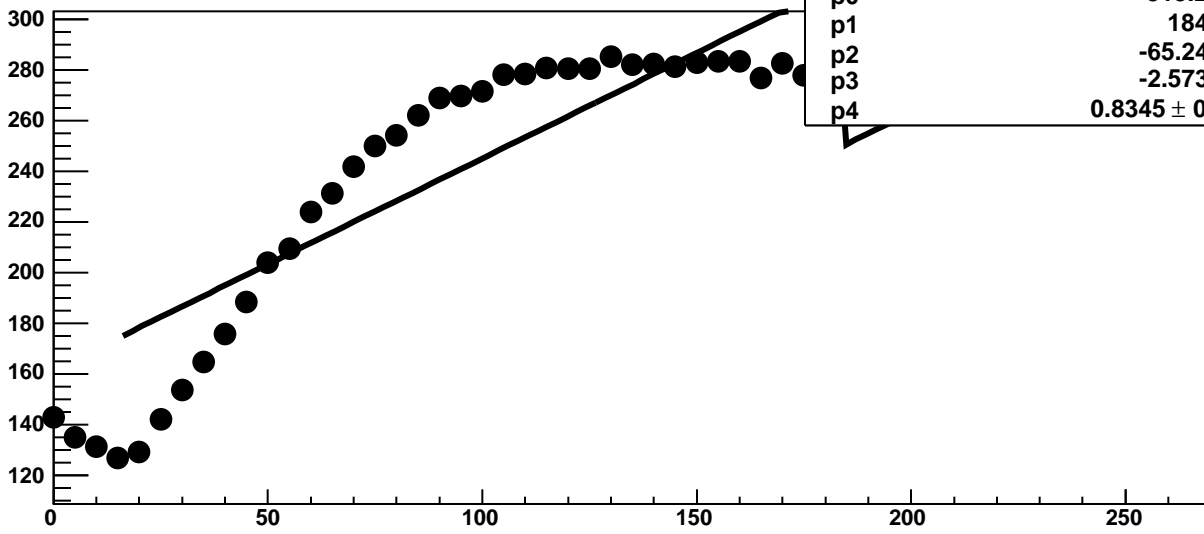
Chip 8, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold

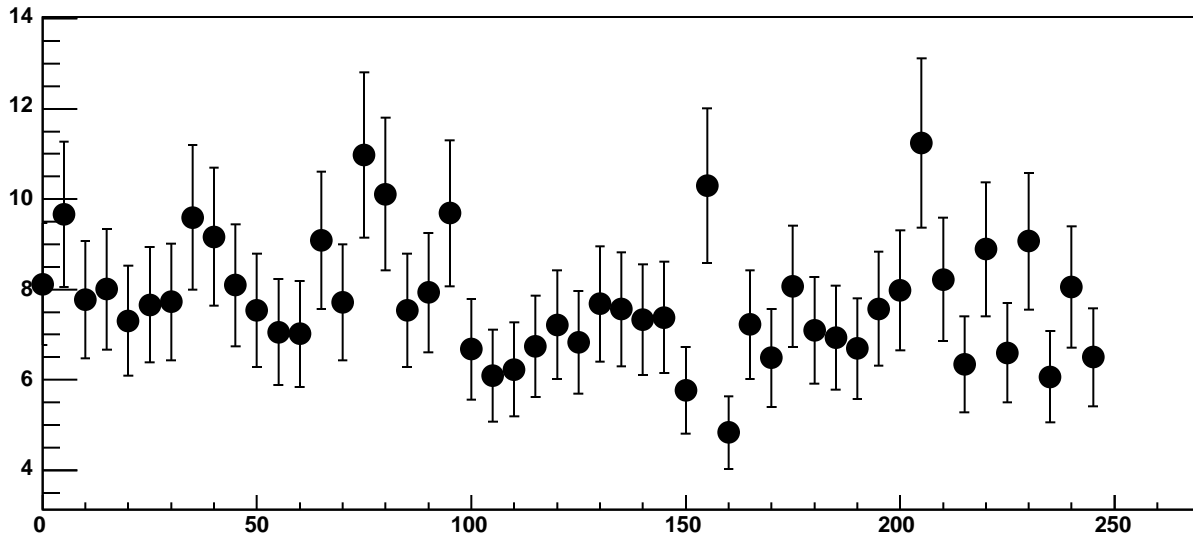


Chip 8, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

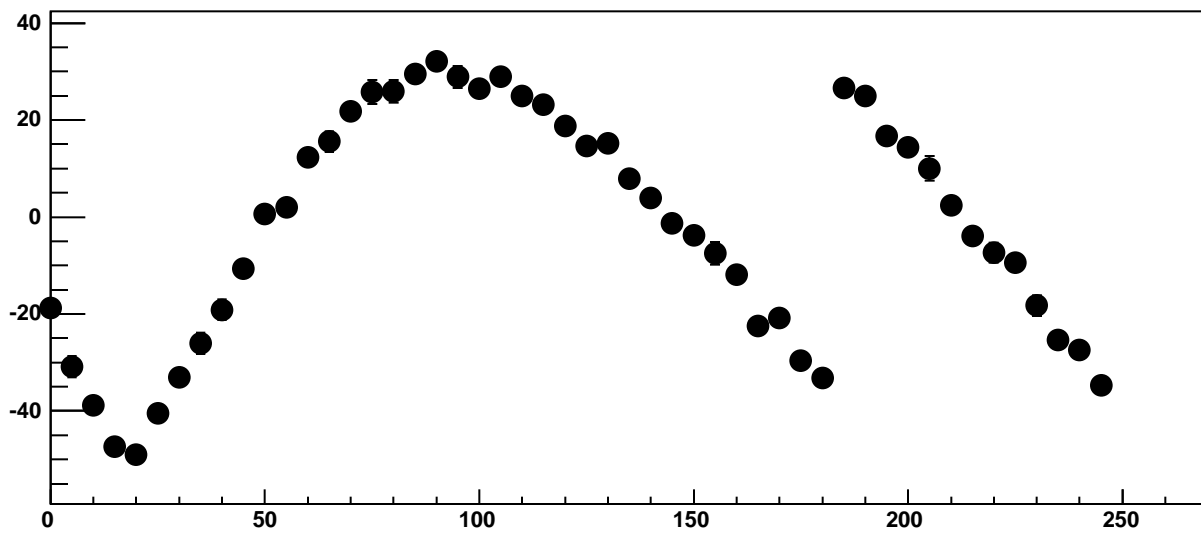


χ^2 / ndf	8084 / 41
p0	315.2 ± 1.909
p1	184 ± 2.355
p2	-65.24 ± 3.719
p3	-2.573 ± 13.04
p4	0.8345 ± 0.002324

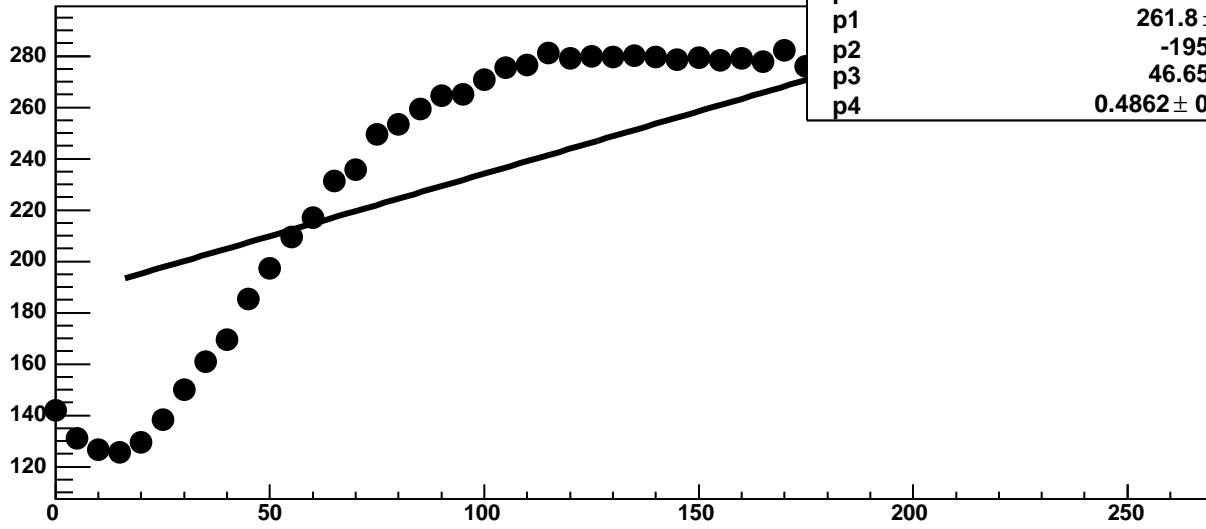
Chip 8, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

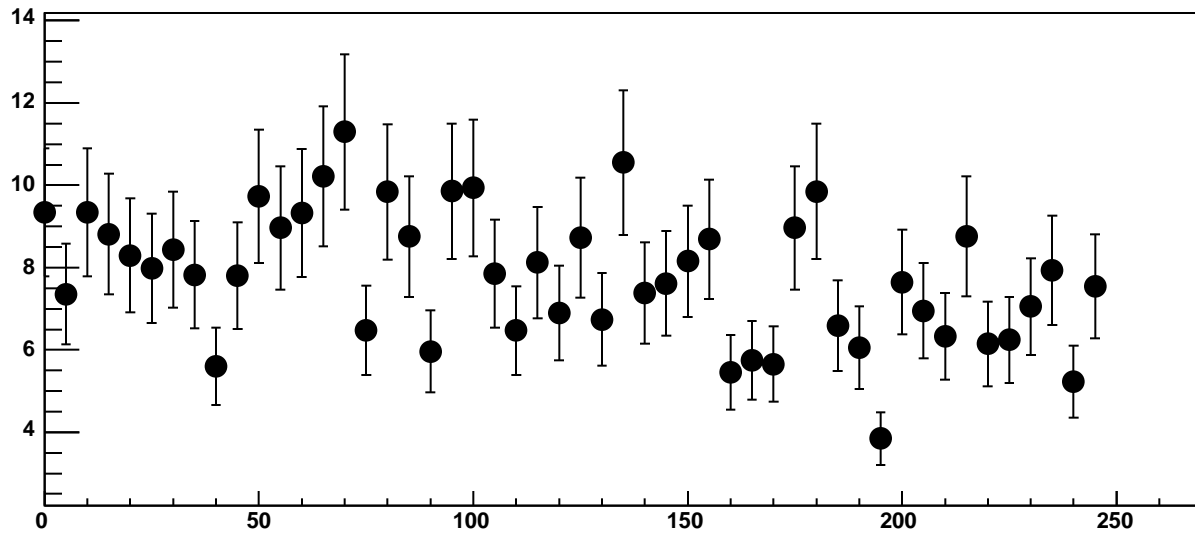


Chip 8, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

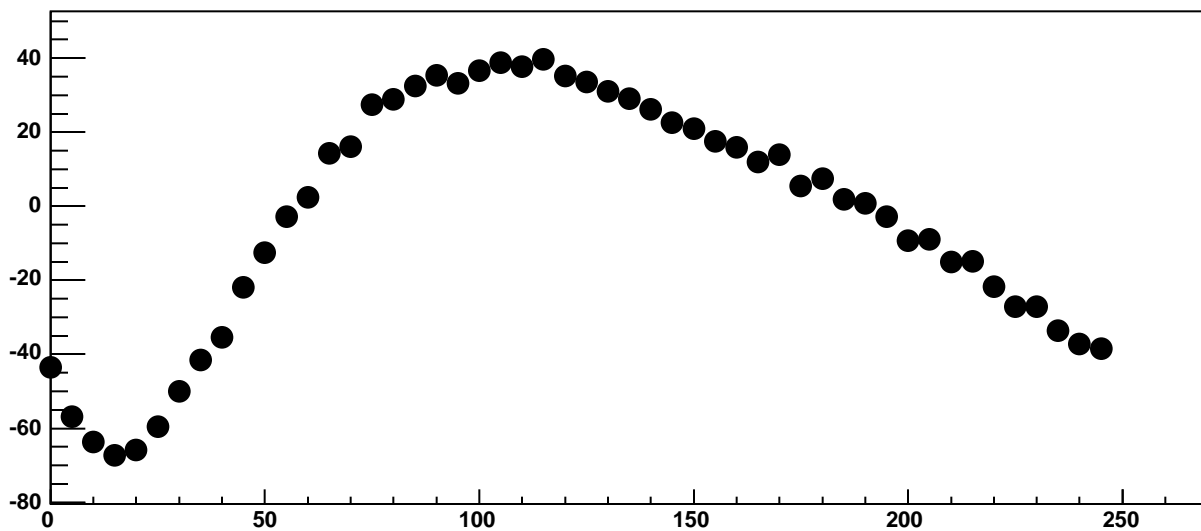


χ^2 / ndf	1.378e+04 / 41
p0	312.9 ± 0.243
p1	261.8 ± 0.4997
p2	-195 ± 84.85
p3	46.65 ± 1.414
p4	0.4862 ± 0.001766

Chip 8, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold

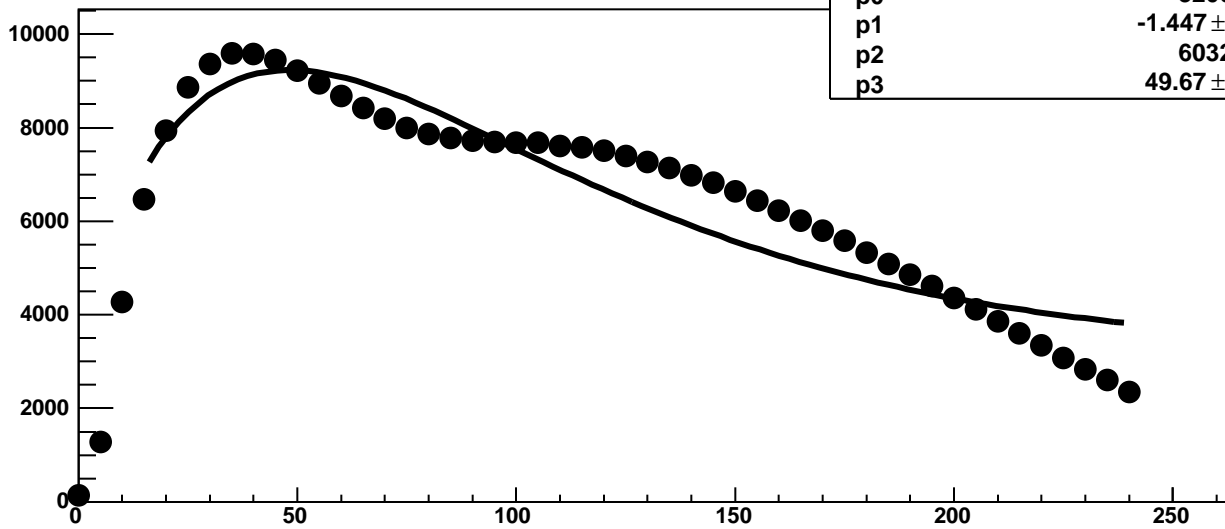


Chip 8, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

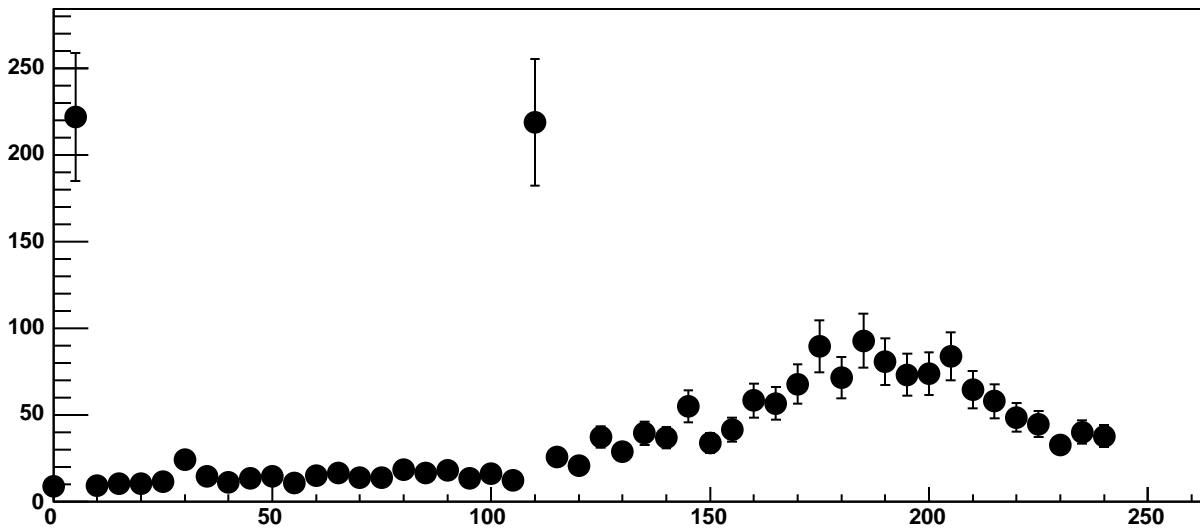


Chip 8, Channel 16, Enable 5!, DAC=4000, ADC Mean vs Hold

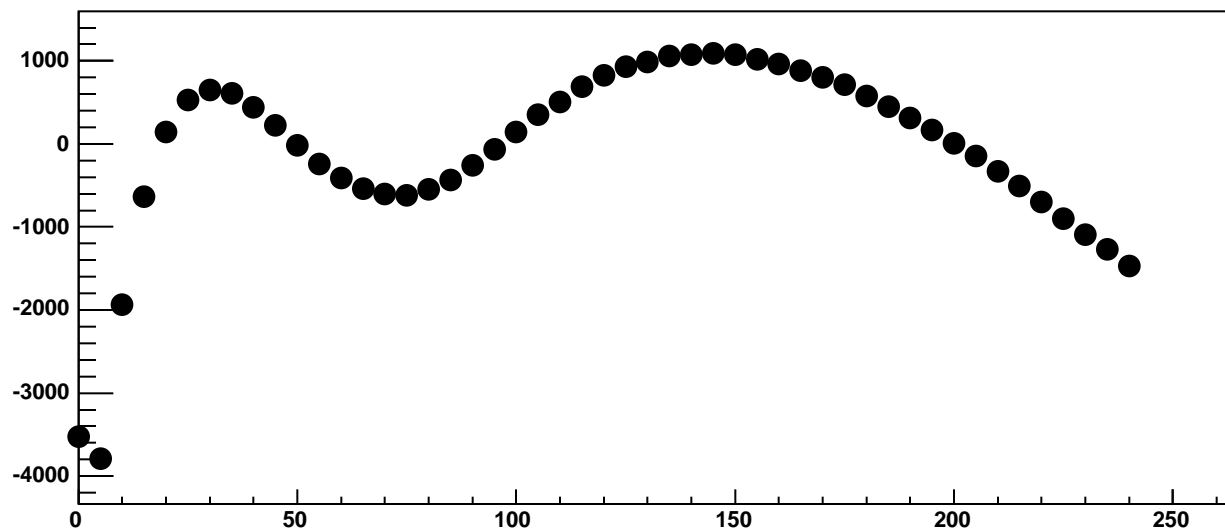
χ^2 / ndf	5.969e+05 / 42
p0	3200 ± 6.952
p1	-1.447 ± 0.04515
p2	6032 ± 6.519
p3	49.67 ± 0.05832



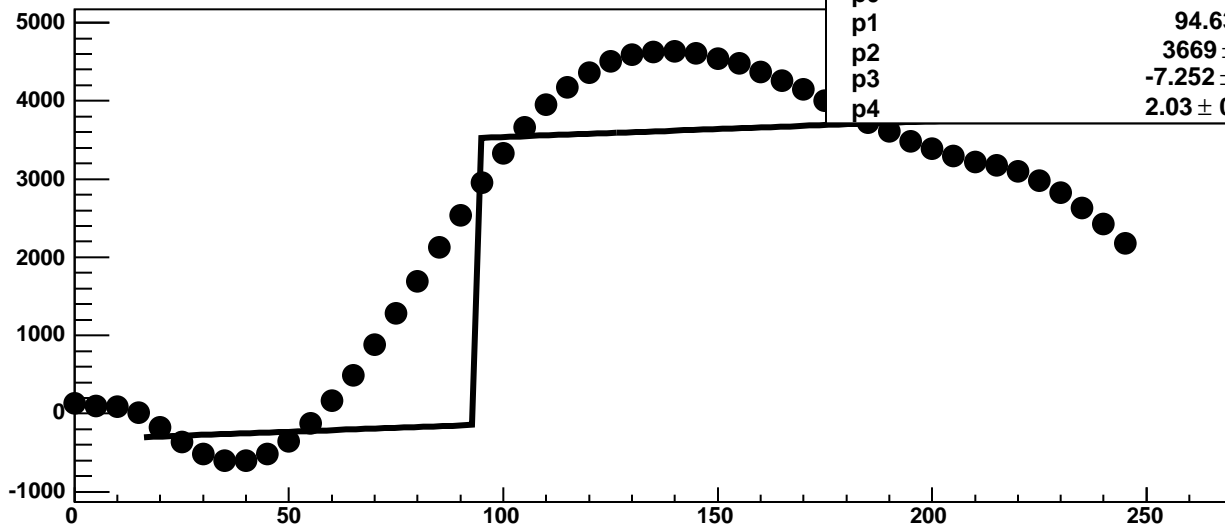
Chip 8, Channel 16, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 16, Enable 5!, DAC=4000, ADC Residuals vs Hold

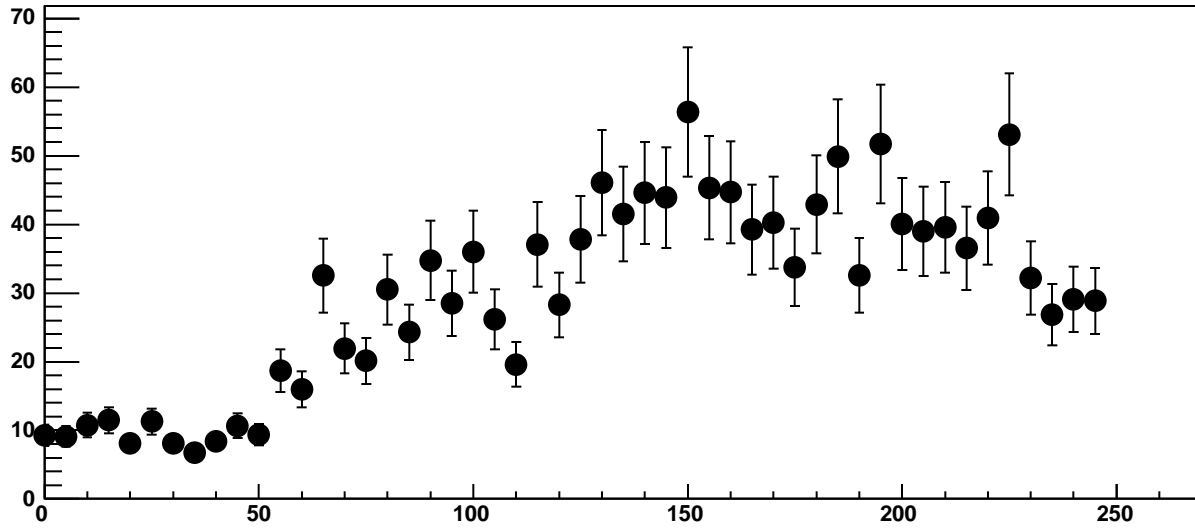


Chip 8, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

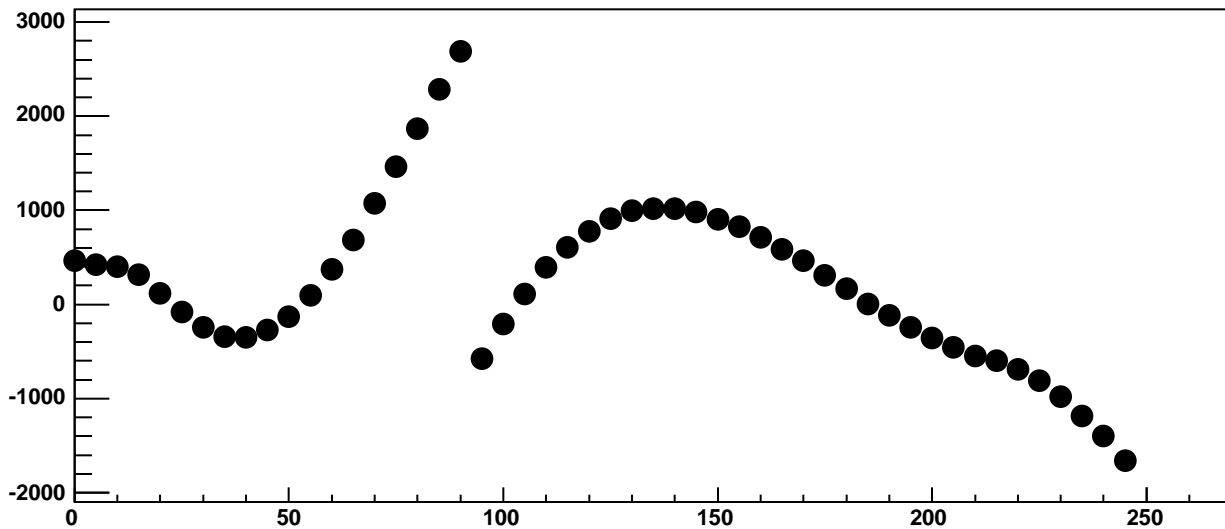


χ^2 / ndf	8.887e+05 / 41
p0	-141.3 ± 0.1306
p1	94.63 ± 1.24
p2	3669 ± 0.2813
p3	-7.252 ± 0.1595
p4	2.03 ± 0.001981

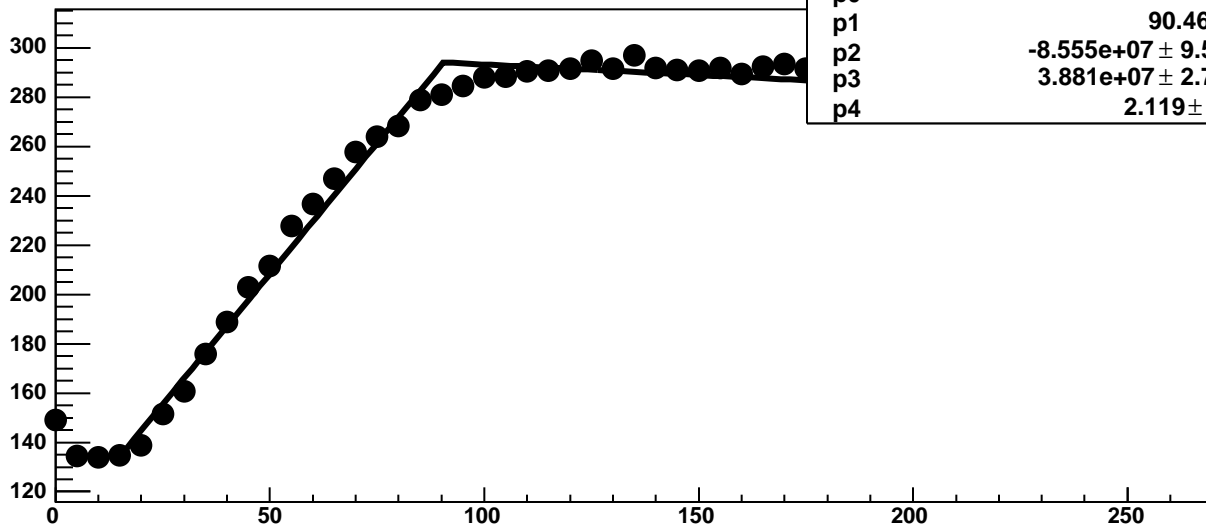
Chip 8, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

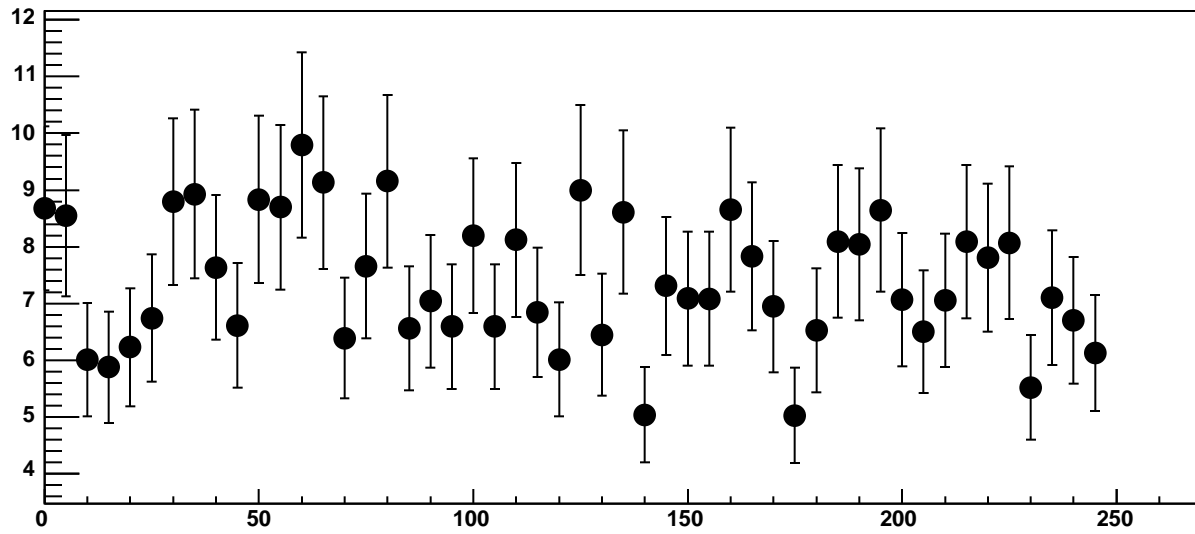


Chip 8, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

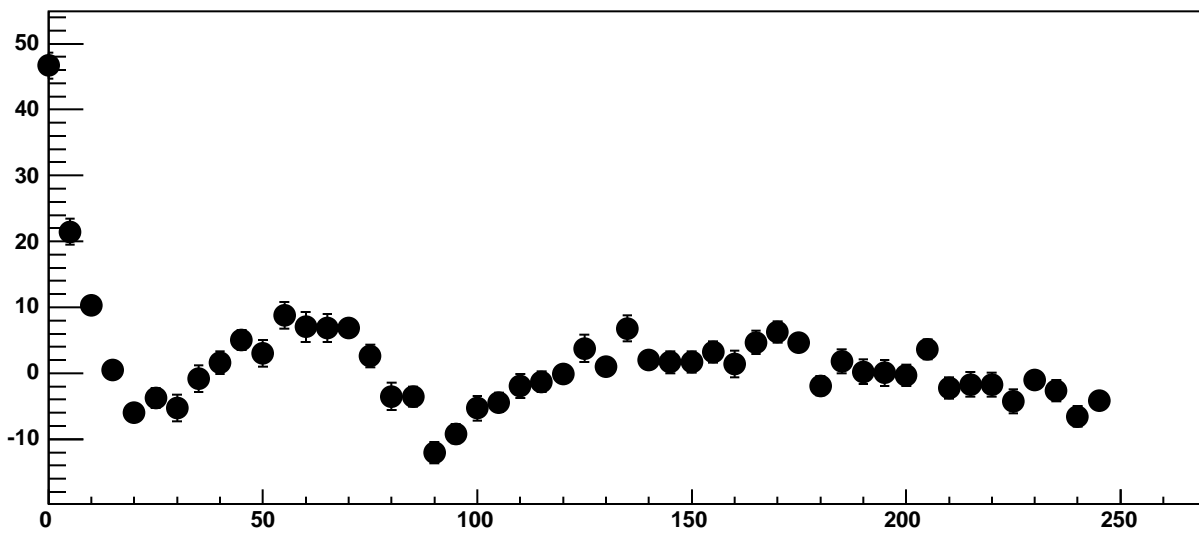


χ^2 / ndf	329.5 / 41
p0	294.1 ± 0.5743
p1	90.46 ± 0.456
p2	$-8.555\text{e}+07 \pm 9.544\text{e}+05$
p3	$3.881\text{e}+07 \pm 2.787\text{e}+05$
p4	2.119 ± 0.01718

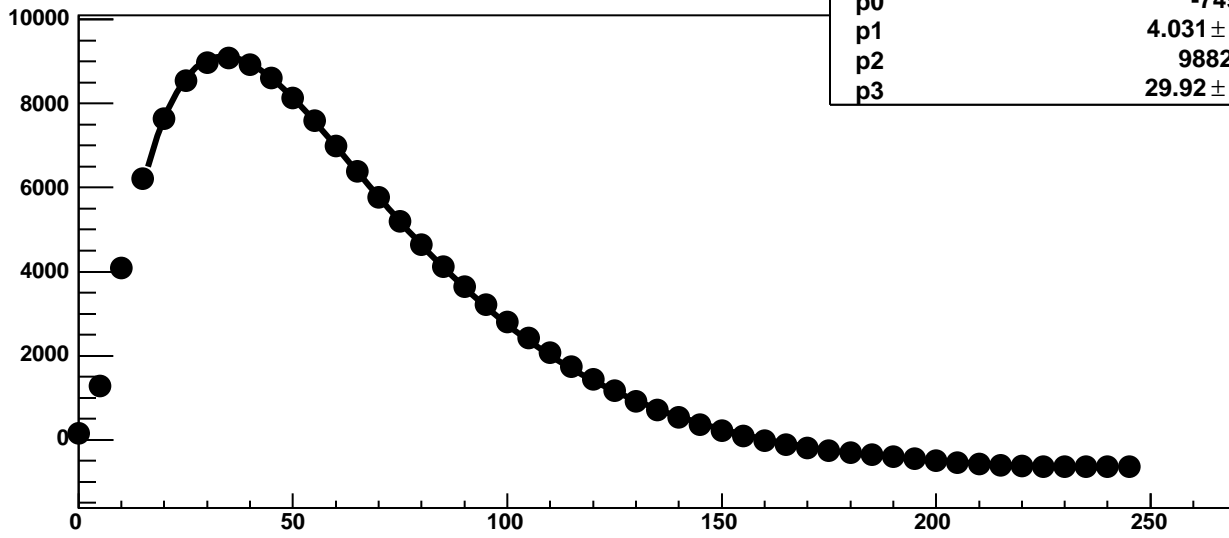
Chip 8, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

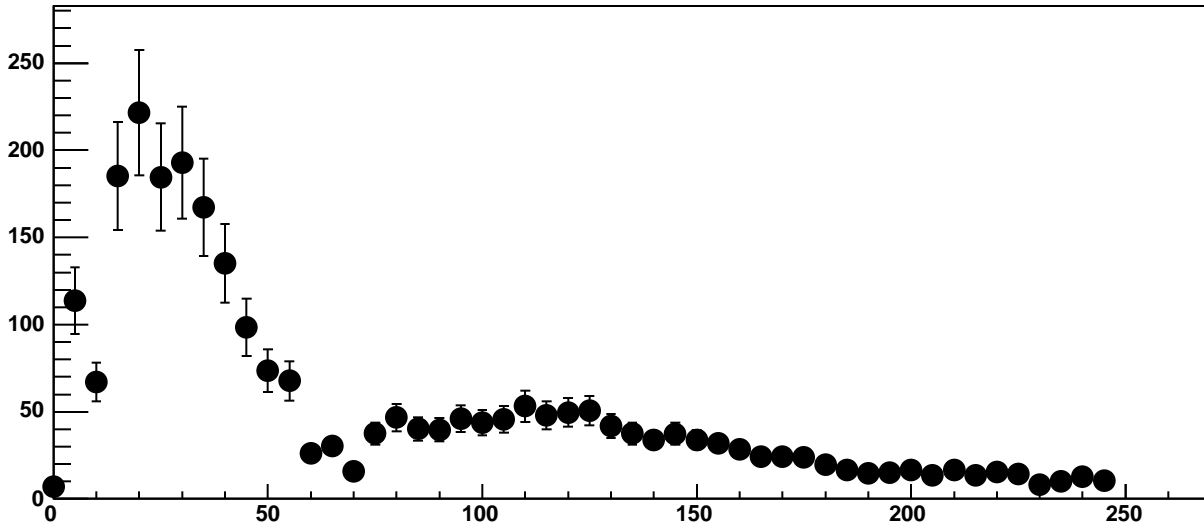


Chip 8, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold

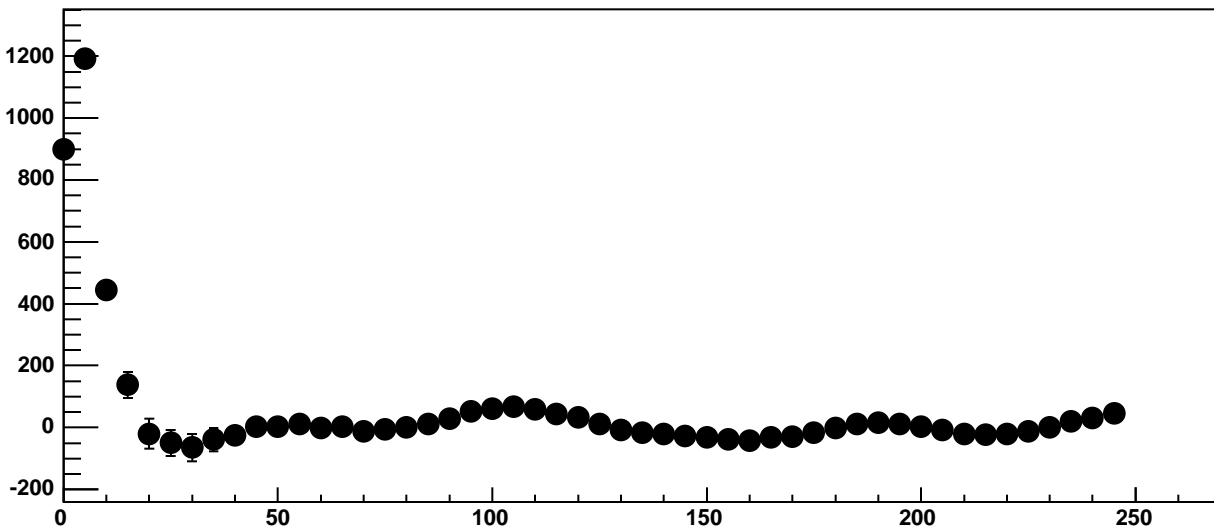


χ^2 / ndf	734.8 / 42
p0	-749 ± 1.22
p1	4.031 ± 0.07488
p2	9882 ± 9.055
p3	29.92 ± 0.02804

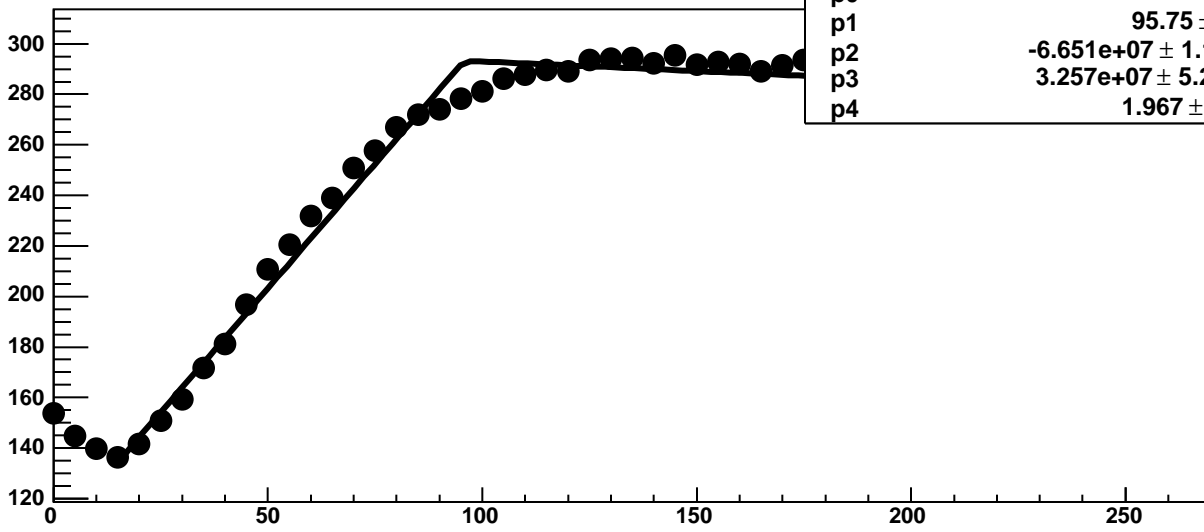
Chip 8, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold

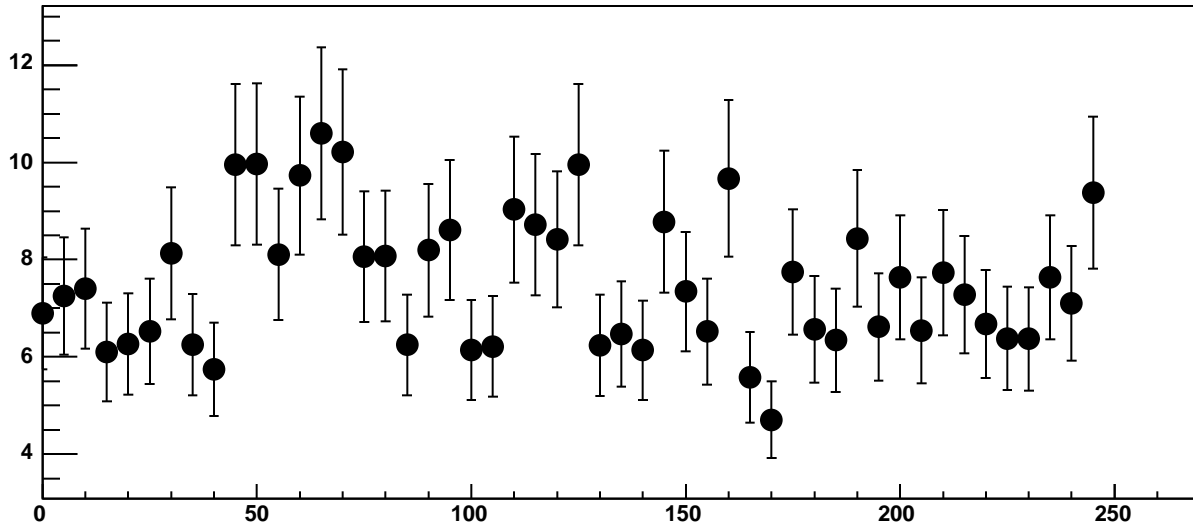


Chip 8, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold

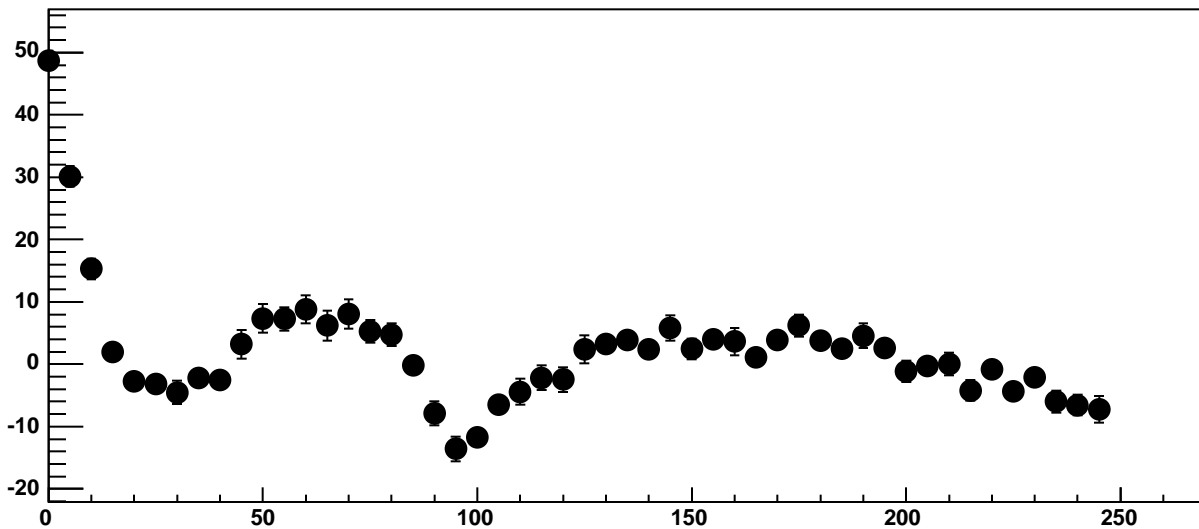


χ^2 / ndf	386.5 / 41
p0	293.3 ± 0.5932
p1	95.75 ± 0.5134
p2	$-6.651\text{e}+07 \pm 1.179\text{e}+06$
p3	$3.257\text{e}+07 \pm 5.295\text{e}+05$
p4	1.967 ± 0.01621

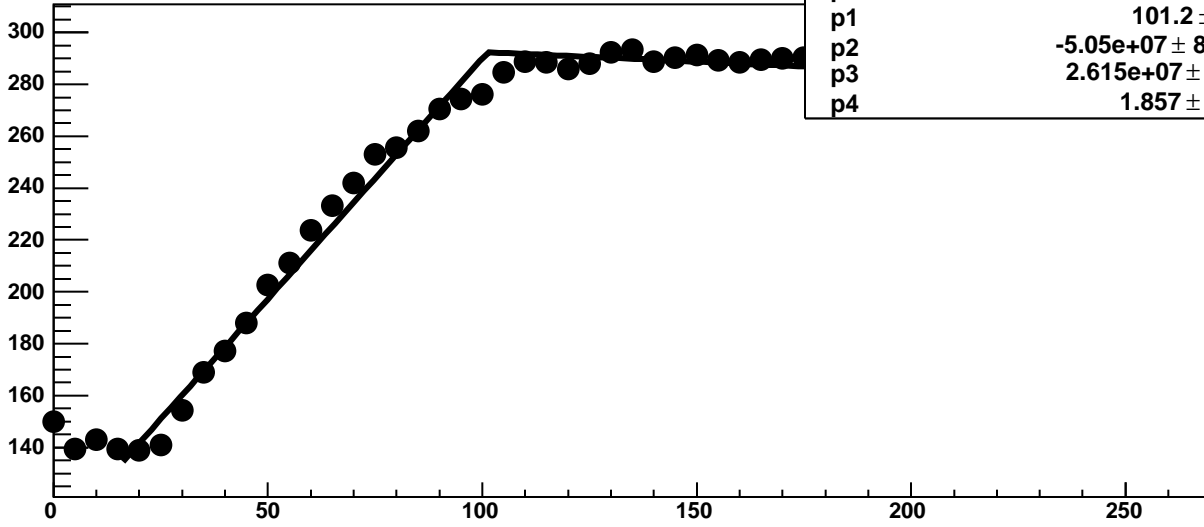
Chip 8, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold

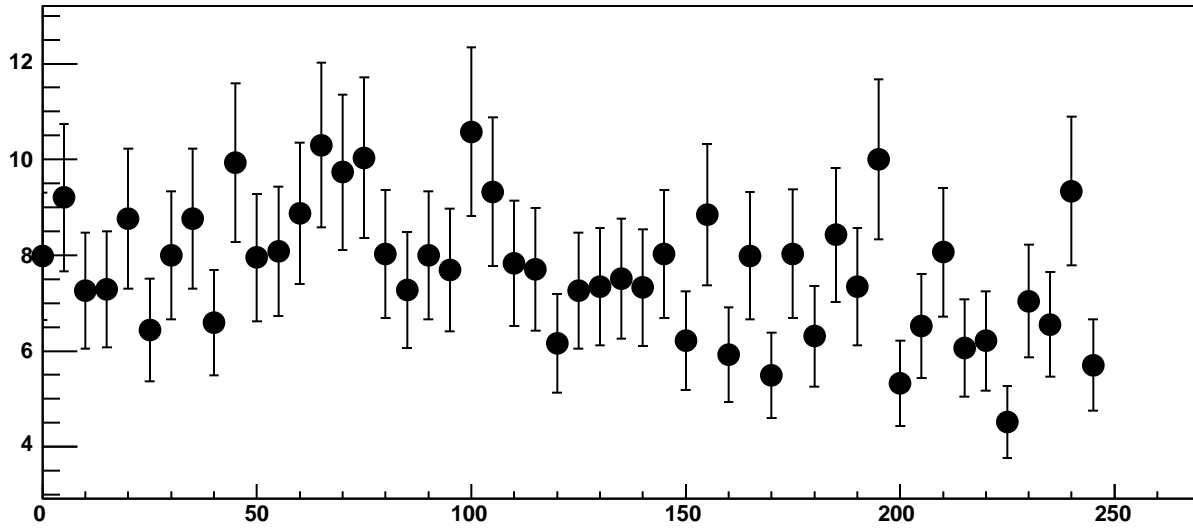


Chip 8, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold

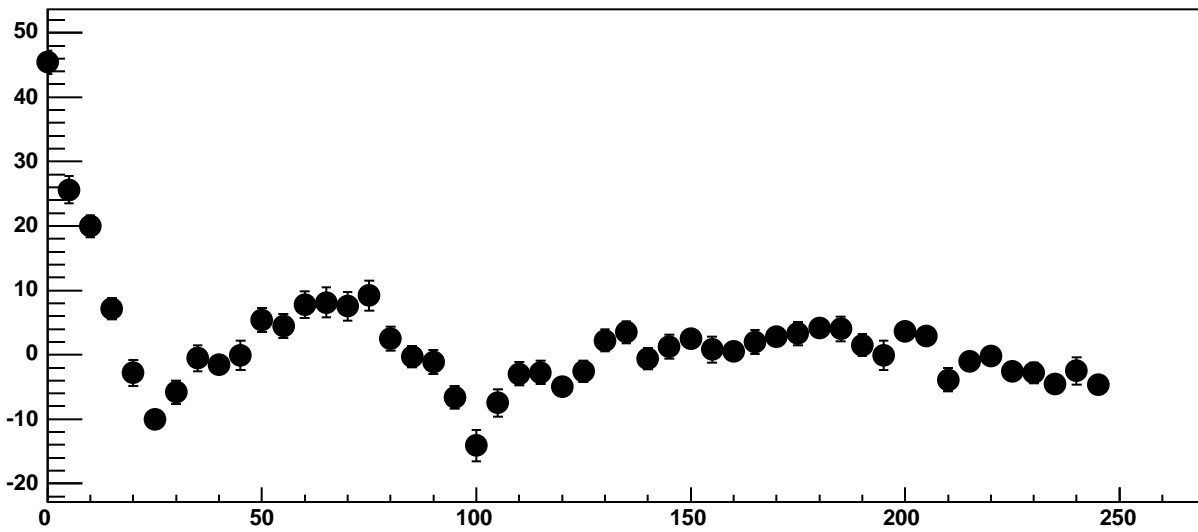


χ^2 / ndf	296.6 / 41
p0	292.4 ± 0.6209
p1	101.2 ± 0.5696
p2	$-5.05\text{e}+07 \pm 8.67\text{e}+05$
p3	$2.615\text{e}+07 \pm 3.7\text{e}+05$
p4	1.857 ± 0.01673

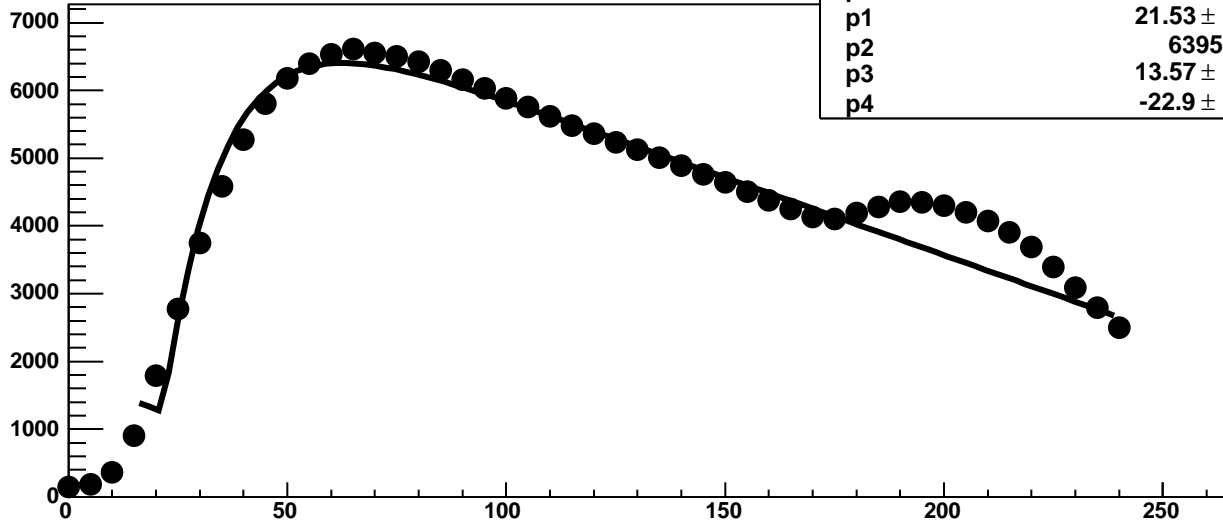
Chip 8, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

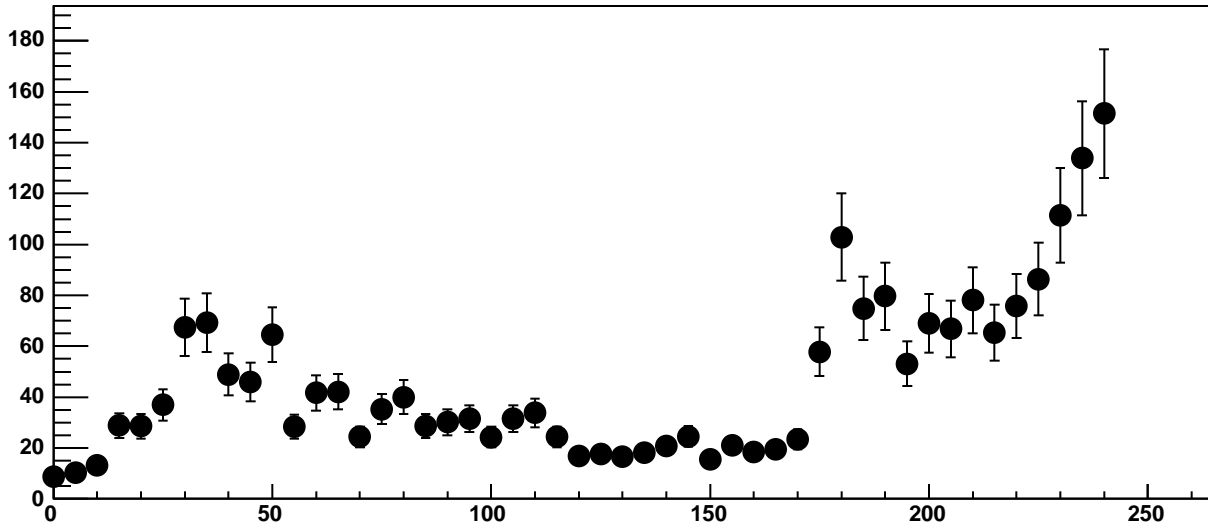


Chip 8, Channel 17, Enable 5, DAC=4000, ADC Mean vs Hold

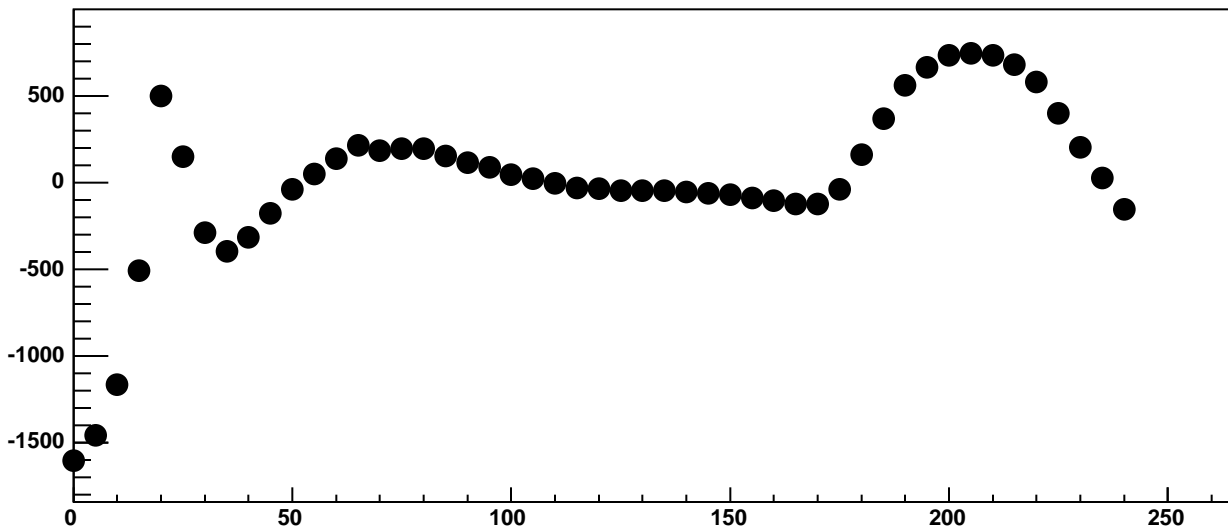


χ^2 / ndf	3.57e+04 / 41
p0	1261 ± 4.475
p1	21.53 ± 0.02889
p2	6395 ± 6.606
p3	13.57 ± 0.04539
p4	-22.9 ± 0.03813

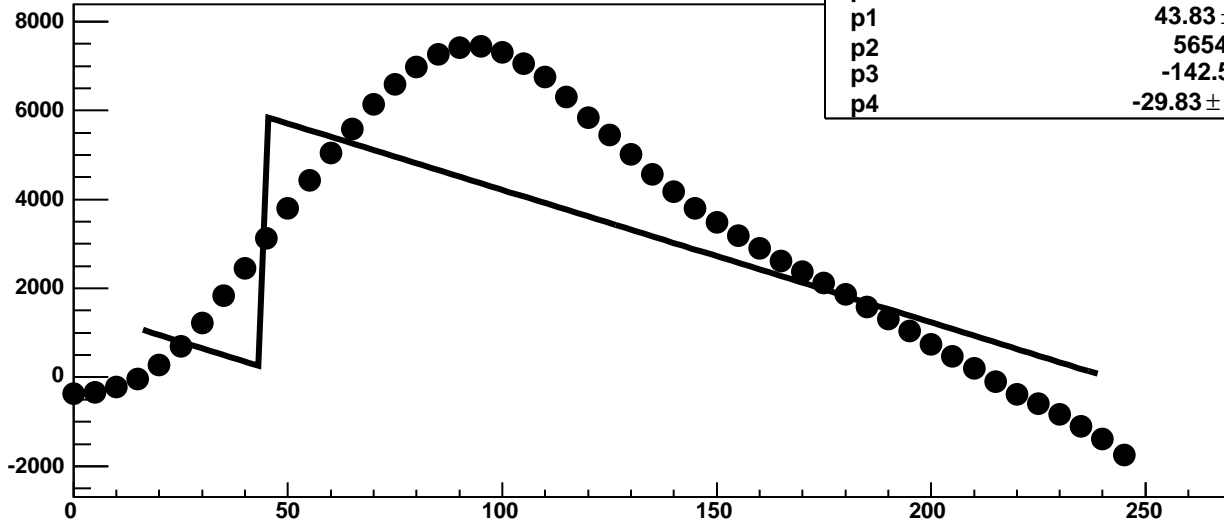
Chip 8, Channel 17, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 8, Channel 17, Enable 5, DAC=4000, ADC Residuals vs Hold

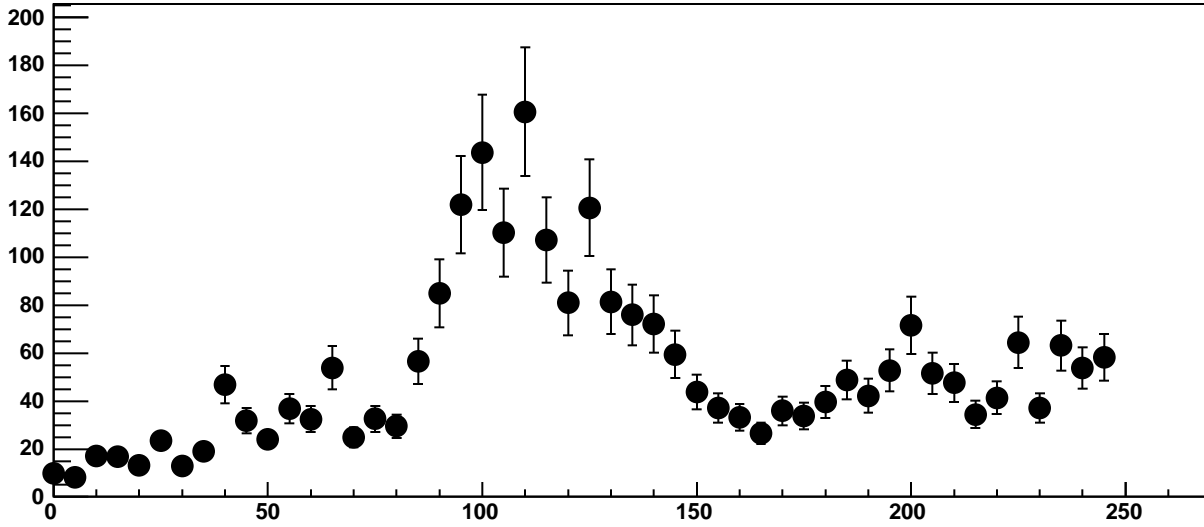


Chip 9, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold

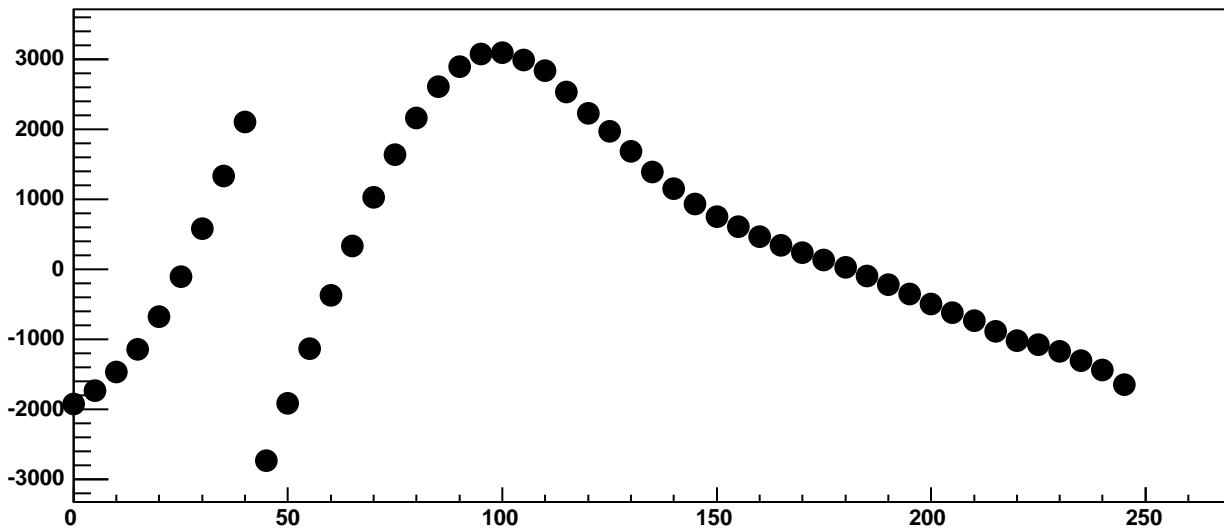


χ^2 / ndf	1.03e+06 / 41
p0	239.8 ± 11.16
p1	43.83 ± 0.1737
p2	5654 ± 7.449
p3	-142.5 ± 1.851
p4	-29.83 ± 0.09664

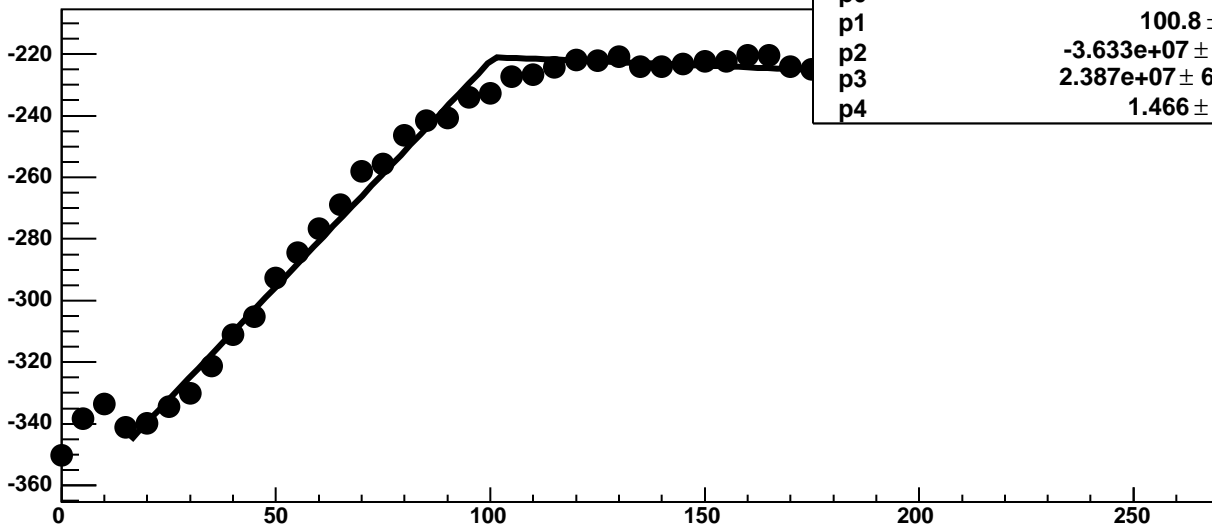
Chip 9, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold

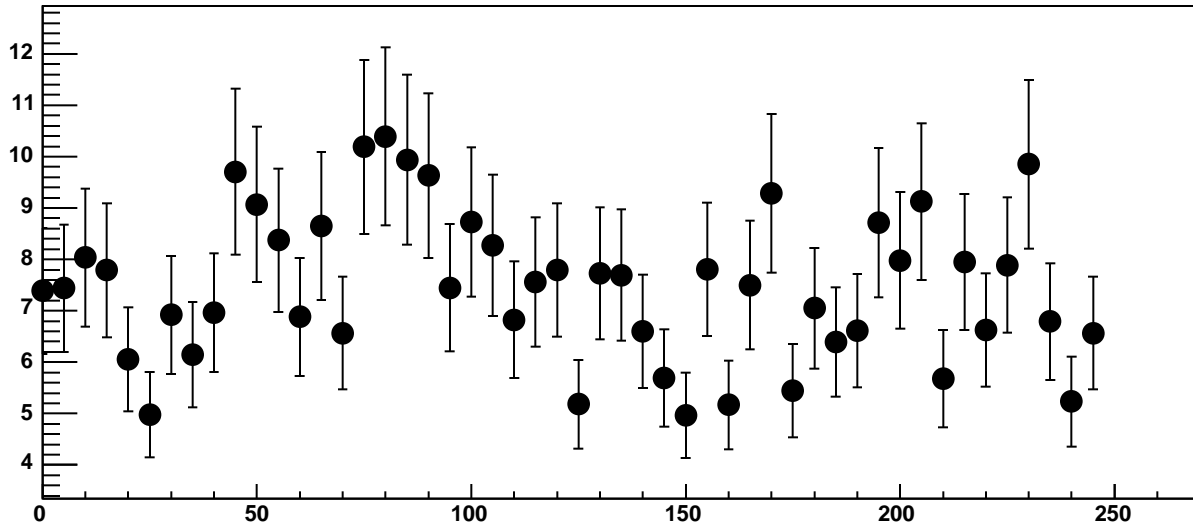


Chip 9, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

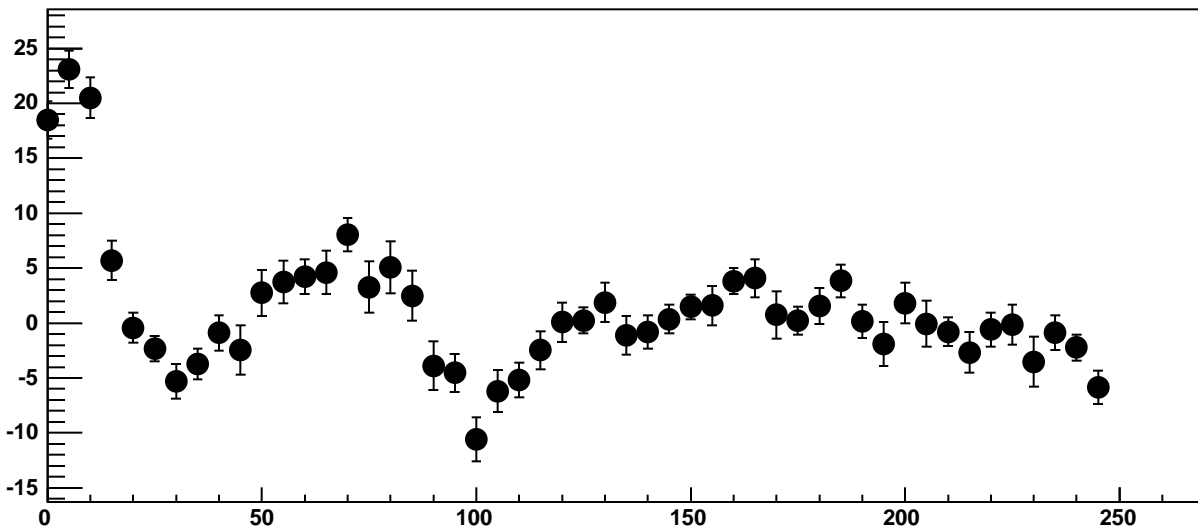


χ^2 / ndf	188.9 / 41
p0	-221 ± 0.5782
p1	100.8 ± 0.7014
p2	-3.633e+07 ± 1.1e+06
p3	2.387e+07 ± 6.77e+05
p4	1.466 ± 0.01567

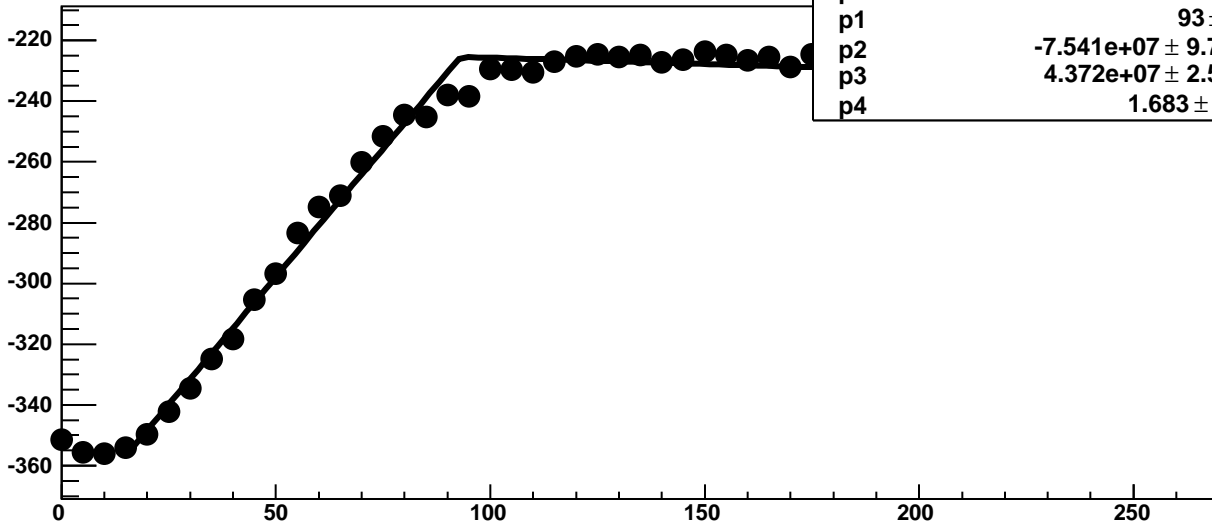
Chip 9, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold

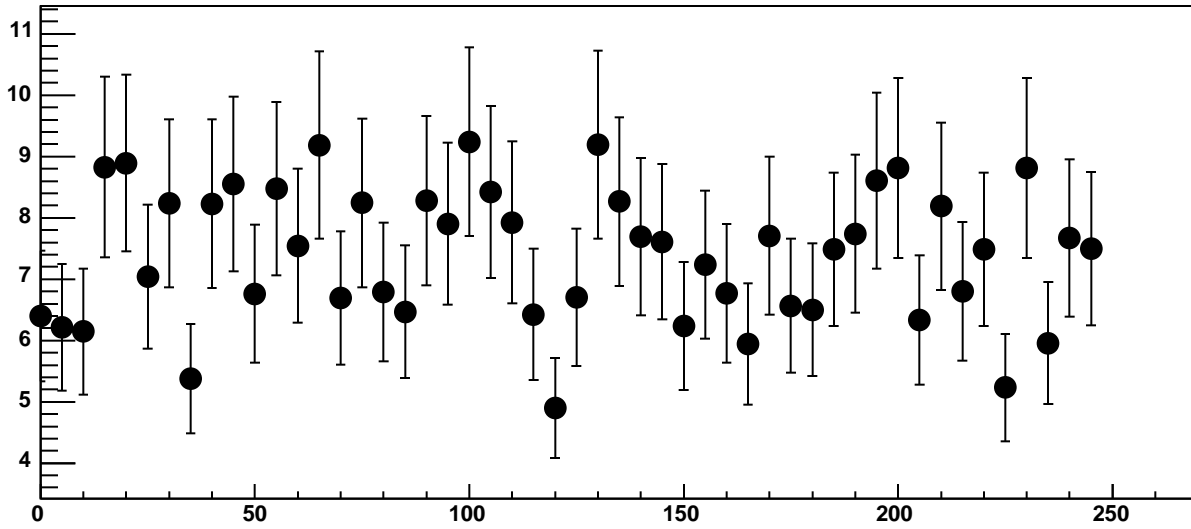


Chip 9, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

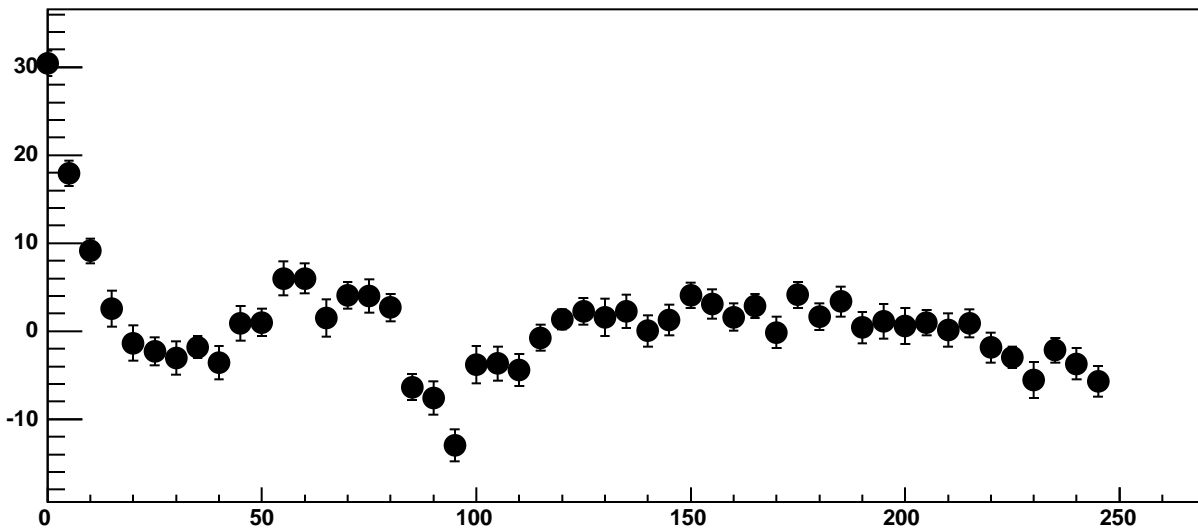


χ^2 / ndf	206.9 / 41
p0	-225.4 ± 0.5908
p1	93 ± 0.6107
p2	$-7.541\text{e}+07 \pm 9.786\text{e}+05$
p3	$4.372\text{e}+07 \pm 2.552\text{e}+05$
p4	1.683 ± 0.01882

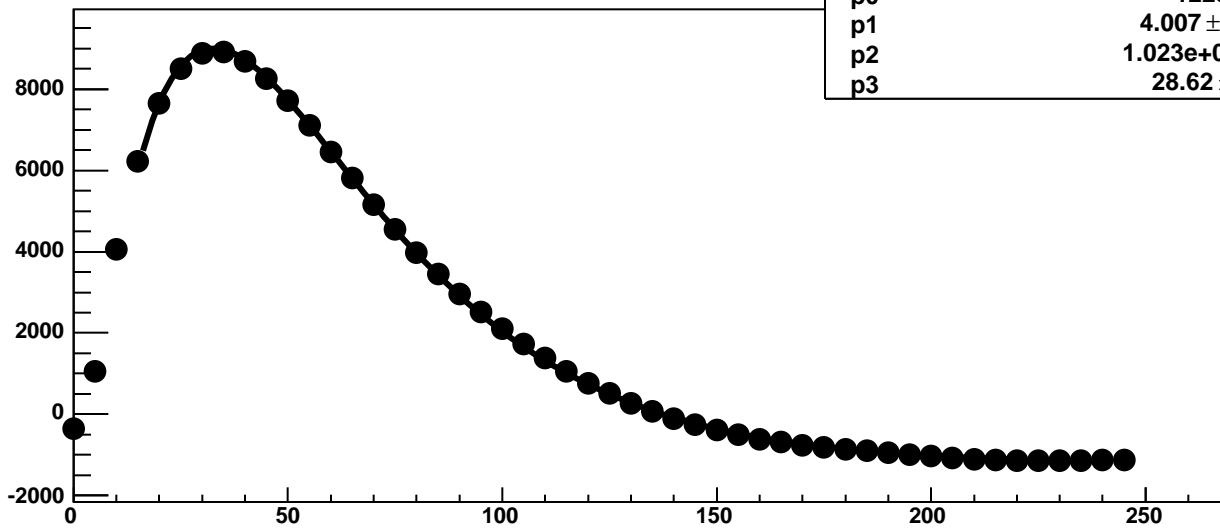
Chip 9, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



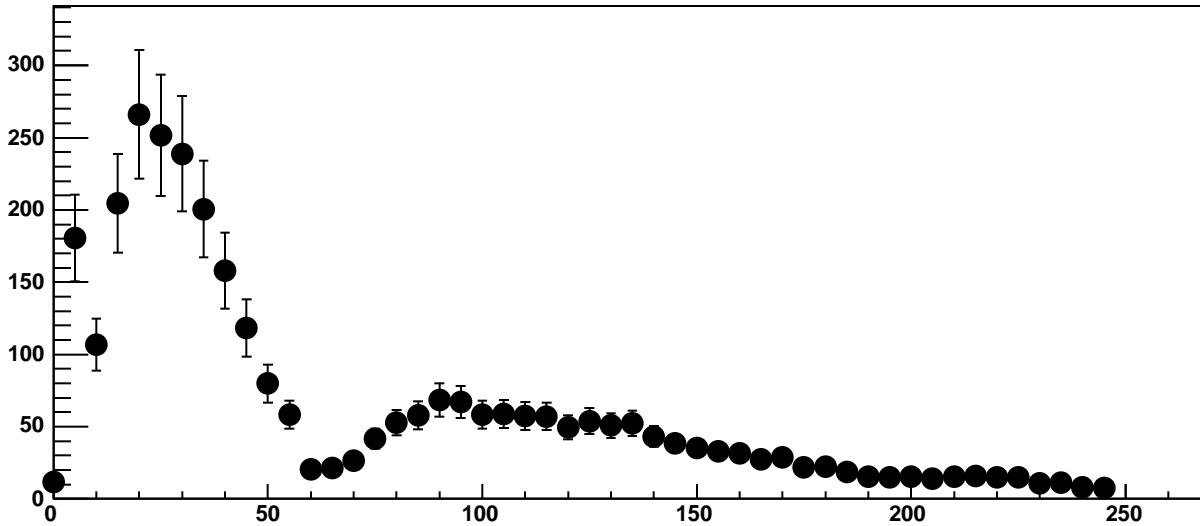
Chip 9, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold



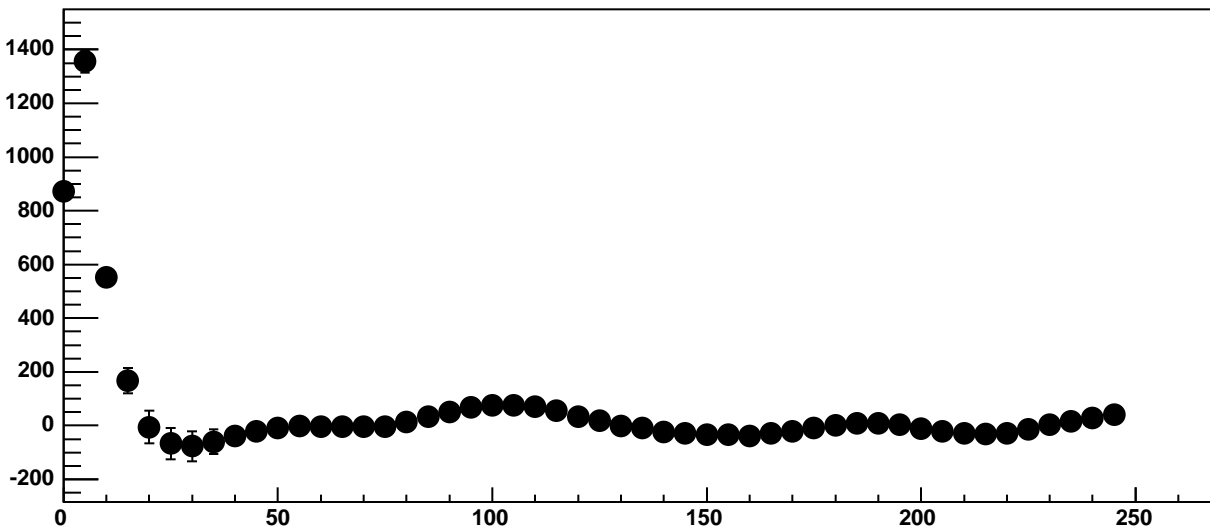
Chip 9, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold



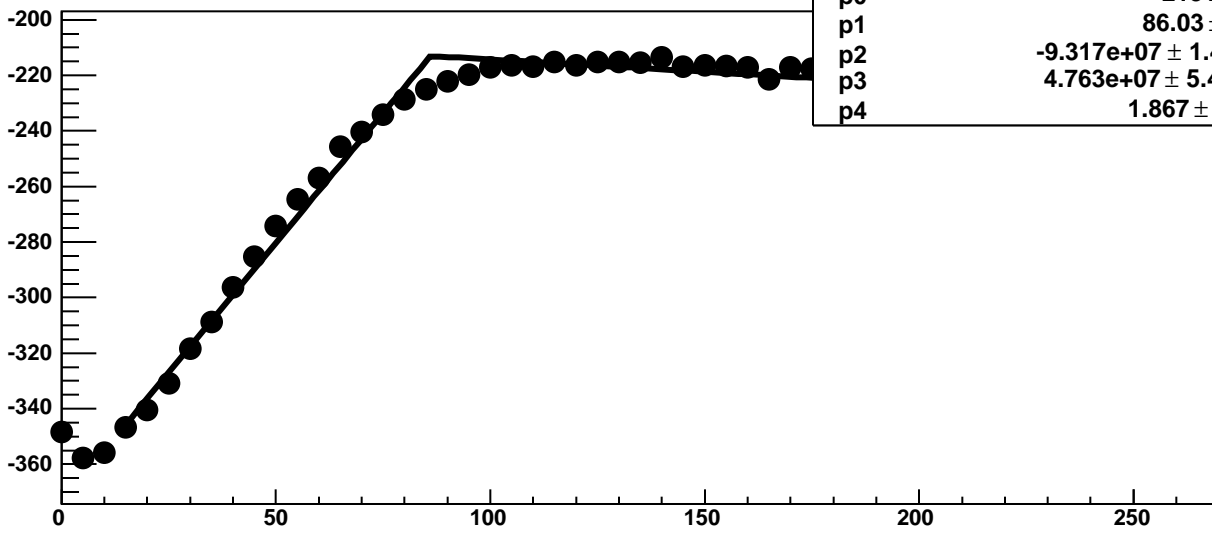
Chip 9, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold

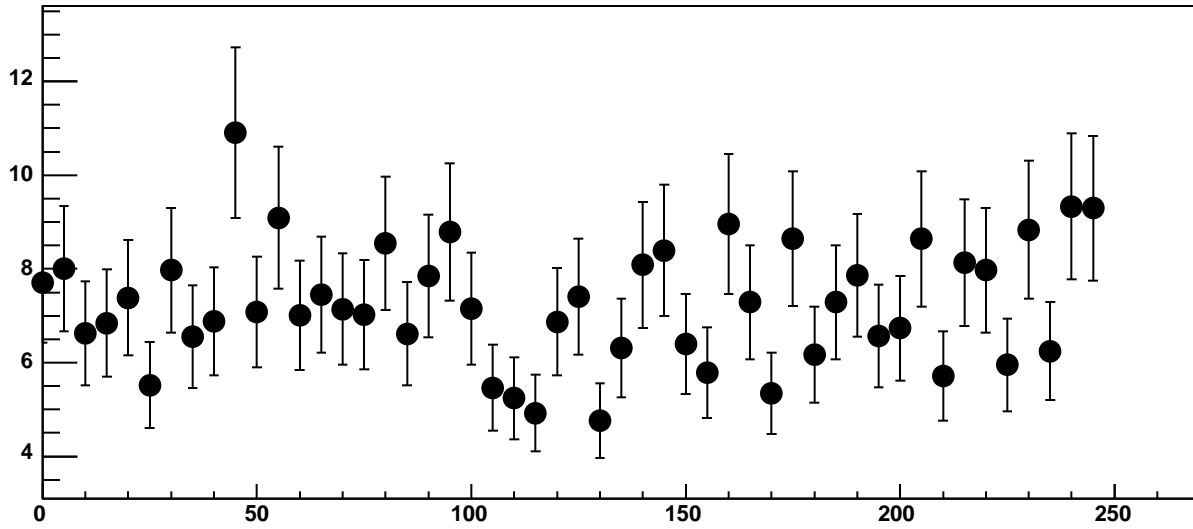


Chip 9, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

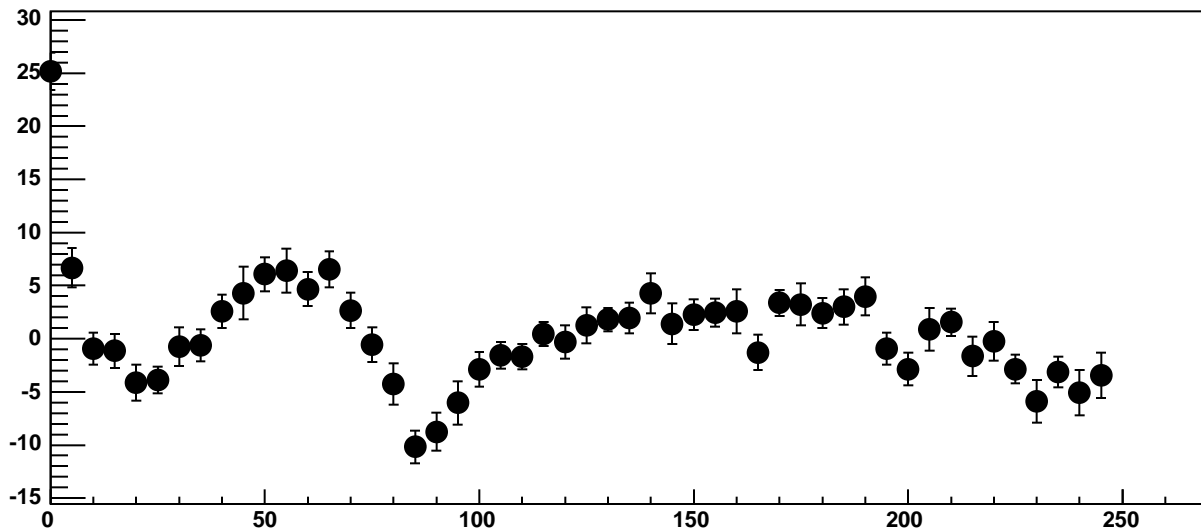


χ^2 / ndf	230 / 41
p0	-213 ± 0.5213
p1	86.03 ± 0.5079
p2	$-9.317\text{e}+07 \pm 1.435\text{e}+06$
p3	$4.763\text{e}+07 \pm 5.401\text{e}+05$
p4	1.867 ± 0.01912

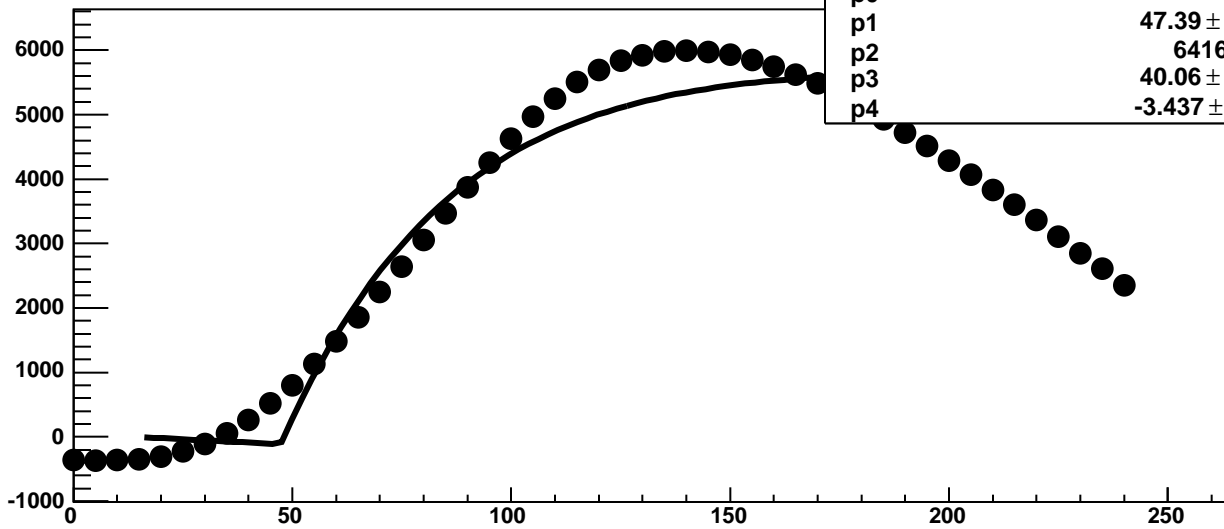
Chip 9, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

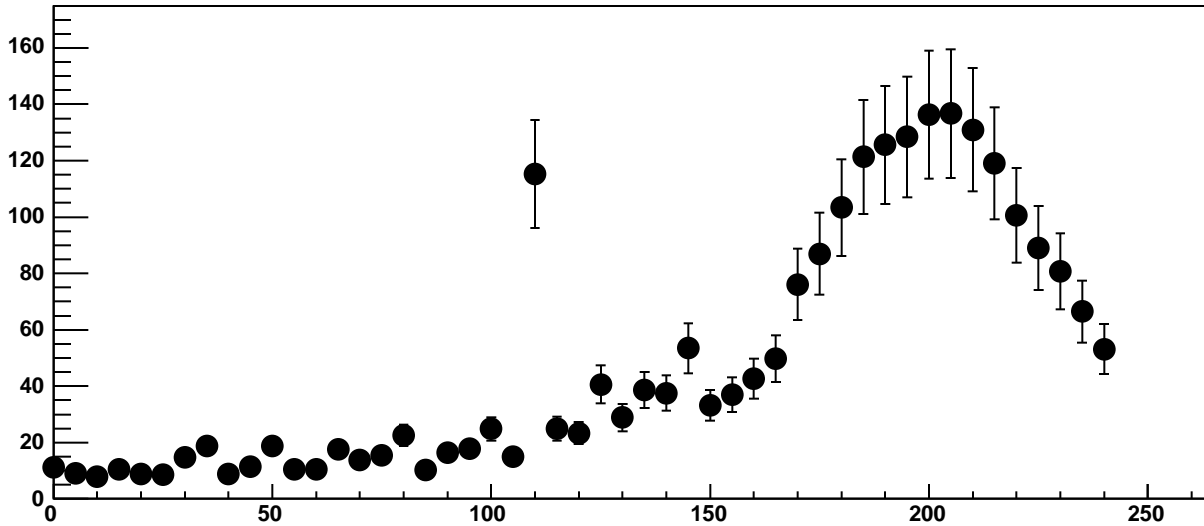


Chip 9, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold

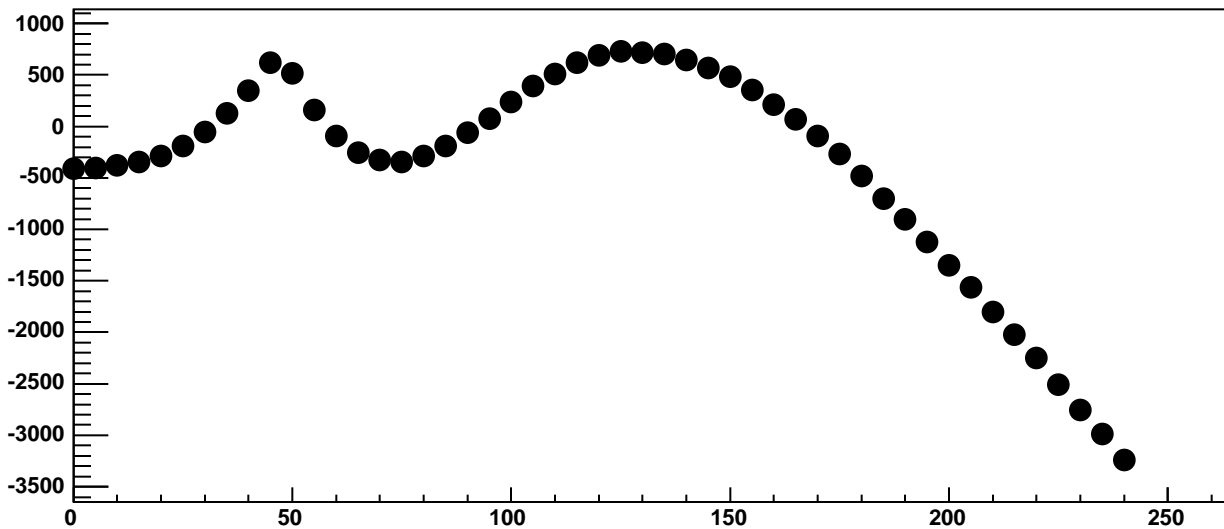


χ^2 / ndf	4.465e+05 / 41
p0	-114.8 ± 1.351
p1	47.39 ± 0.01828
p2	6416 ± 9.334
p3	40.06 ± 0.06685
p4	-3.437 ± 0.05771

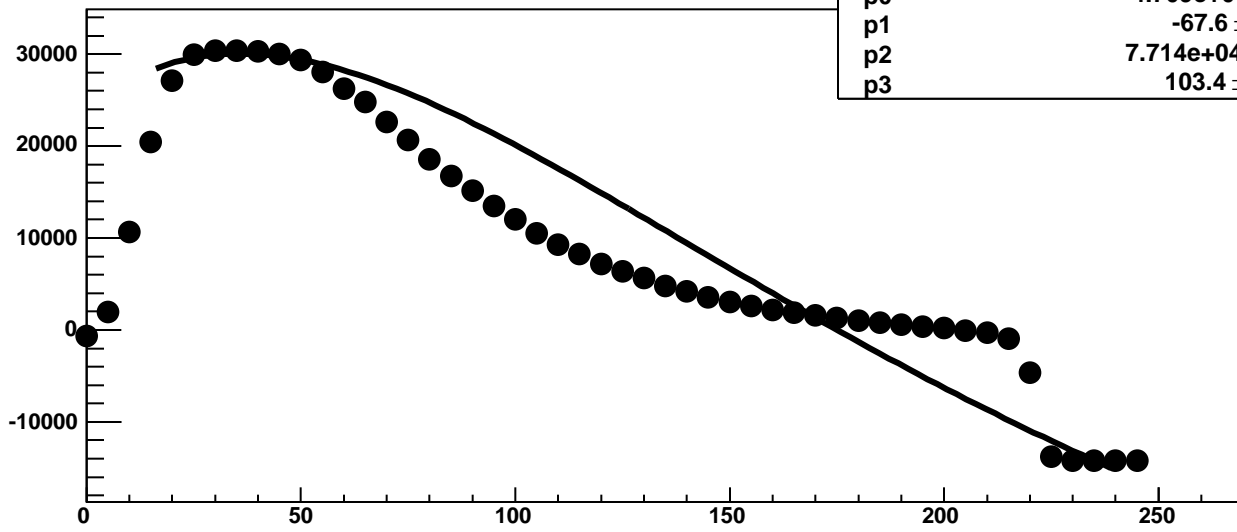
Chip 9, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold

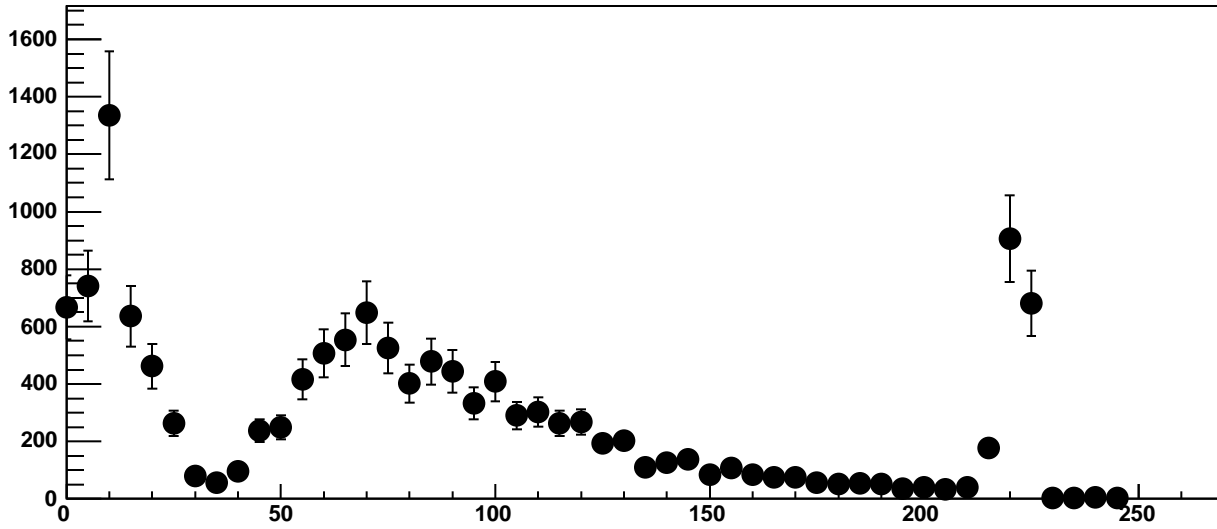


Chip 9, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold

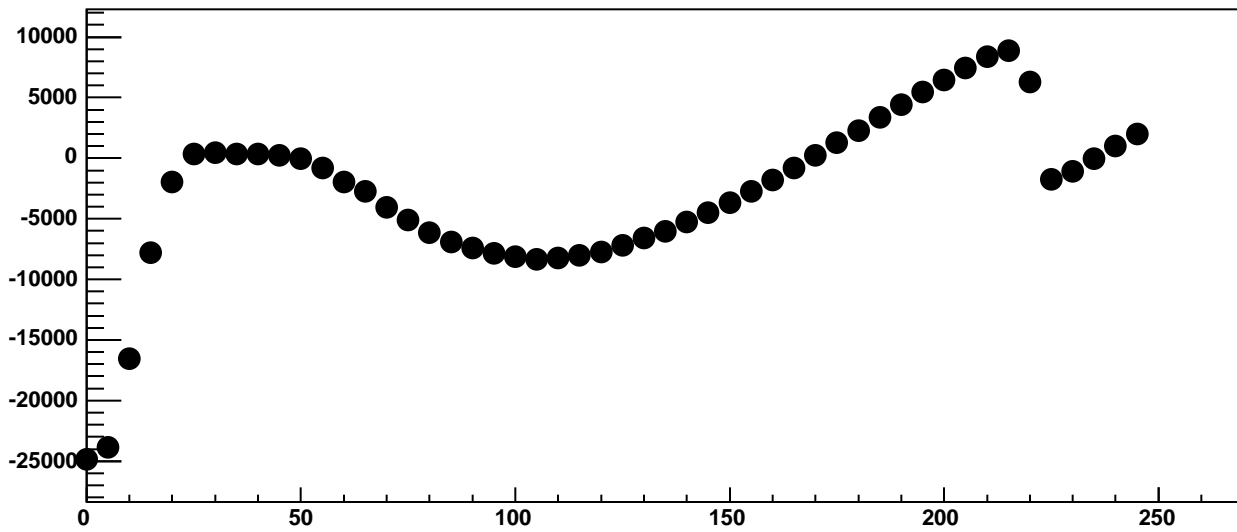


χ^2 / ndf	5.416e+06 / 42
p0	-4.709e+04 ± 60.9
p1	-67.6 ± 0.2167
p2	7.714e+04 ± 60.51
p3	103.4 ± 0.1296

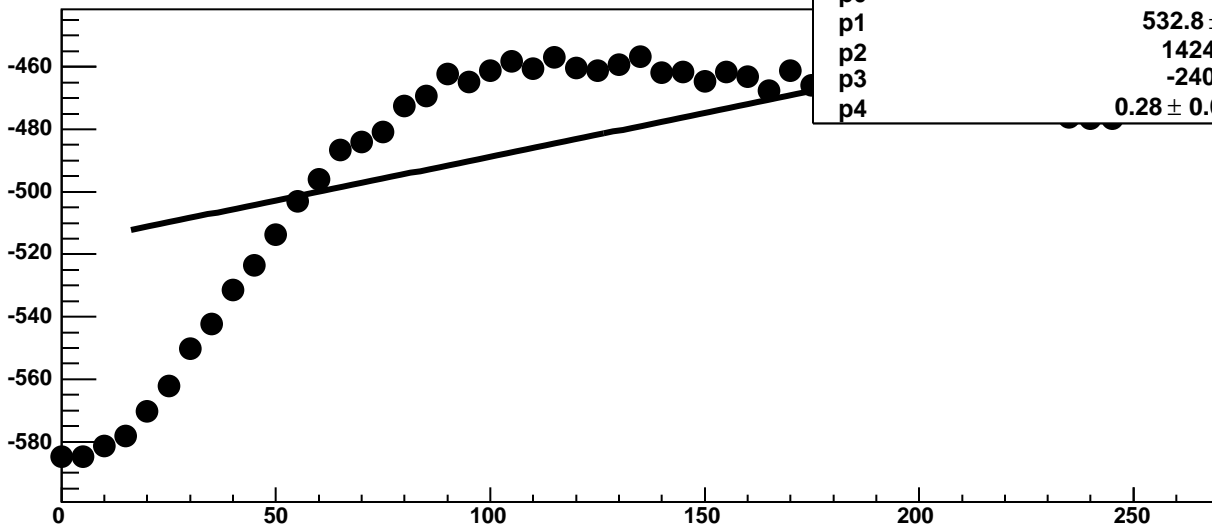
Chip 9, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold

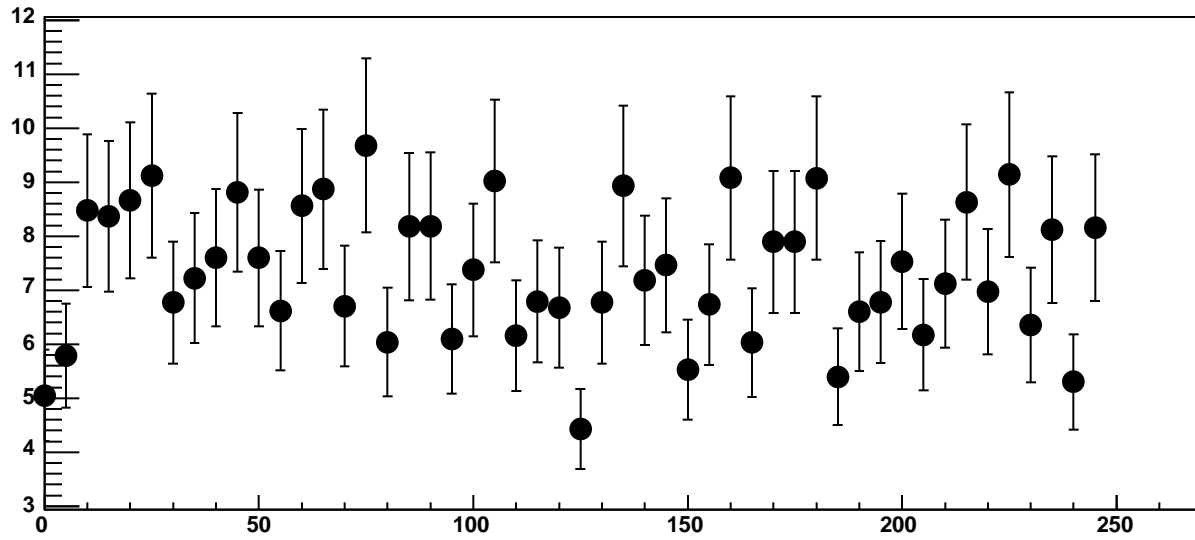


Chip 9, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

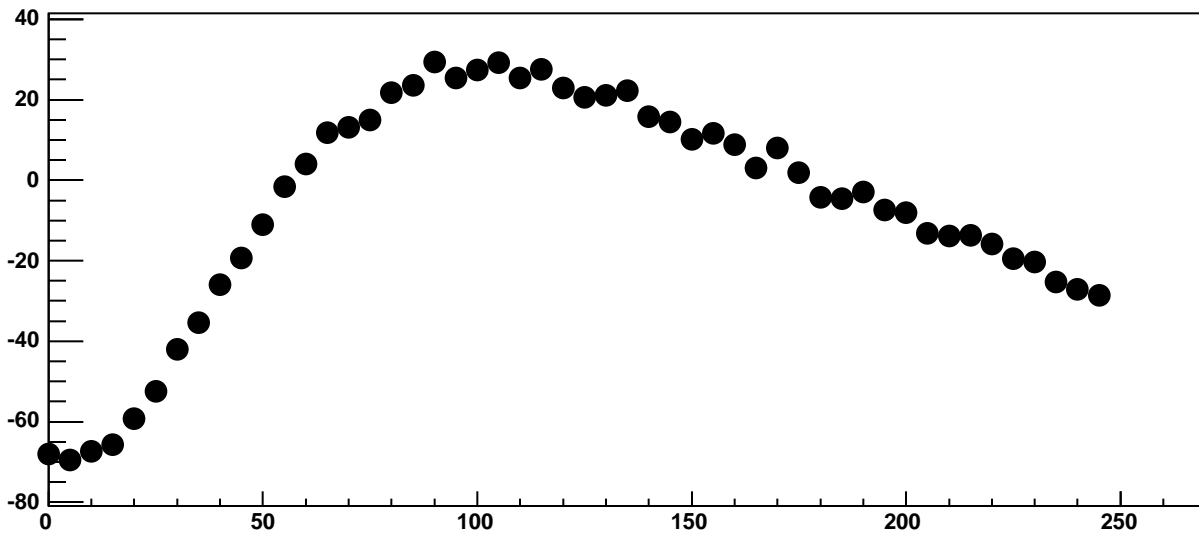


χ^2 / ndf	9042 / 41
p0	-367.6 ± 0.2391
p1	532.8 ± 0.8538
p2	1424 ± 84.85
p3	-240 ± 1.414
p4	0.28 ± 0.0005909

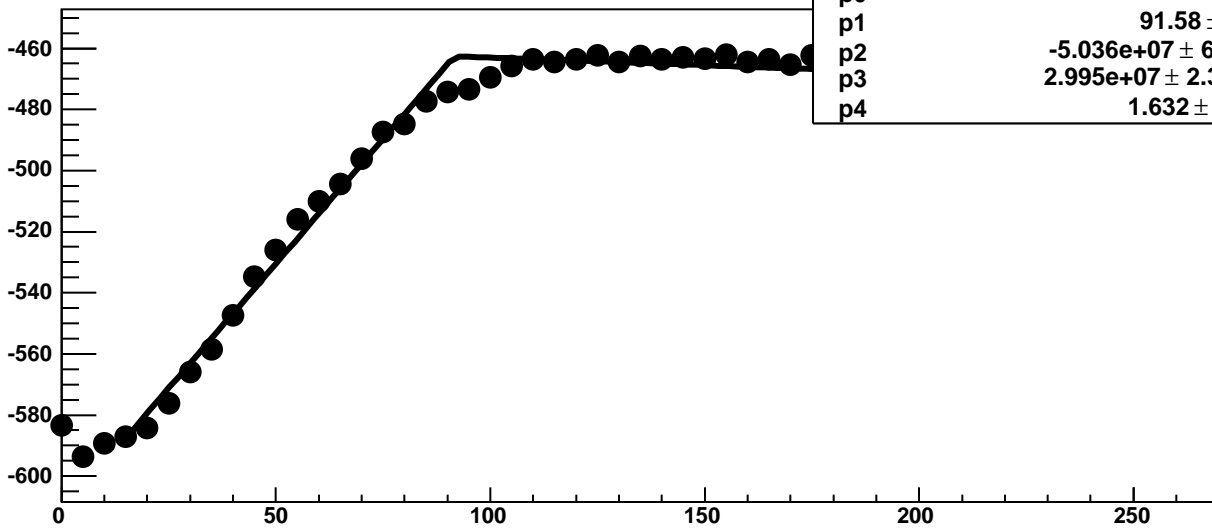
Chip 9, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

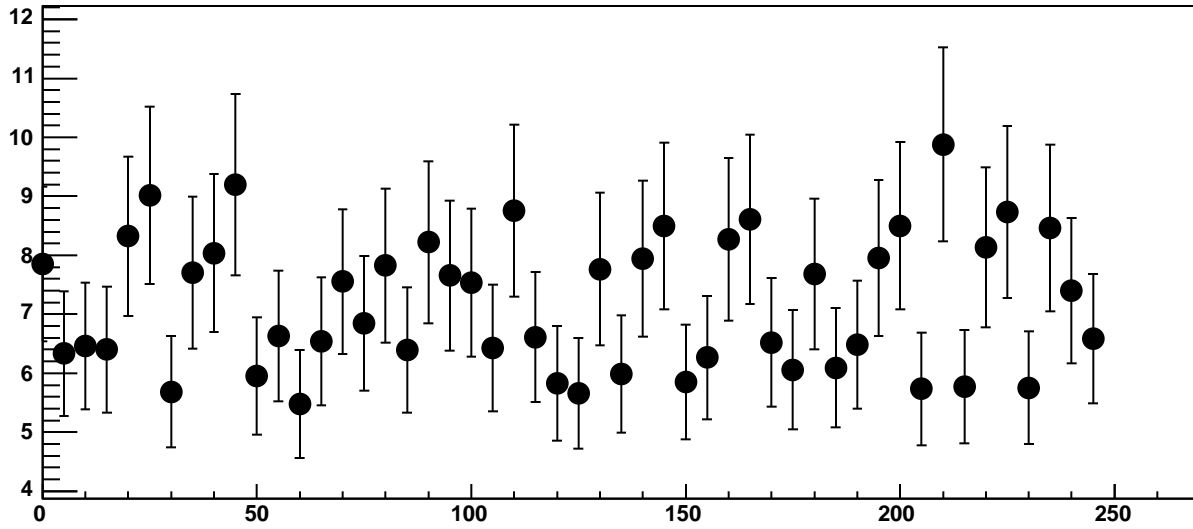


Chip 9, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

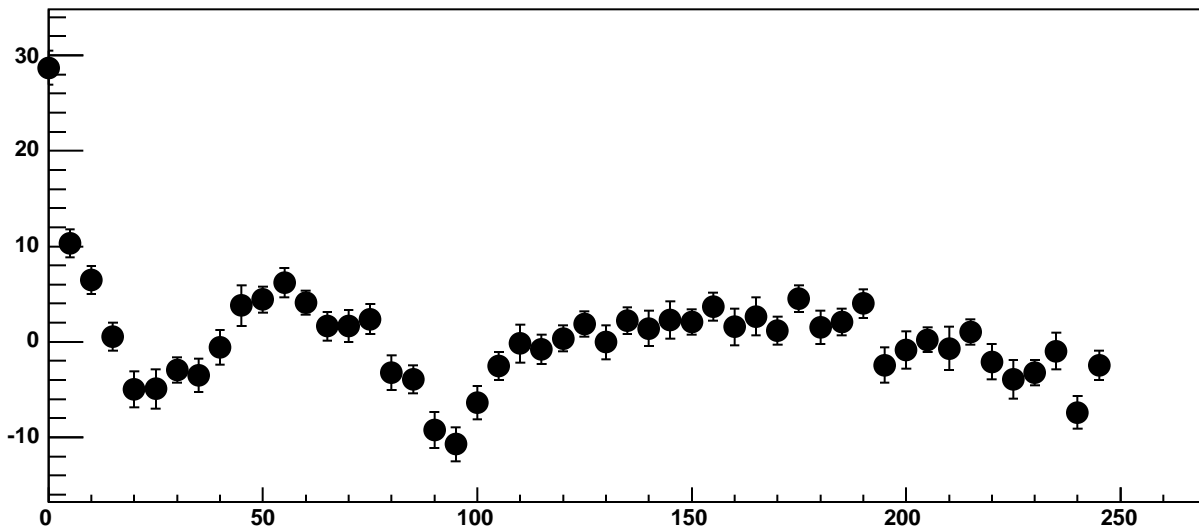


χ^2 / ndf	228.6 / 41
p0	-462.6 ± 0.5707
p1	91.58 ± 0.5804
p2	$-5.036\text{e}+07 \pm 6.89\text{e}+05$
p3	$2.995\text{e}+07 \pm 2.382\text{e}+05$
p4	1.632 ± 0.01788

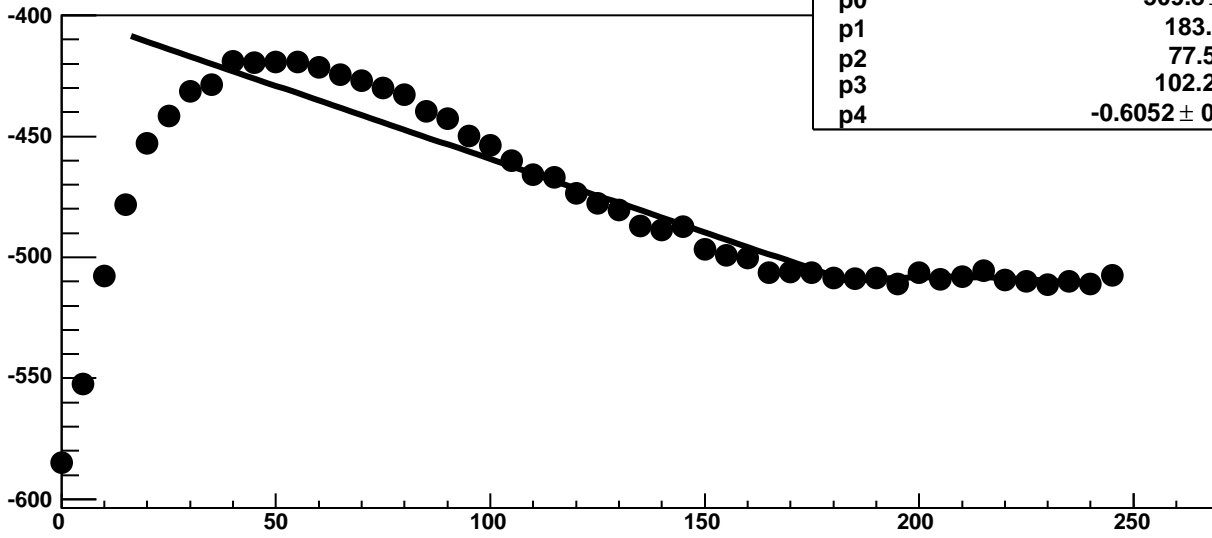
Chip 9, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

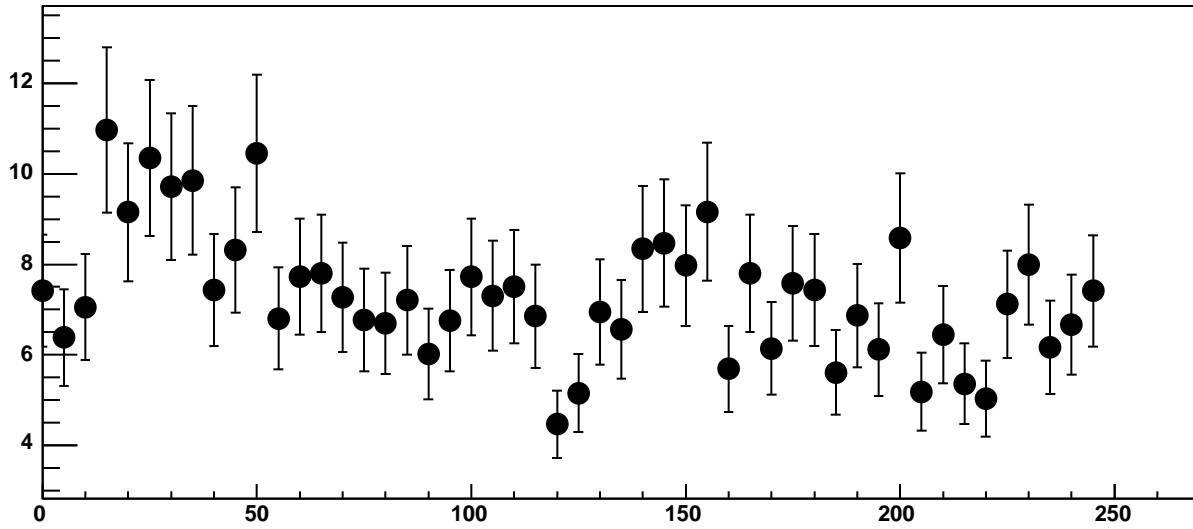


Chip 9, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold

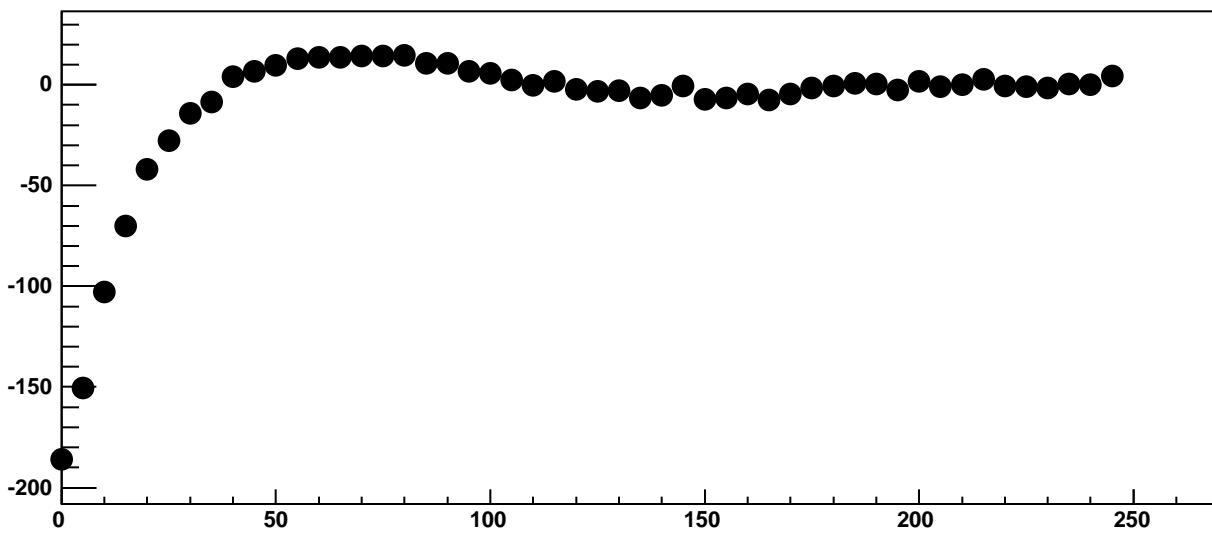


χ^2 / ndf	2084 / 41
p0	-509.8 ± 0.8747
p1	183.3 ± 1.61
p2	77.5 ± 26.24
p3	102.2 ± 44.93
p4	-0.6052 ± 0.006403

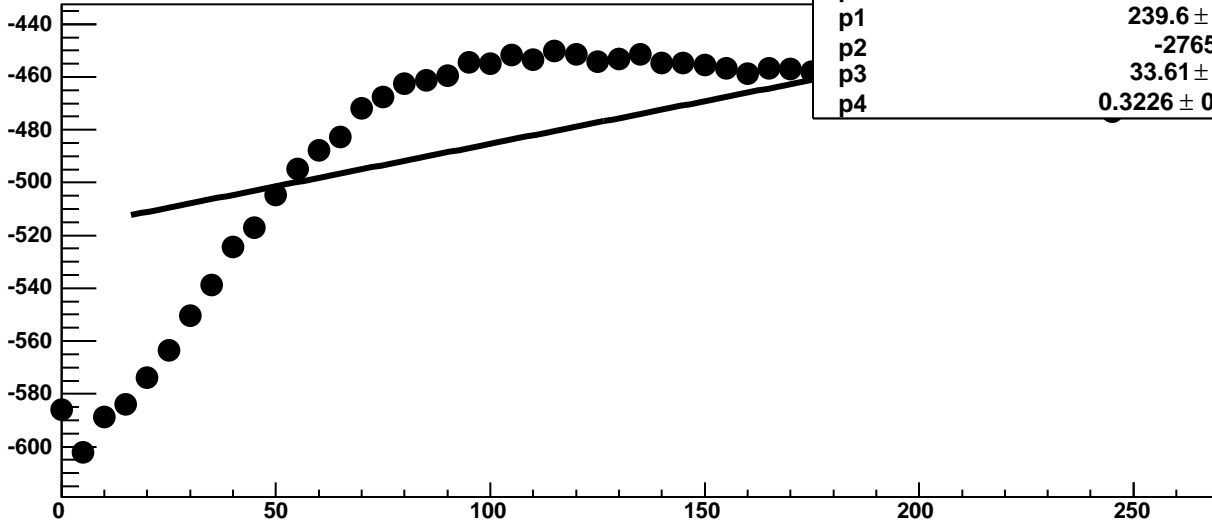
Chip 9, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold

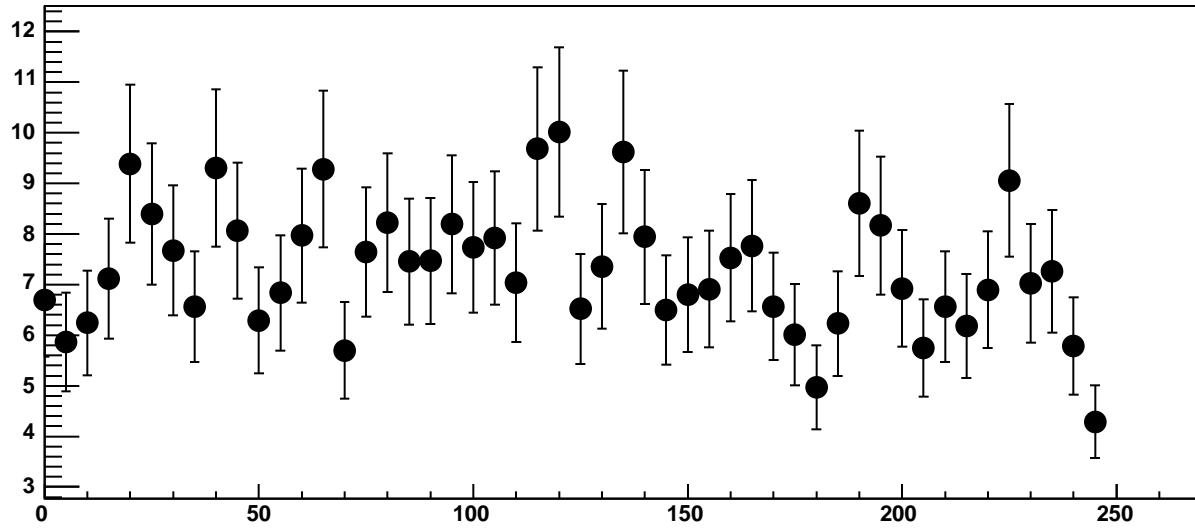


Chip 9, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

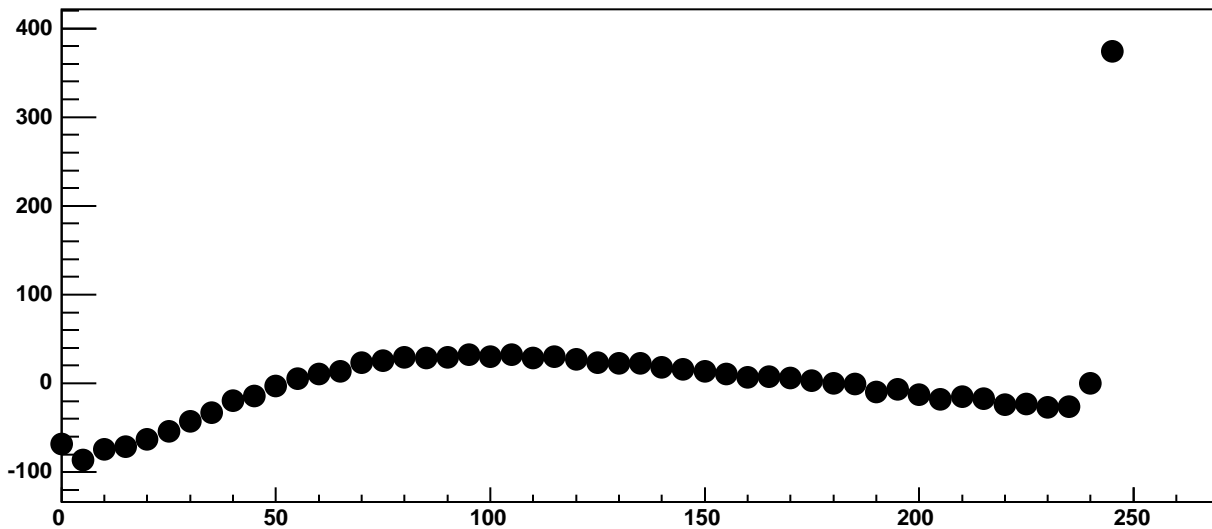


χ^2 / ndf	1.004e+04 / 41
p0	-440.3 ± 0.4789
p1	239.6 ± 0.01755
p2	-2765 ± 9.101
p3	33.61 ± 0.09407
p4	0.3226 ± 0.003806

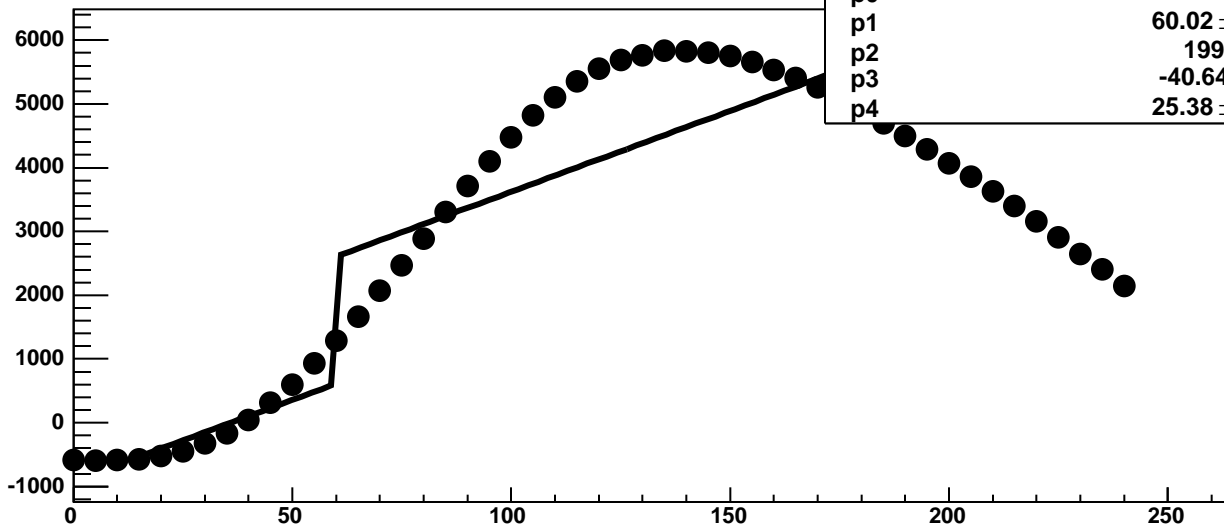
Chip 9, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



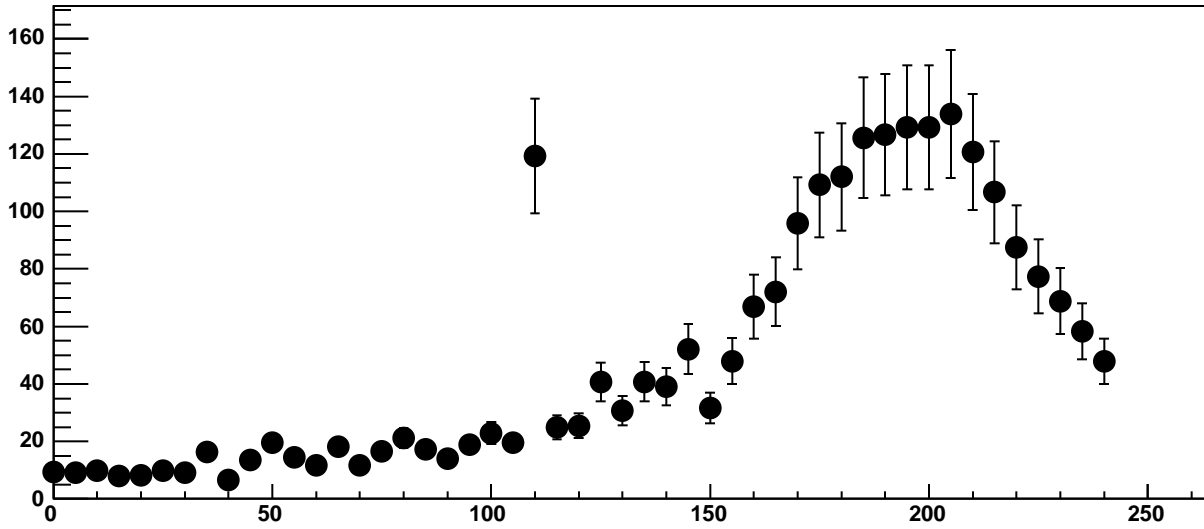
Chip 9, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold



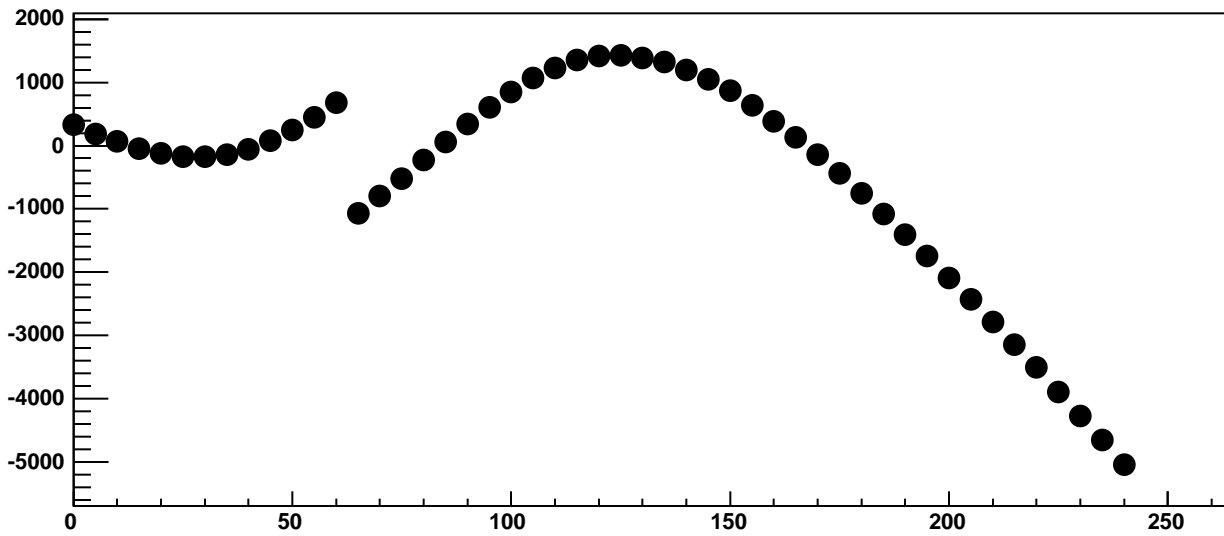
Chip 9, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold



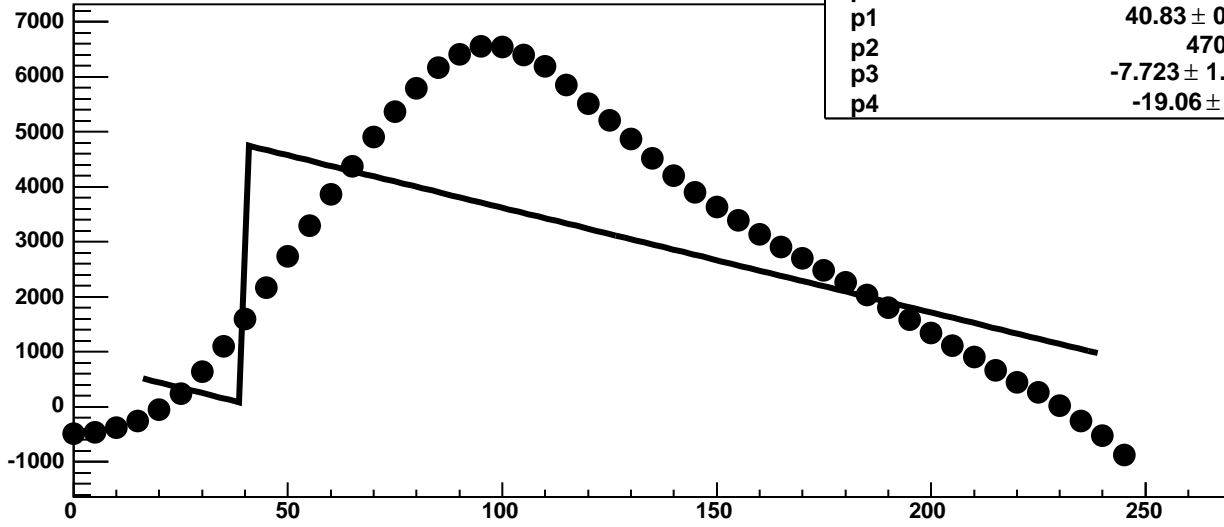
Chip 9, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold

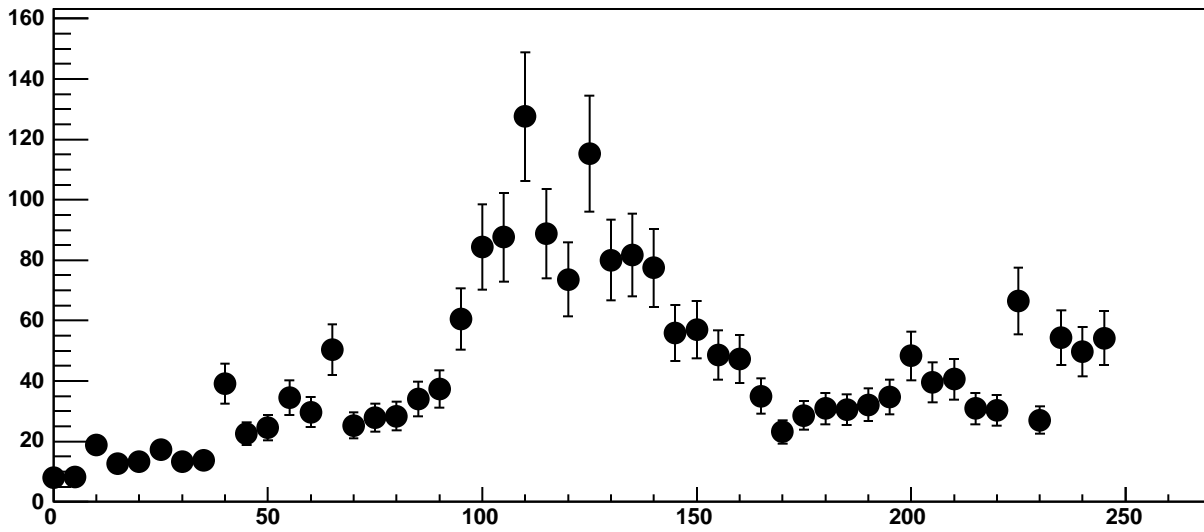


Chip 9, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold

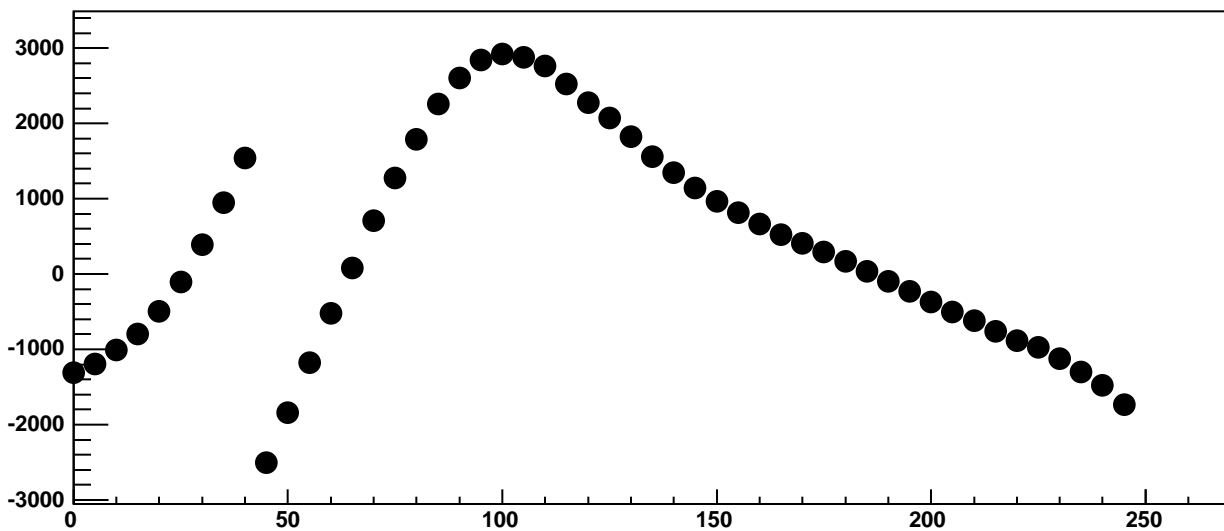


χ^2 / ndf	1.206e+06 / 41
p0	45.41 ± 1.665
p1	40.83 ± 0.001225
p2	4704 ± 3.11
p3	-7.723 ± 1.312e-06
p4	-19.06 ± 0.02092

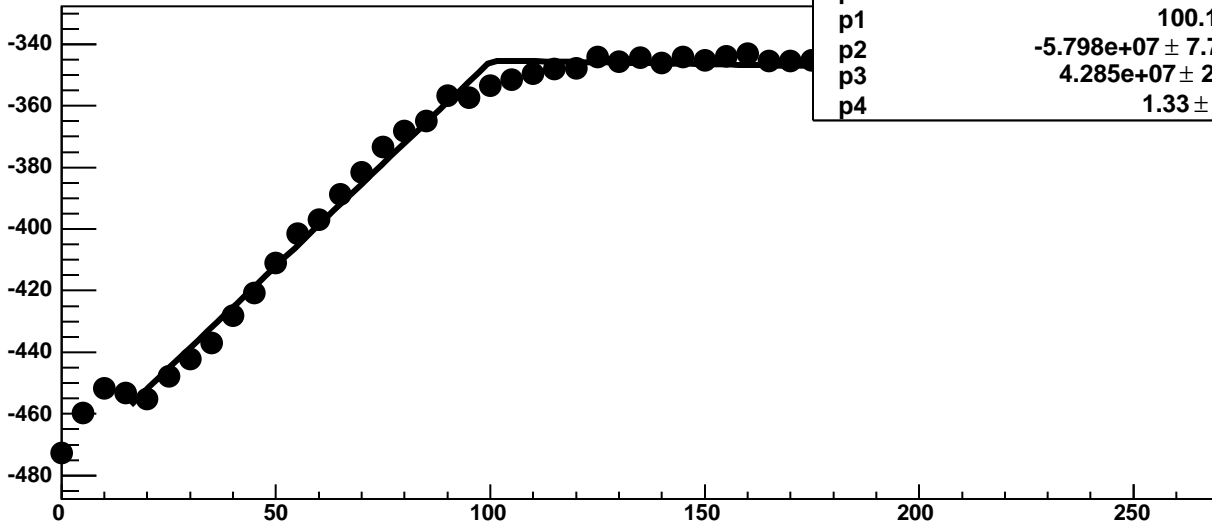
Chip 9, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold

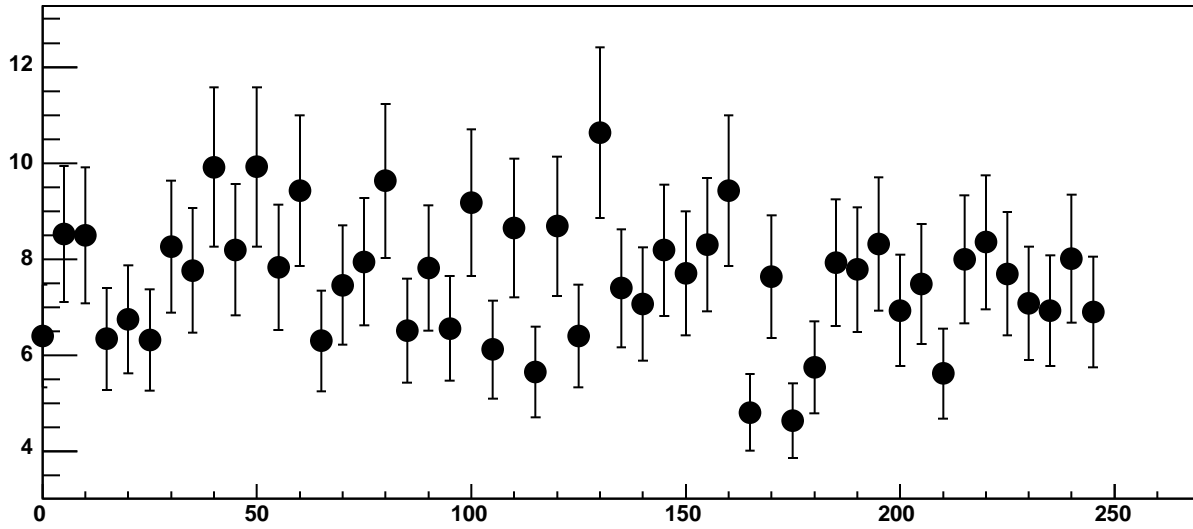


Chip 9, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

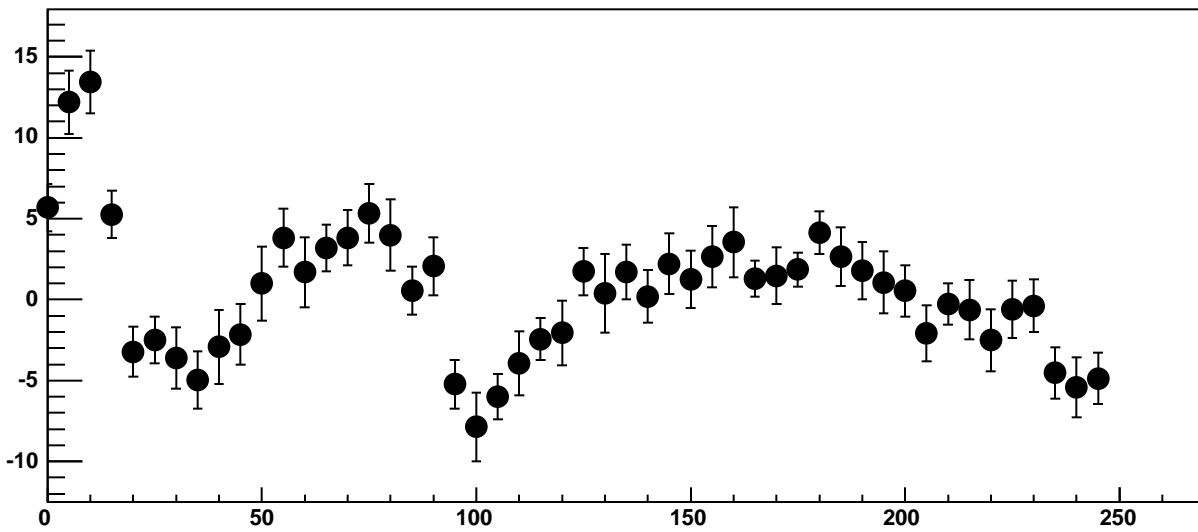


χ^2 / ndf	163.8 / 41
p0	-345.3 ± 0.6217
p1	100.1 ± 0.743
p2	$-5.798e+07 \pm 7.784e+05$
p3	$4.285e+07 \pm 2.09e+05$
p4	1.33 ± 0.01509

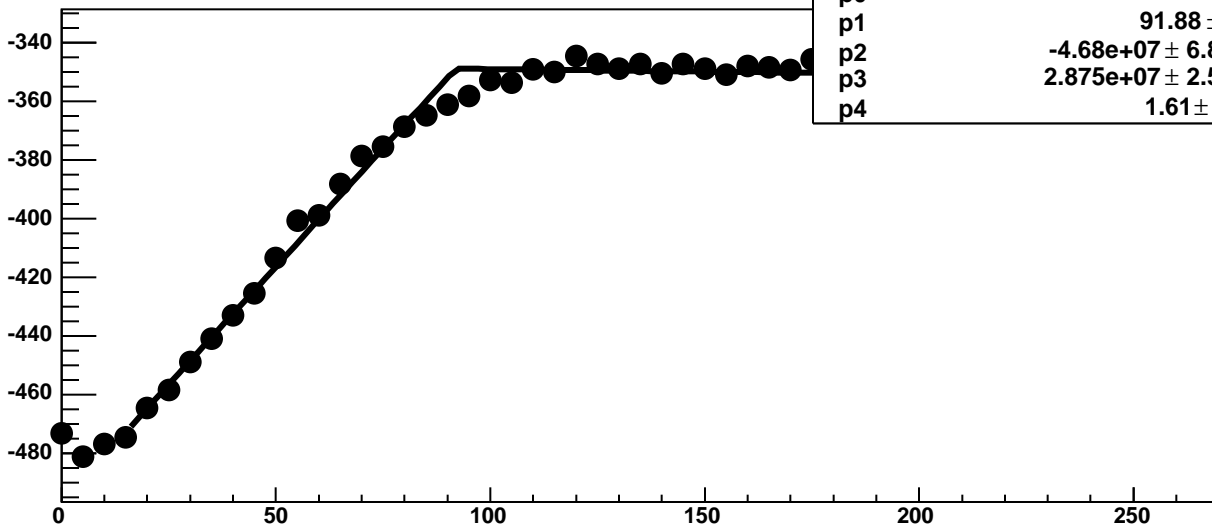
Chip 9, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold

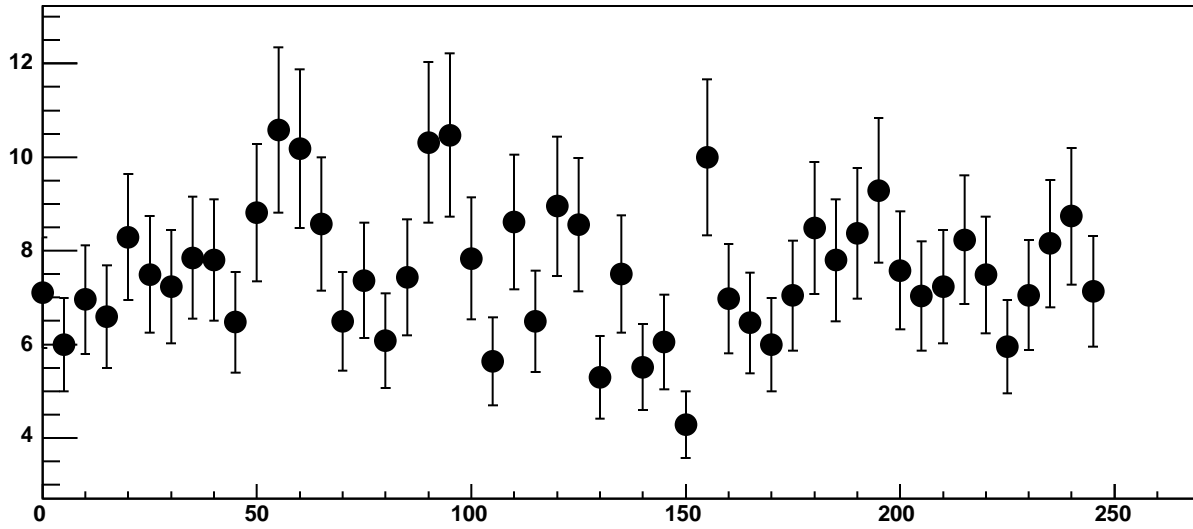


Chip 9, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

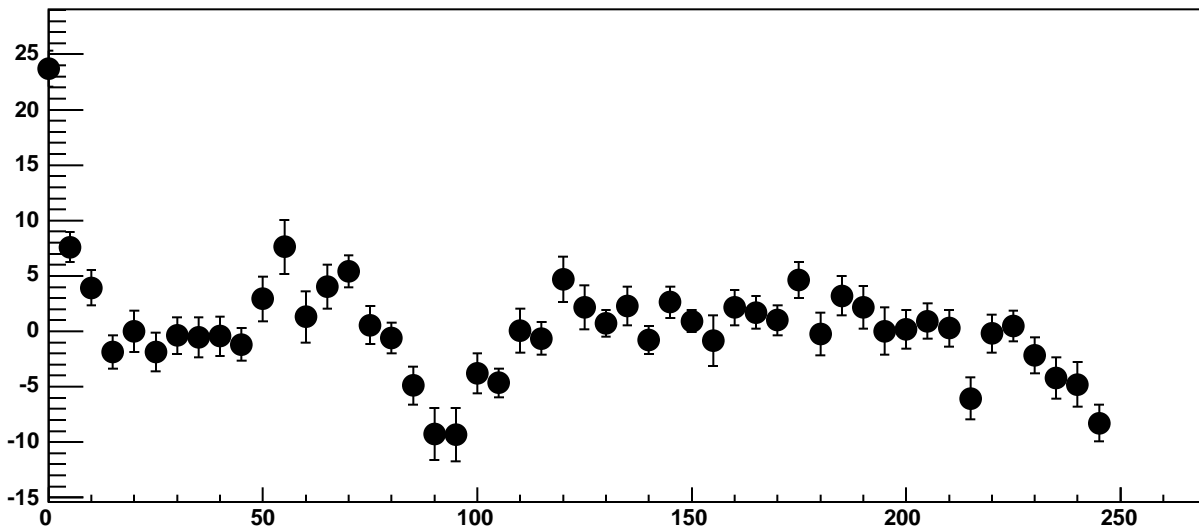


χ^2 / ndf	143.5 / 41
p0	-349 ± 0.5896
p1	91.88 ± 0.6444
p2	$-4.68\text{e}+07 \pm 6.863\text{e}+05$
p3	$2.875\text{e}+07 \pm 2.535\text{e}+05$
p4	1.61 ± 0.01854

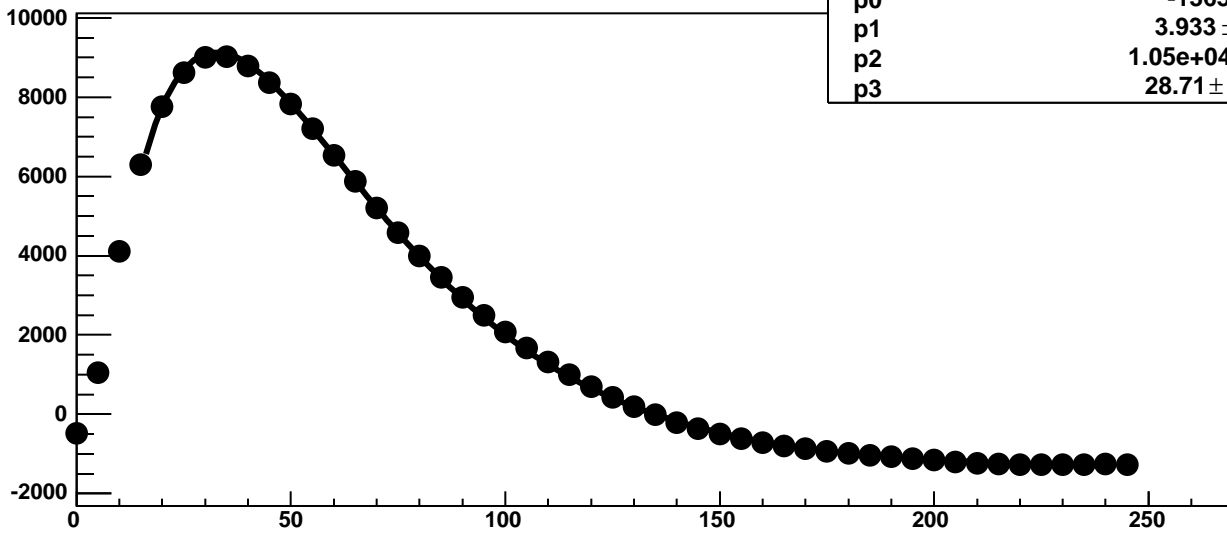
Chip 9, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

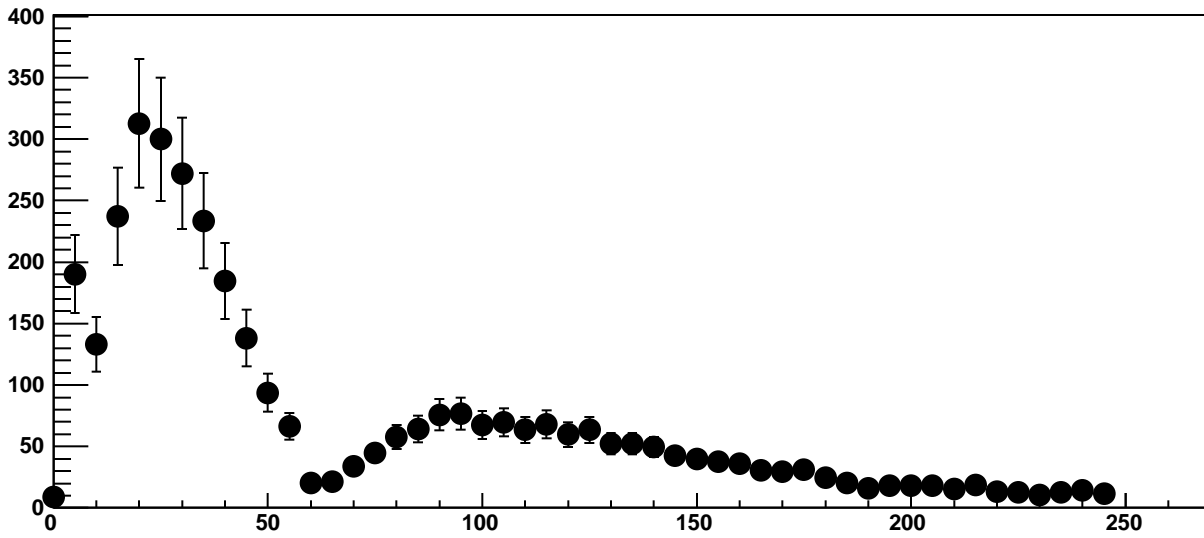


Chip 9, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold

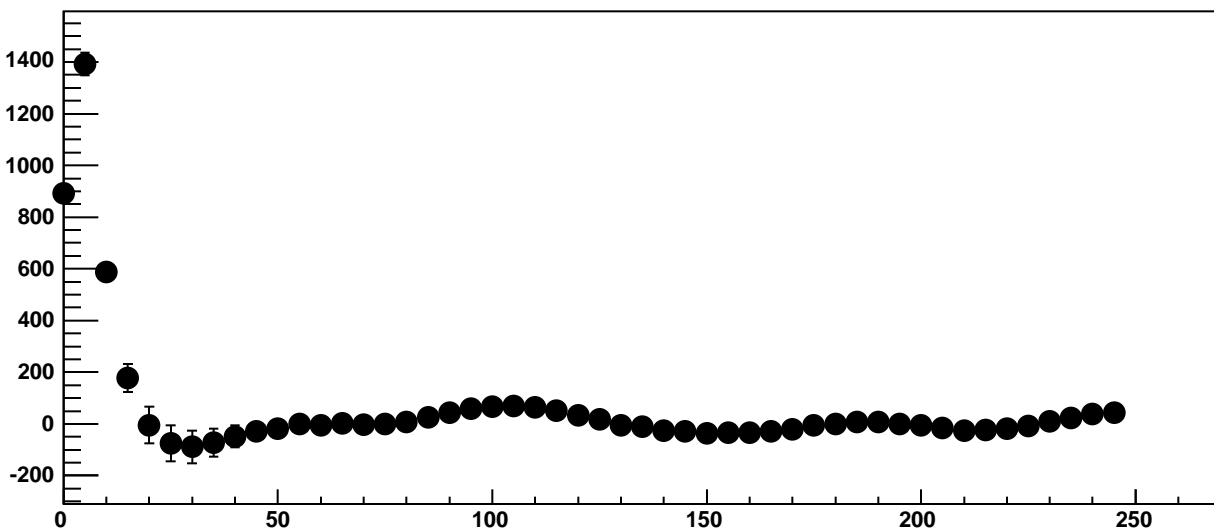


χ^2 / ndf	576.8 / 42
p0	-1365 ± 1.355
p1	3.933 ± 0.0936
p2	1.05e+04 ± 12.74
p3	28.71 ± 0.03299

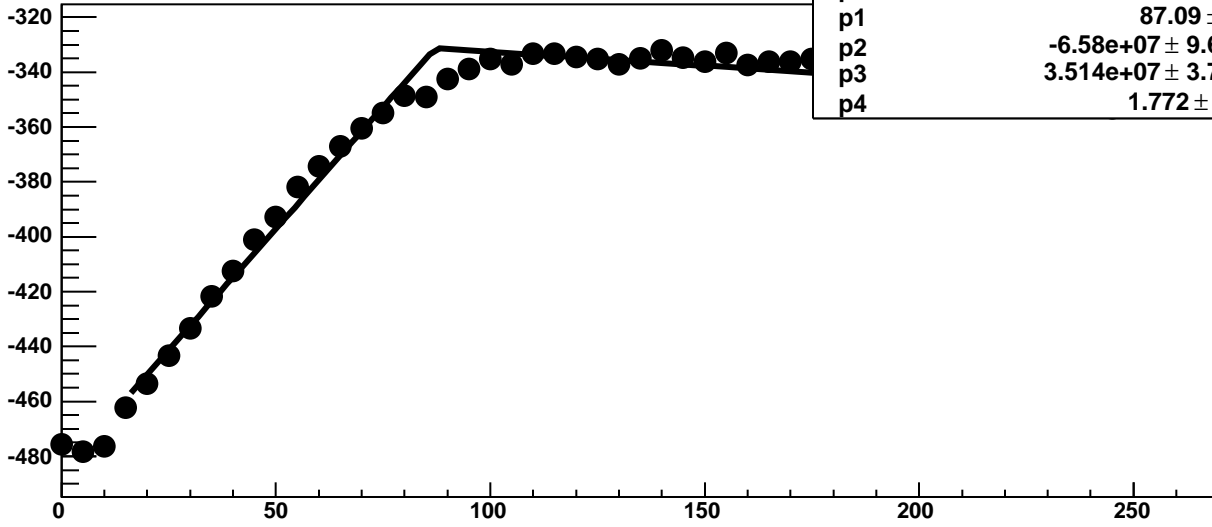
Chip 9, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold

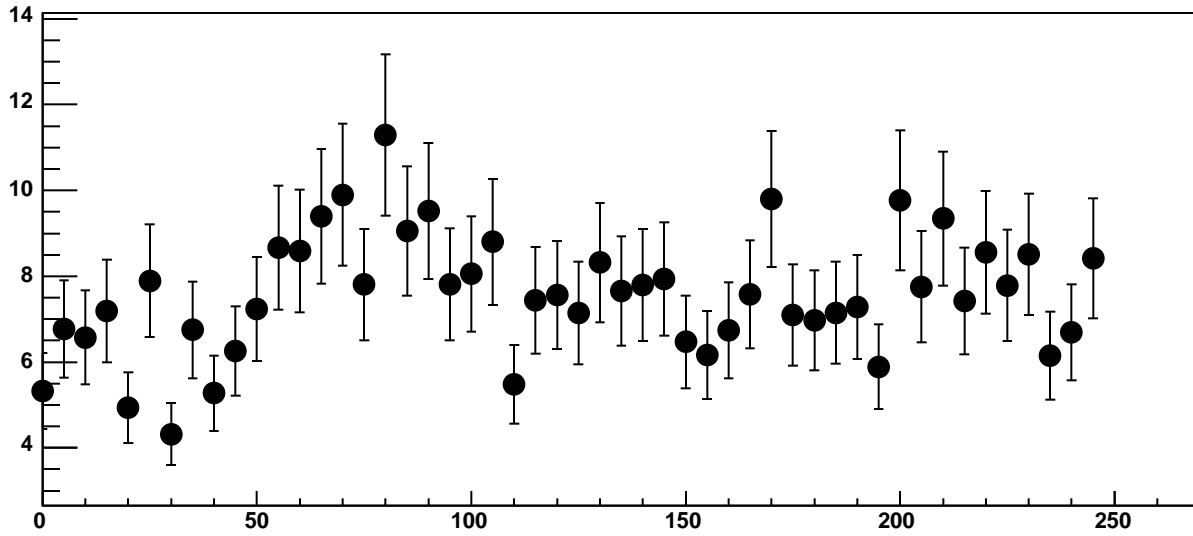


Chip 9, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold

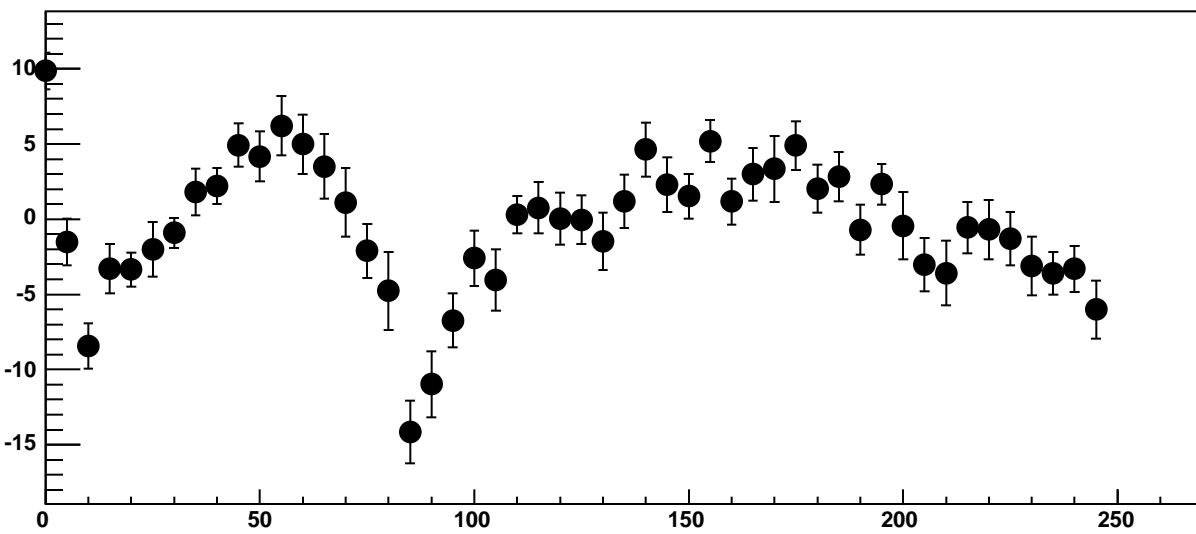


χ^2 / ndf	220.3 / 41
p0	-331.3 ± 0.5926
p1	87.09 ± 0.6376
p2	$-6.58\text{e}+07 \pm 9.696\text{e}+05$
p3	$3.514\text{e}+07 \pm 3.767\text{e}+05$
p4	1.772 ± 0.02073

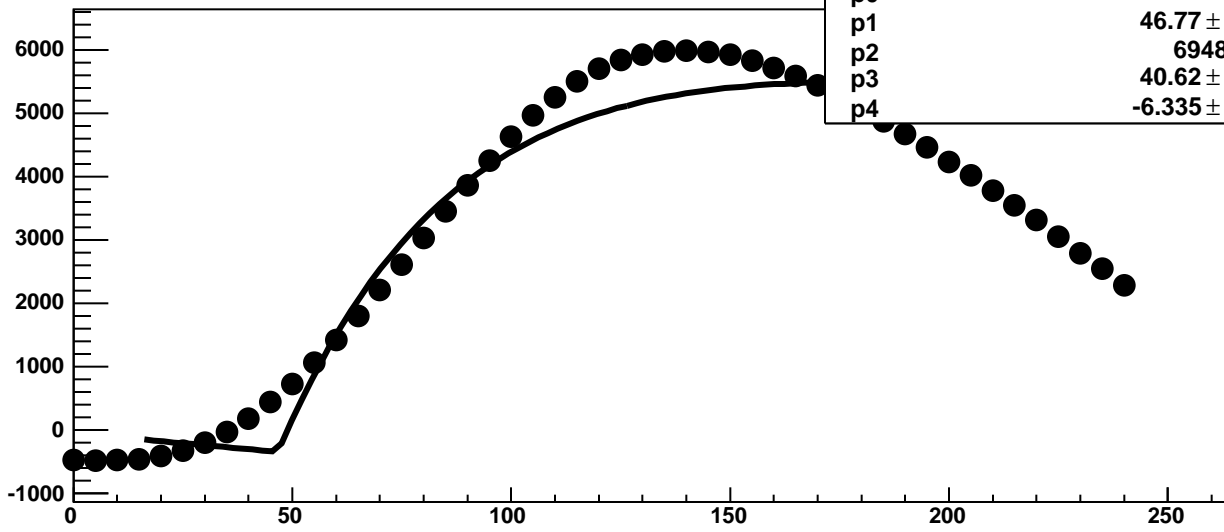
Chip 9, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

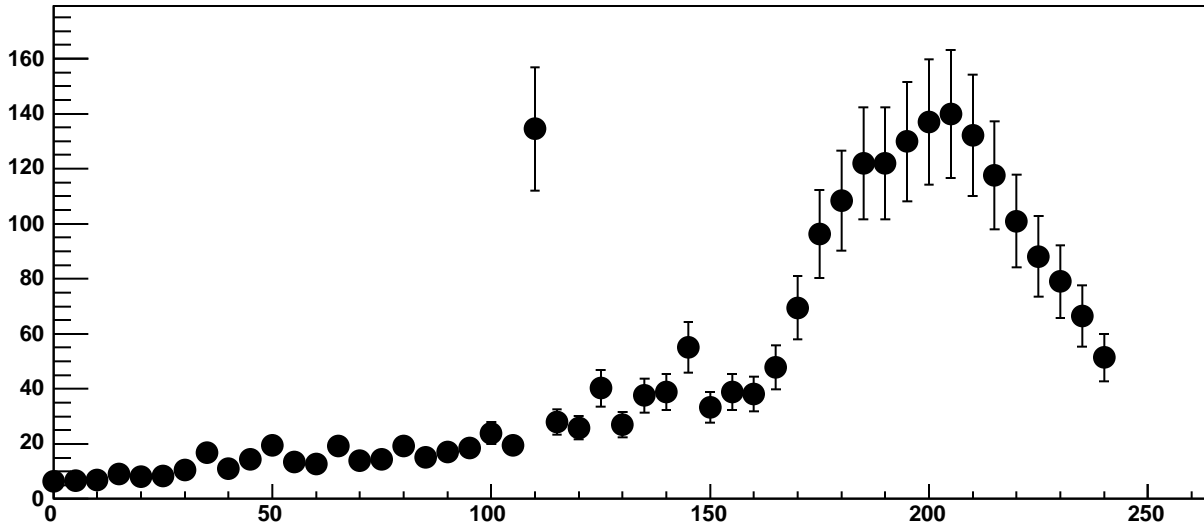


Chip 9, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

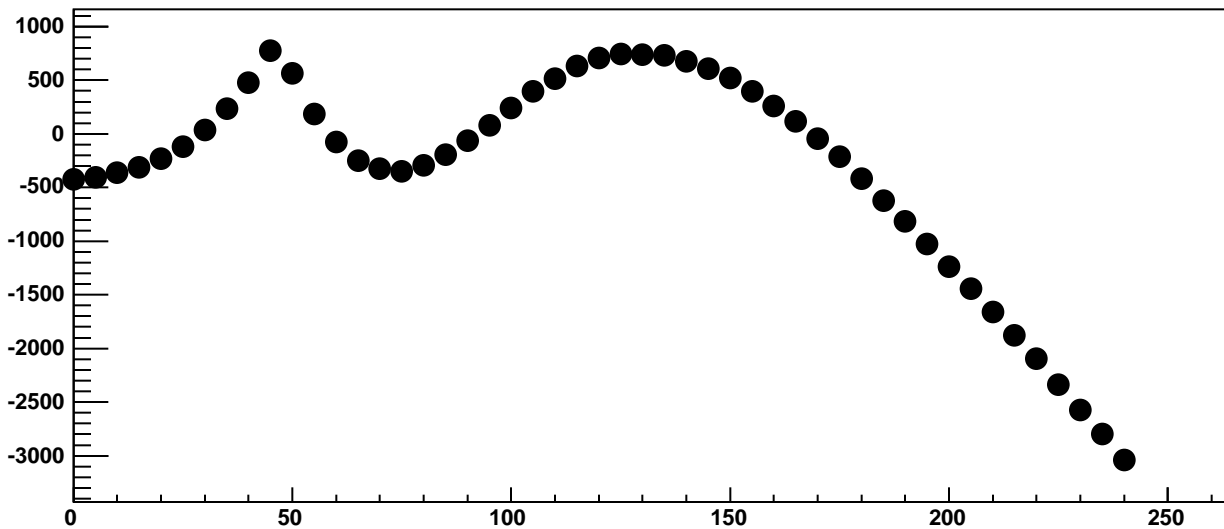


χ^2 / ndf	4.241e+05 / 41
p0	-348.2 ± 1.5
p1	46.77 ± 0.01992
p2	6948 ± 10.69
p3	40.62 ± 0.06876
p4	-6.335 ± 0.06347

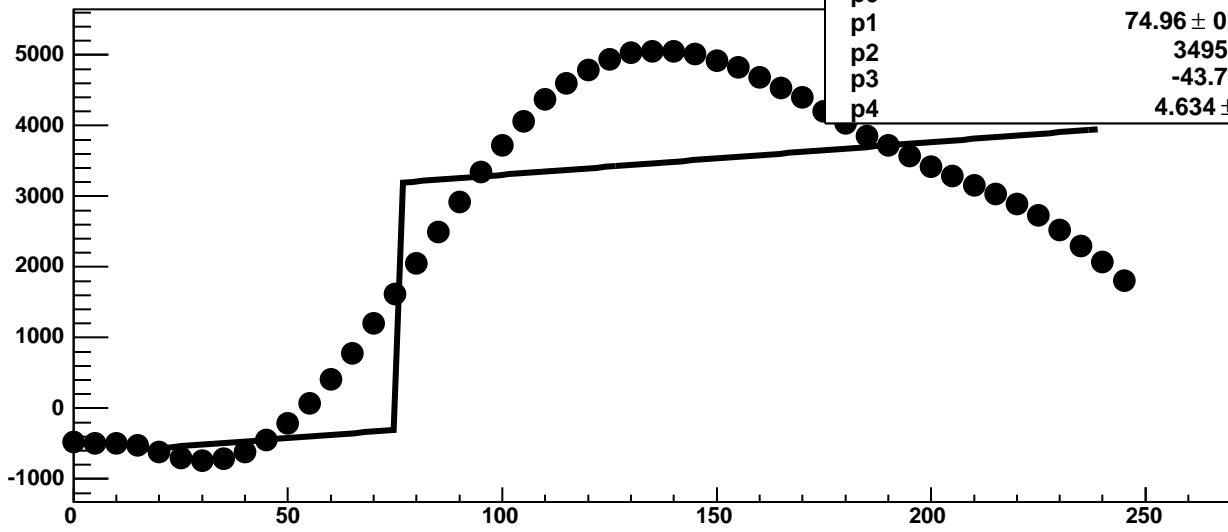
Chip 9, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

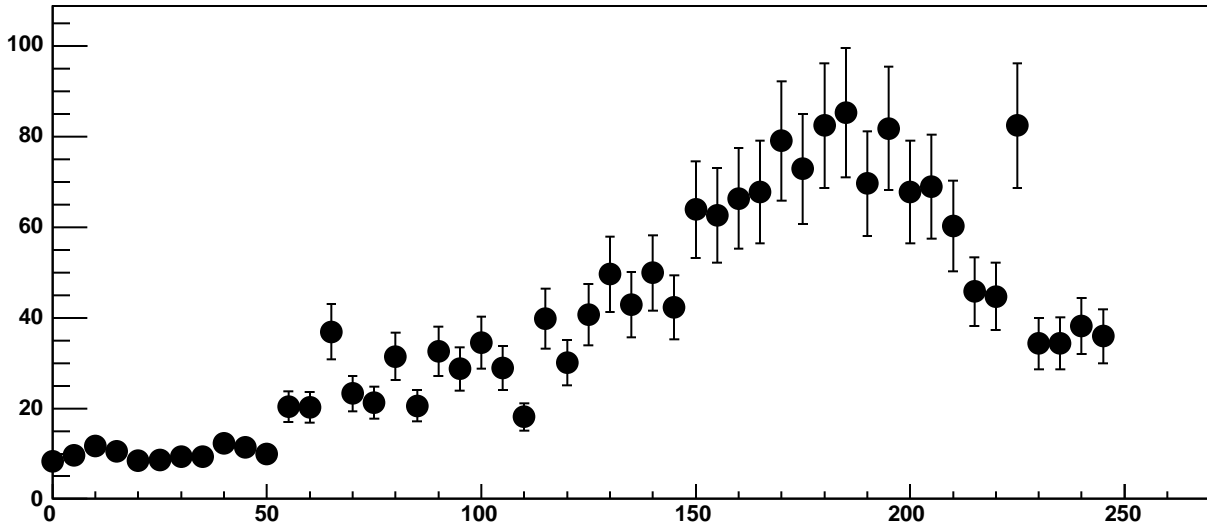


Chip 9, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold

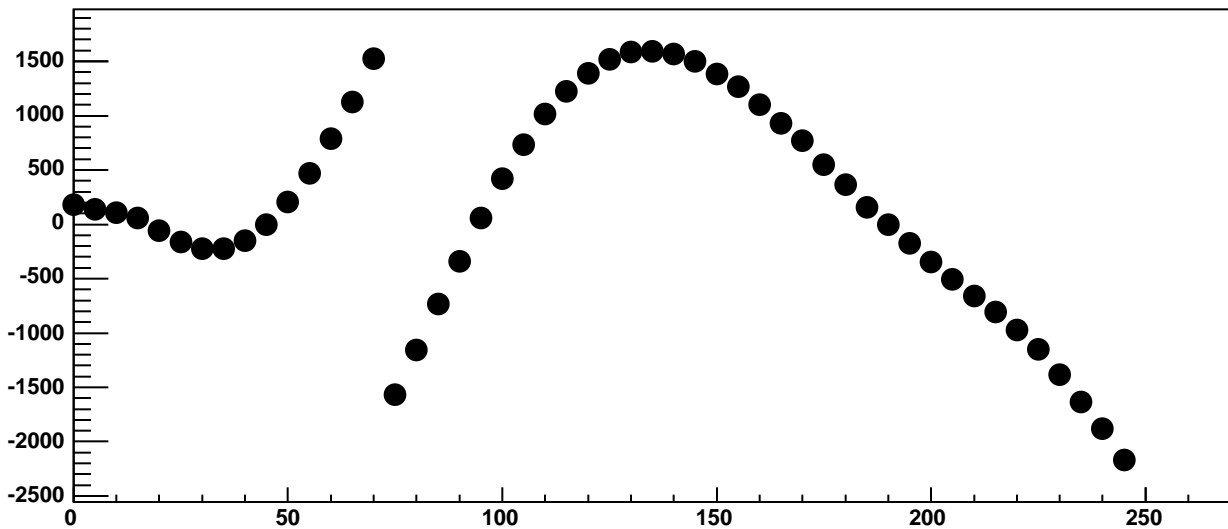


χ^2 / ndf	7.574e+05 / 41
p0	-306.7 ± 8.597
p1	74.96 ± 0.001829
p2	3495 ± 7.482
p3	-43.7 ± 2.872
p4	4.634 ± 0.1413

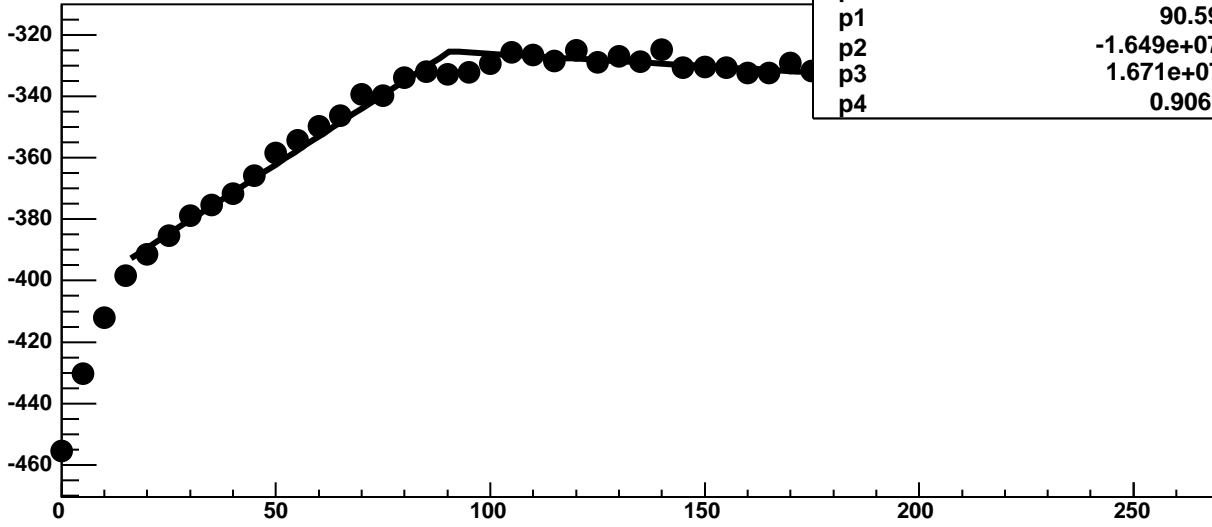
Chip 9, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

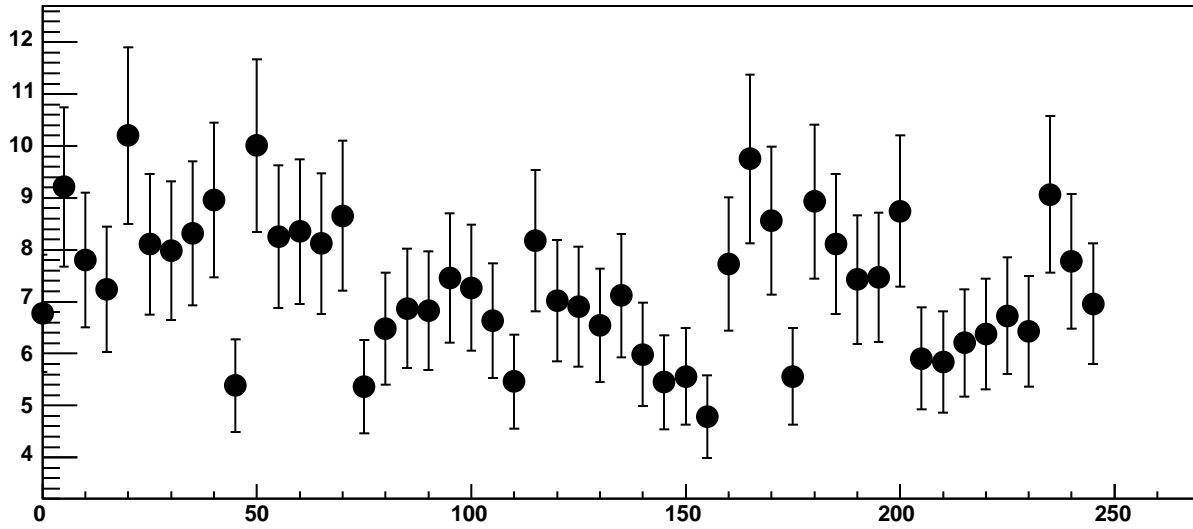


Chip 9, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold

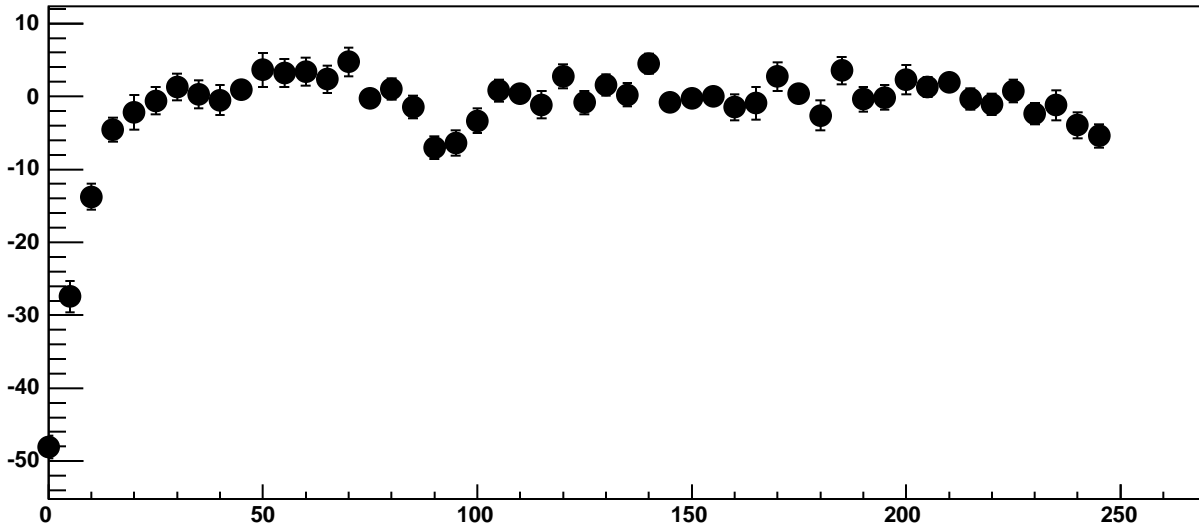


χ^2 / ndf	102.4 / 41
p0	-325.4 ± nan
p1	90.59 ± nan
p2	-1.649e+07 ± nan
p3	1.671e+07 ± nan
p4	0.9061 ± nan

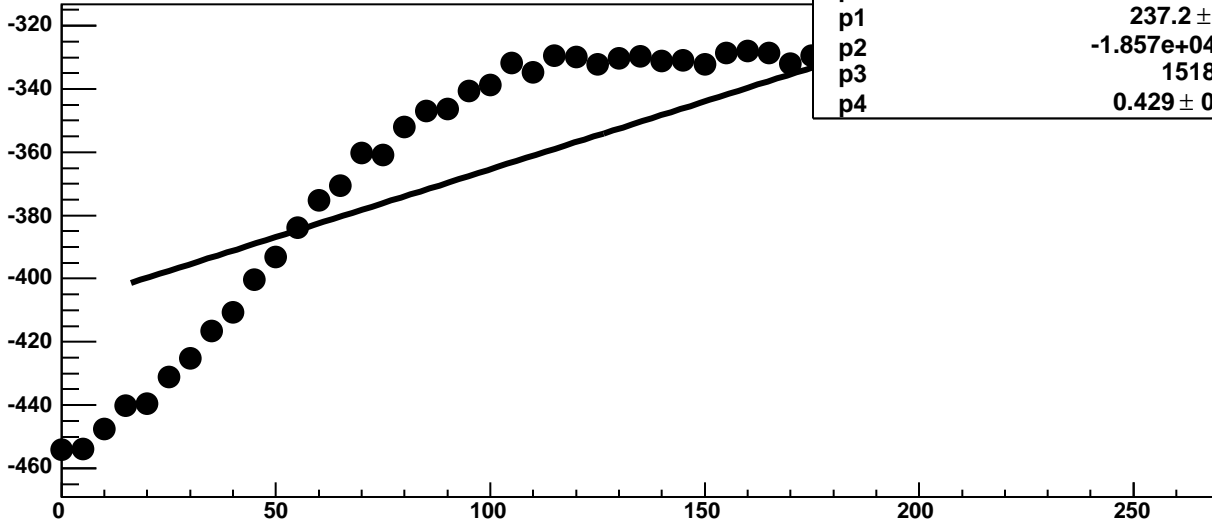
Chip 9, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold



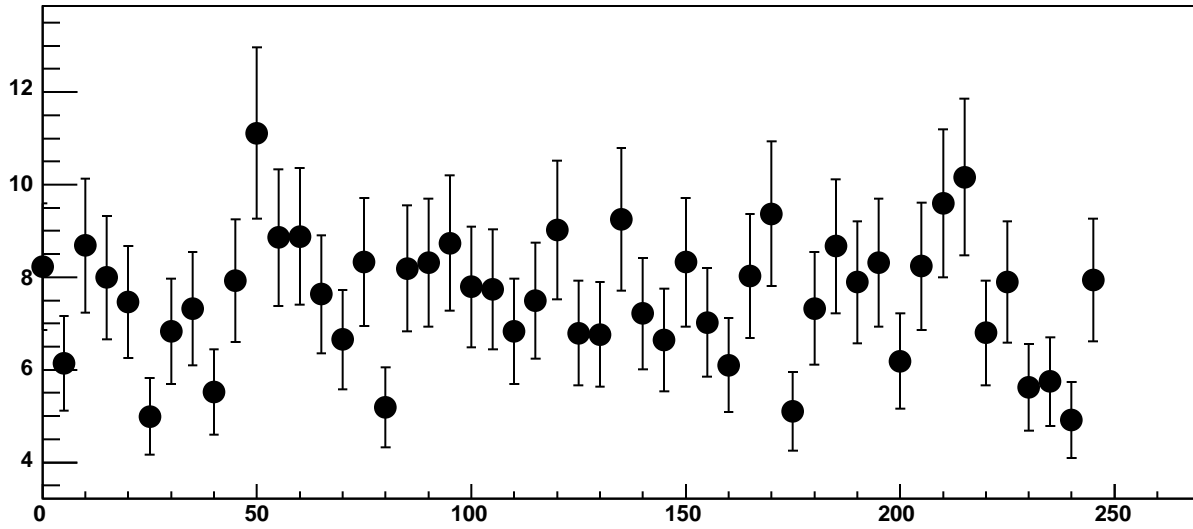
Chip 9, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold



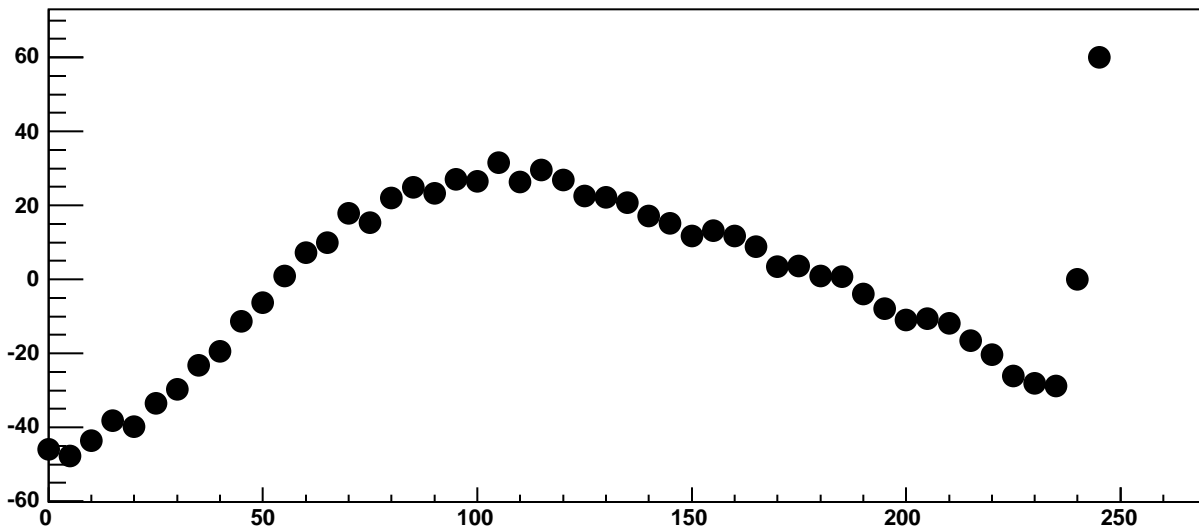
Chip 9, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold



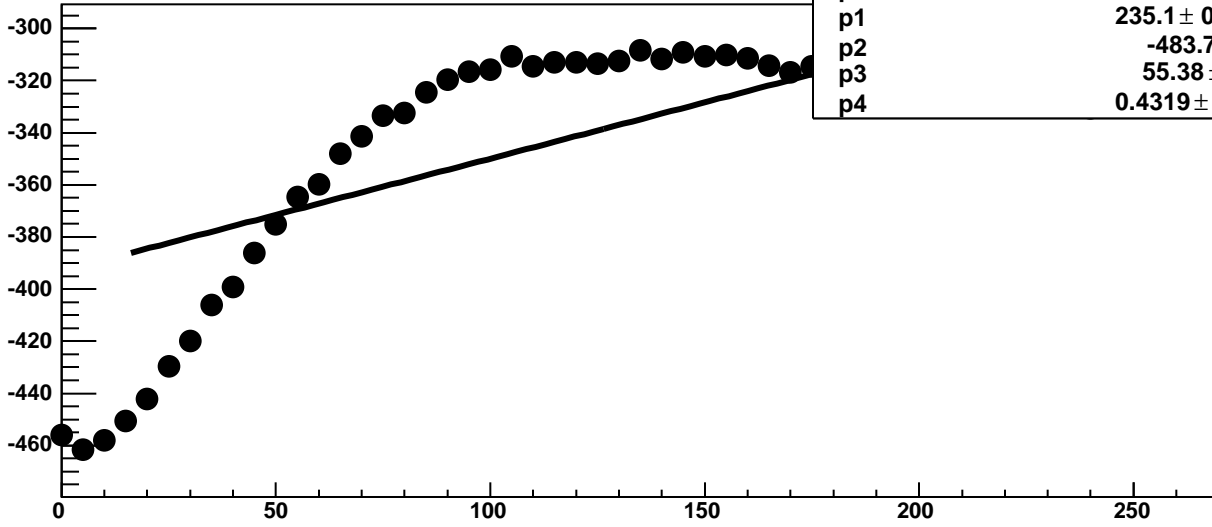
Chip 9, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

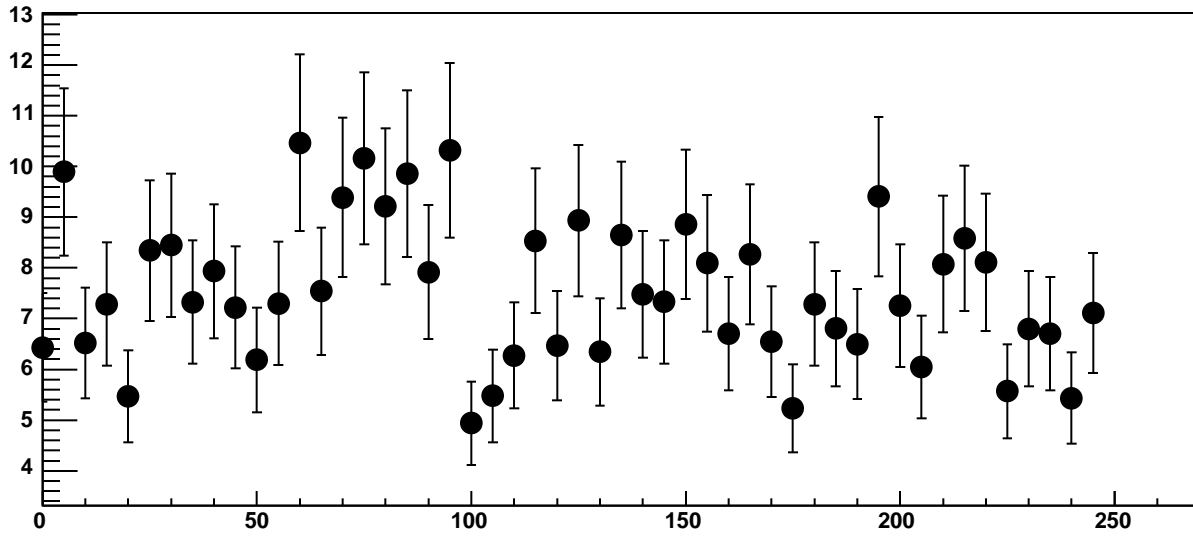


Chip 9, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

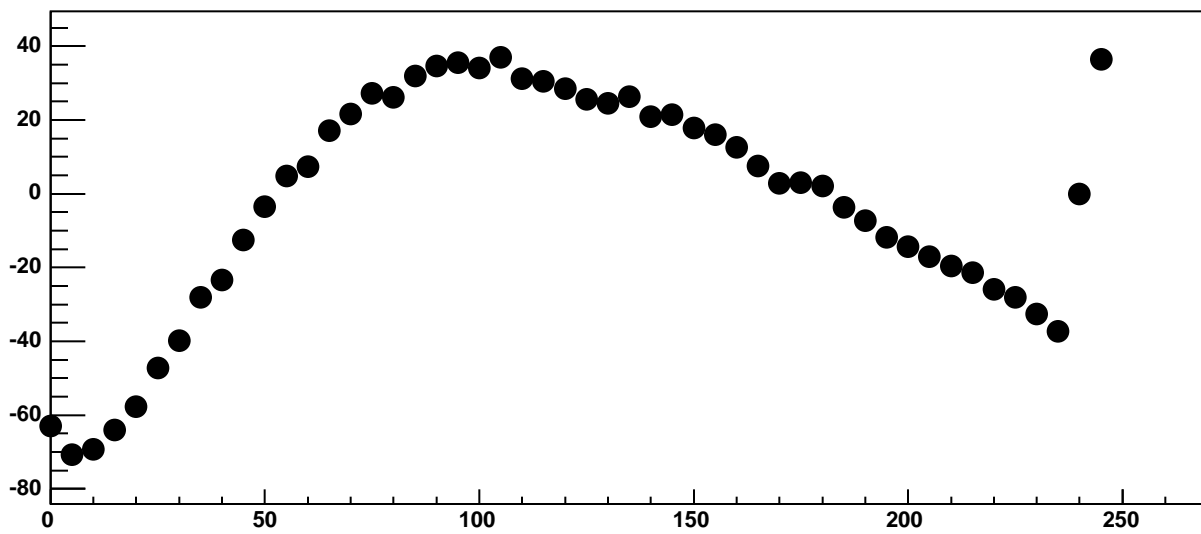


χ^2 / ndf	1.266e+04 / 41
p0	-291.6 ± 0.4697
p1	235.1 ± 0.001556
p2	-483.7 ± 15.83
p3	55.38 ± 0.1002
p4	0.4319 ± 0.00375

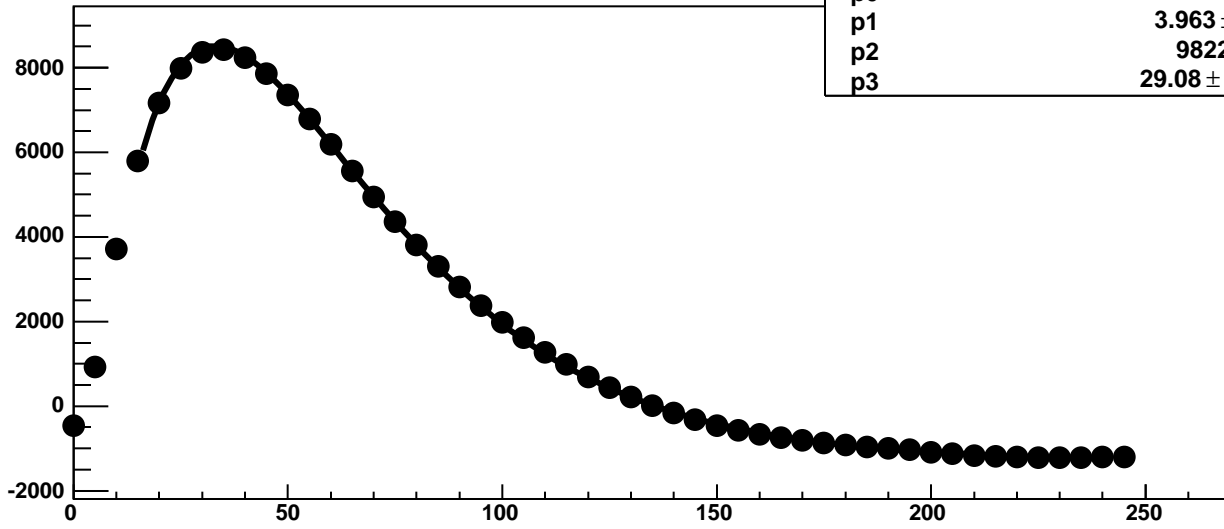
Chip 9, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

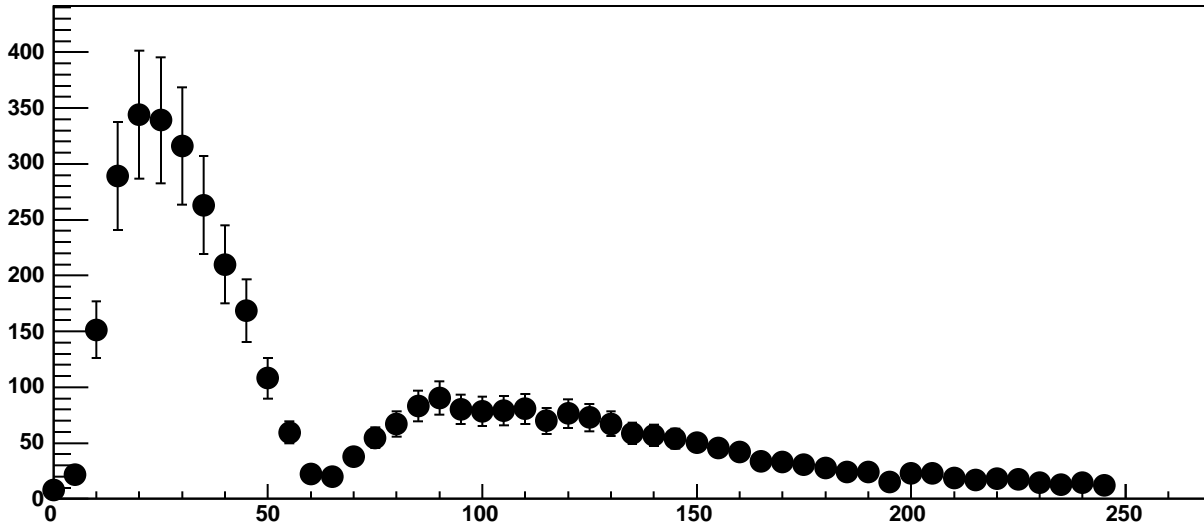


Chip 9, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold

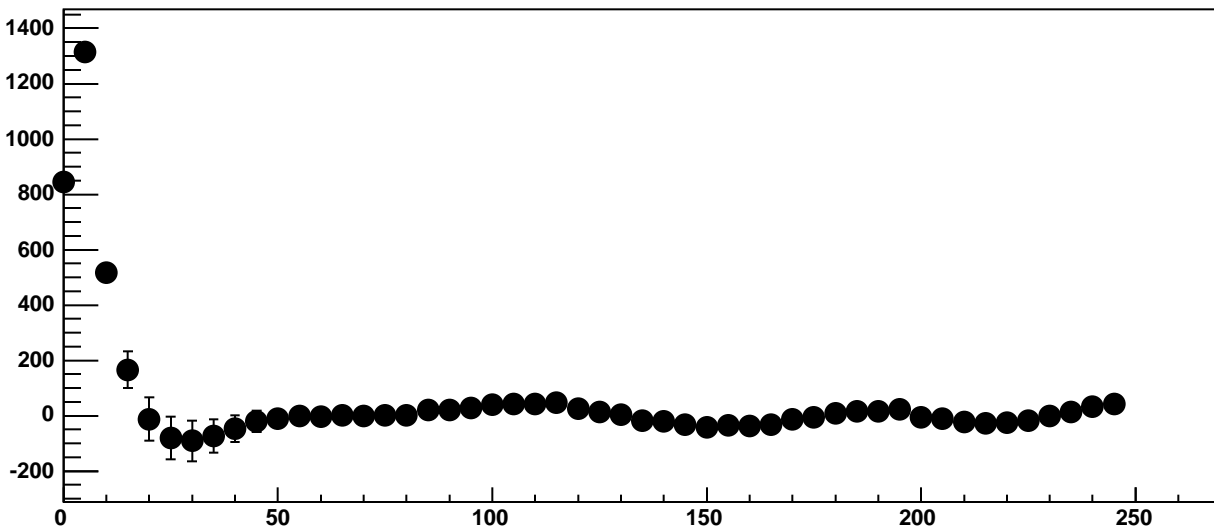


χ^2 / ndf	427.6 / 42
p0	-1303 ± 1.626
p1	3.963 ± 0.1176
p2	9822 ± 14.41
p3	29.08 ± 0.04115

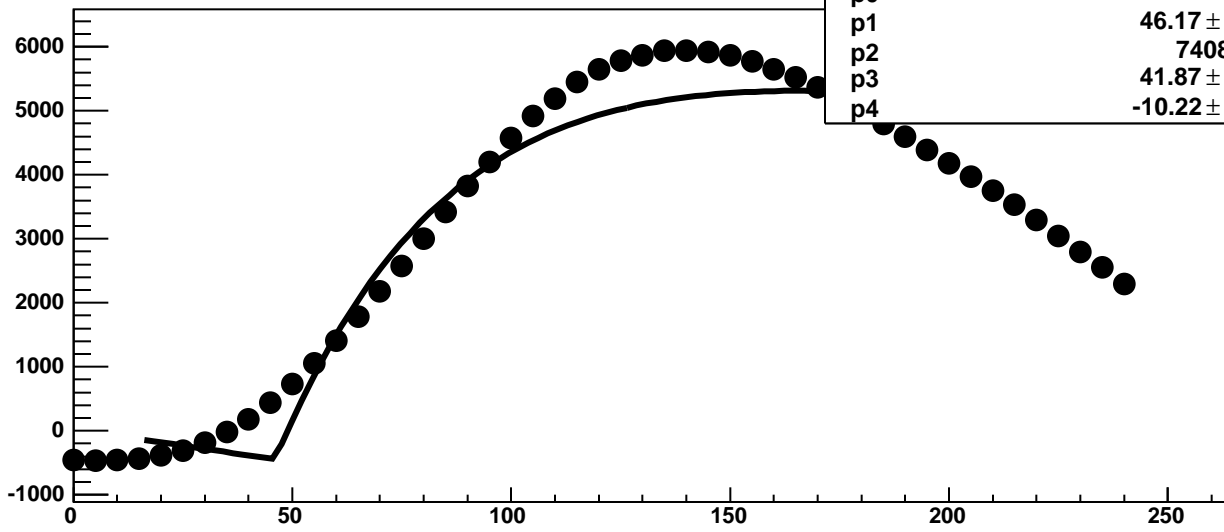
Chip 9, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold

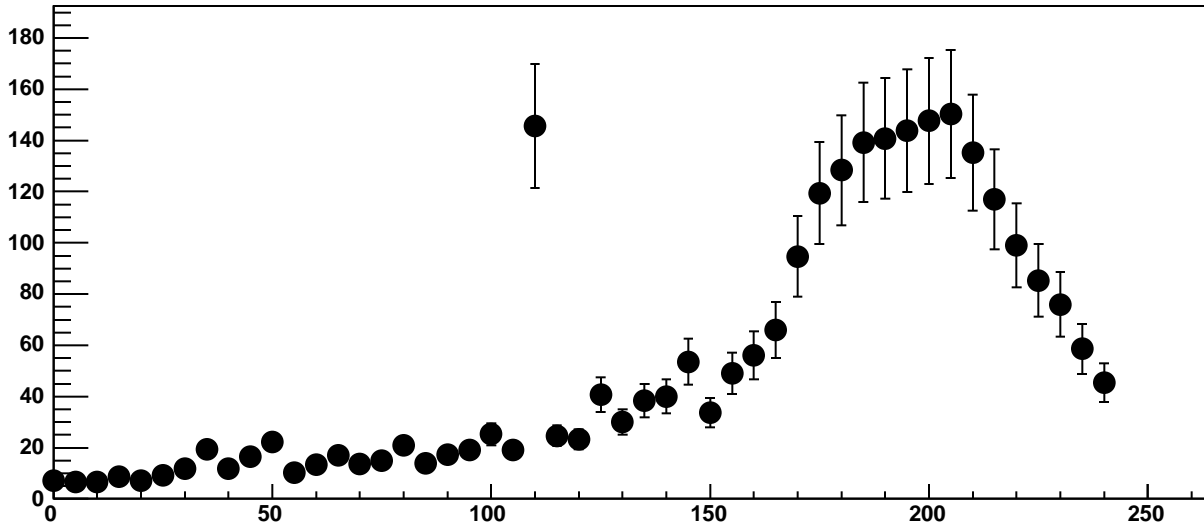


Chip 9, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold

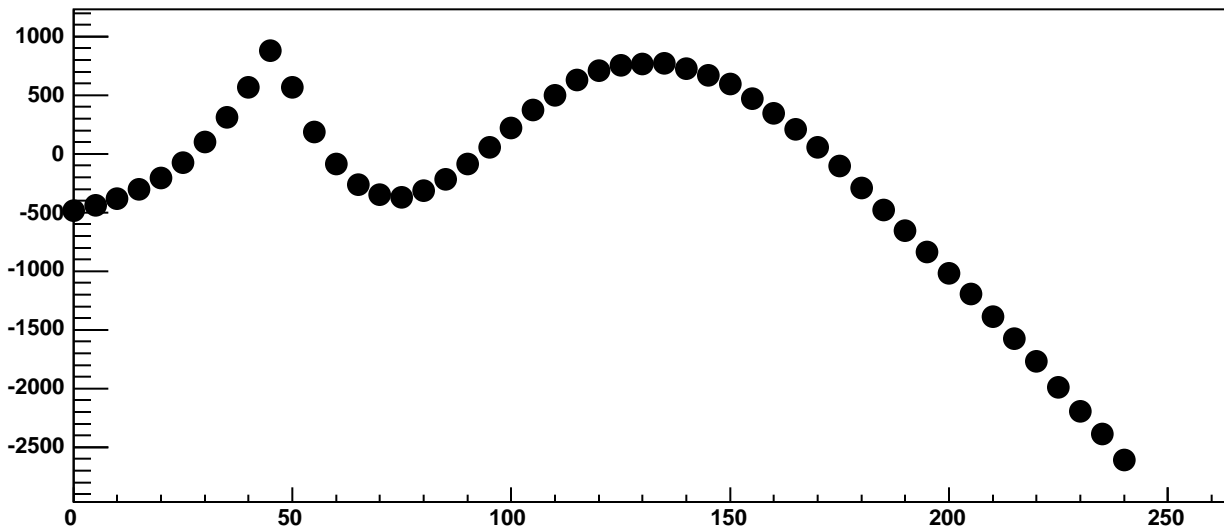


χ^2 / ndf	4.199e+05 / 41
p0	-449.6 ± 1.527
p1	46.17 ± 0.01913
p2	7408 ± 11.22
p3	41.87 ± 0.06999
p4	-10.22 ± 0.06413

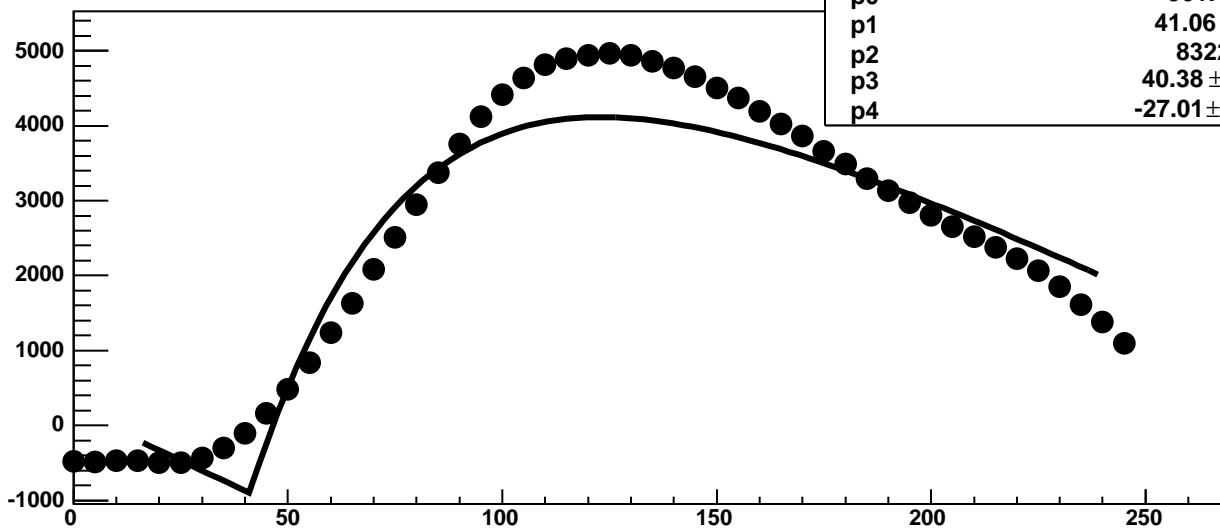
Chip 9, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



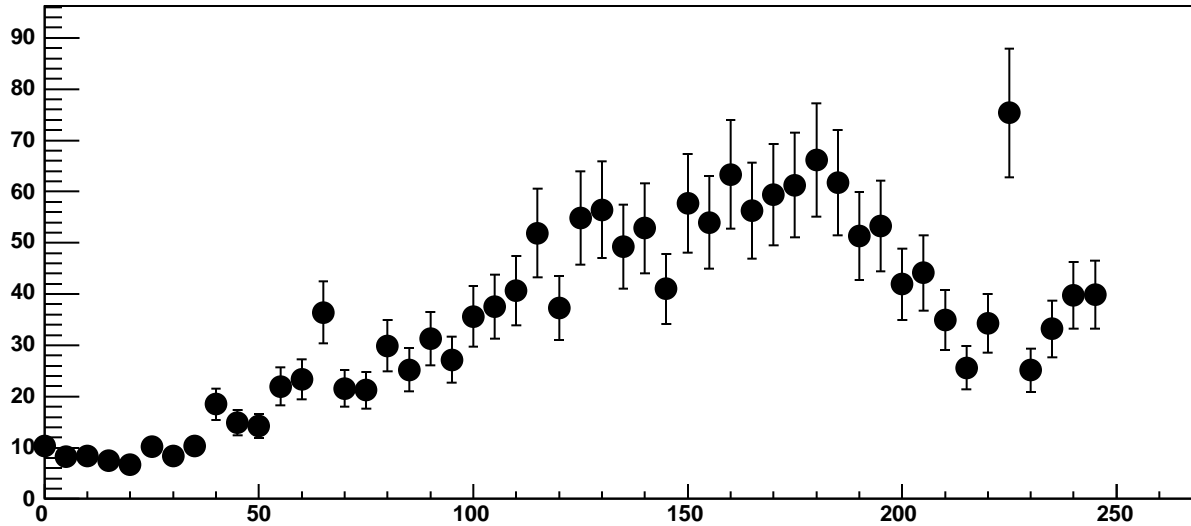
Chip 9, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold



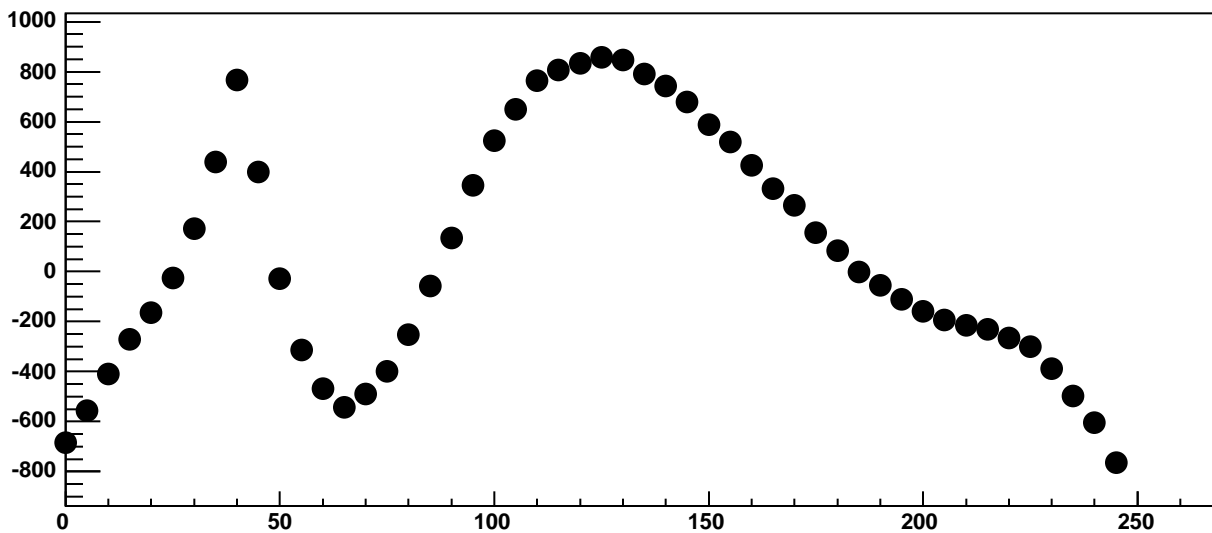
Chip 9, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold



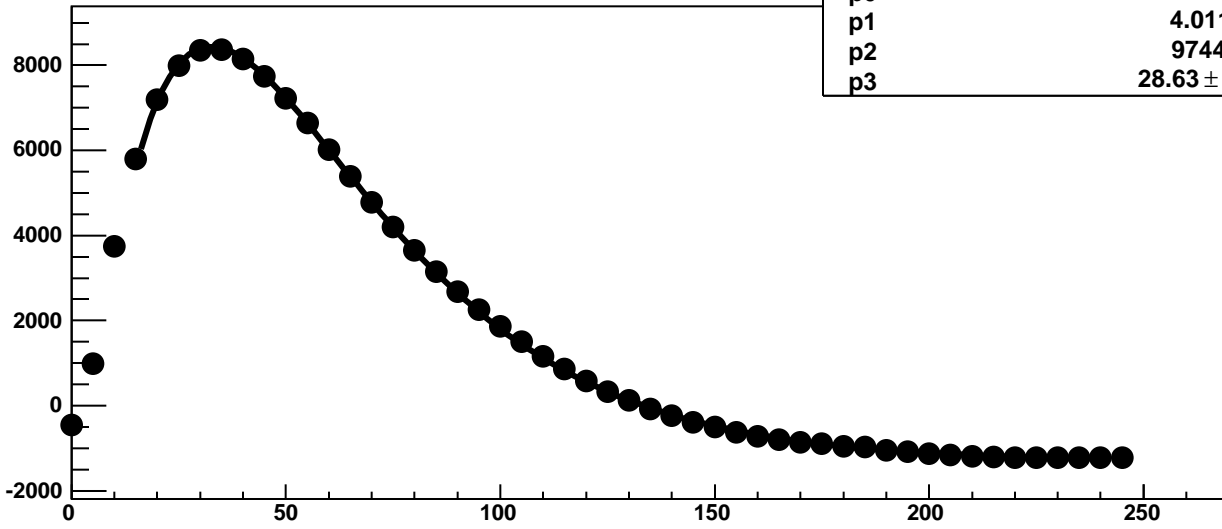
Chip 9, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



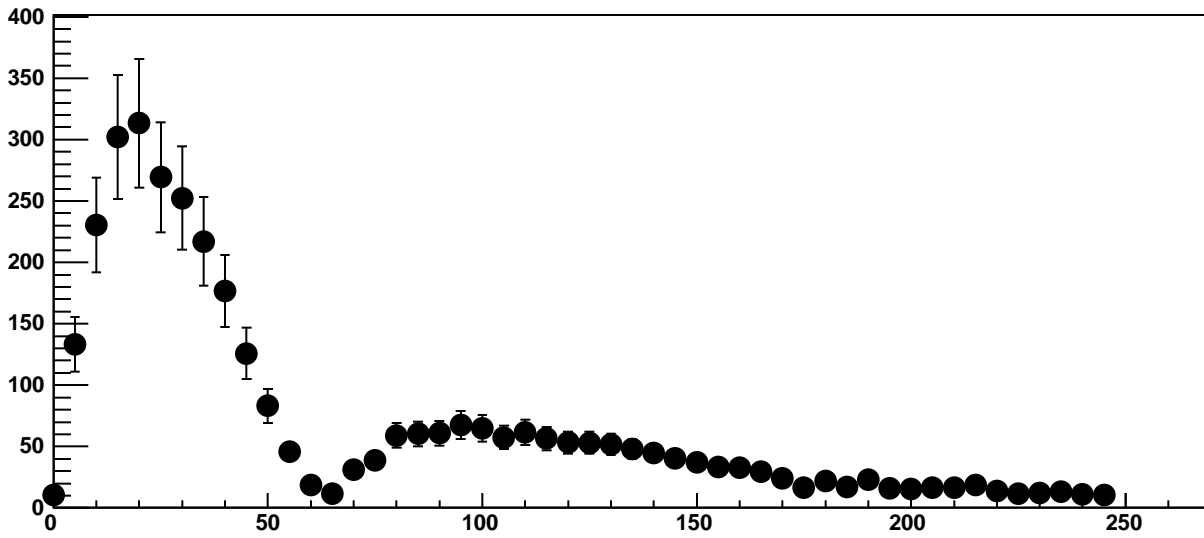
Chip 9, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold



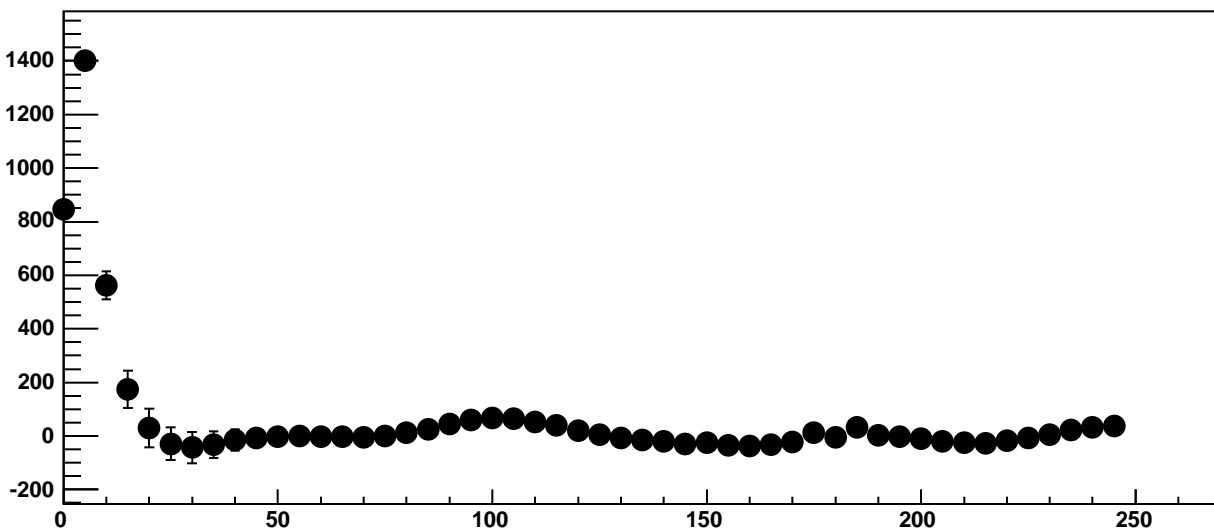
Chip 9, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold



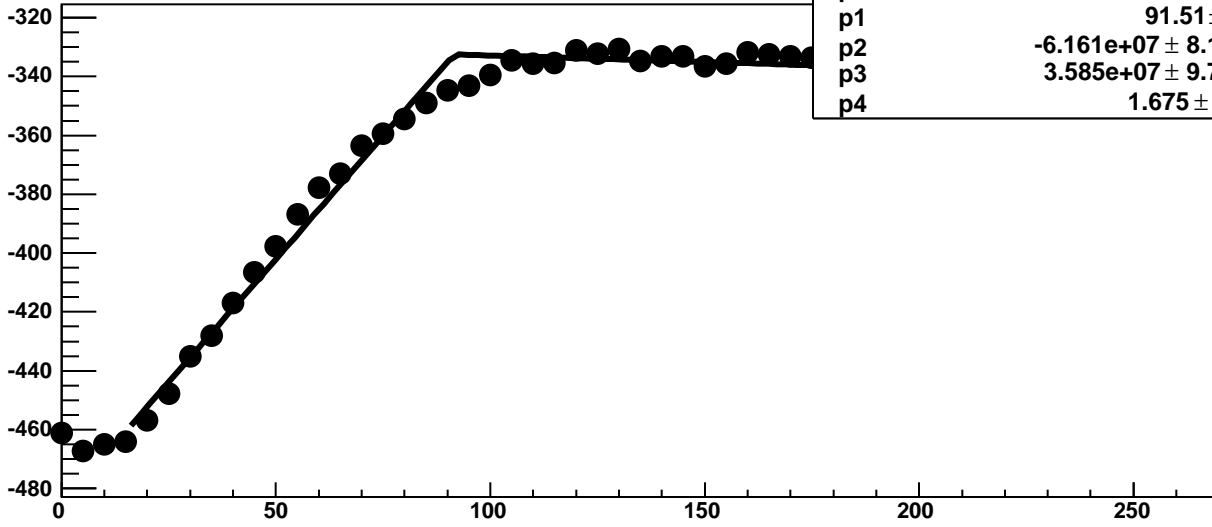
Chip 9, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold

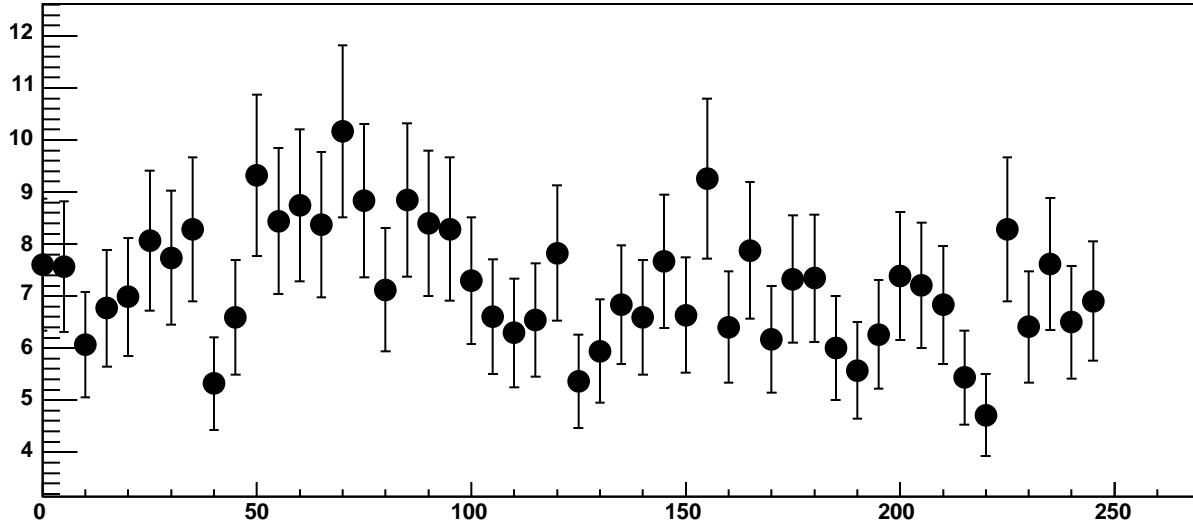


Chip 9, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

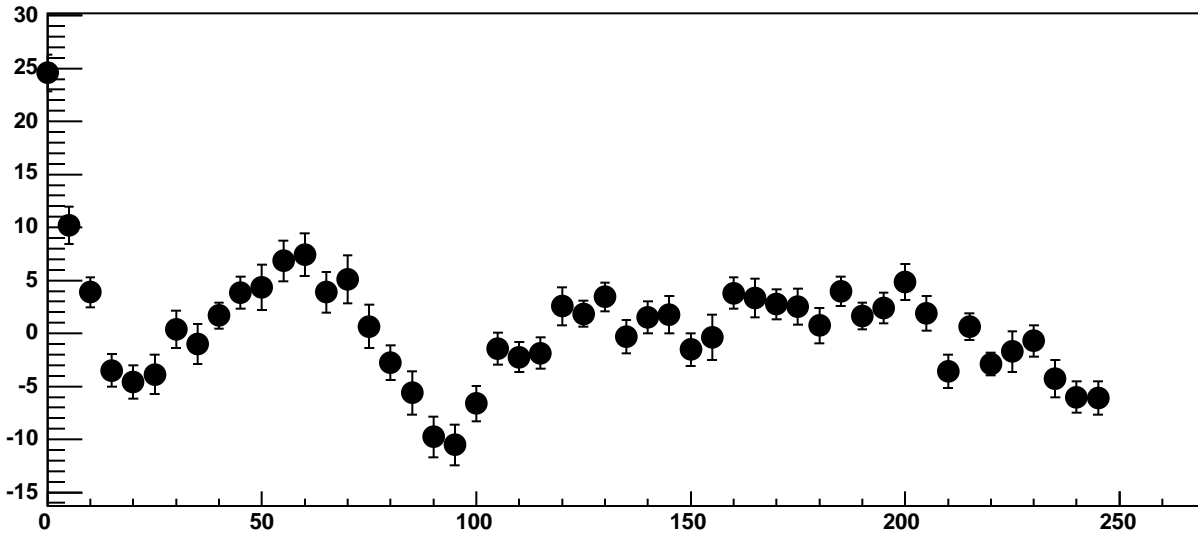


χ^2 / ndf	241.5 / 41
p0	-332.5 ± 0.7142
p1	91.51 ± 0.3529
p2	$-6.161\text{e}+07 \pm 8.149\text{e}+05$
p3	$3.585\text{e}+07 \pm 9.764\text{e}+05$
p4	1.675 ± 0.01698

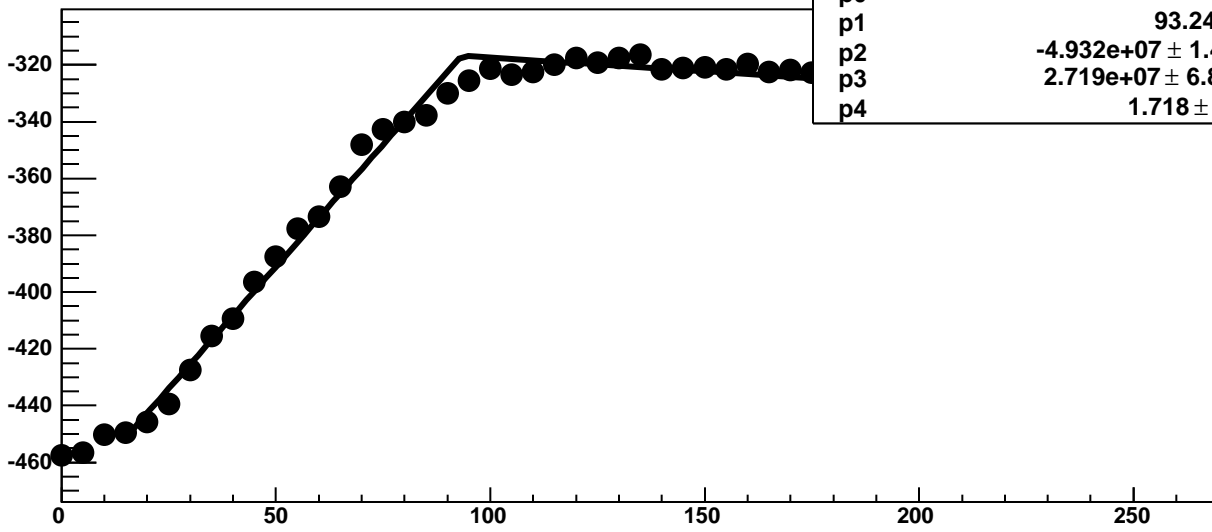
Chip 9, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

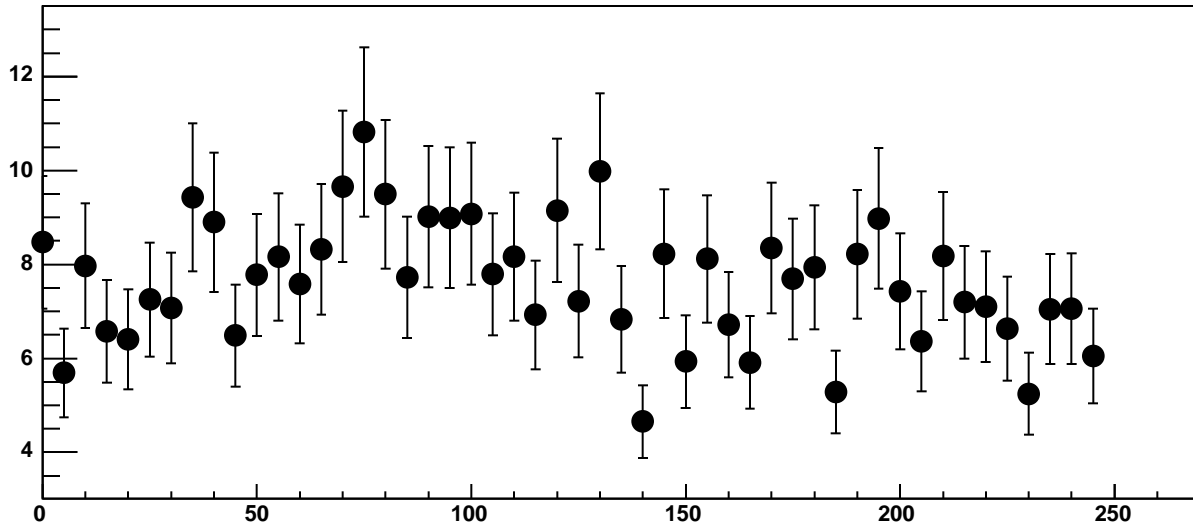


Chip 9, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

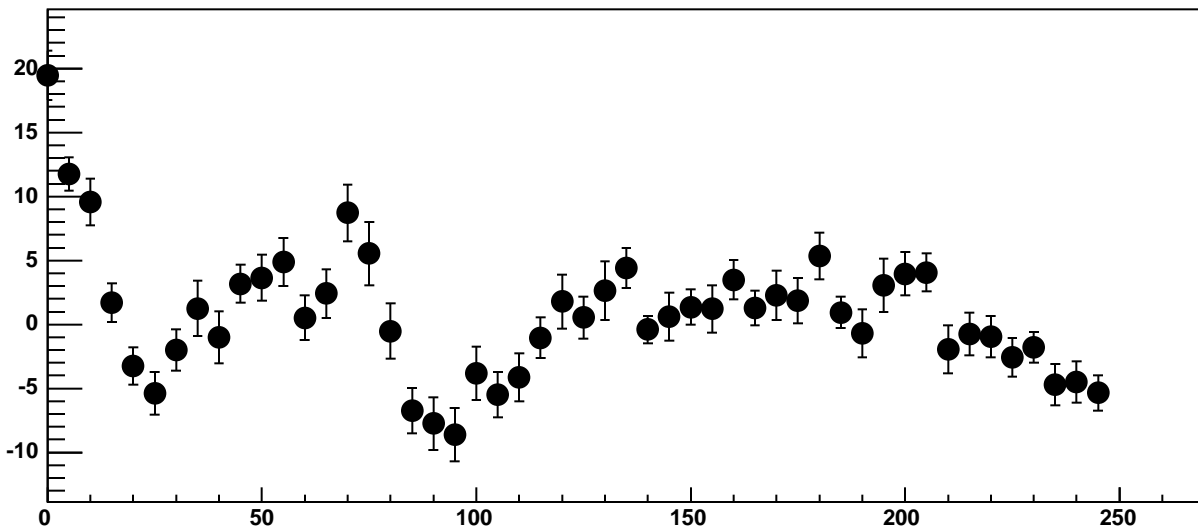


χ^2 / ndf	188.4 / 41
p0	-316.8 ± 0.5997
p1	93.24 ± 0.664
p2	$-4.932\text{e}+07 \pm 1.441\text{e}+06$
p3	$2.719\text{e}+07 \pm 6.843\text{e}+05$
p4	1.718 ± 0.01987

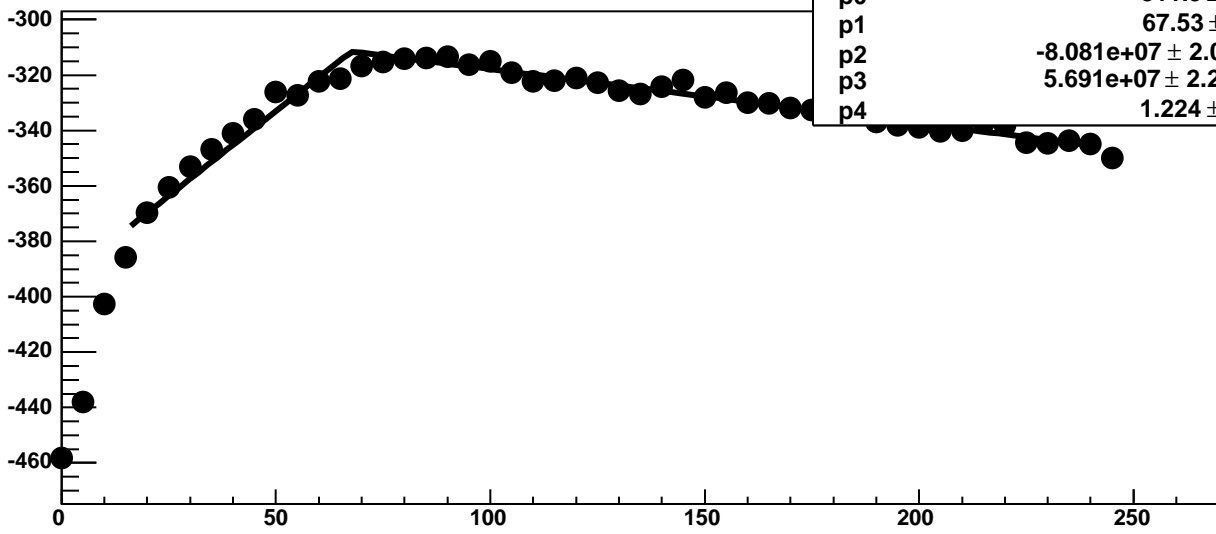
Chip 9, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

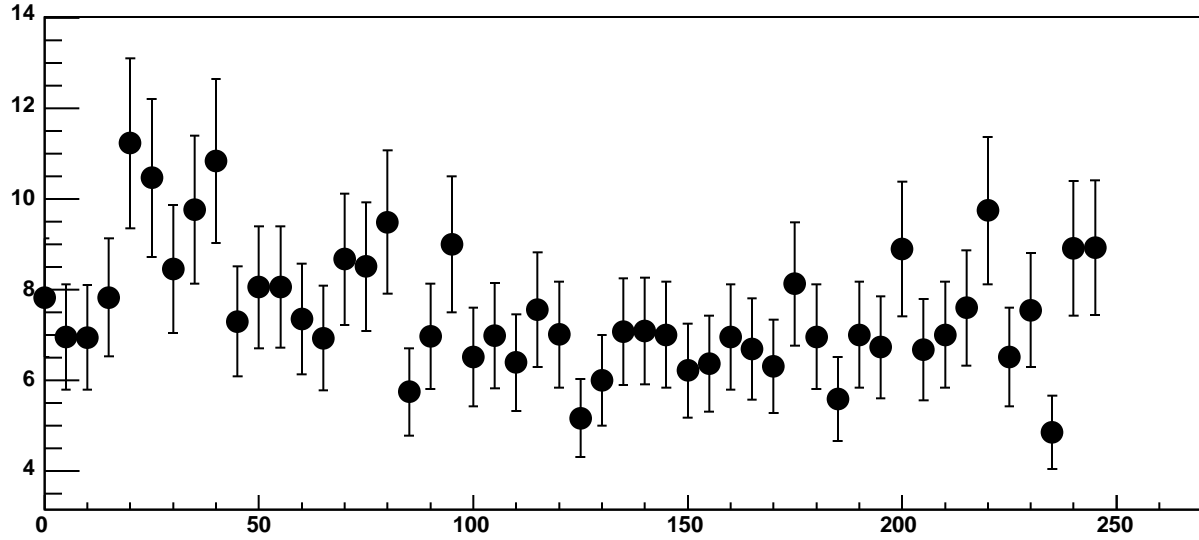


Chip 9, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold

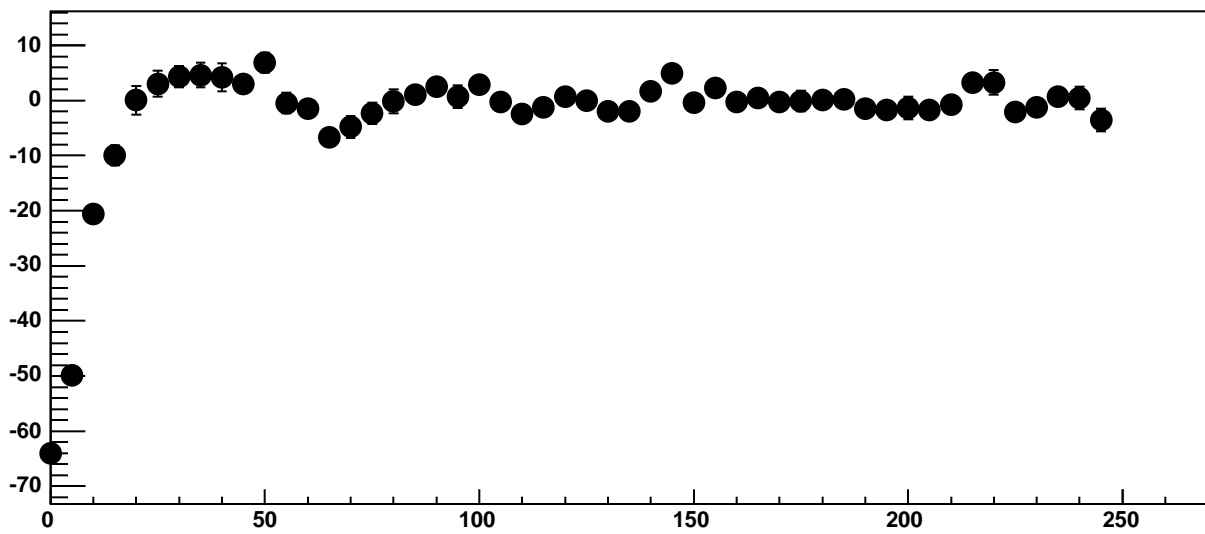


χ^2 / ndf	127.1 / 41
p0	-311.5 ± 0.4994
p1	67.53 ± 0.8353
p2	$-8.081\text{e}+07 \pm 2.067\text{e}+06$
p3	$5.691\text{e}+07 \pm 2.205\text{e}+05$
p4	1.224 ± 0.0356

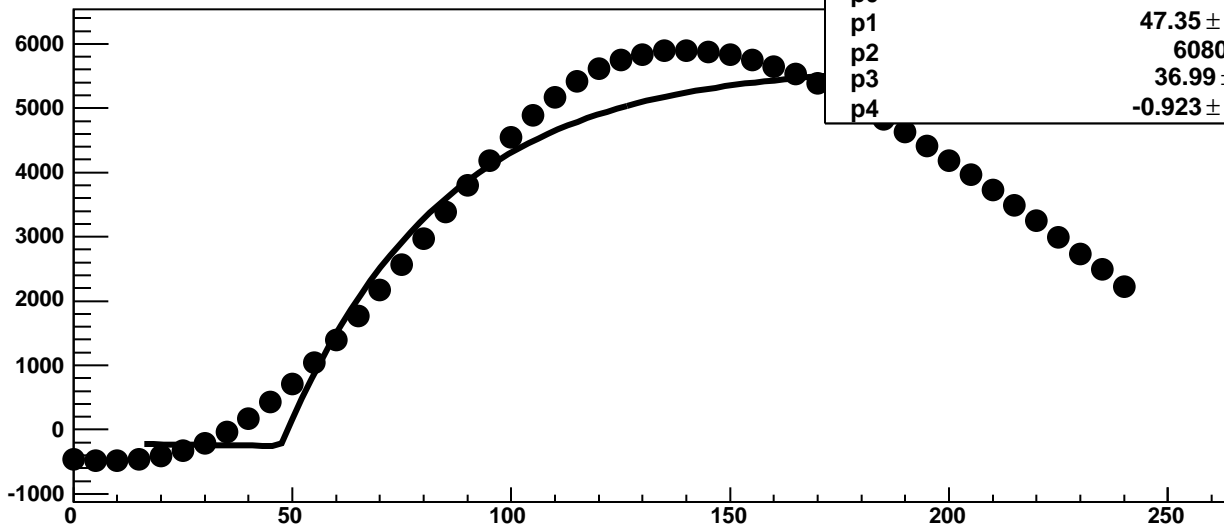
Chip 9, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold



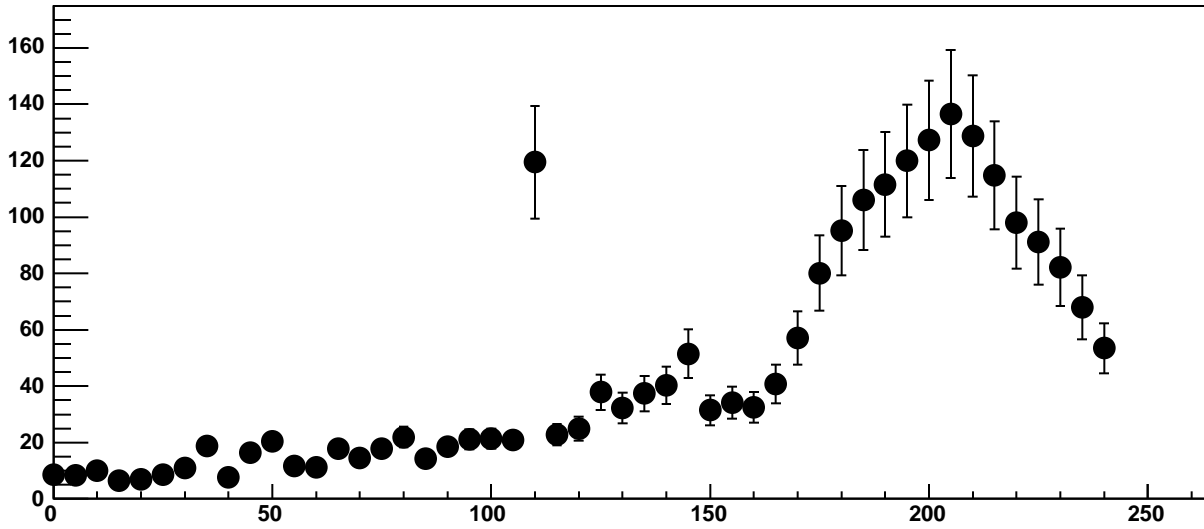
Chip 9, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold



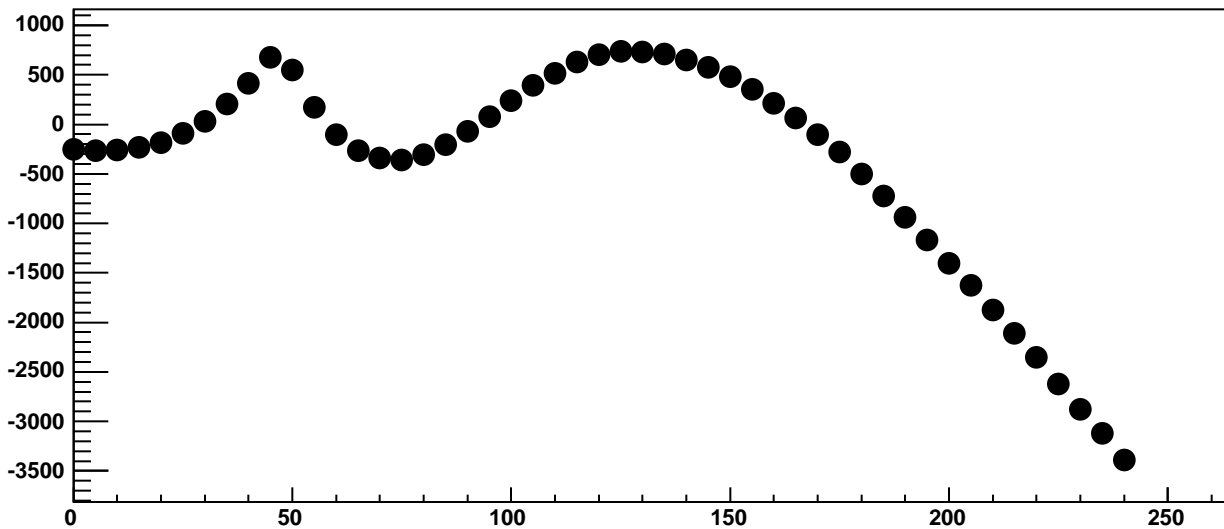
Chip 9, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold



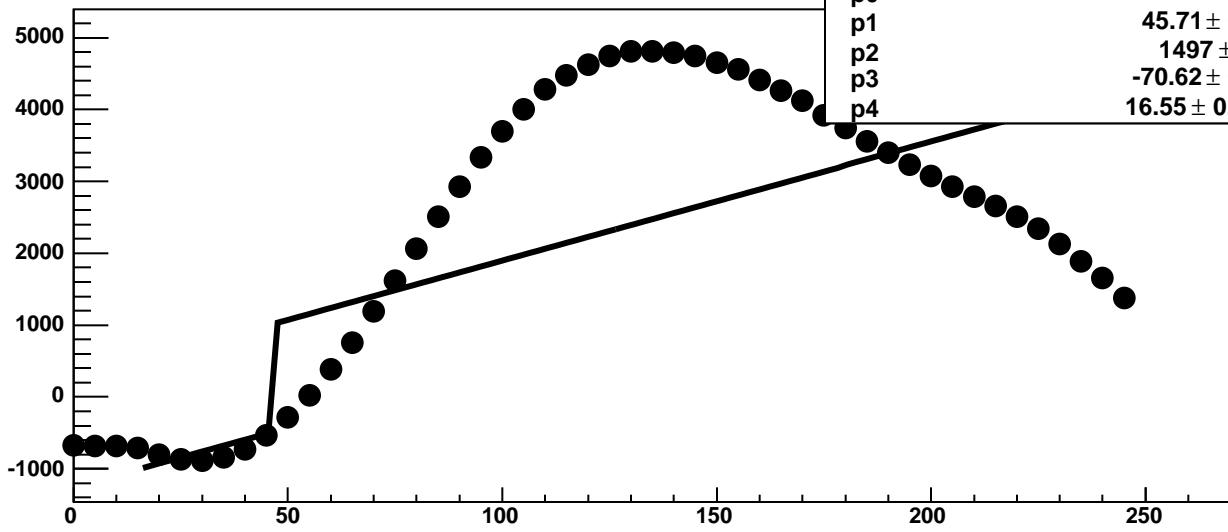
Chip 9, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold

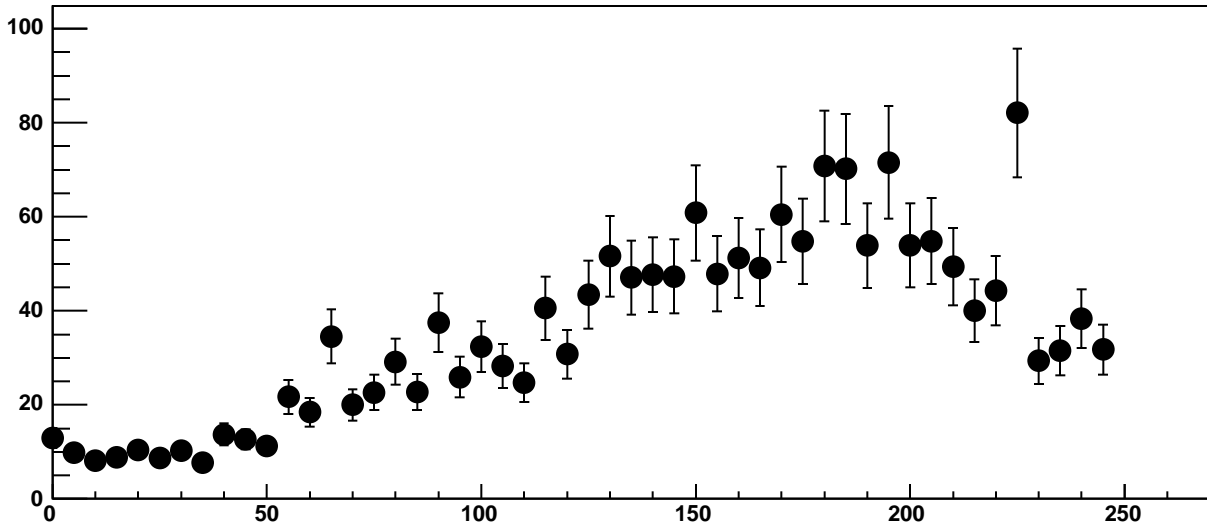


Chip 9, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold

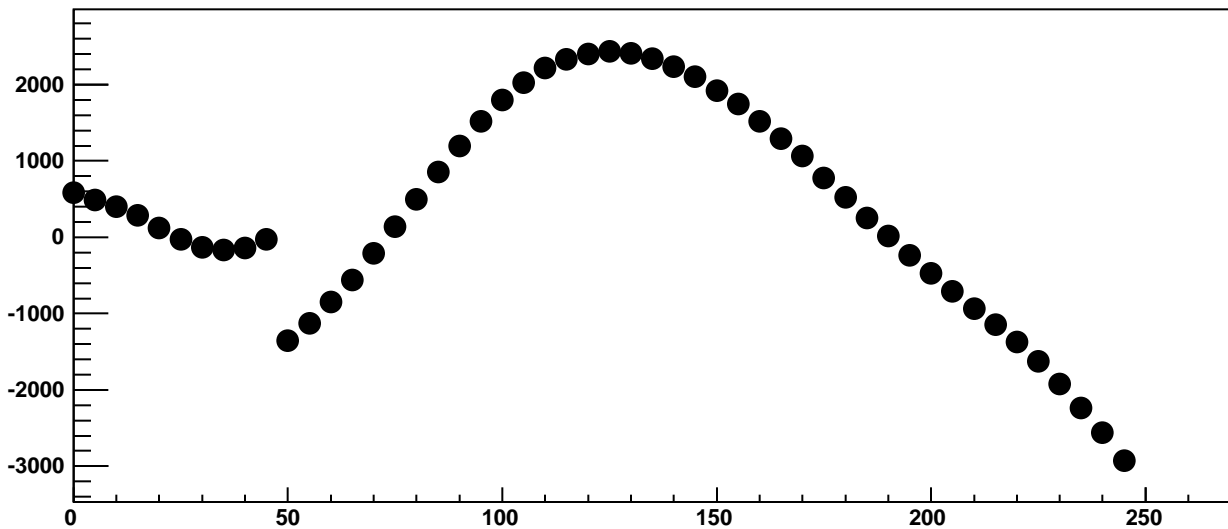


χ^2 / ndf	1.648e+06 / 41
p0	-496.4 ± 0.08897
p1	45.71 ± 0.01294
p2	1497 ± 0.1054
p3	-70.62 ± 0.01845
p4	16.55 ± 0.005537

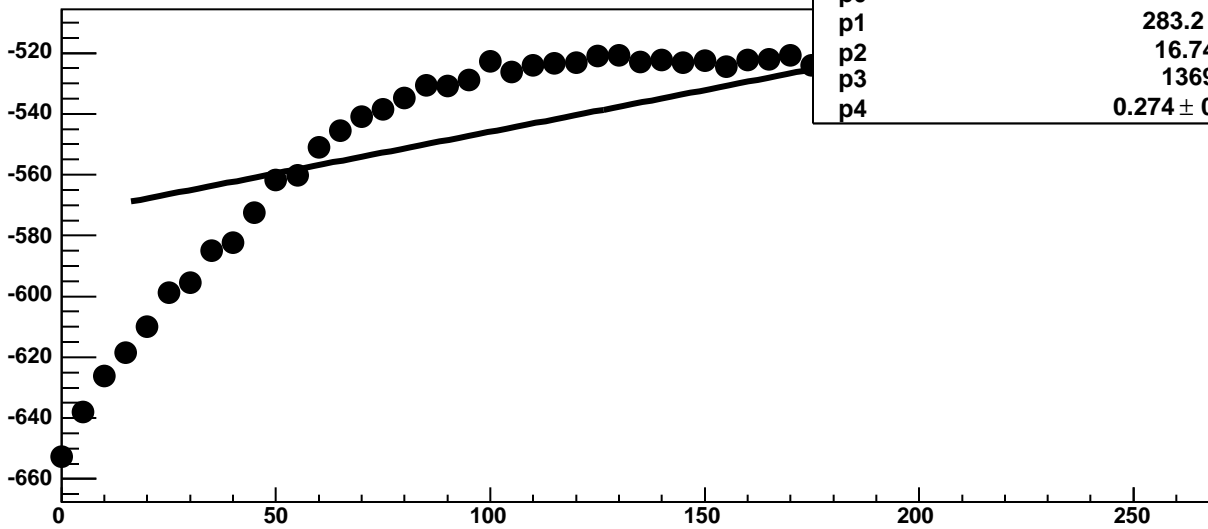
Chip 9, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



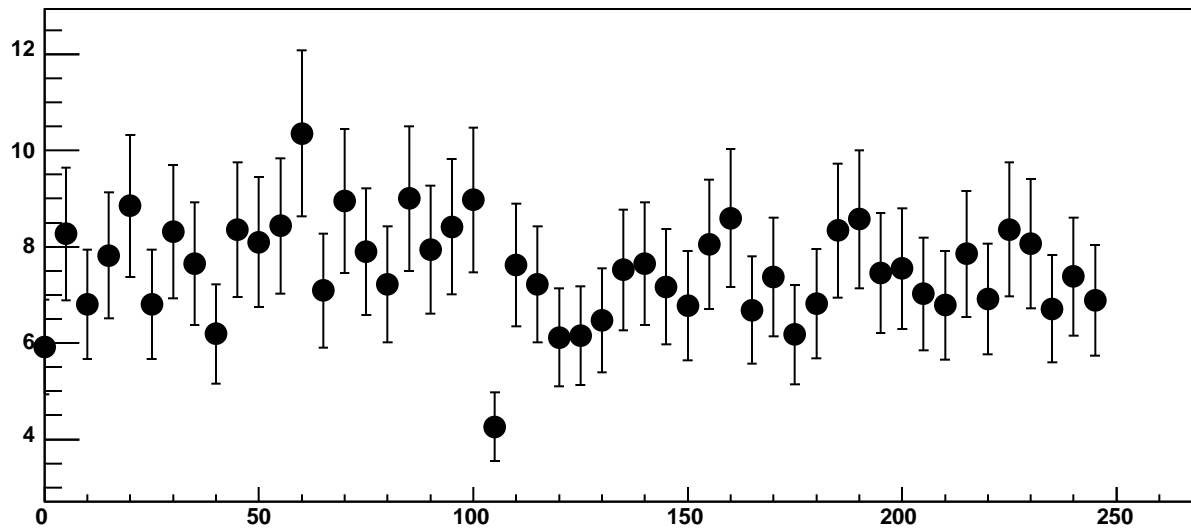
Chip 9, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold



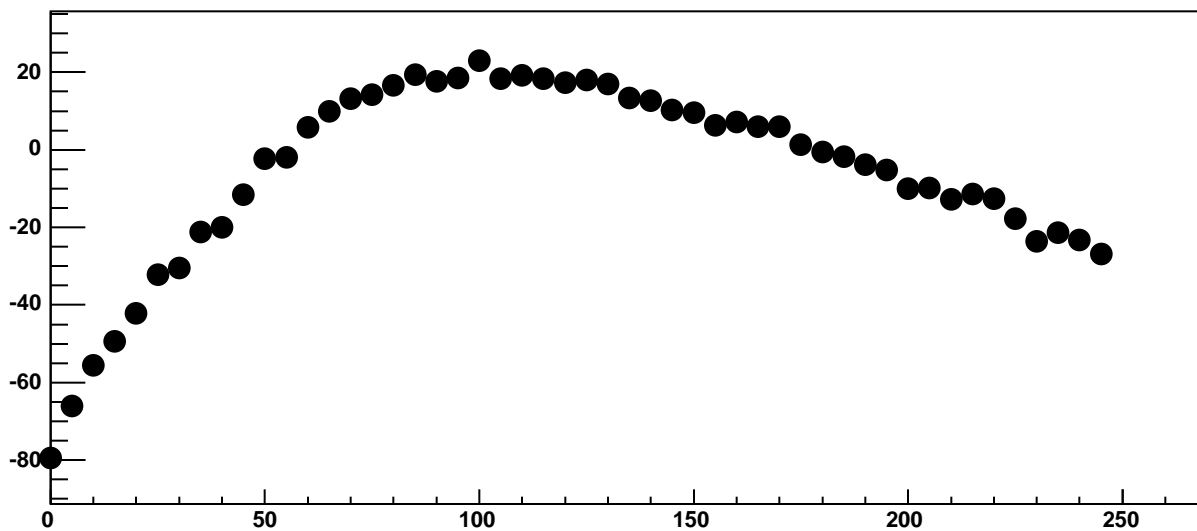
Chip 9, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold



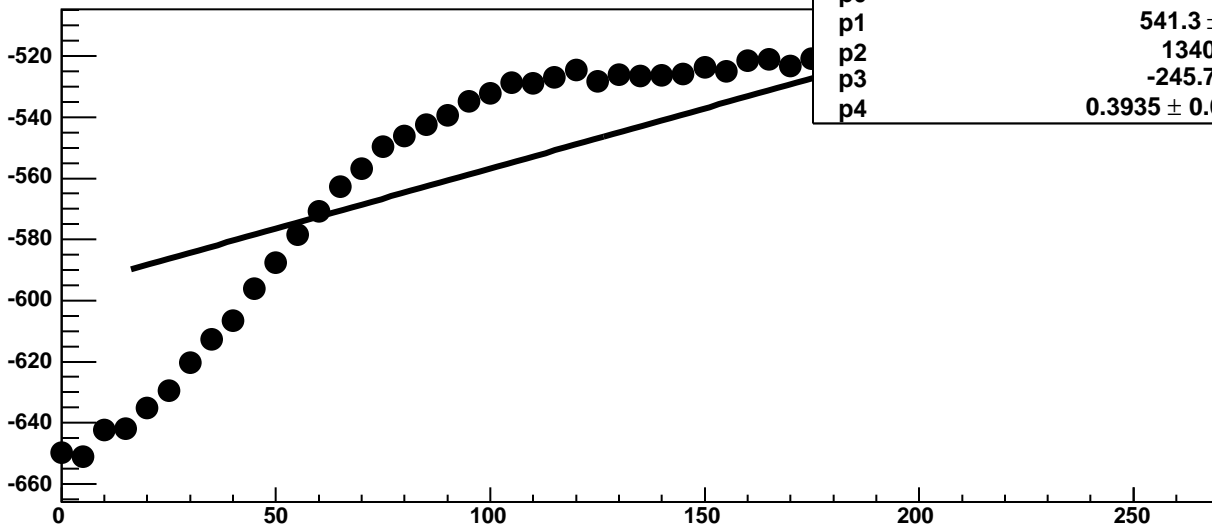
Chip 9, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold

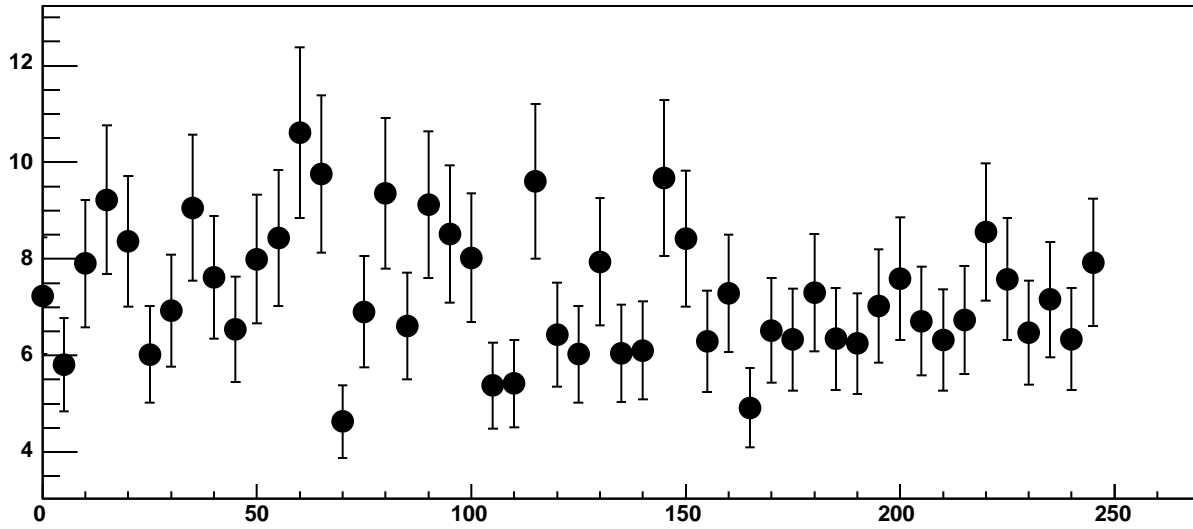


Chip 9, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

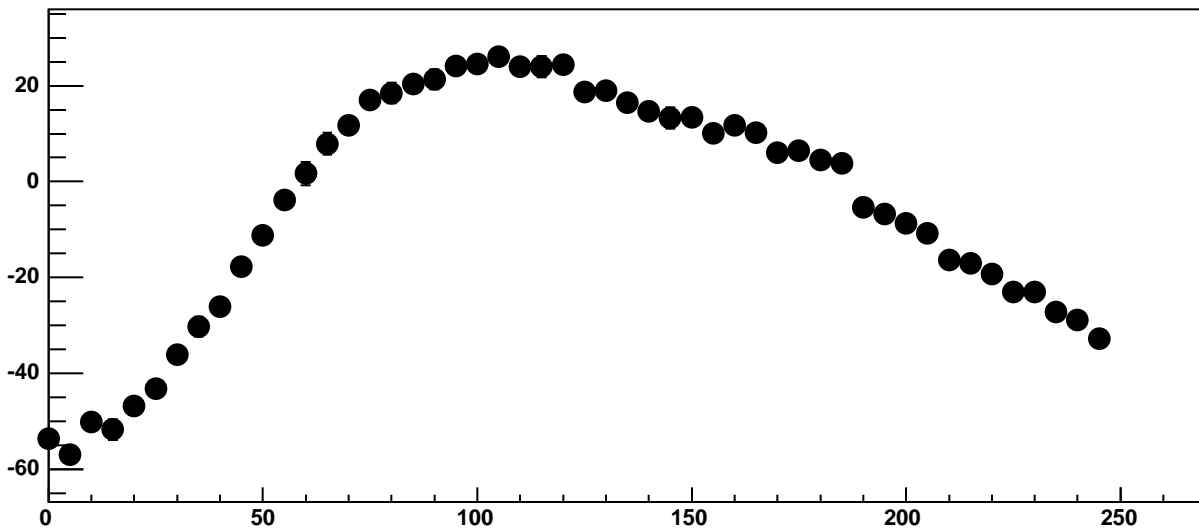


χ^2 / ndf	7885 / 41
p0	-383.1 ± 0.2343
p1	541.3 ± 0.5955
p2	1340 ± 84.85
p3	-245.7 ± 1.414
p4	0.3935 ± 0.0005658

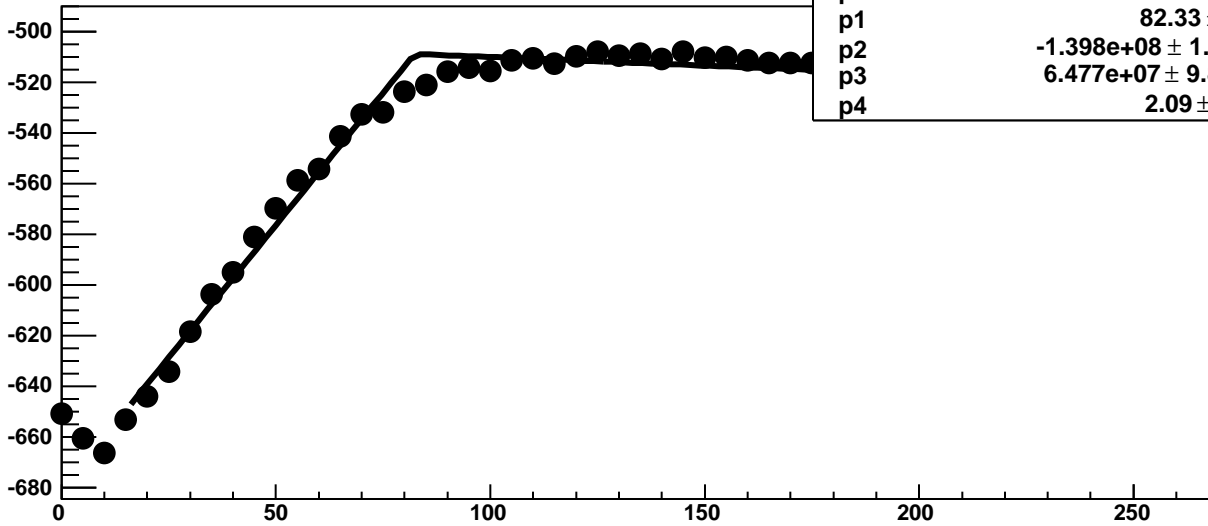
Chip 9, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

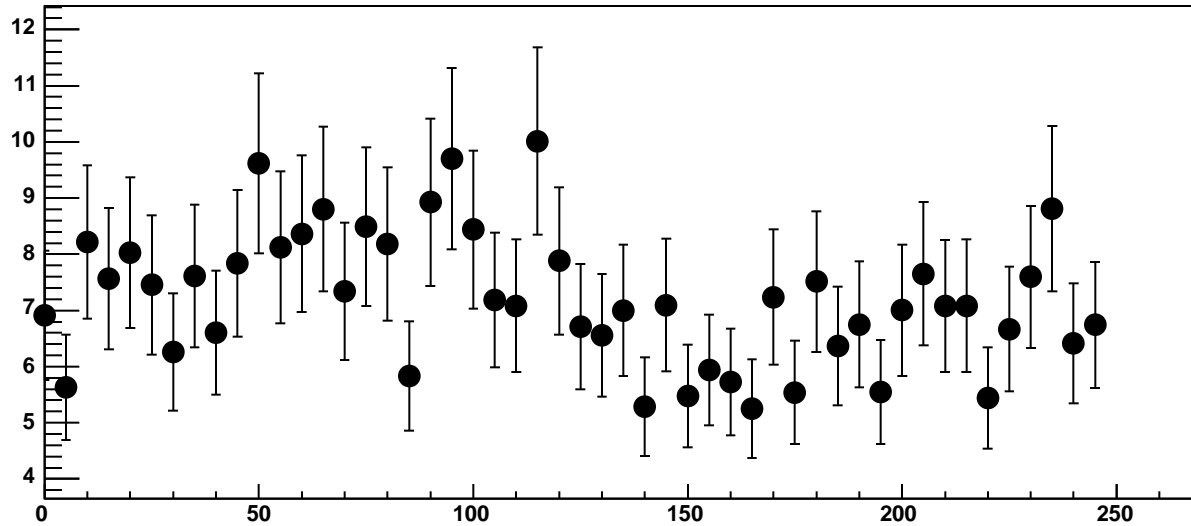


Chip 9, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

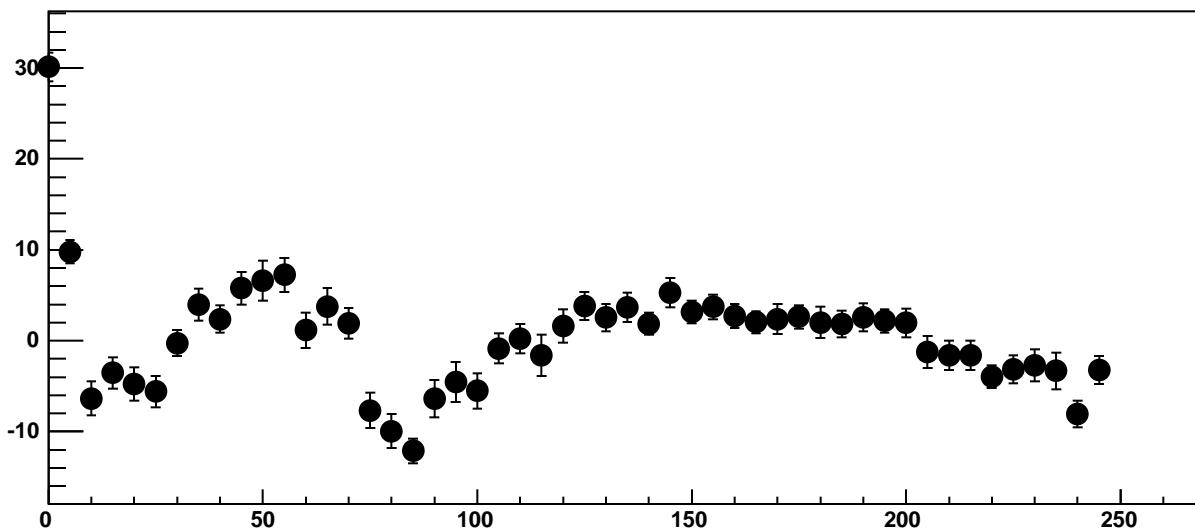


χ^2 / ndf	334.5 / 41
p0	-508.7 ± 0.5669
p1	82.33 ± 0.5316
p2	$-1.398\text{e}+08 \pm 1.596\text{e}+06$
p3	$6.477\text{e}+07 \pm 9.876\text{e}+04$
p4	2.09 ± 0.02371

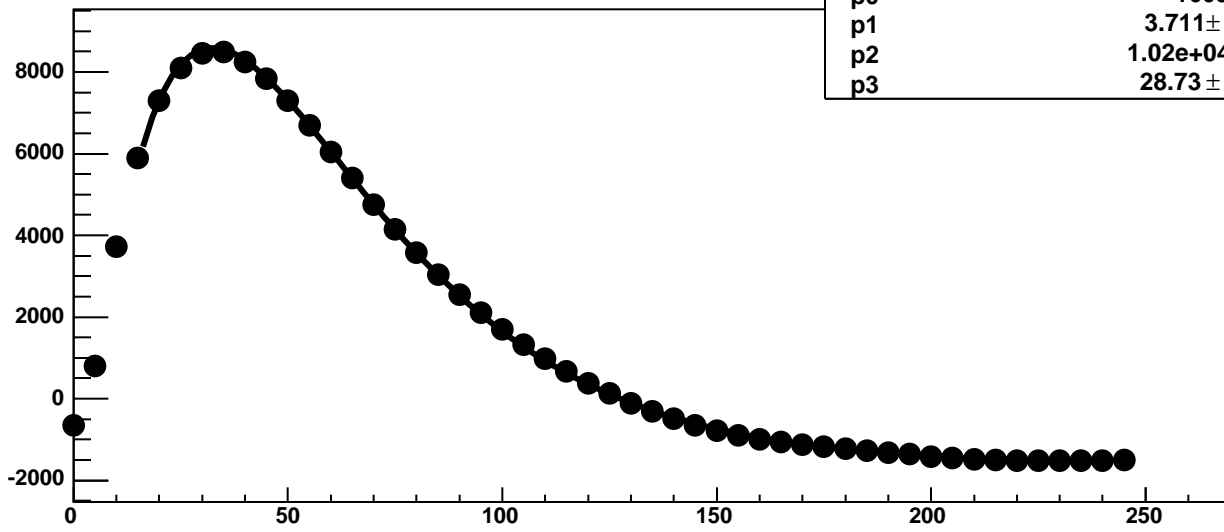
Chip 9, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

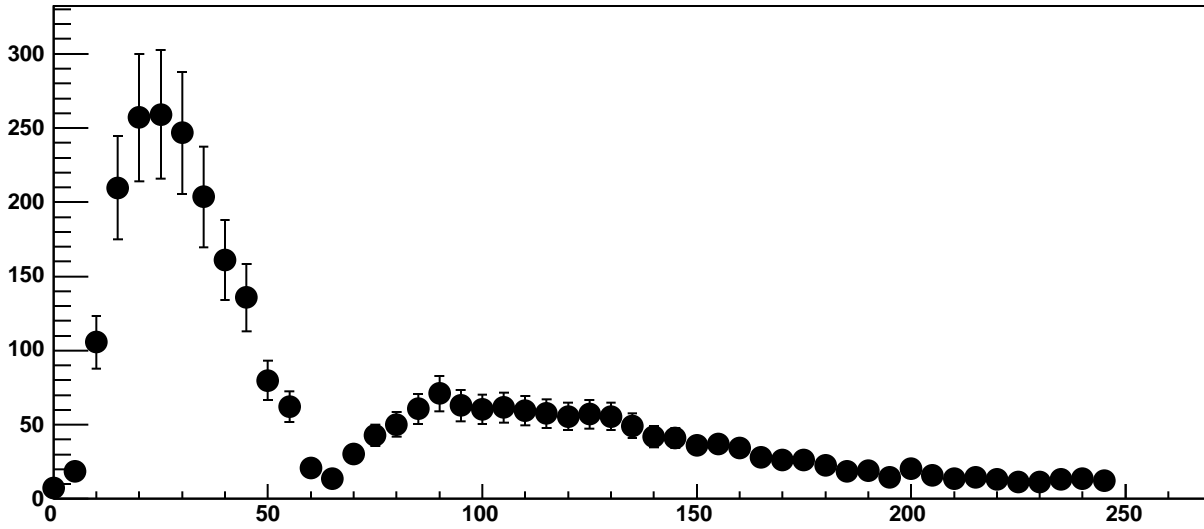


Chip 9, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold

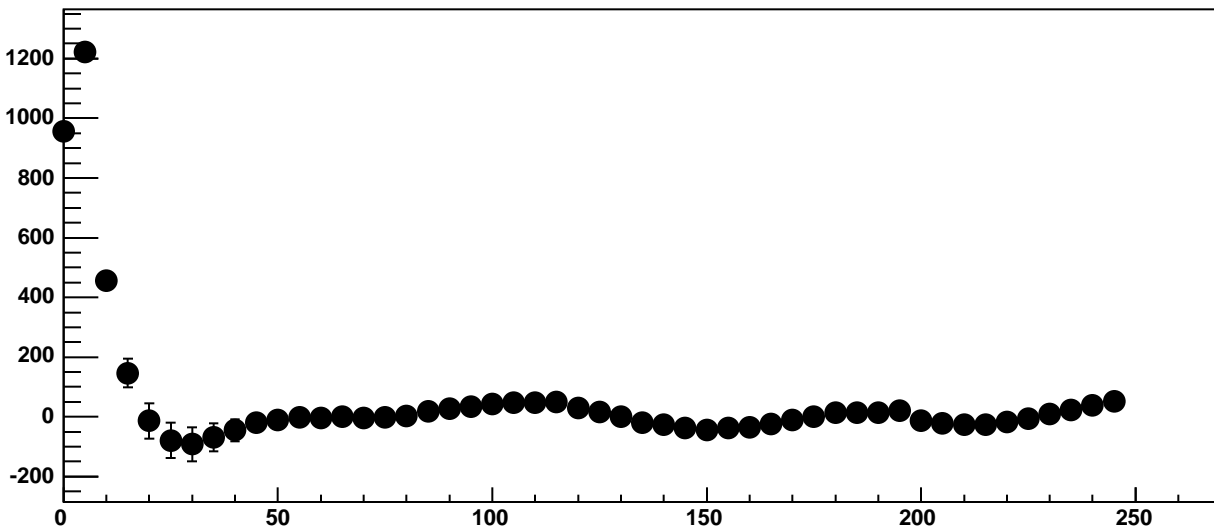


χ^2 / ndf	691.8 / 42
p0	-1609 ± 1.304
p1	3.711 ± 0.08615
p2	$1.02\text{e}+04 \pm 11.39$
p3	28.73 ± 0.03167

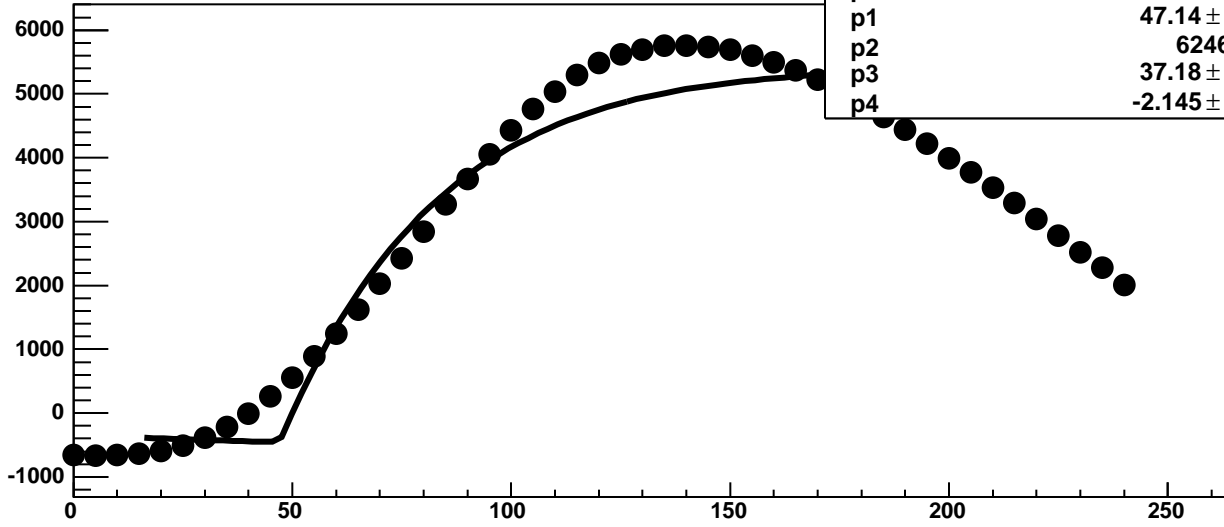
Chip 9, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold

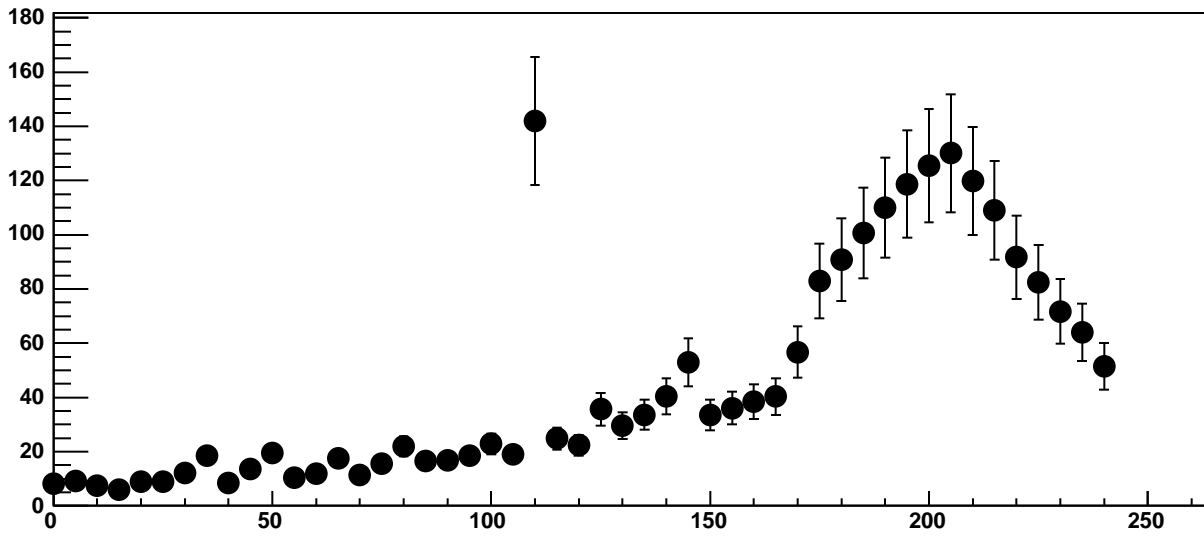


Chip 9, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

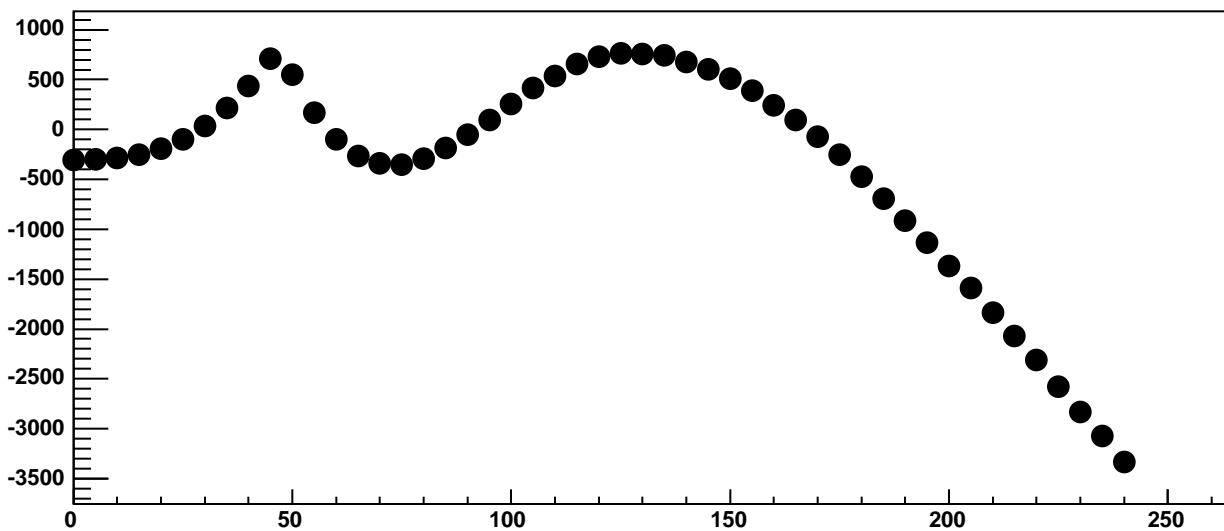


χ^2 / ndf	5.068e+05 / 41
p0	-456.8 ± 1.387
p1	47.14 ± 0.01829
p2	6246 ± 8.641
p3	37.18 ± 0.05945
p4	-2.145 ± 0.05323

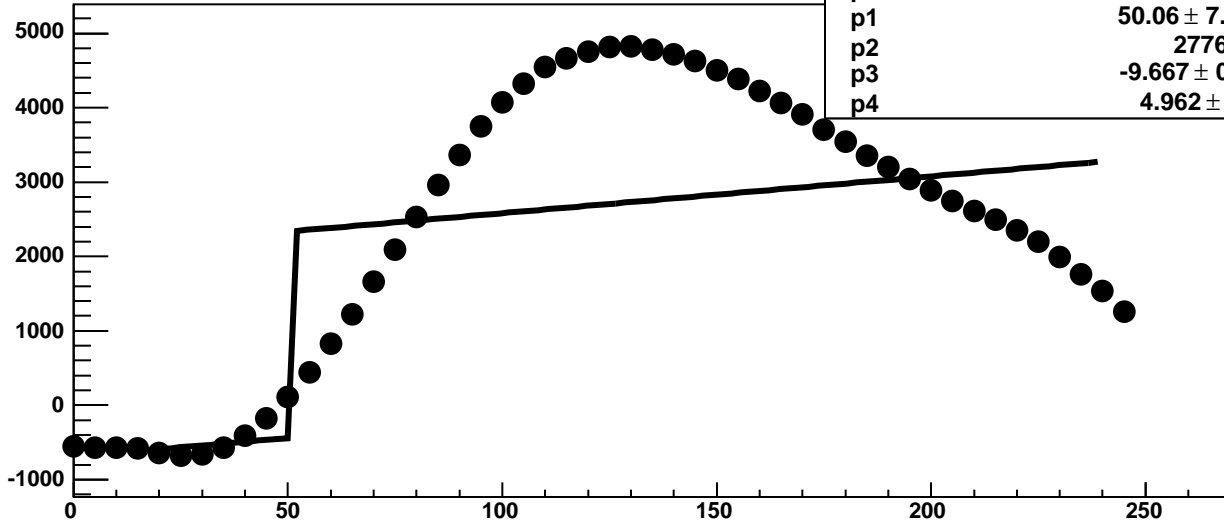
Chip 9, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



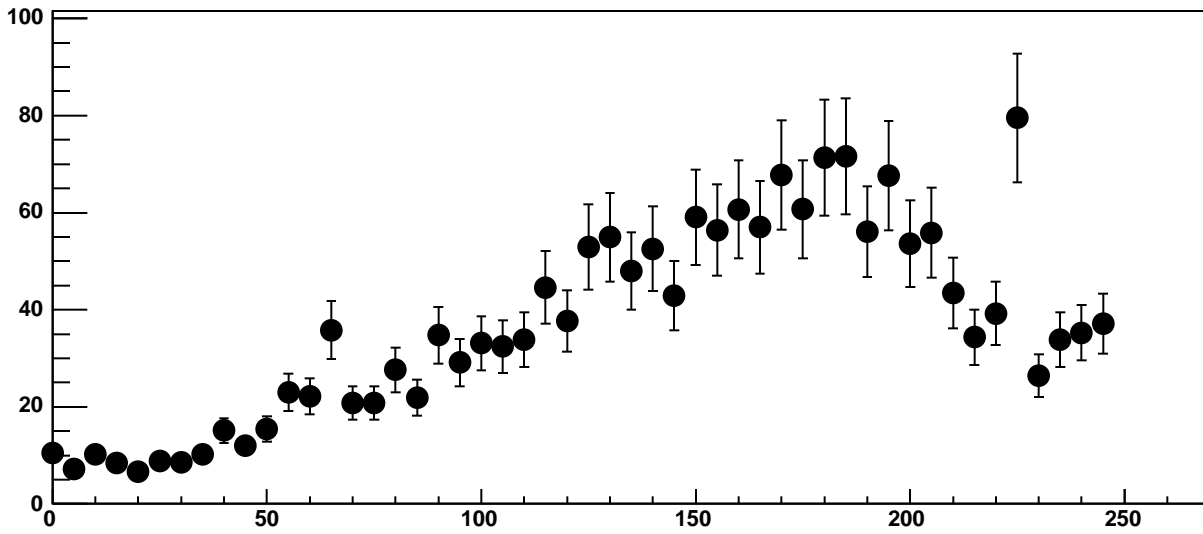
Chip 9, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold



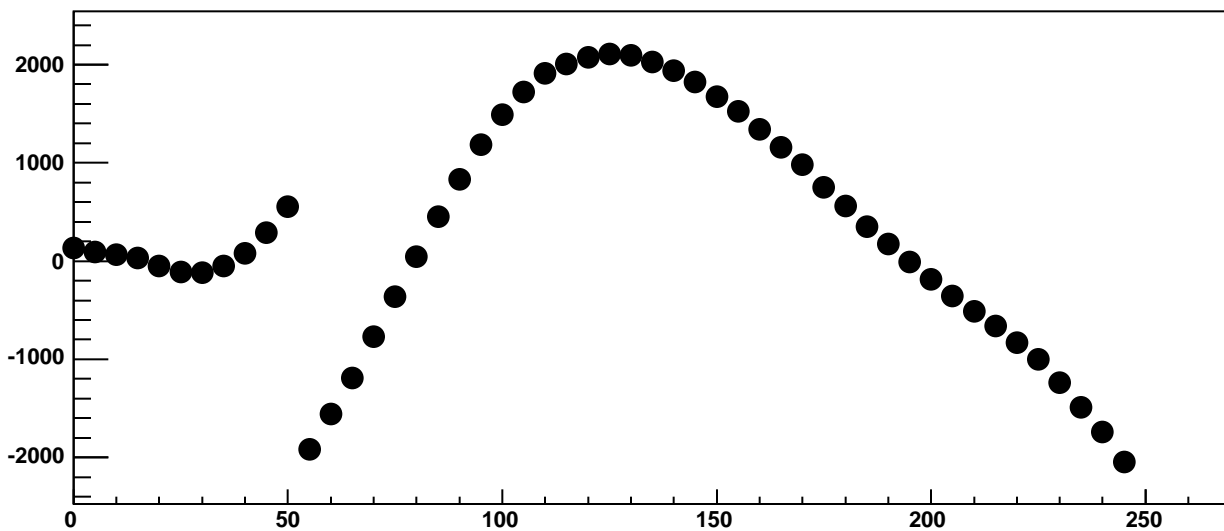
Chip 9, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold



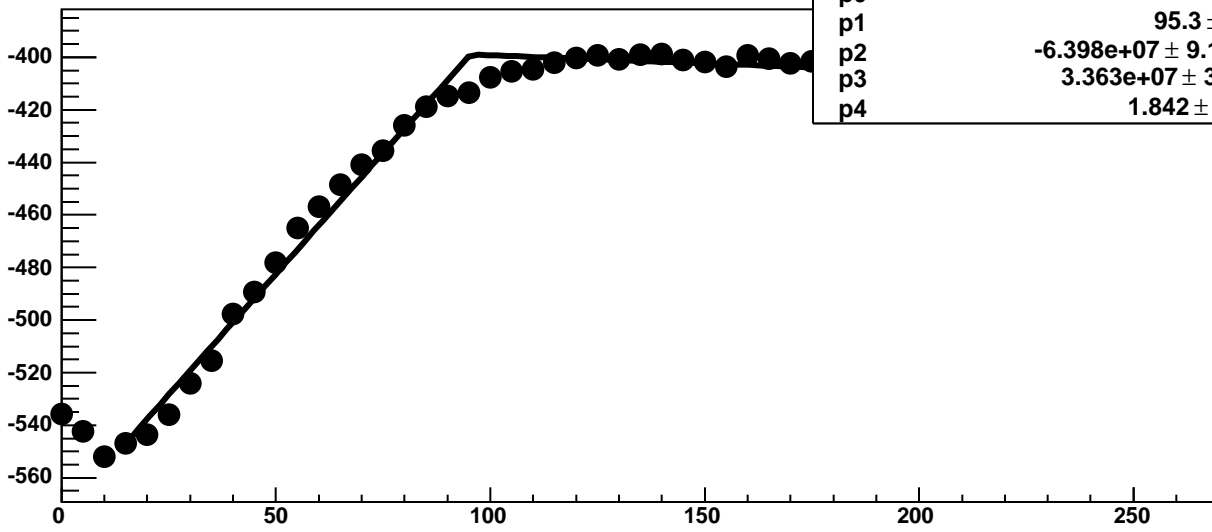
Chip 9, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold

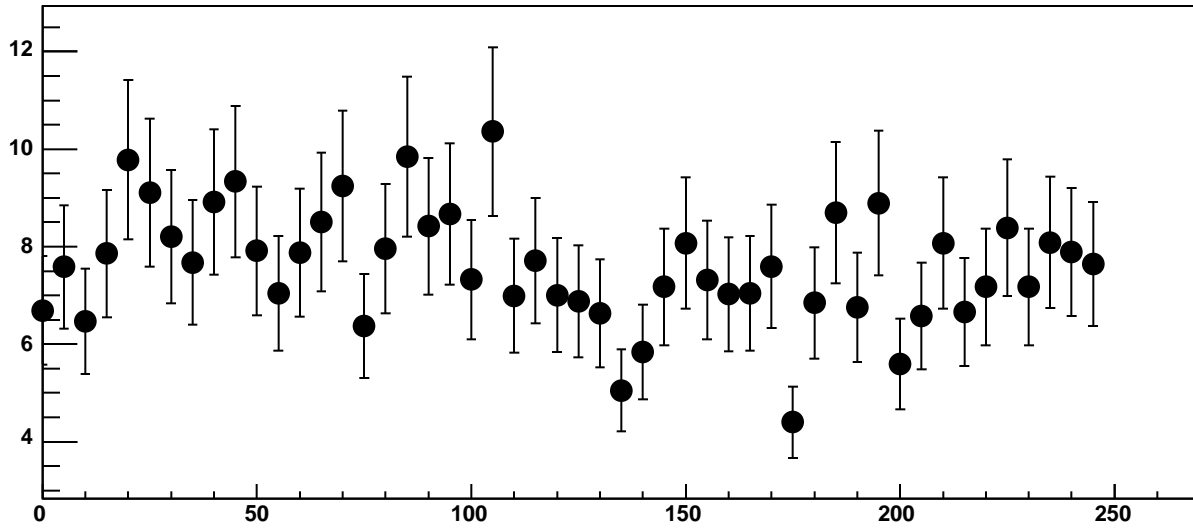


Chip 9, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold

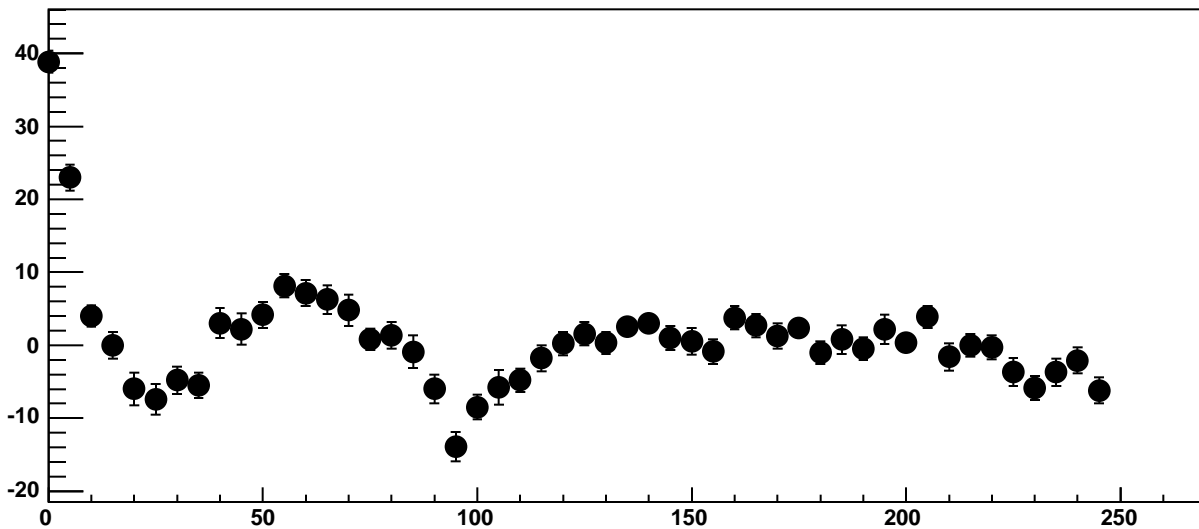


χ^2 / ndf	258.3 / 41
p0	-399 ± 0.6042
p1	95.3 ± 0.5726
p2	$-6.398\text{e}+07 \pm 9.121\text{e}+05$
p3	$3.363\text{e}+07 \pm 3.11\text{e}+05$
p4	1.842 ± 0.01928

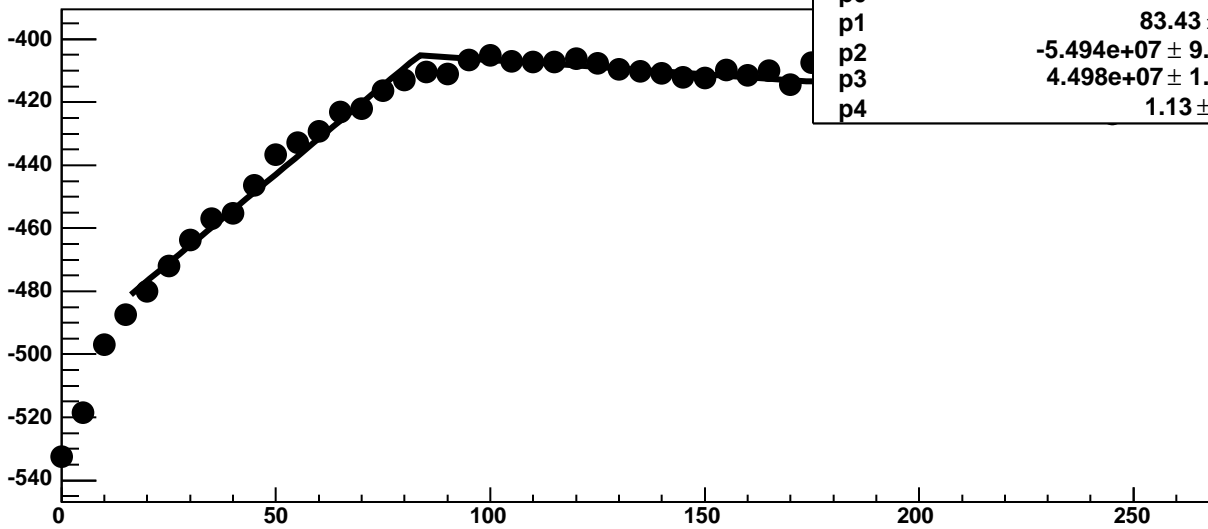
Chip 9, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold

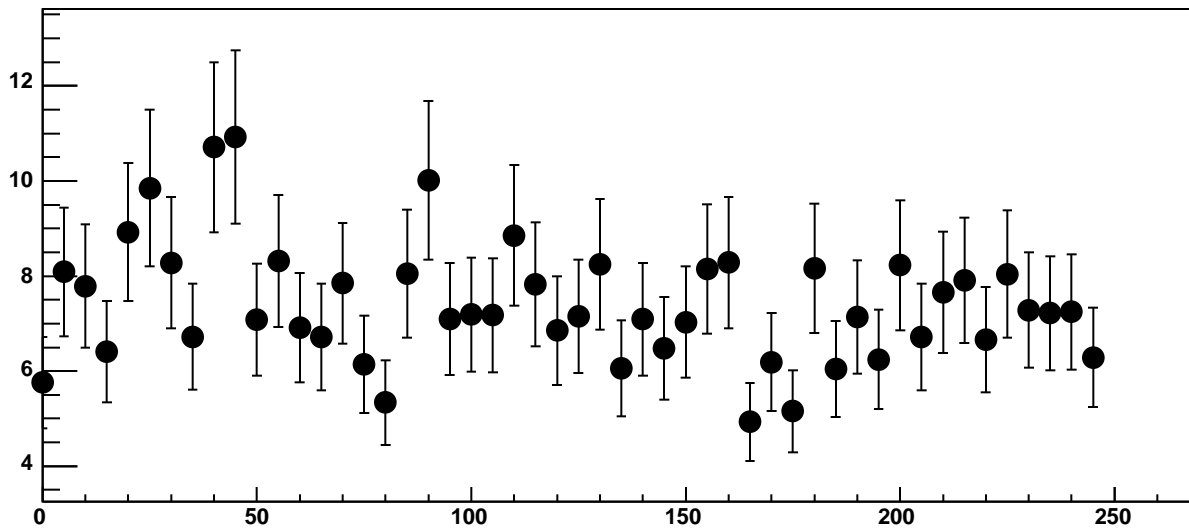


Chip 9, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold

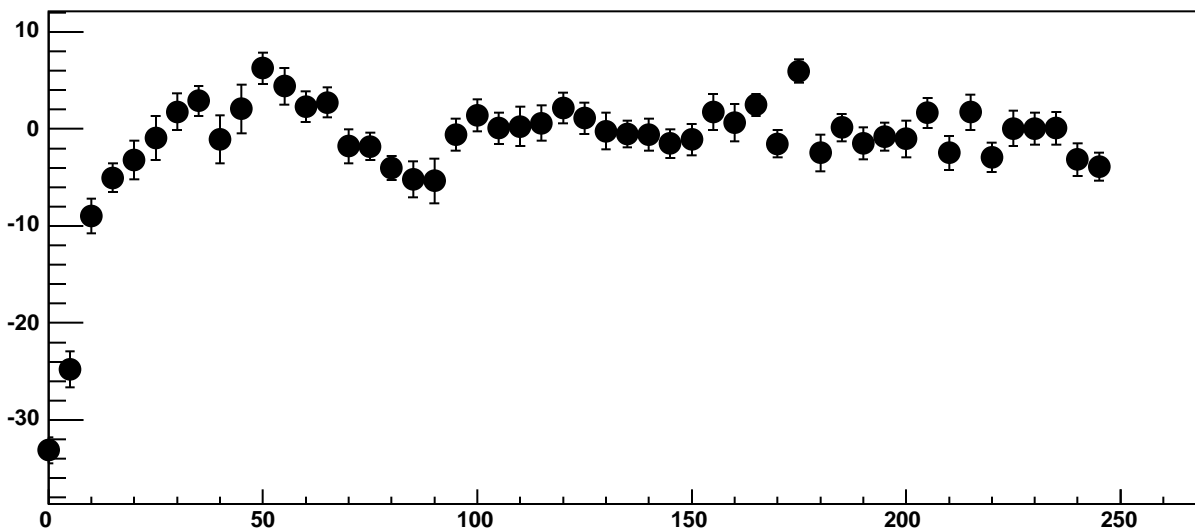


χ^2 / ndf	123.7 / 41
p0	-405 ± 0.7666
p1	83.43 ± 0.5244
p2	-5.494e+07 ± 9.611e+05
p3	4.498e+07 ± 1.743e+06
p4	1.13 ± 0.01741

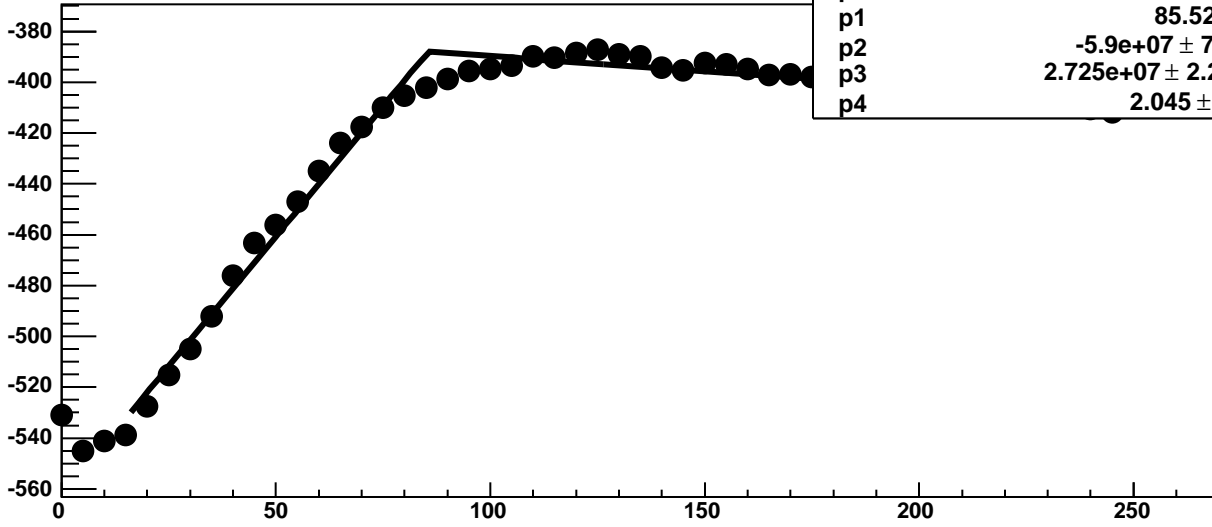
Chip 9, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold

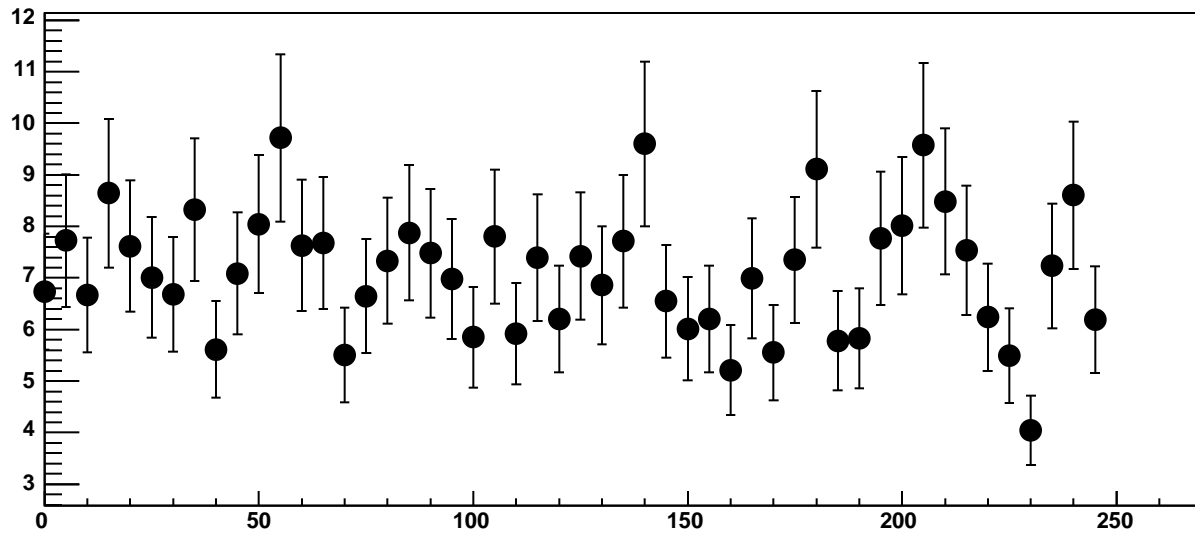


Chip 9, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

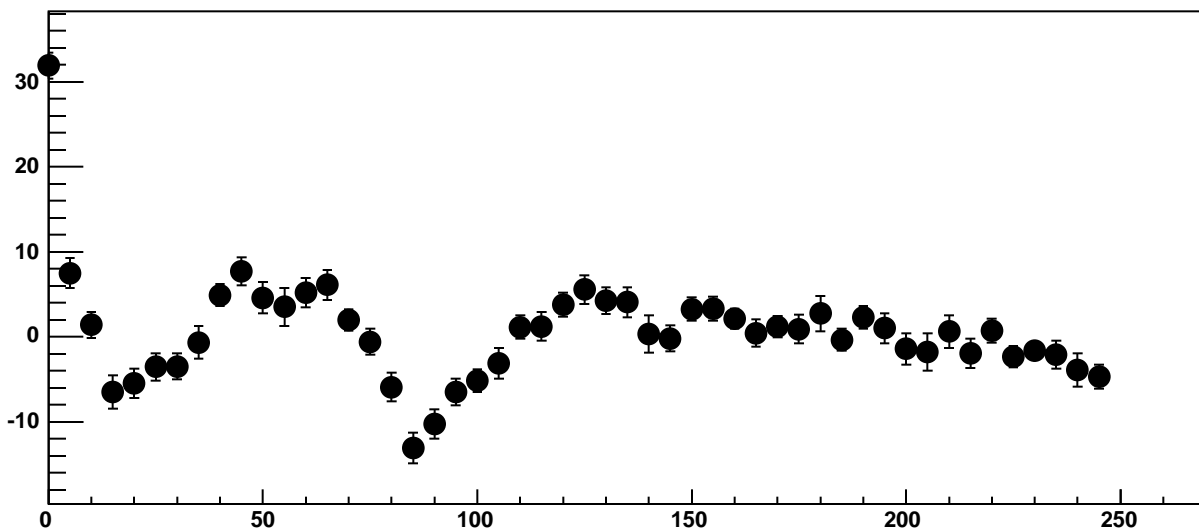


χ^2 / ndf	301.6 / 41
p0	-388 ± 0.53
p1	85.52 ± 0.458
p2	$-5.9\text{e}+07 \pm 7.12\text{e}+05$
p3	$2.725\text{e}+07 \pm 2.279\text{e}+05$
p4	2.045 ± 0.02001

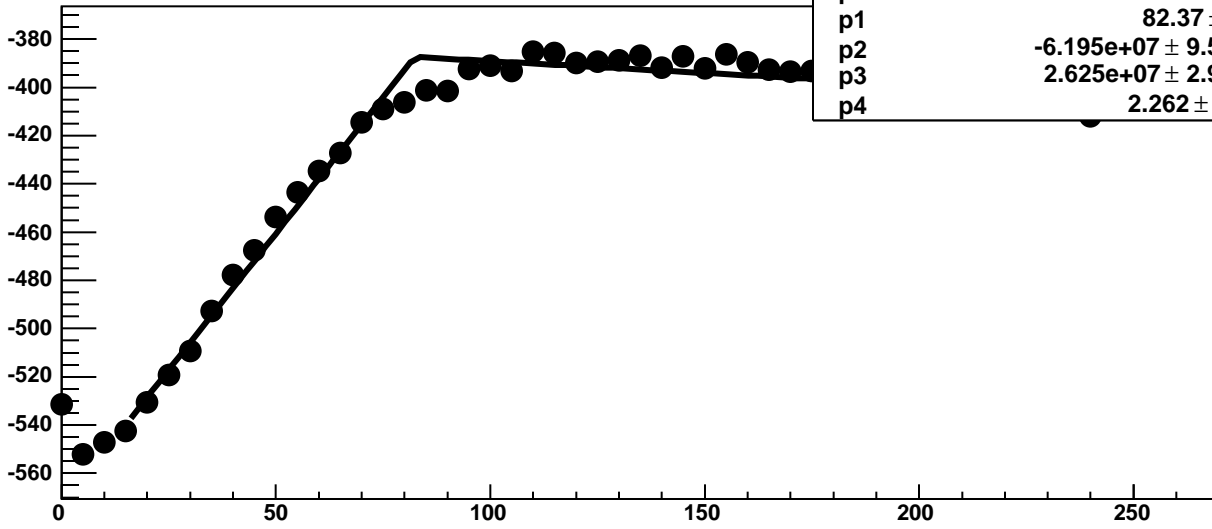
Chip 9, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

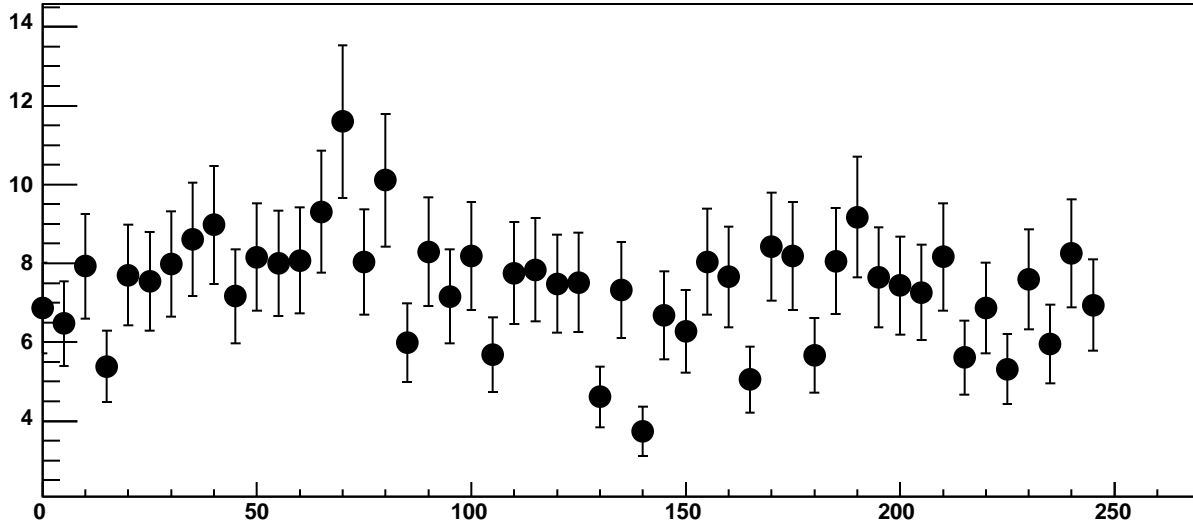


Chip 9, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

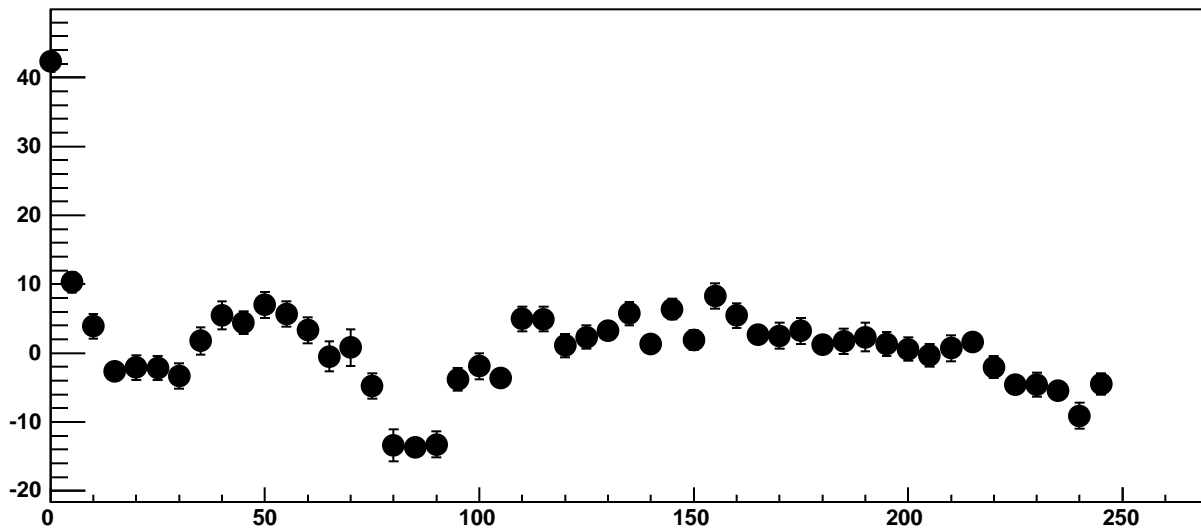


χ^2 / ndf	421.5 / 41
p0	-387.4 ± 0.5232
p1	82.37 ± 0.5048
p2	$-6.195e+07 \pm 9.574e+05$
p3	$2.625e+07 \pm 2.922e+05$
p4	2.262 ± 0.02375

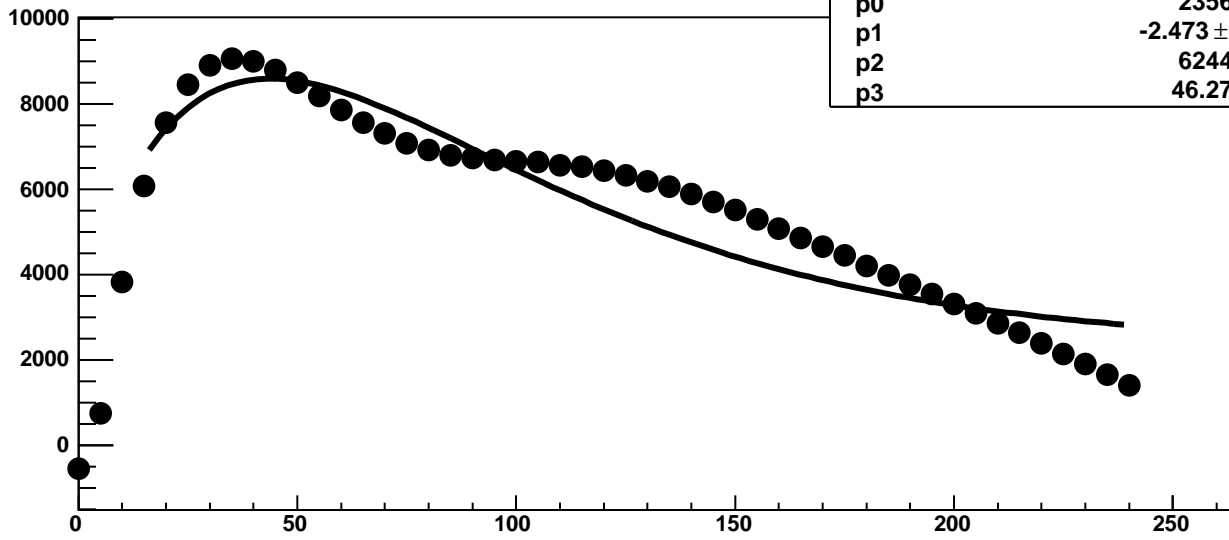
Chip 9, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

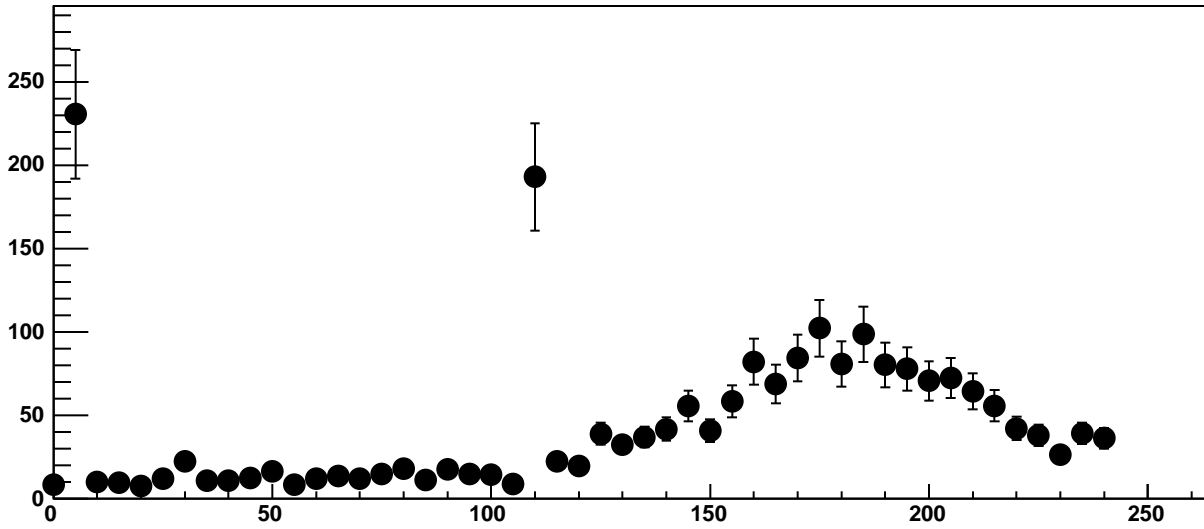


Chip 9, Channel 6, Enable 5!, DAC=4000, ADC Mean vs Hold

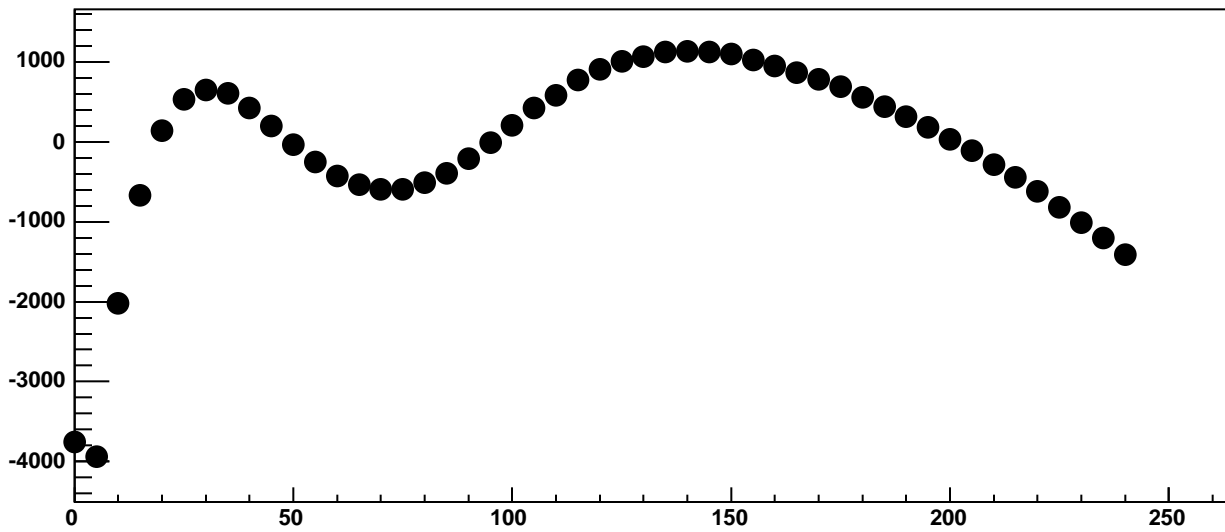


χ^2 / ndf	7.193e+05 / 42
p0	2356 ± 5.605
p1	-2.473 ± 0.03761
p2	6244 ± 5.189
p3	46.27 ± 0.045

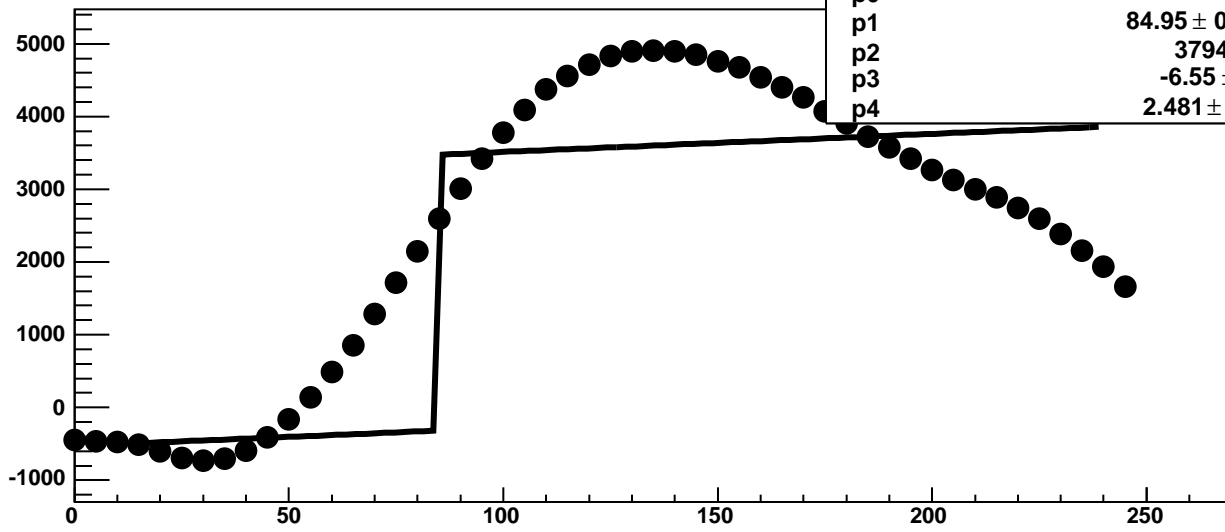
Chip 9, Channel 6, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 6, Enable 5!, DAC=4000, ADC Residuals vs Hold

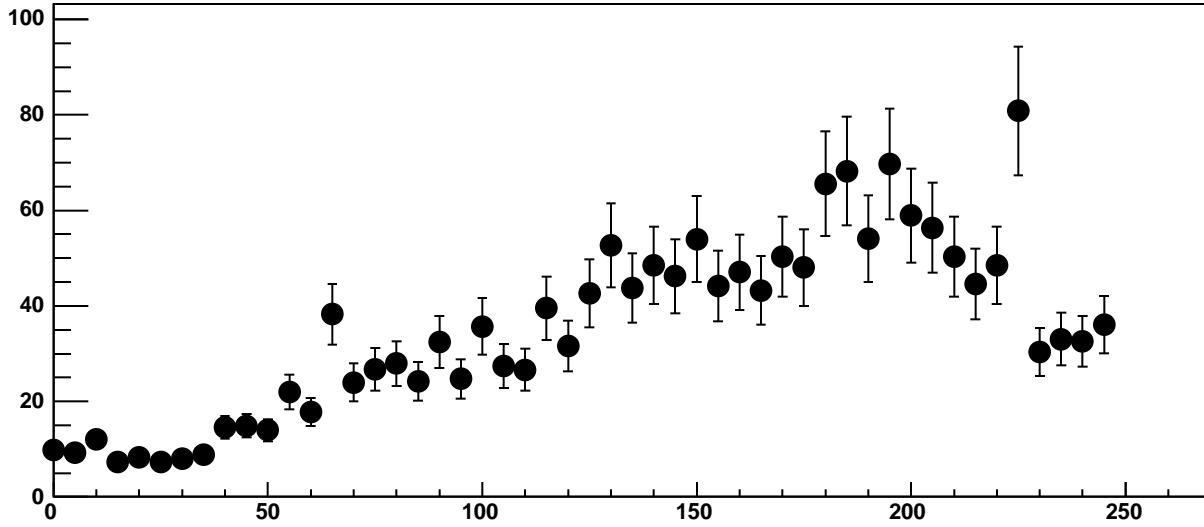


Chip 9, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

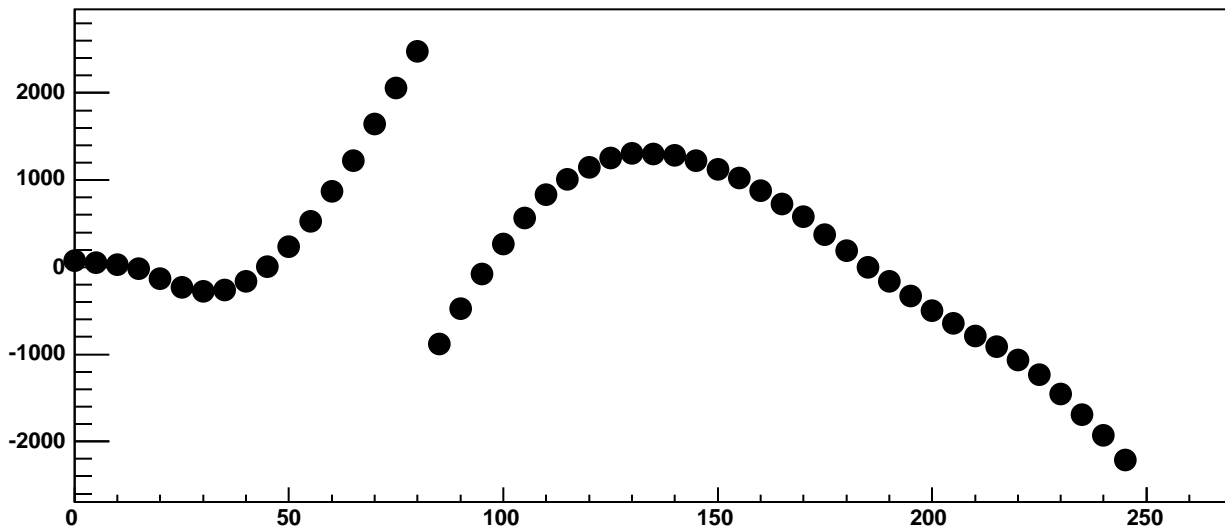


χ^2 / ndf	8.91e+05 / 41
p0	-317.6 ± 7.673
p1	84.95 ± 0.004294
p2	3794 ± 3.539
p3	-6.55 ± 0.4692
p4	2.481 ± 0.08073

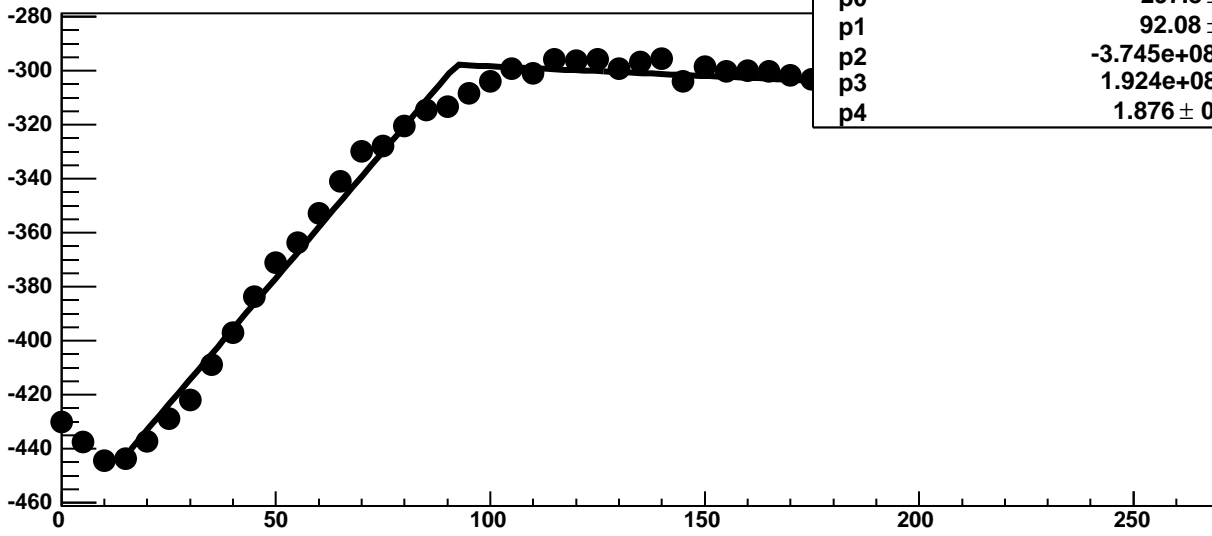
Chip 9, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

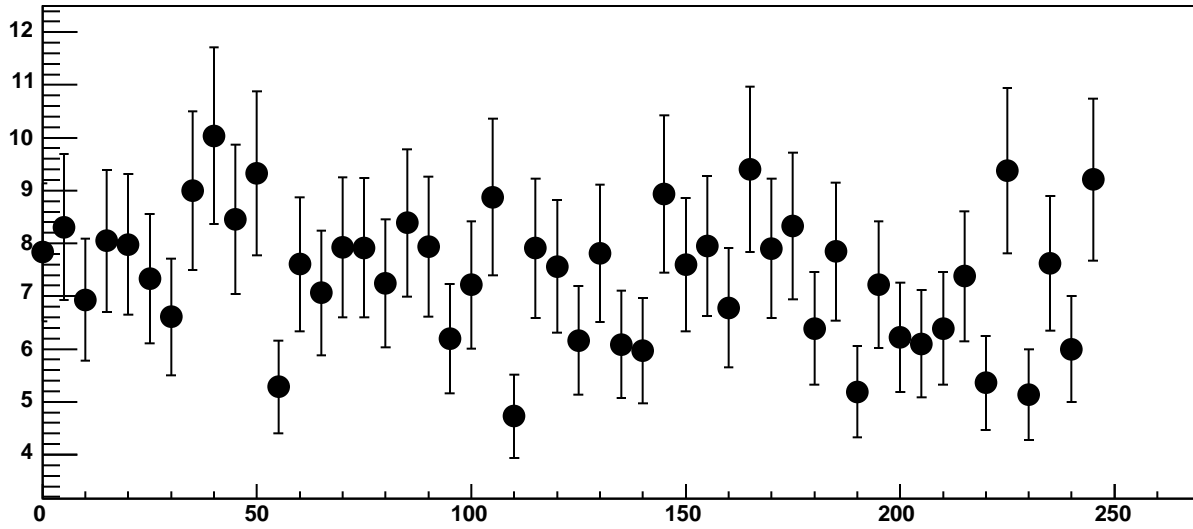


Chip 9, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold

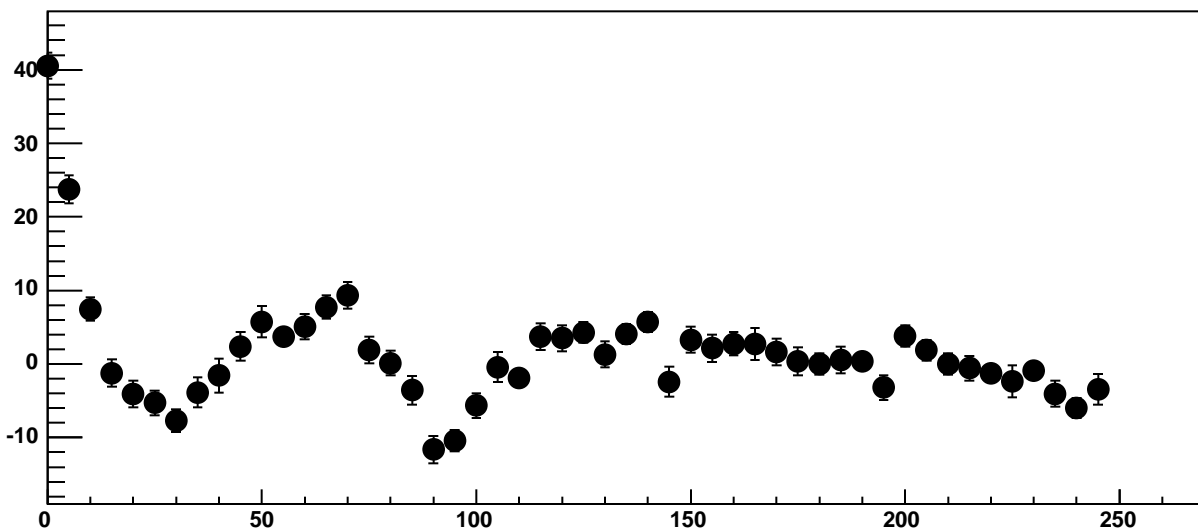


χ^2 / ndf	330 / 41
p0	-297.8 ± 0.5224
p1	92.08 ± 0.4575
p2	$-3.745\text{e}+08 \pm 1.414$
p3	$1.924\text{e}+08 \pm 1.414$
p4	1.876 ± 0.006067

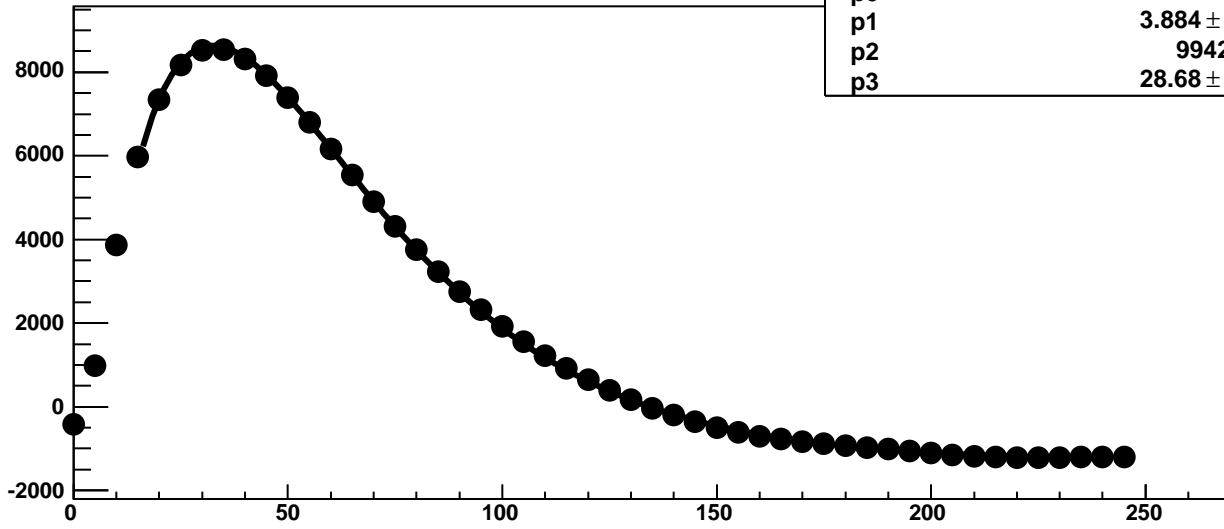
Chip 9, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

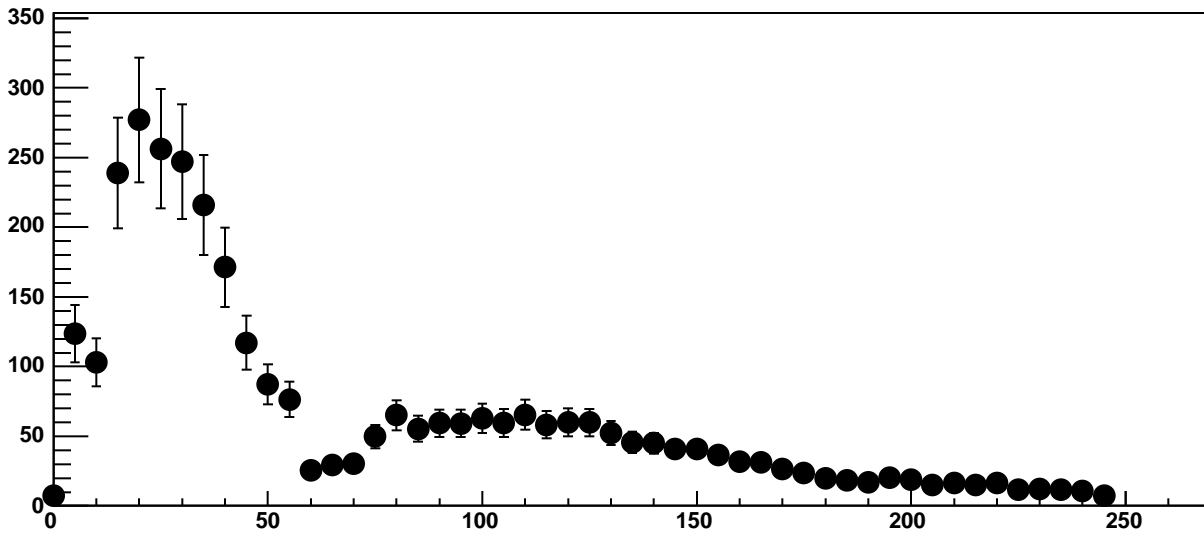


Chip 9, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold

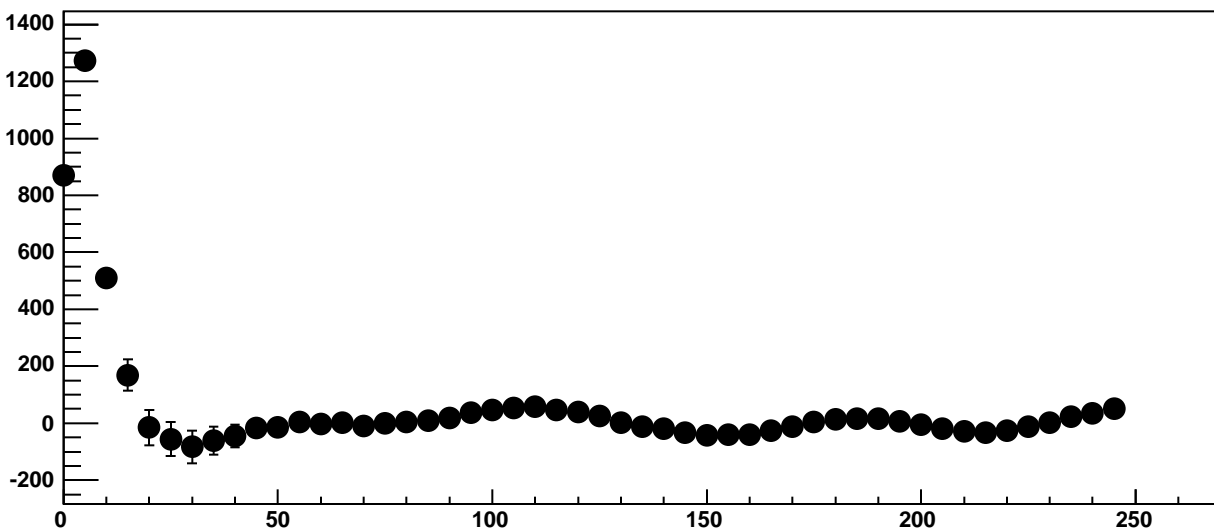


χ^2 / ndf	780.6 / 42
p0	-1300 ± 1.299
p1	3.884 ± 0.09572
p2	9942 ± 12.31
p3	28.68 ± 0.03352

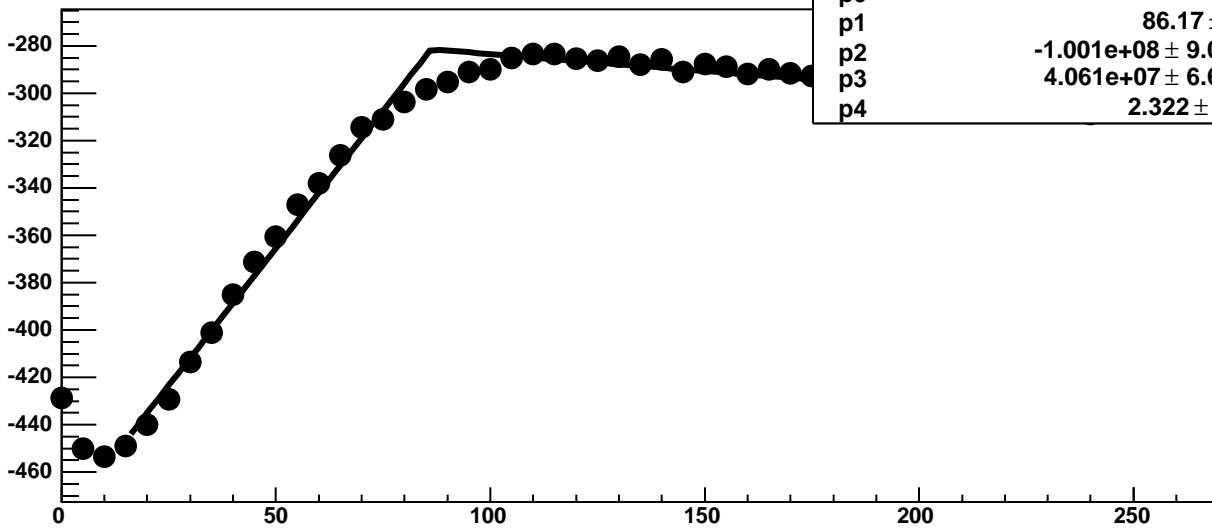
Chip 9, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold

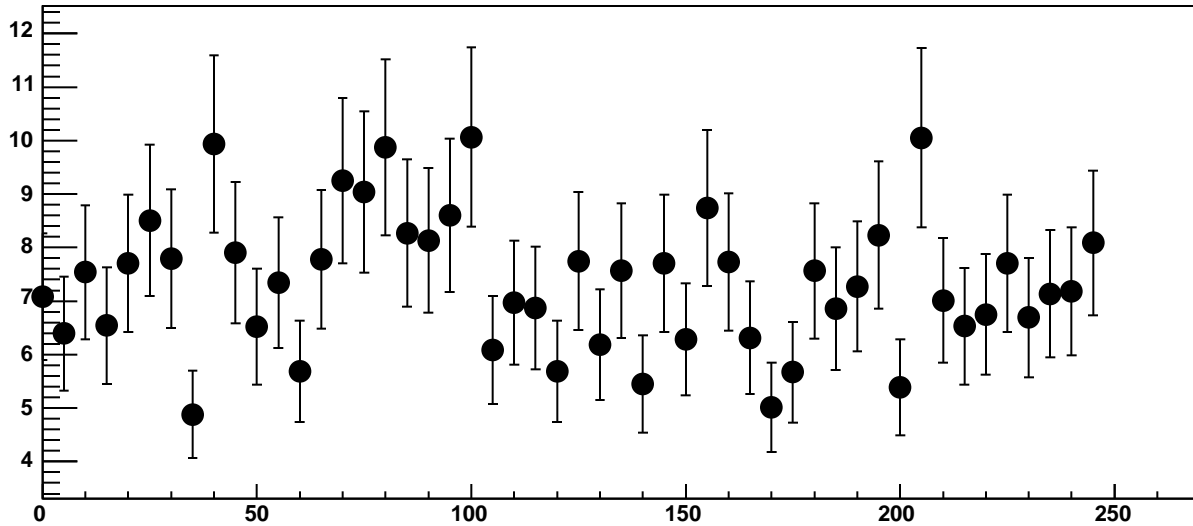


Chip 9, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

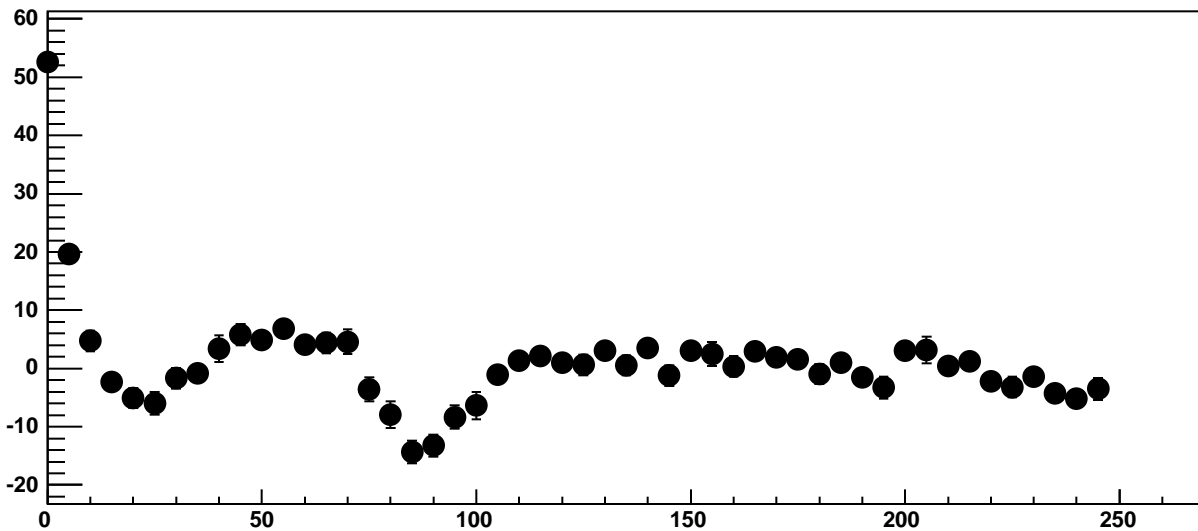


χ^2 / ndf	297.1 / 41
p0	-281.4 ± 0.5626
p1	86.17 ± 0.4518
p2	$-1.001\text{e}+08 \pm 9.035\text{e}+05$
p3	$4.061\text{e}+07 \pm 6.609\text{e}+04$
p4	2.322 ± 0.02145

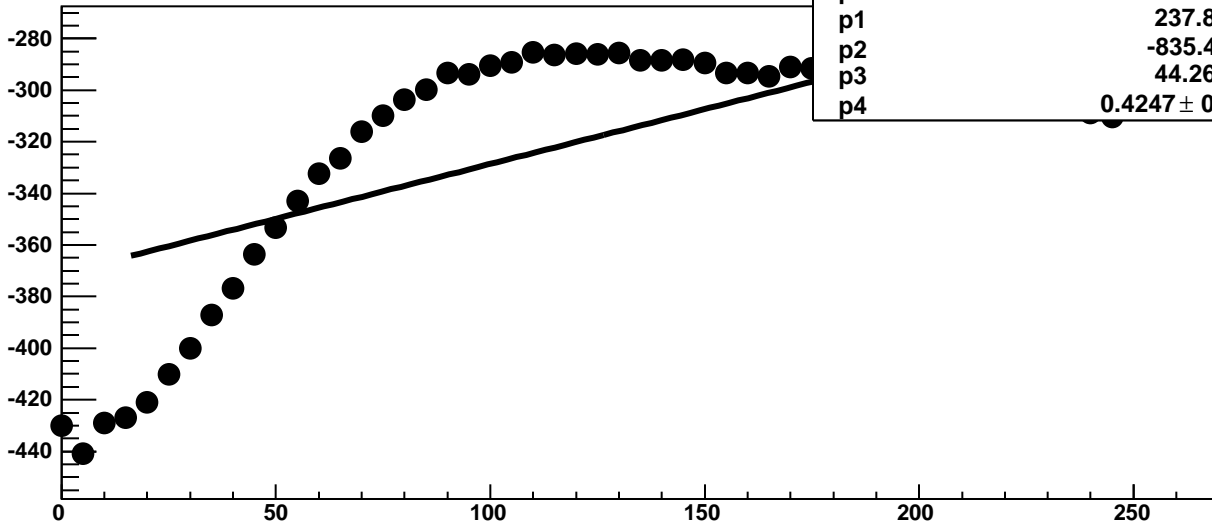
Chip 9, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

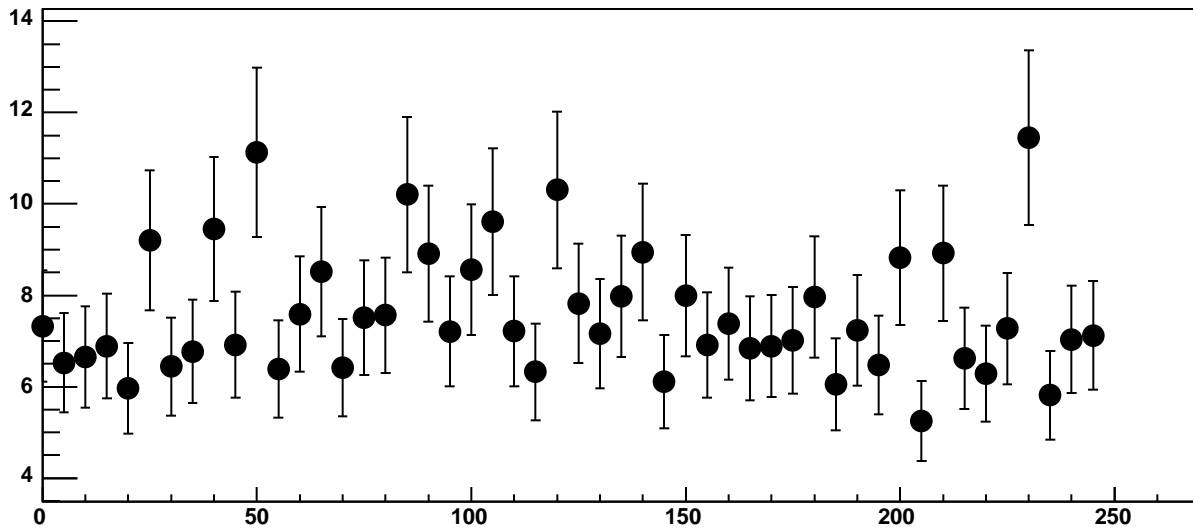


Chip 9, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

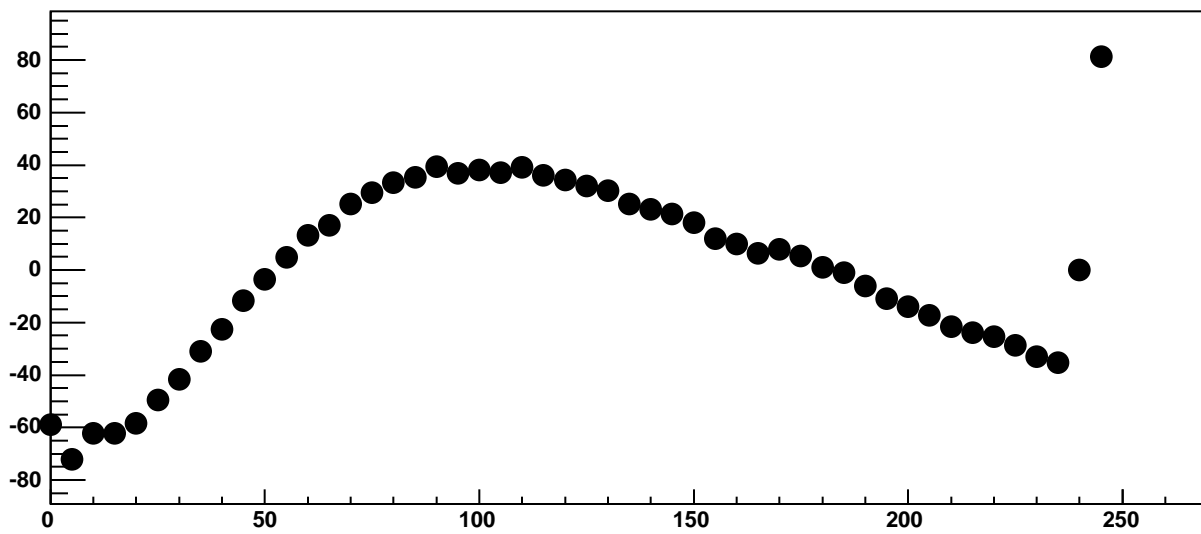


χ^2 / ndf	1.31e+04 / 41
p0	-270.1 ± 0.65
p1	237.8 ± 1.048
p2	-835.4 ± 391.6
p3	44.26 ± 17.47
p4	0.4247 ± 0.003737

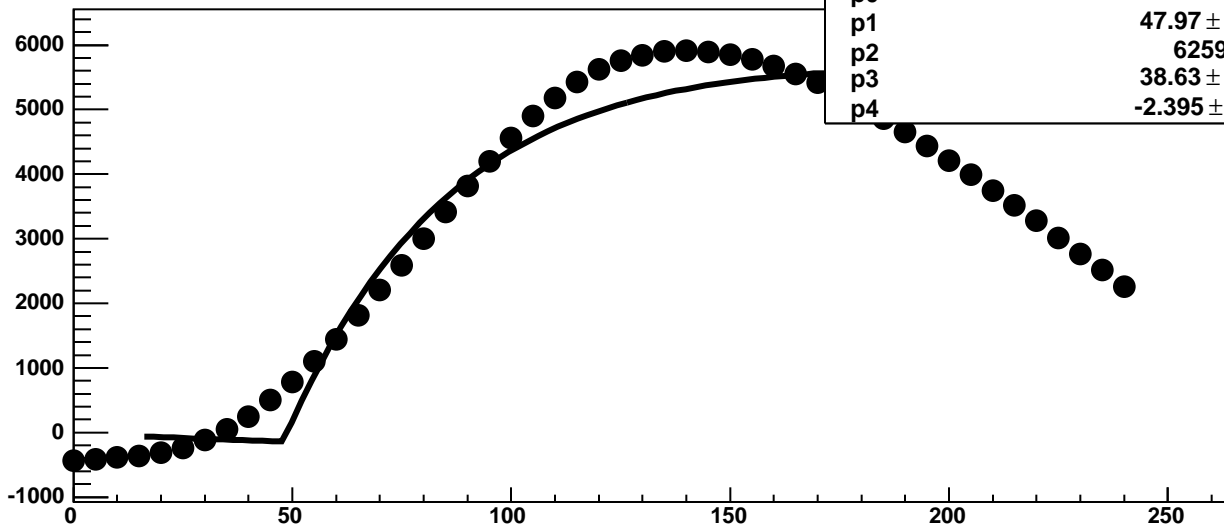
Chip 9, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

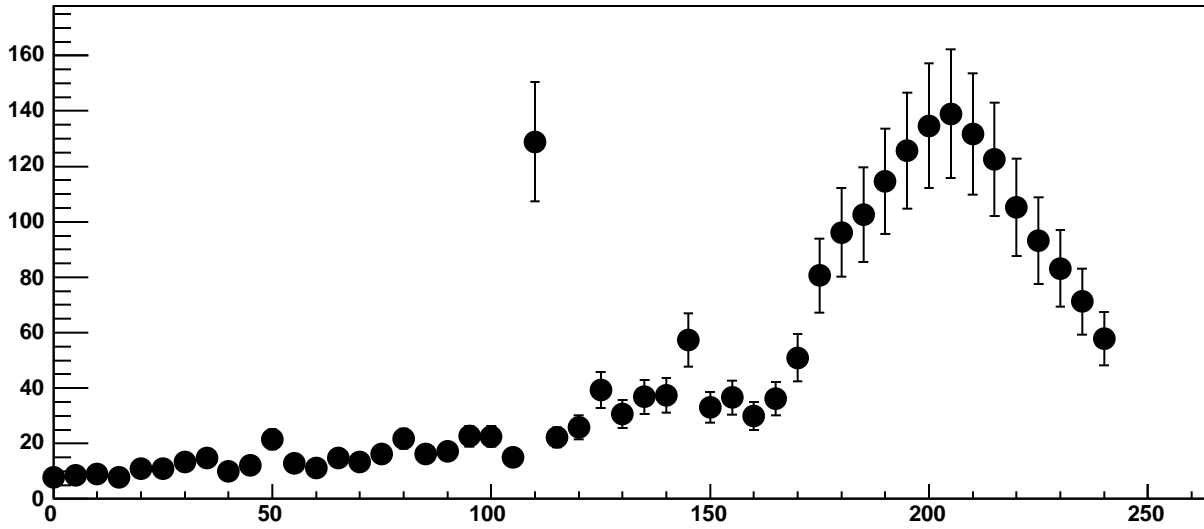


Chip 9, Channel 7, Enable 5, DAC=4000, ADC Mean vs Hold

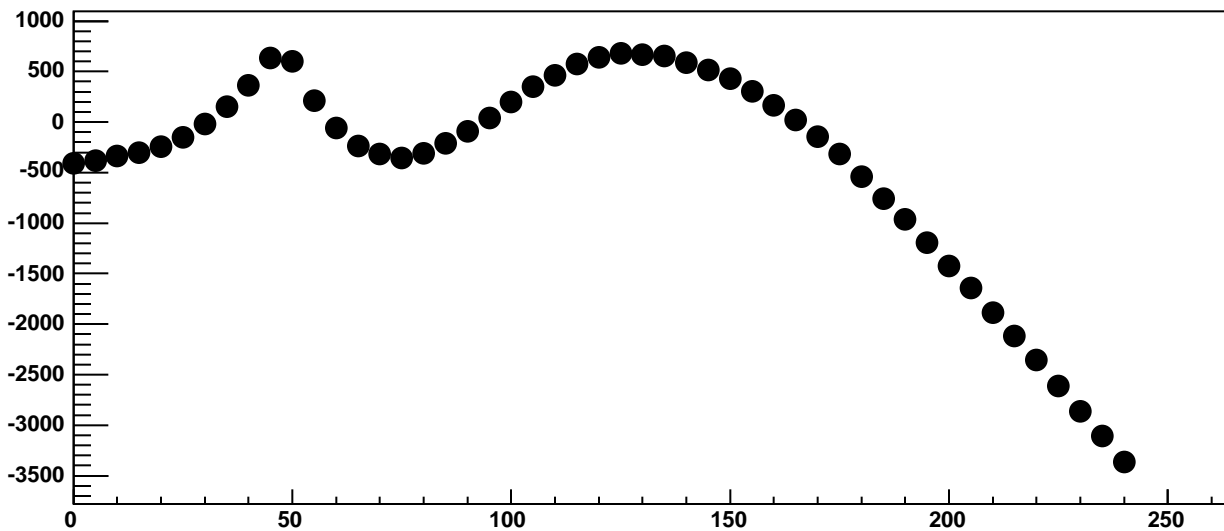


χ^2 / ndf	4.09e+05 / 41
p0	-140.1 ± 1.524
p1	47.97 ± 0.01967
p2	6259 ± 9.698
p3	38.63 ± 0.06676
p4	-2.395 ± 0.05991

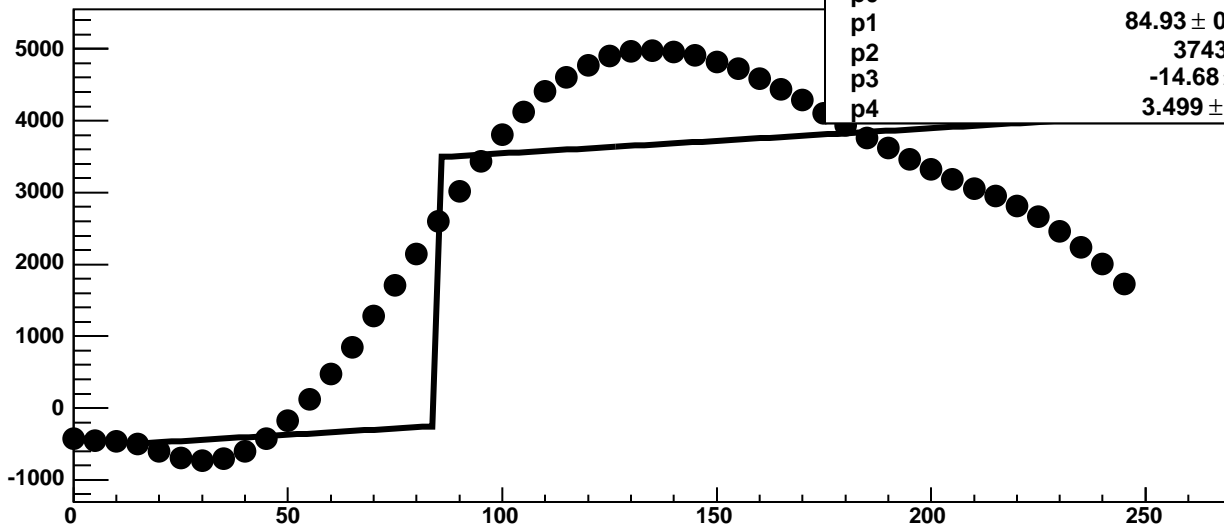
Chip 9, Channel 7, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 7, Enable 5, DAC=4000, ADC Residuals vs Hold

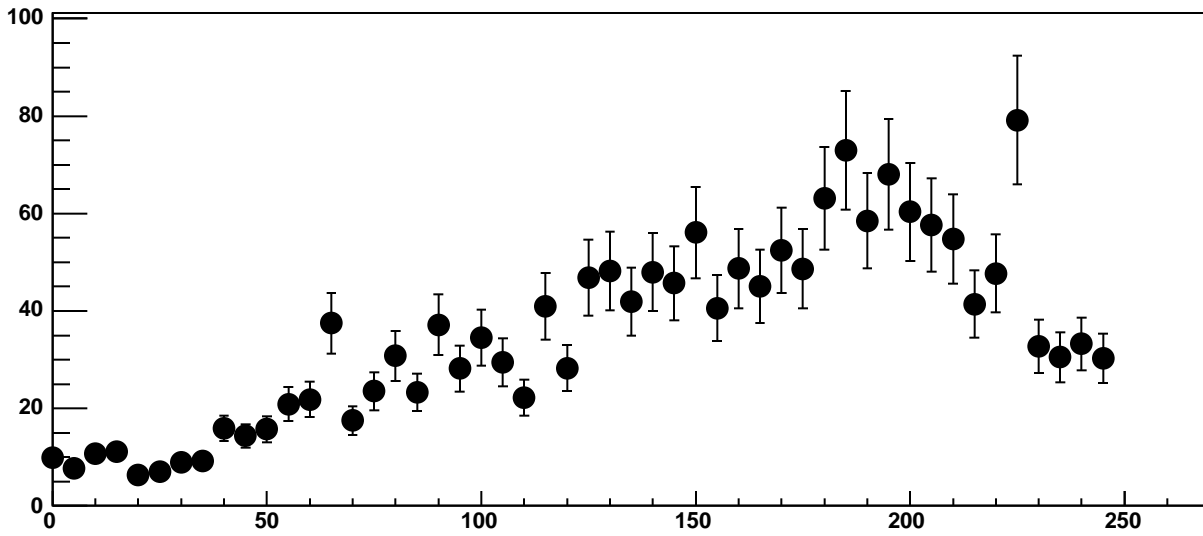


Chip 9, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

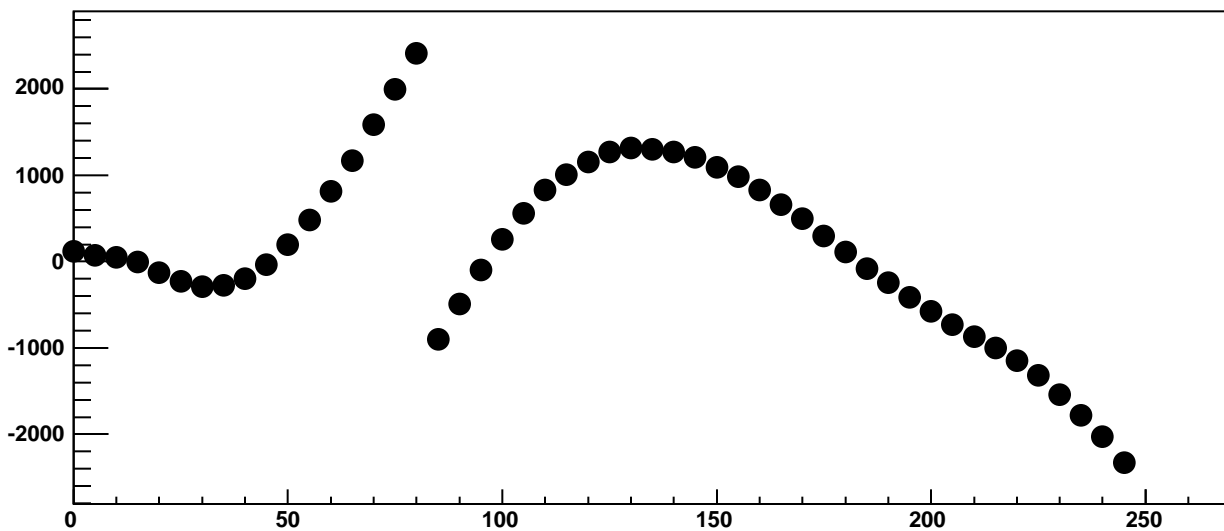


χ^2 / ndf	9.655e+05 / 41
p0	-249.9 ± 5.369
p1	84.93 ± 0.001218
p2	3743 ± 5.617
p3	-14.68 ± 0.3221
p4	3.499 ± 0.06831

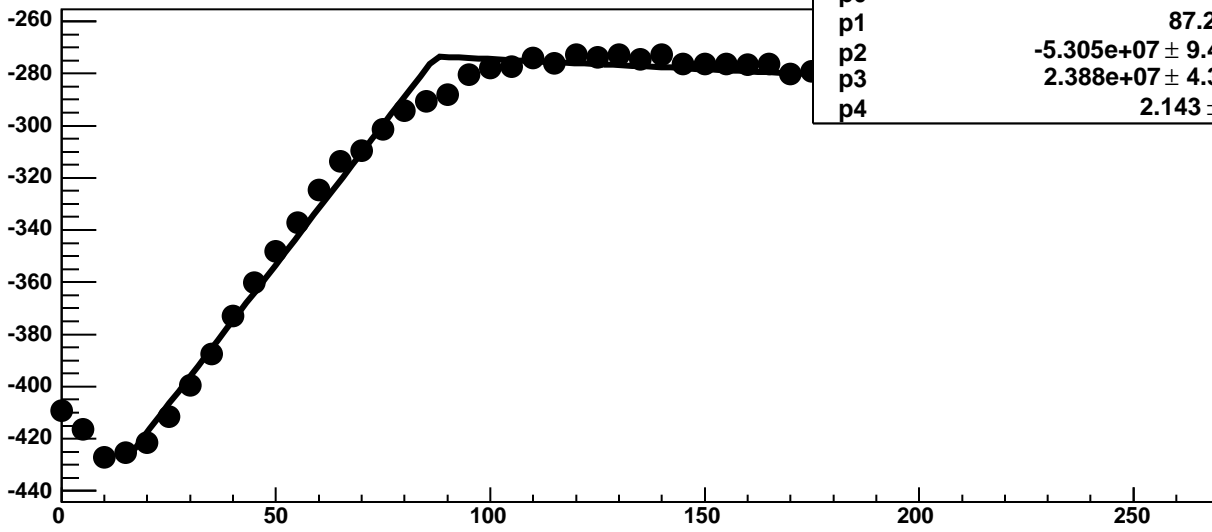
Chip 9, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

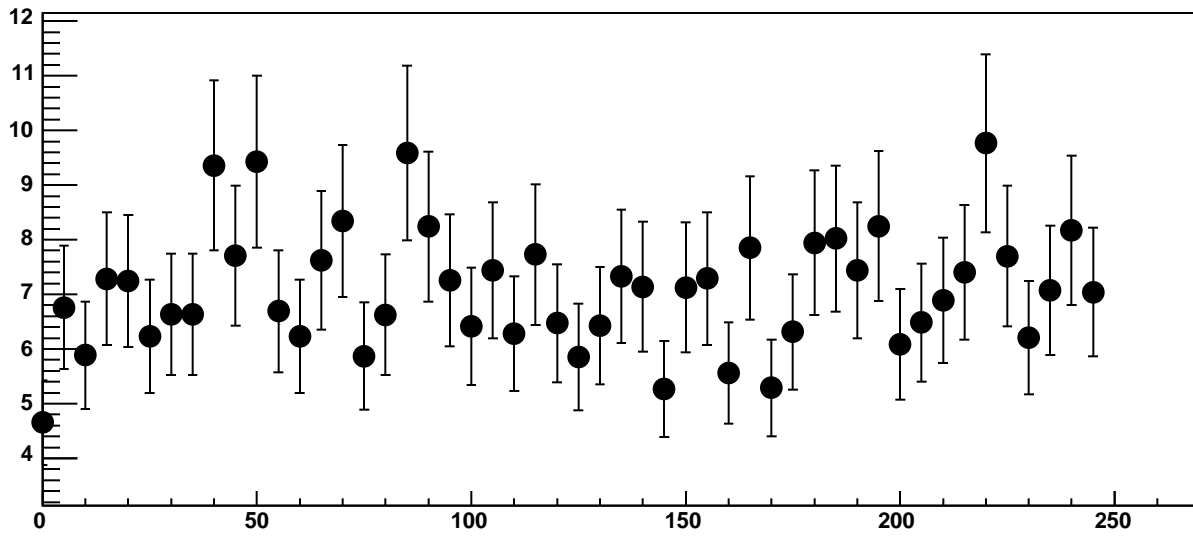


Chip 9, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

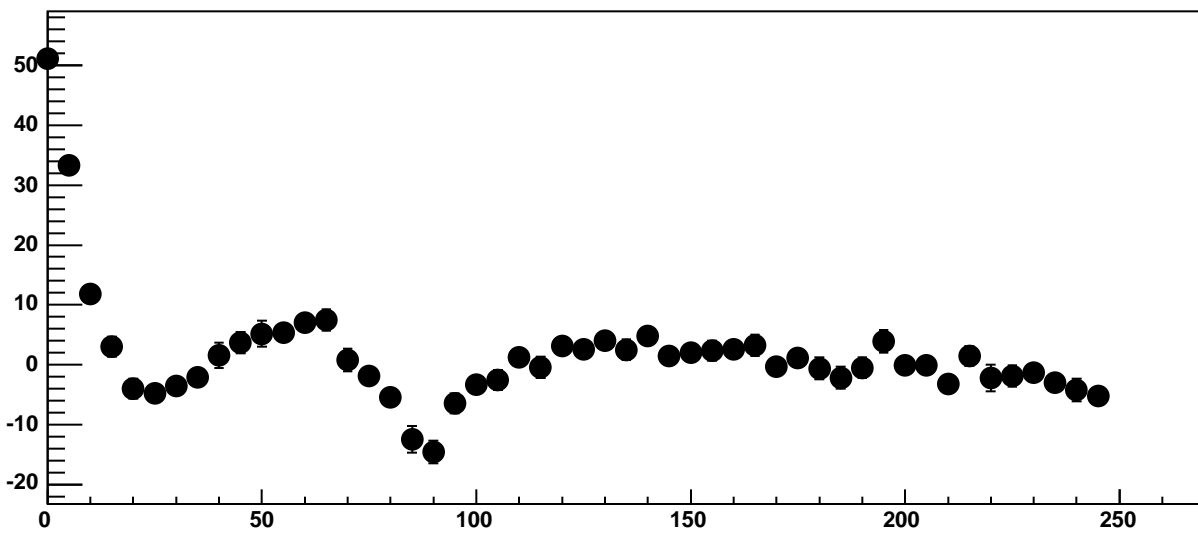


χ^2 / ndf	283.7 / 41
p0	-273.5 ± 0.5438
p1	87.2 ± 0.436
p2	$-5.305e+07 \pm 9.498e+05$
p3	$2.388e+07 \pm 4.343e+05$
p4	2.143 ± 0.0176

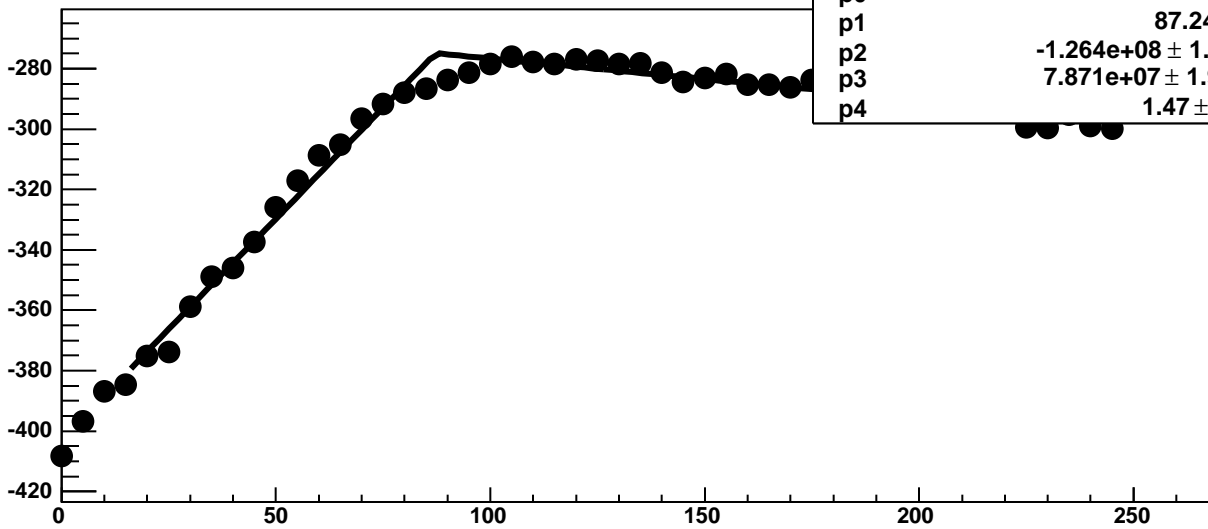
Chip 9, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

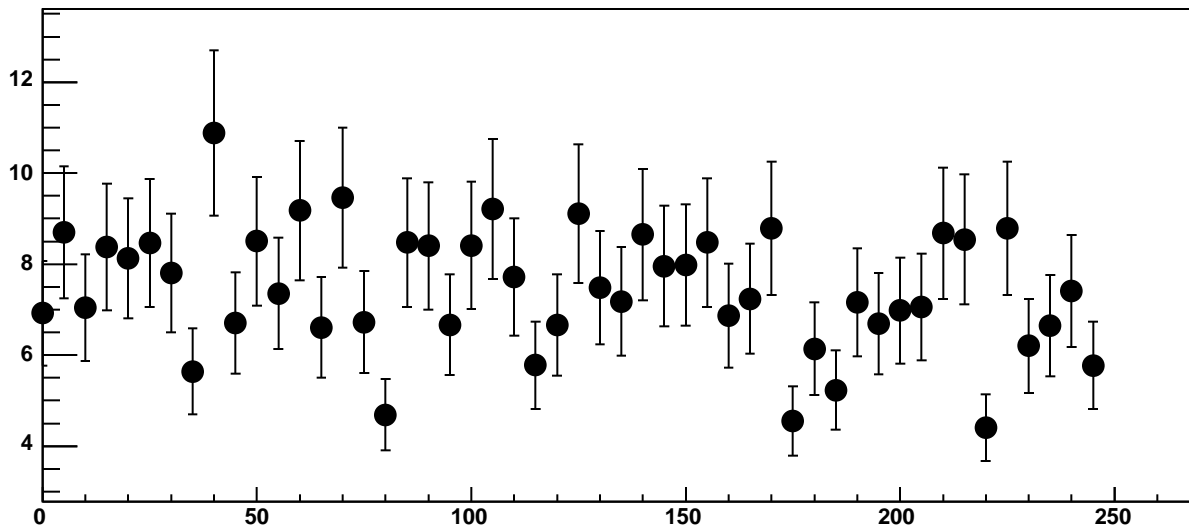


Chip 9, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold

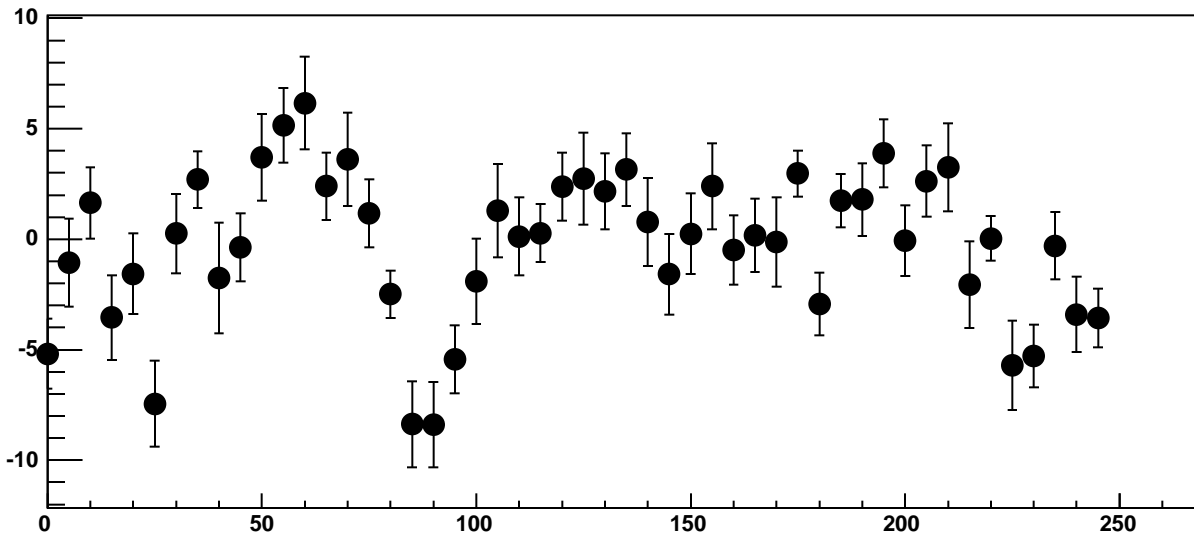


χ^2 / ndf	174.5 / 41
p0	-274.9 ± 0.5656
p1	87.24 ± 0.625
p2	$-1.264\text{e}+08 \pm 1.664\text{e}+06$
p3	$7.871\text{e}+07 \pm 1.914\text{e}+05$
p4	1.47 ± 0.01963

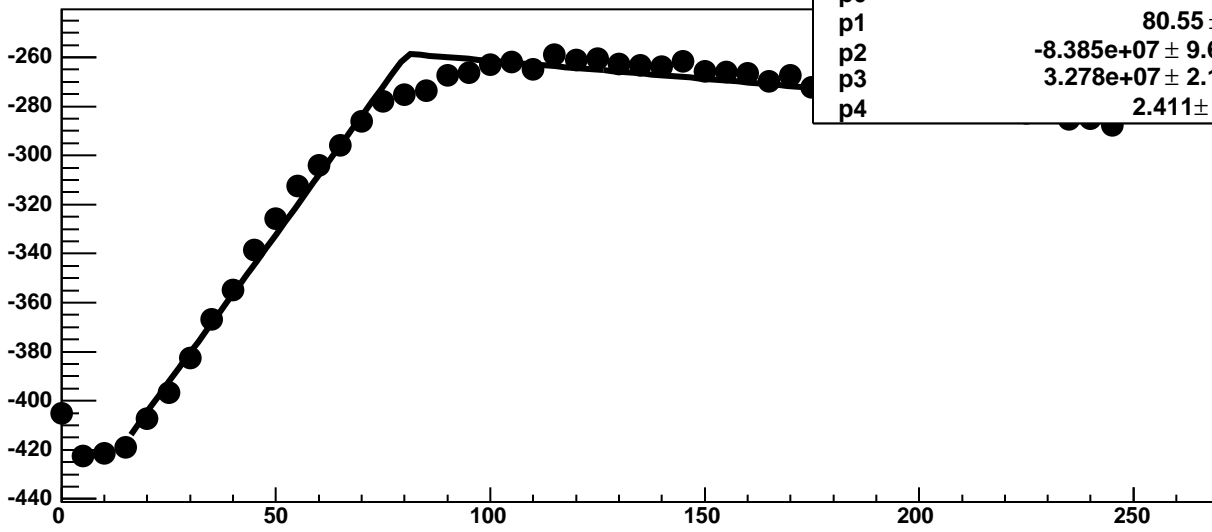
Chip 9, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold

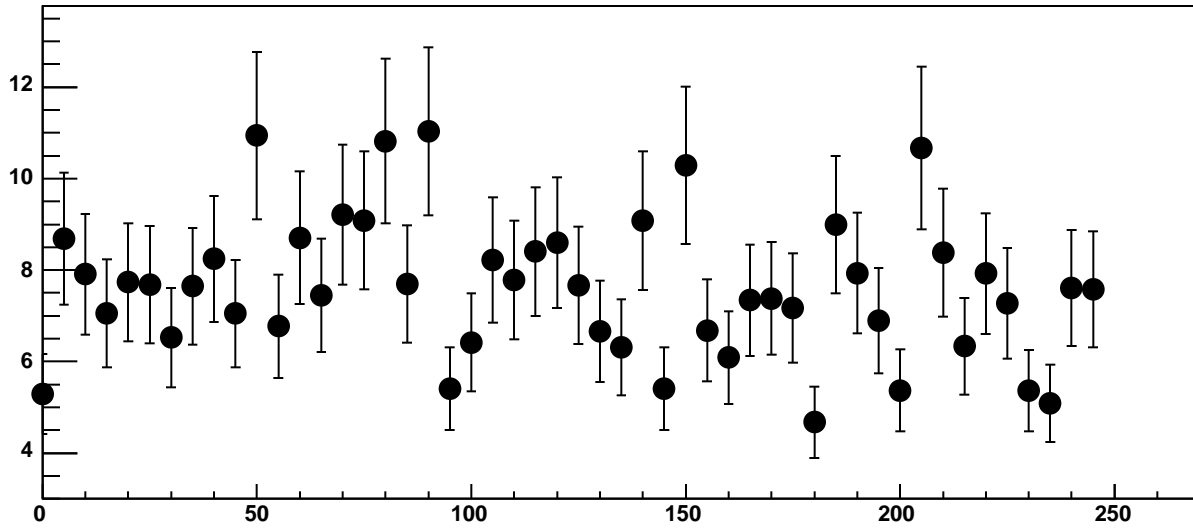


Chip 9, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

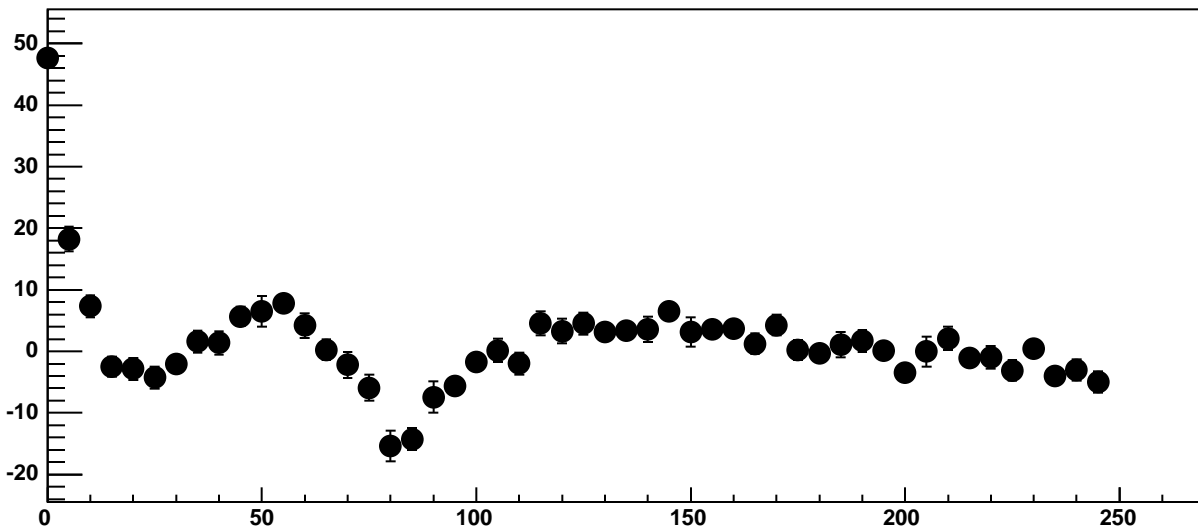


χ^2 / ndf	312.5 / 41
p0	-258.6 ± 0.559
p1	80.55 ± 0.4649
p2	$-8.385e+07 \pm 9.664e+05$
p3	$3.278e+07 \pm 2.143e+05$
p4	2.411 ± 0.02509

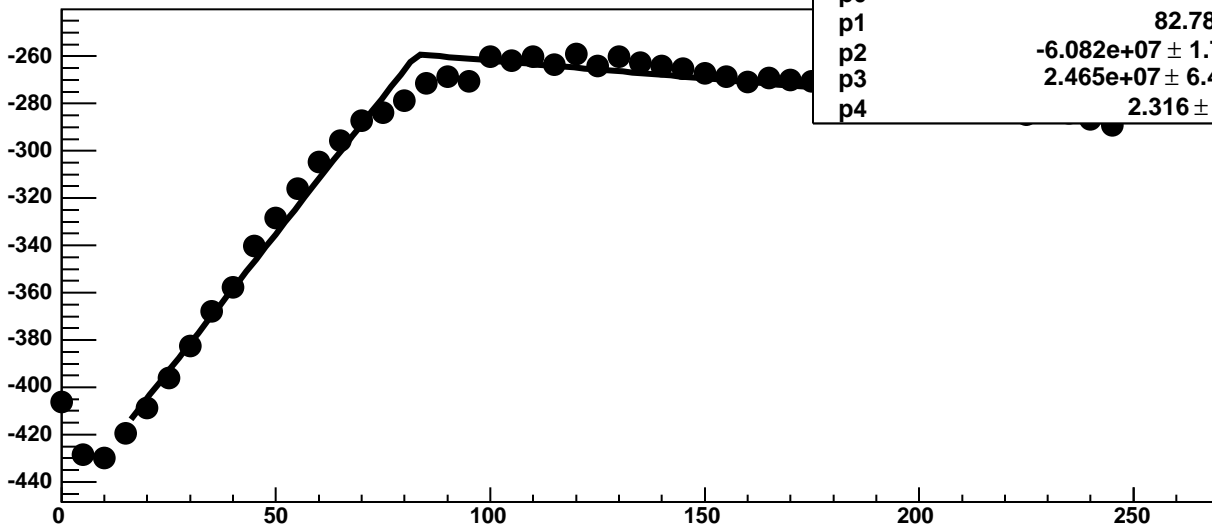
Chip 9, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

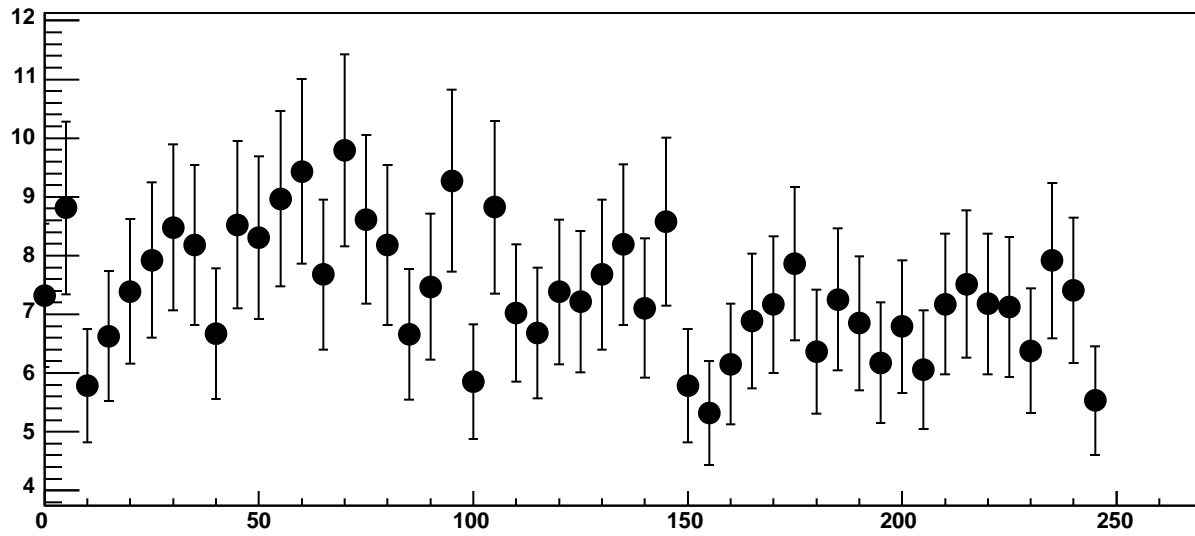


Chip 9, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

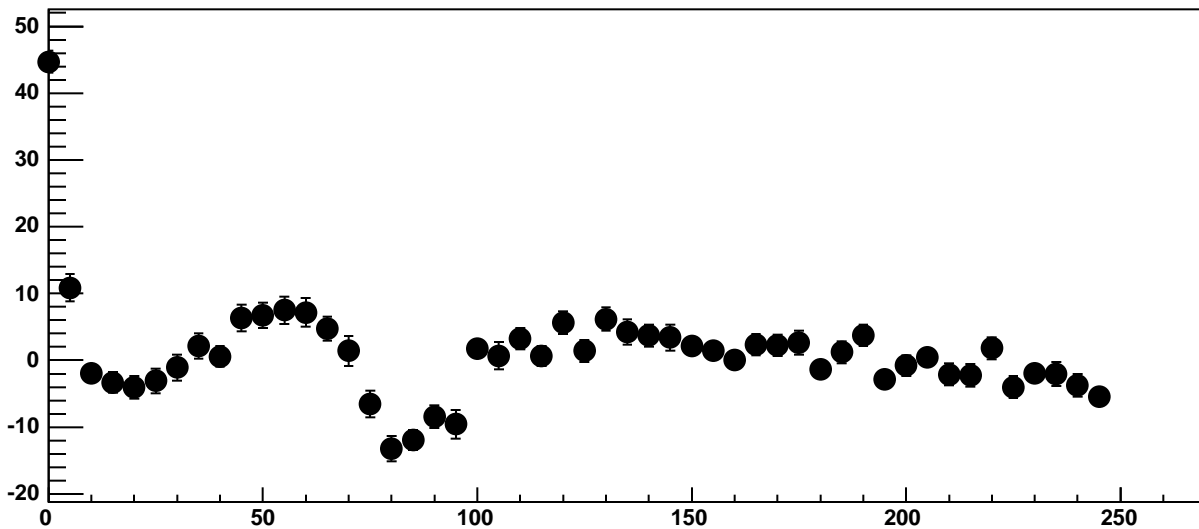


χ^2 / ndf	318.9 / 41
p0	-259.2 ± 0.5535
p1	82.78 ± 0.484
p2	$-6.082\text{e}+07 \pm 1.785\text{e}+06$
p3	$2.465\text{e}+07 \pm 6.495\text{e}+05$
p4	2.316 ± 0.02417

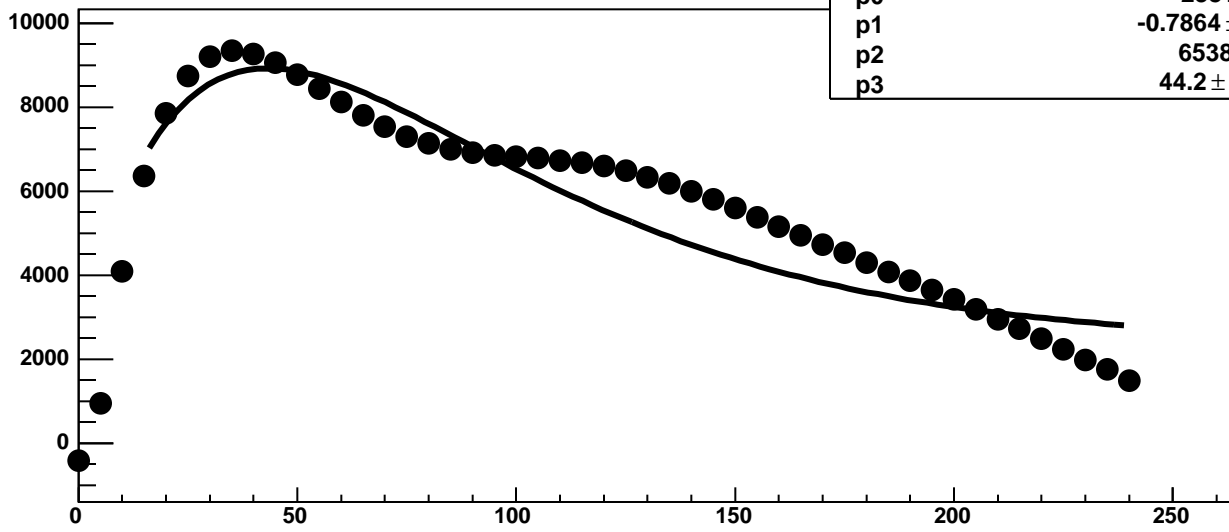
Chip 9, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

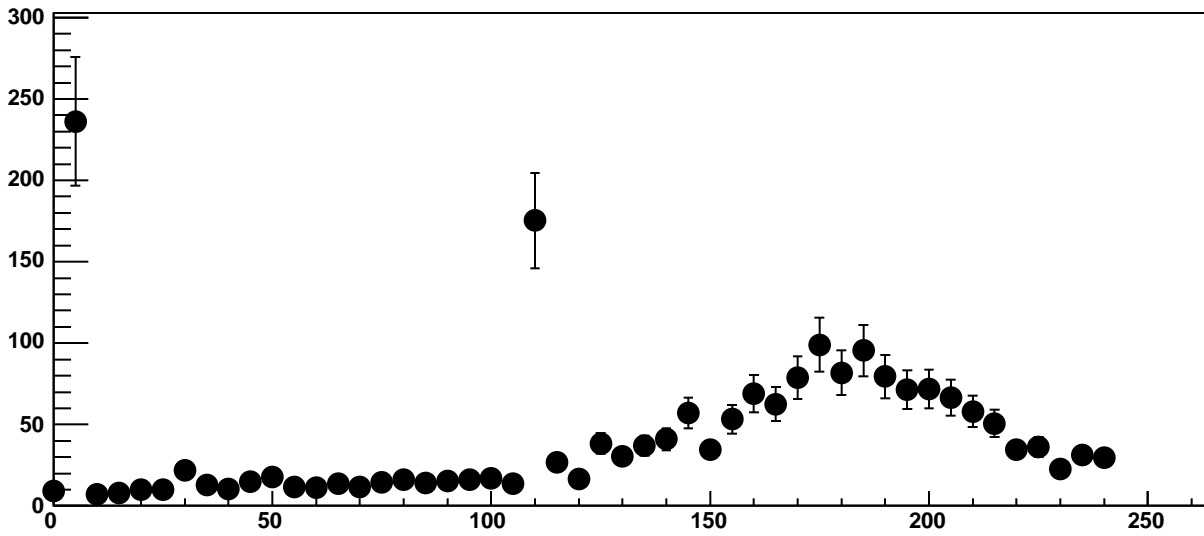


Chip 9, Channel 8, Enable 5!, DAC=4000, ADC Mean vs Hold

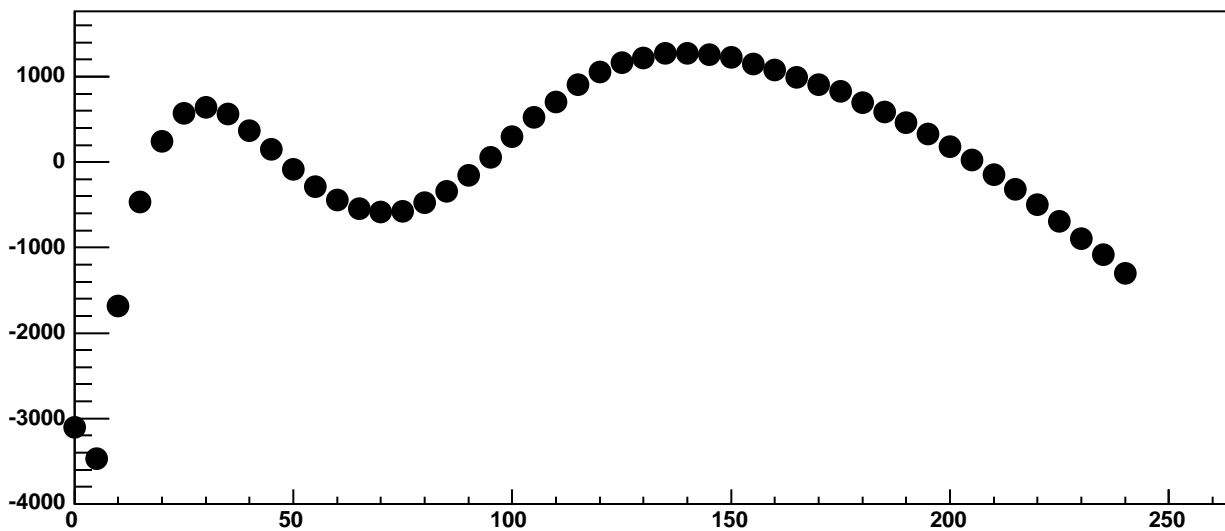


χ^2 / ndf	7.782e+05 / 42
p0	2381 ± 4.526
p1	-0.7864 ± 0.0289
p2	6538 ± 4.146
p3	44.2 ± 0.03684

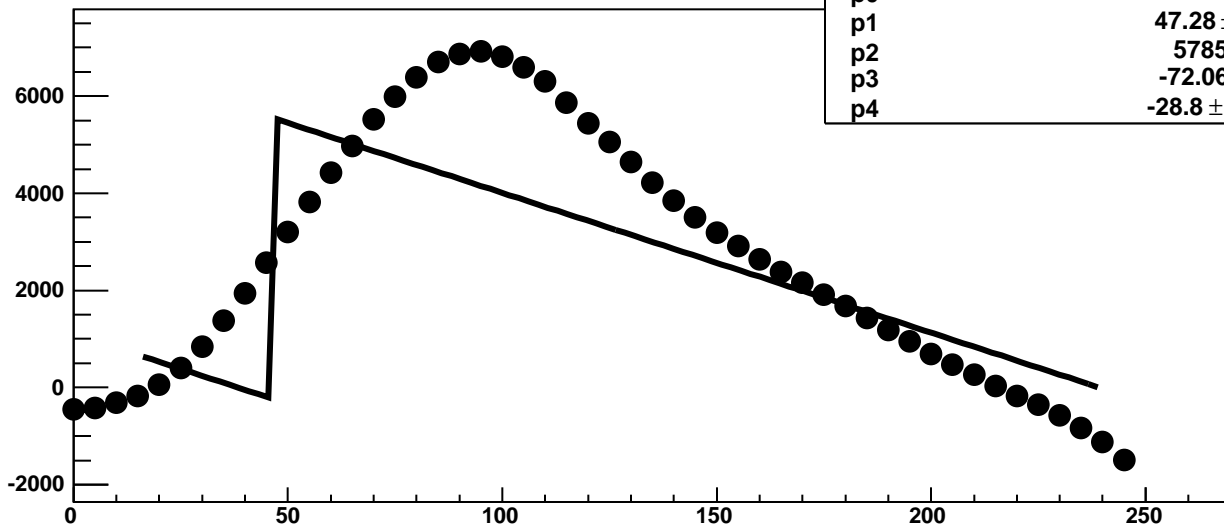
Chip 9, Channel 8, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 8, Enable 5!, DAC=4000, ADC Residuals vs Hold

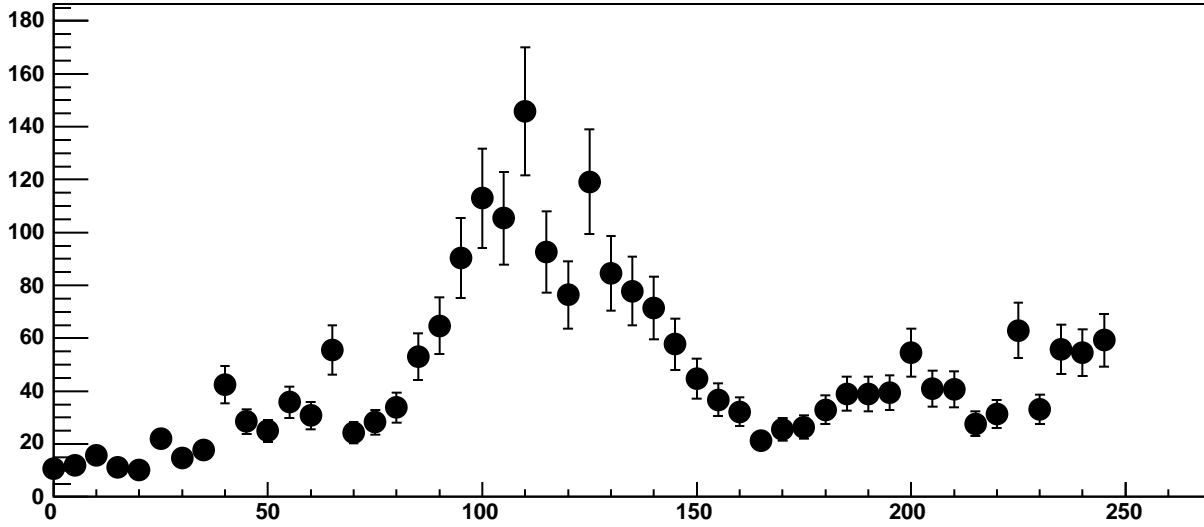


Chip 9, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold

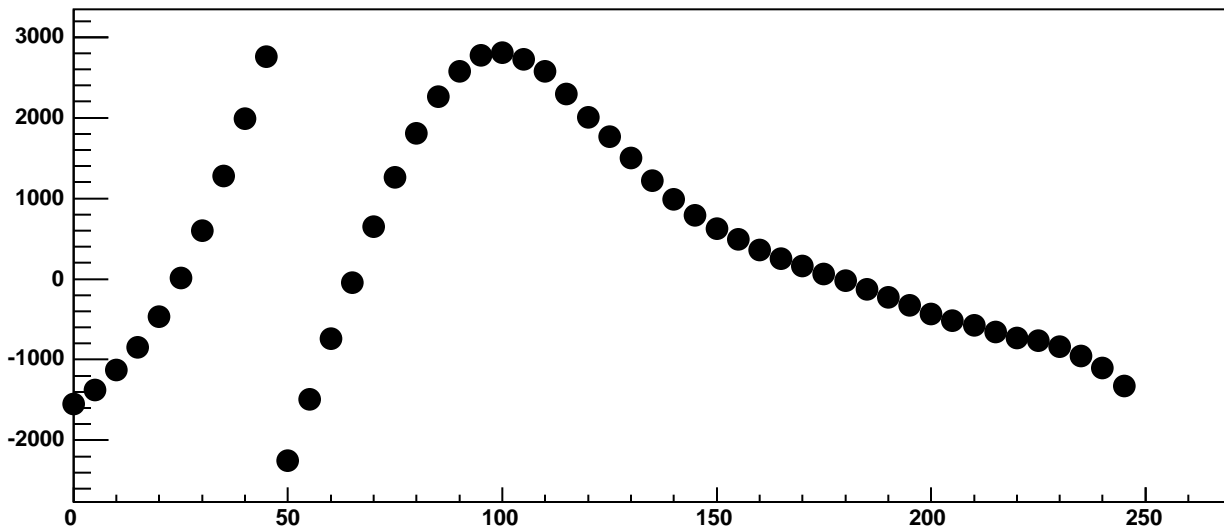


χ^2 / ndf	1.037e+06 / 41
p0	-257.6 ± 10.03
p1	47.28 ± 0.2203
p2	5785 ± 4.788
p3	-72.06 ± 1.227
p4	-28.8 ± 0.07941

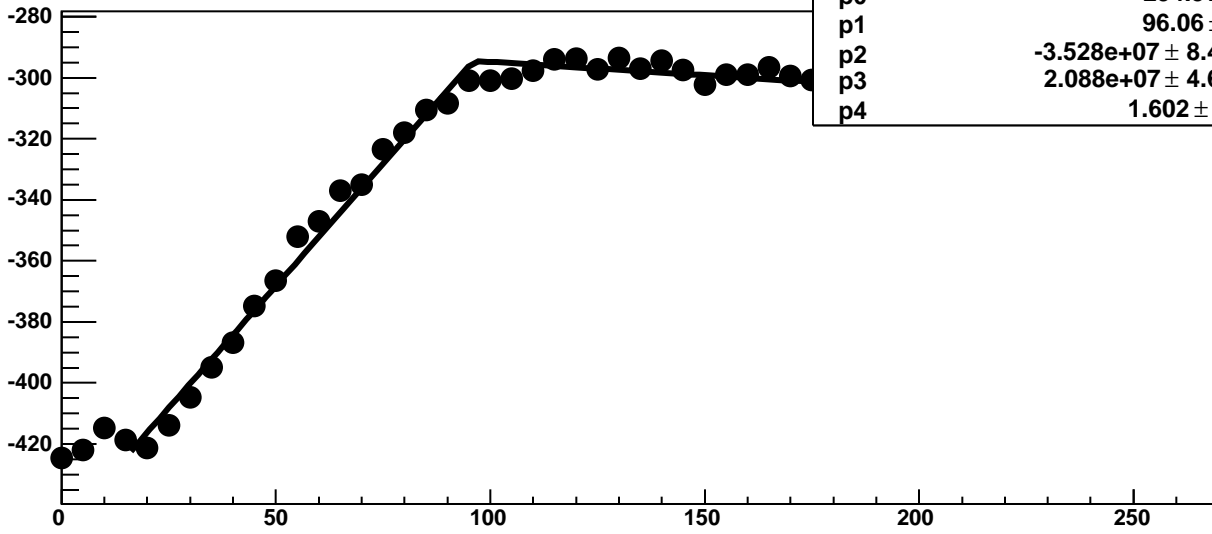
Chip 9, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold

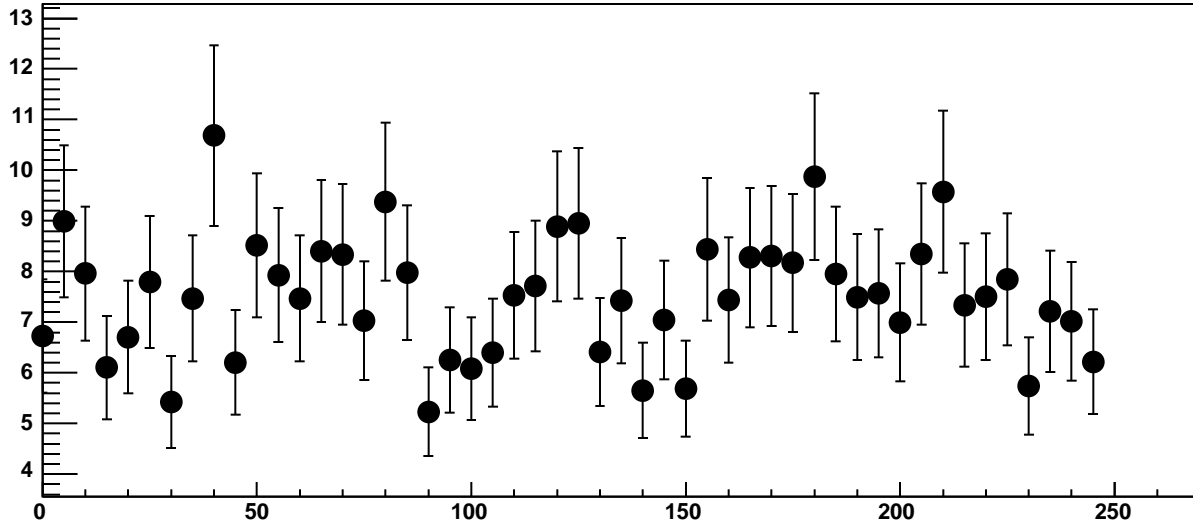


Chip 9, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

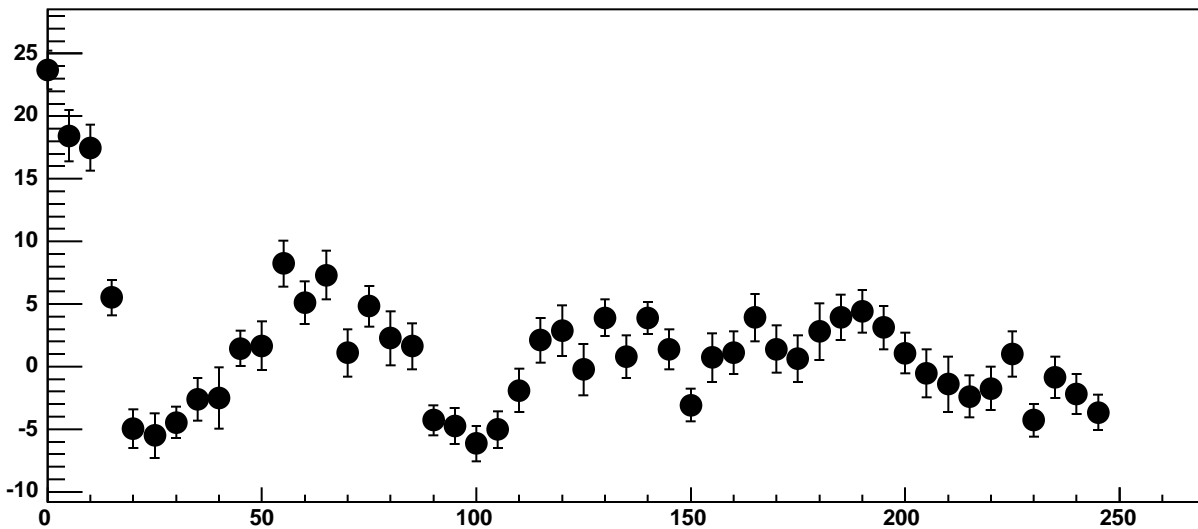


χ^2 / ndf	228.7 / 41
p0	-294.5 ± 0.5748
p1	96.06 ± 0.5618
p2	$-3.528e+07 \pm 8.484e+05$
p3	$2.088e+07 \pm 4.634e+05$
p4	1.602 ± 0.01487

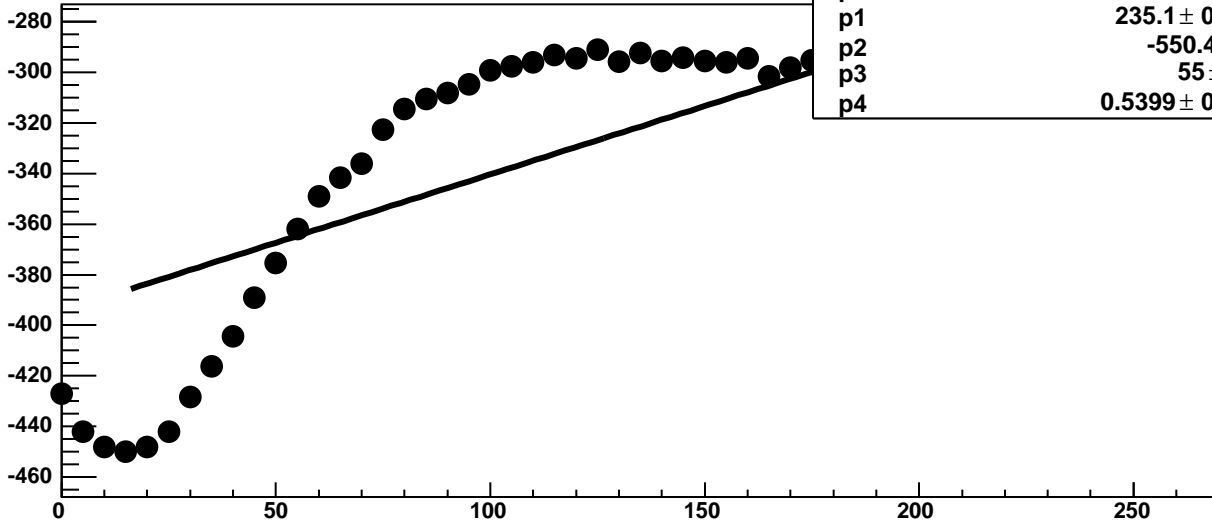
Chip 9, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

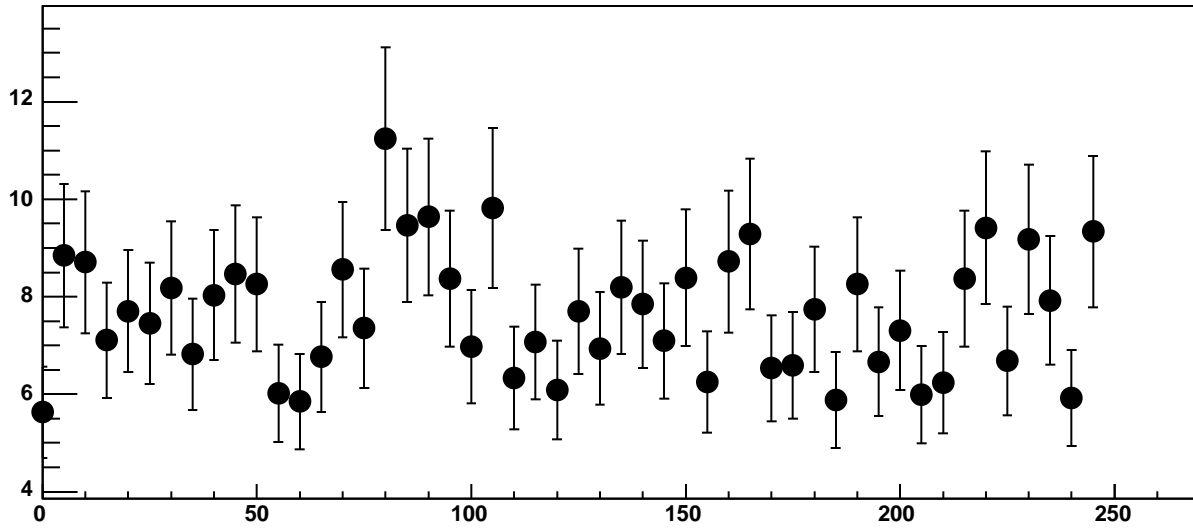


Chip 9, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

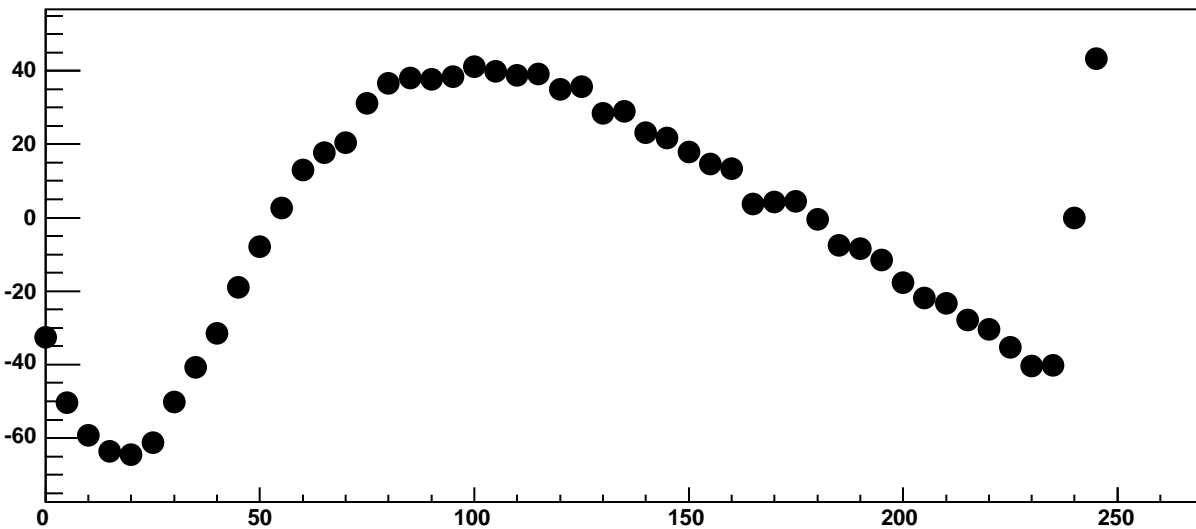


χ^2 / ndf	1.488e+04 / 41
p0	-267.4 ± 0.494
p1	235.1 ± 0.002479
p2	-550.4 ± 17.27
p3	55 ± 0.1122
p4	0.5399 ± 0.003897

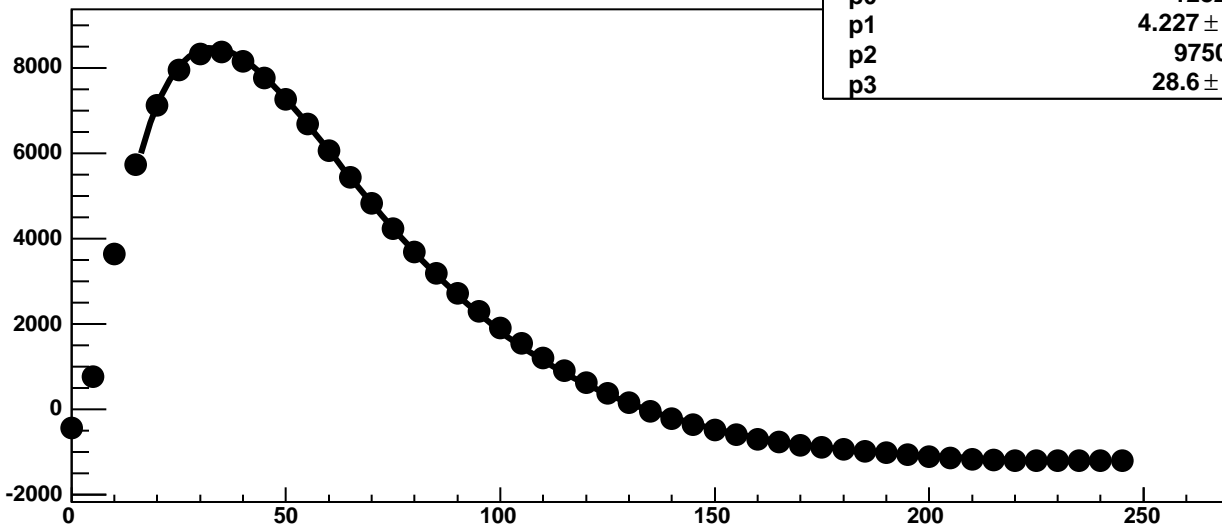
Chip 9, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



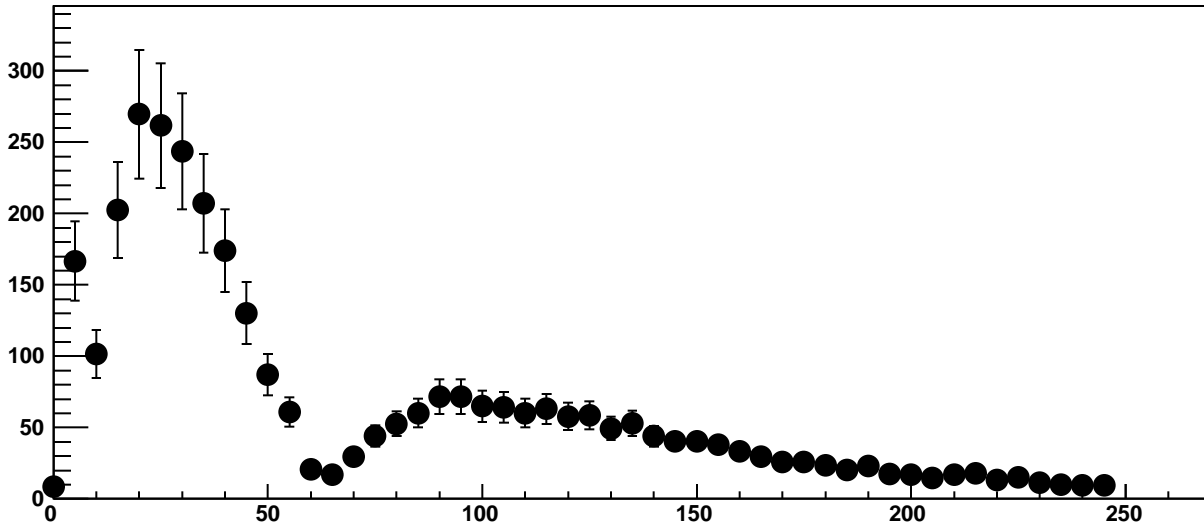
Chip 9, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold



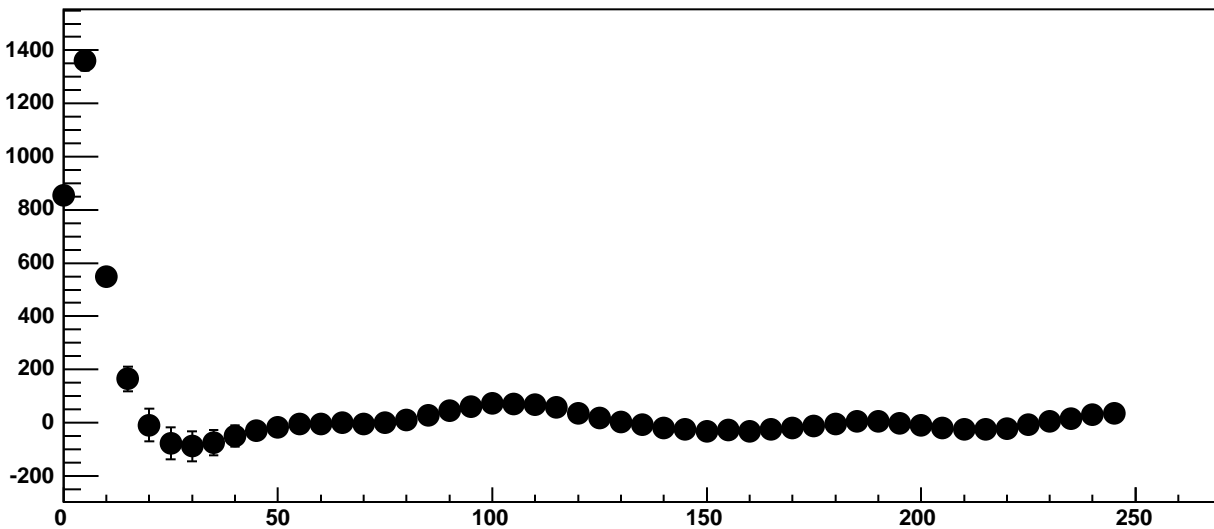
Chip 9, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold



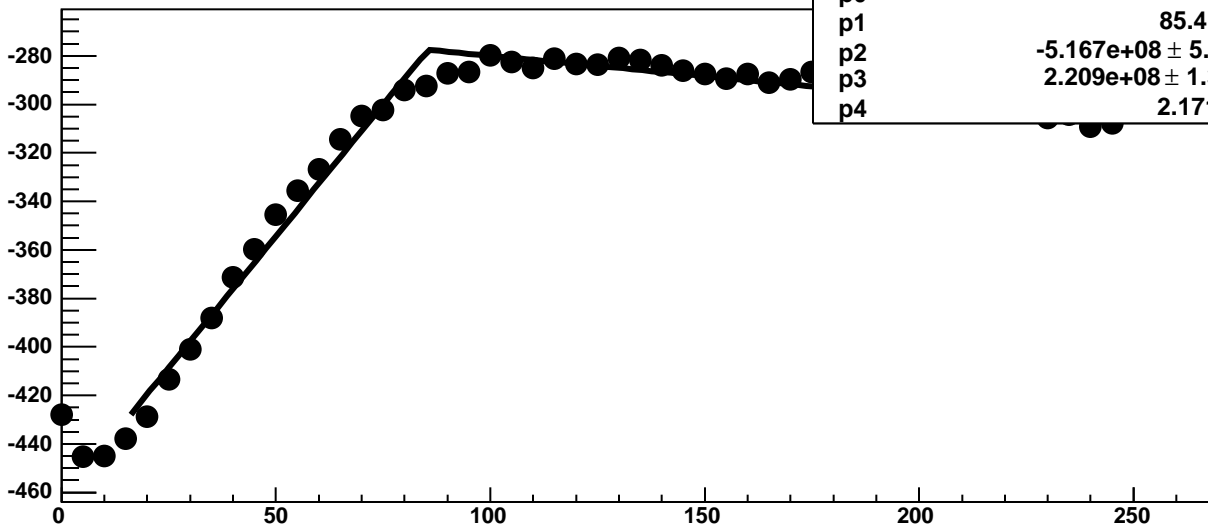
Chip 9, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold

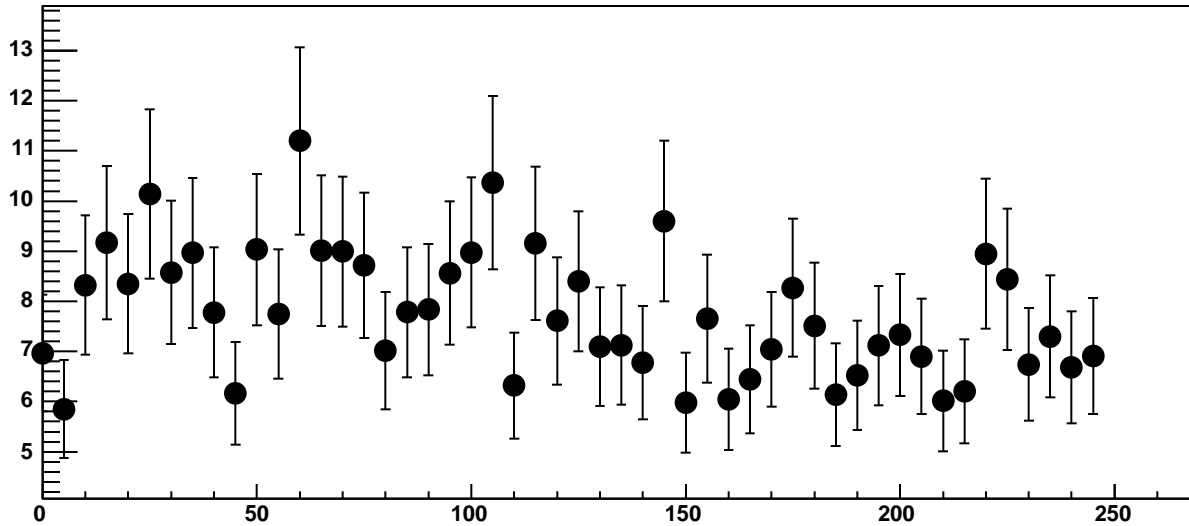


Chip 9, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

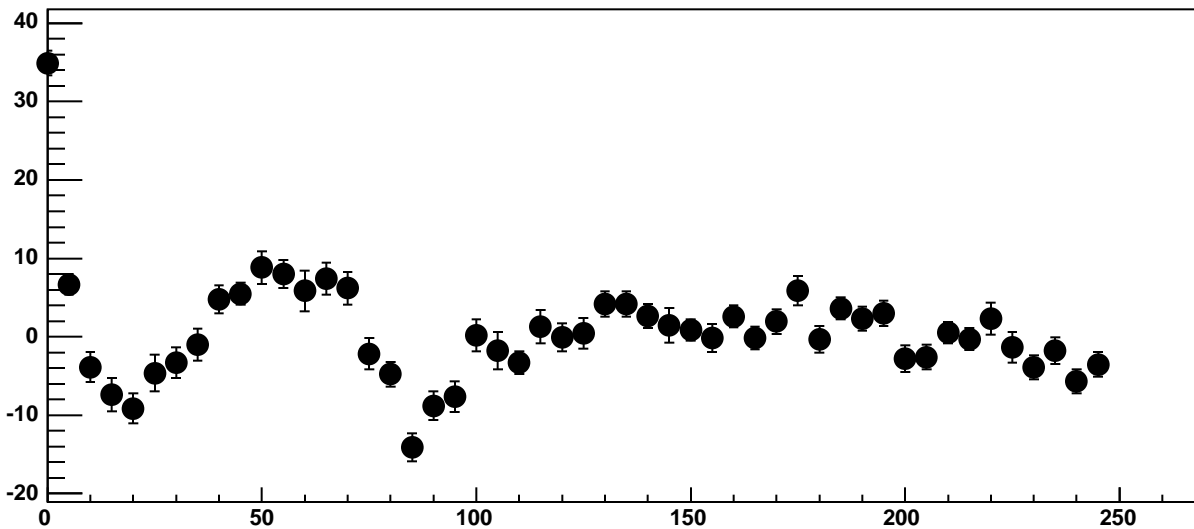


χ^2 / ndf	319.6 / 41
p0	-277.5 ± 0.6111
p1	85.4 ± 0.4851
p2	$-5.167\text{e}+08 \pm 5.288\text{e}+06$
p3	$2.209\text{e}+08 \pm 1.326\text{e}+05$
p4	2.171 ± 0.023

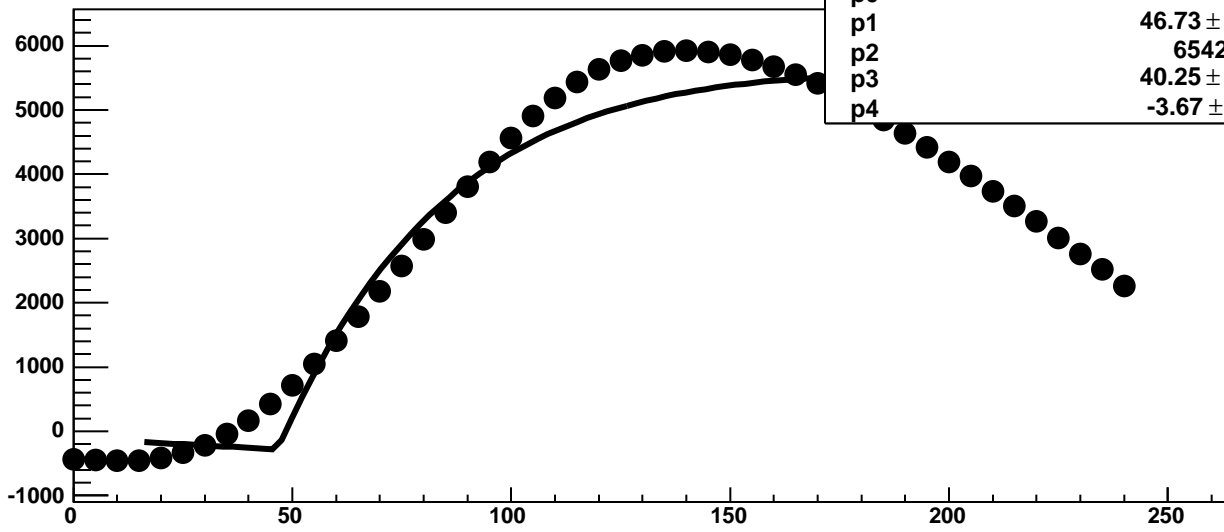
Chip 9, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

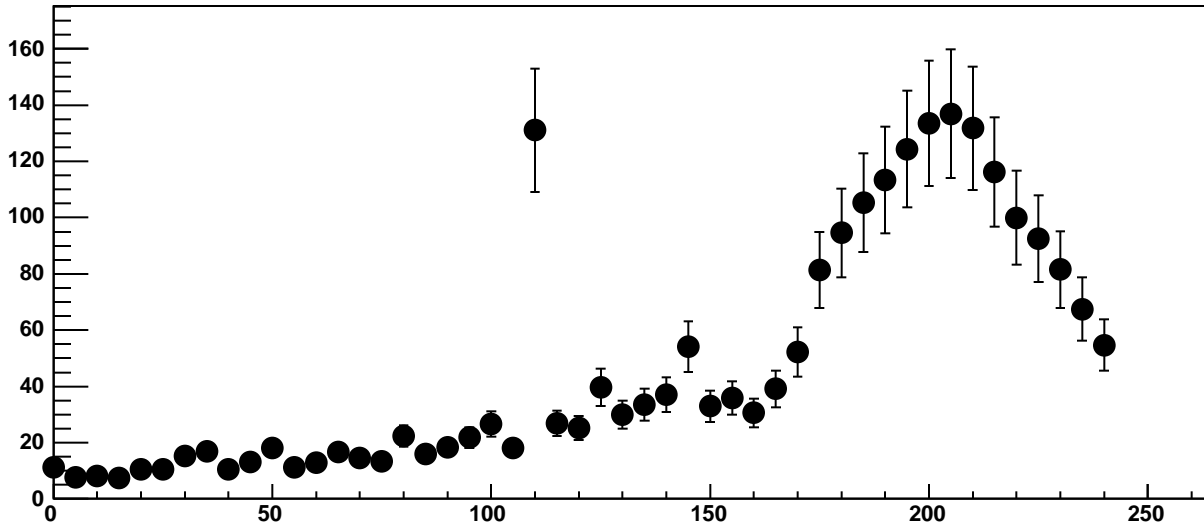


Chip 9, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold

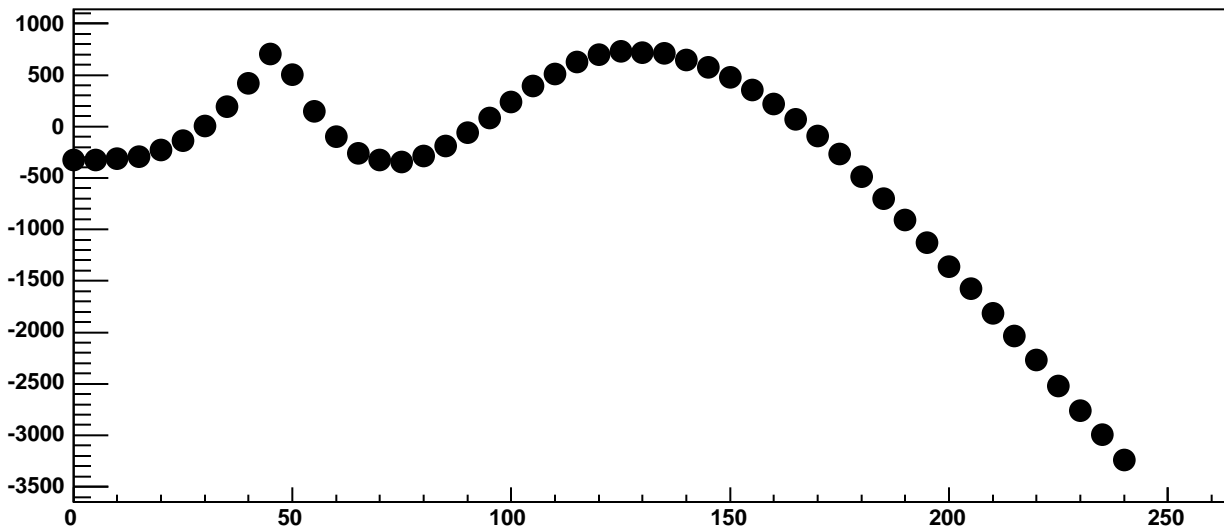


χ^2 / ndf	4.23e+05 / 41
p0	-279.9 ± 1.528
p1	46.73 ± 0.01967
p2	6542 ± 10.19
p3	40.25 ± 0.06874
p4	-3.67 ± 0.06118

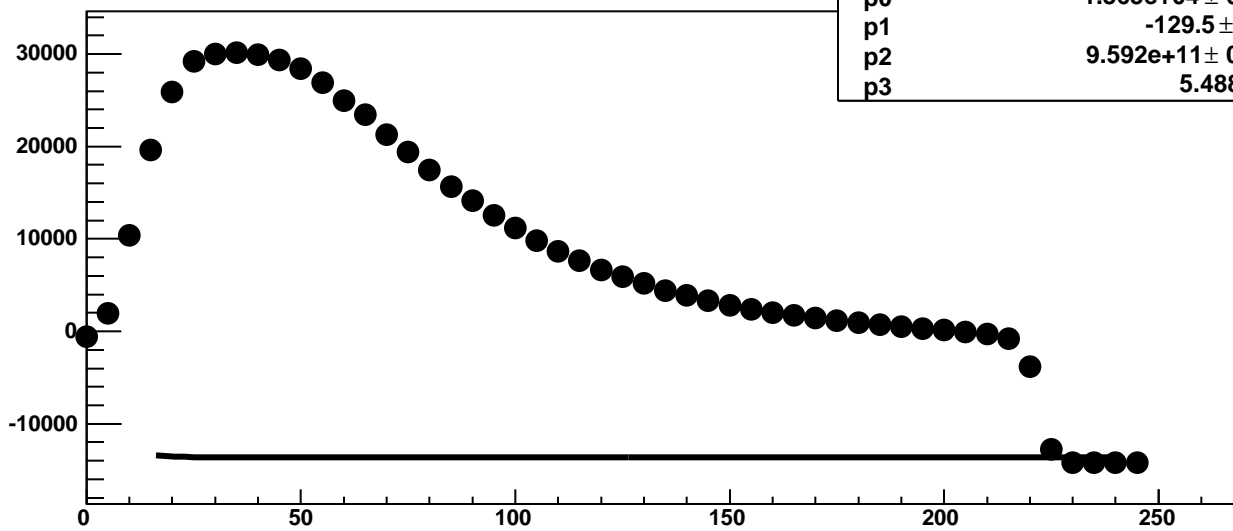
Chip 9, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



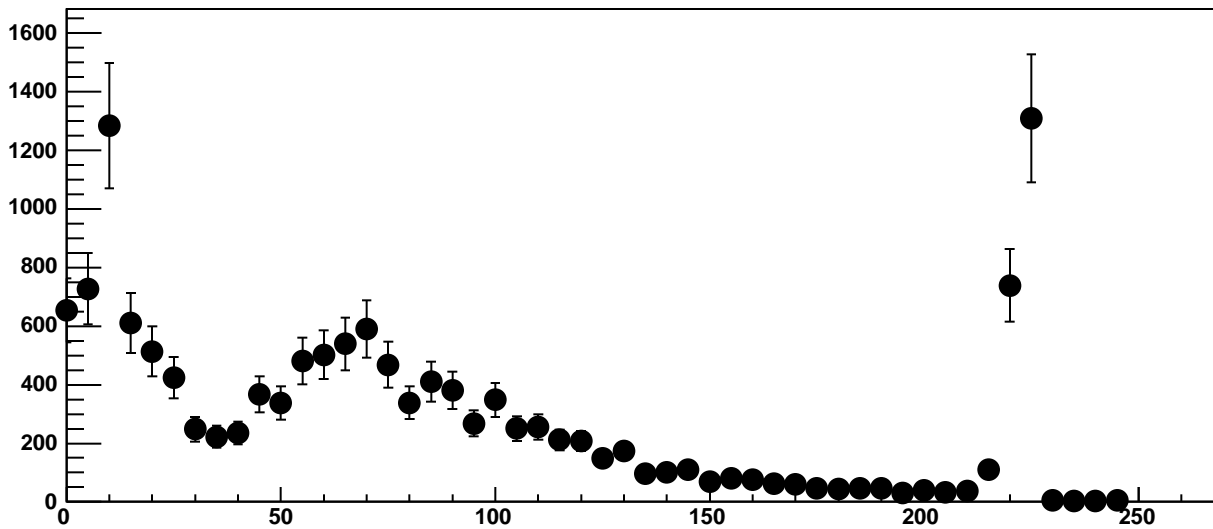
Chip 9, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold



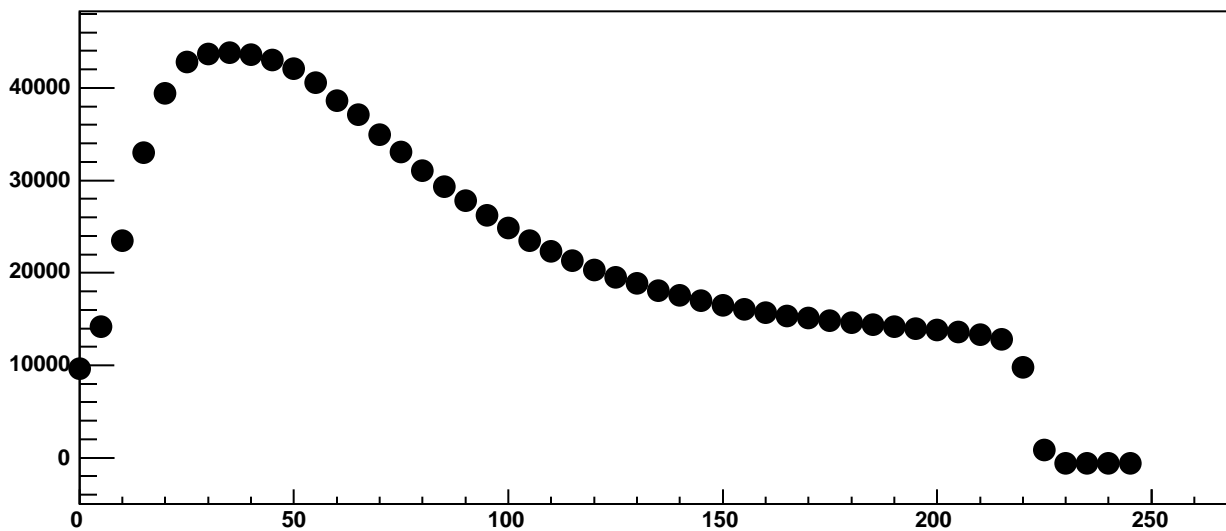
Chip 9, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold



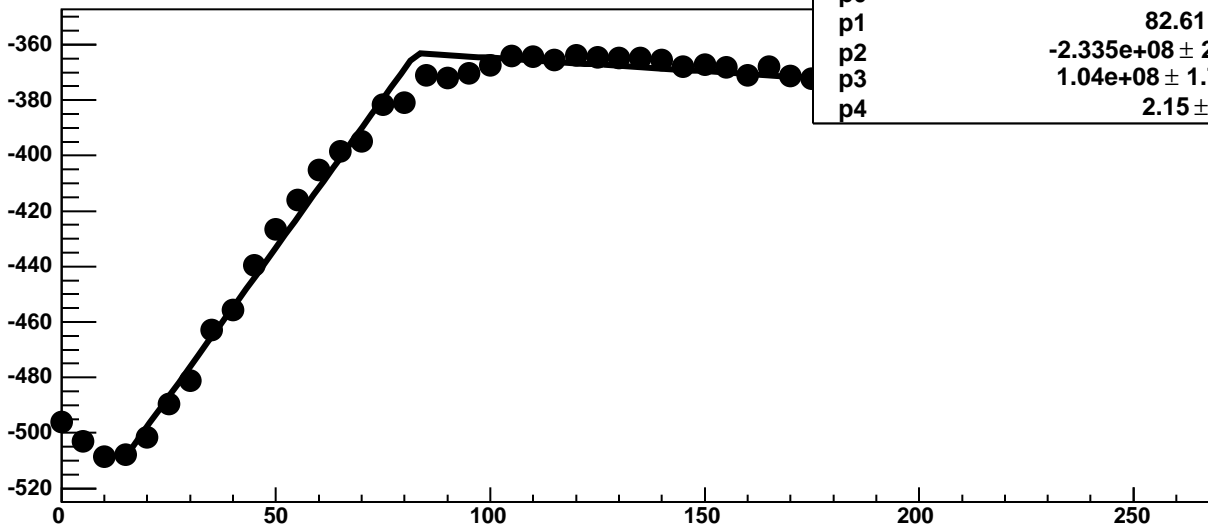
Chip 9, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold

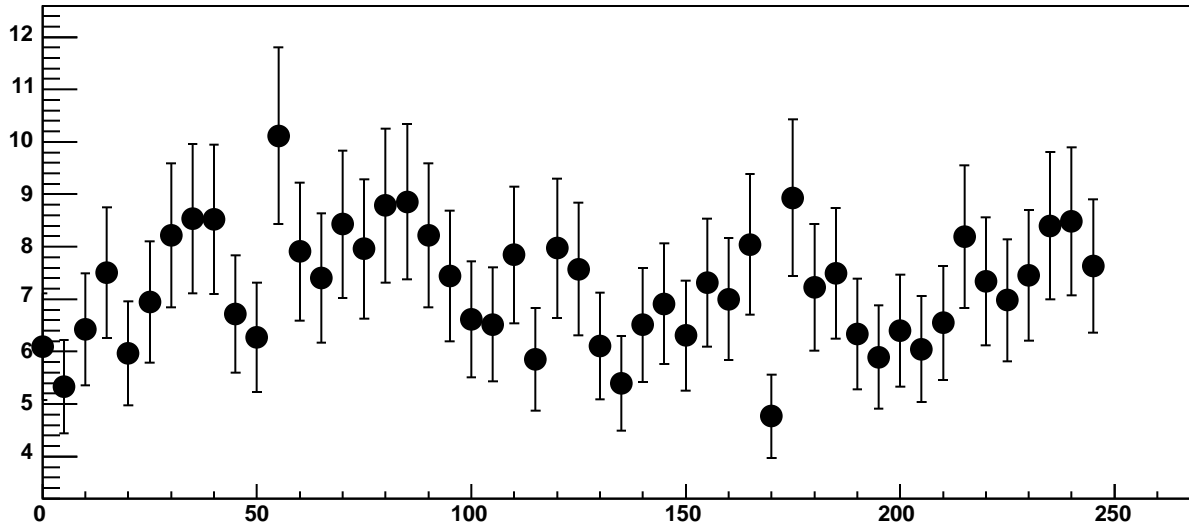


Chip 9, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

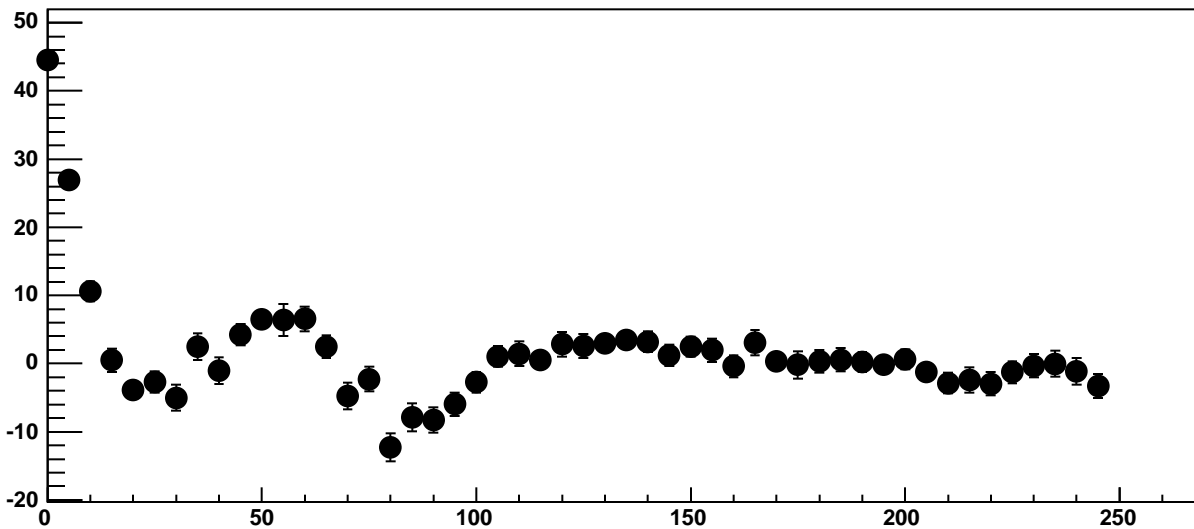


χ^2 / ndf	205.7 / 41
p0	-363 ± 0.5614
p1	82.61 ± 0.5102
p2	$-2.335\text{e}+08 \pm 2.47\text{e}+06$
p3	$1.04\text{e}+08 \pm 1.713\text{e}+05$
p4	2.15 ± 0.02306

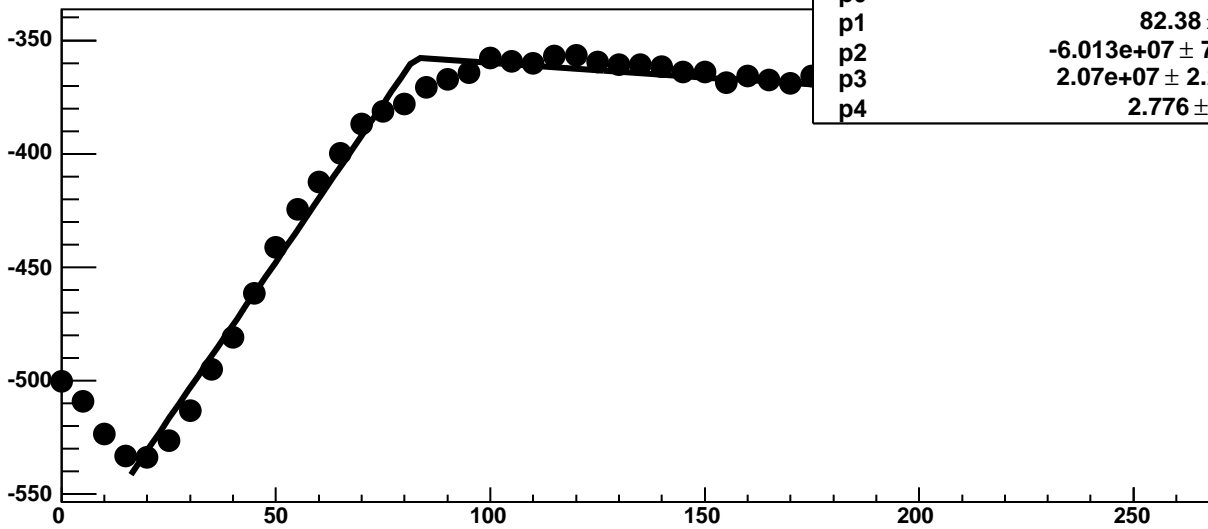
Chip 9, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

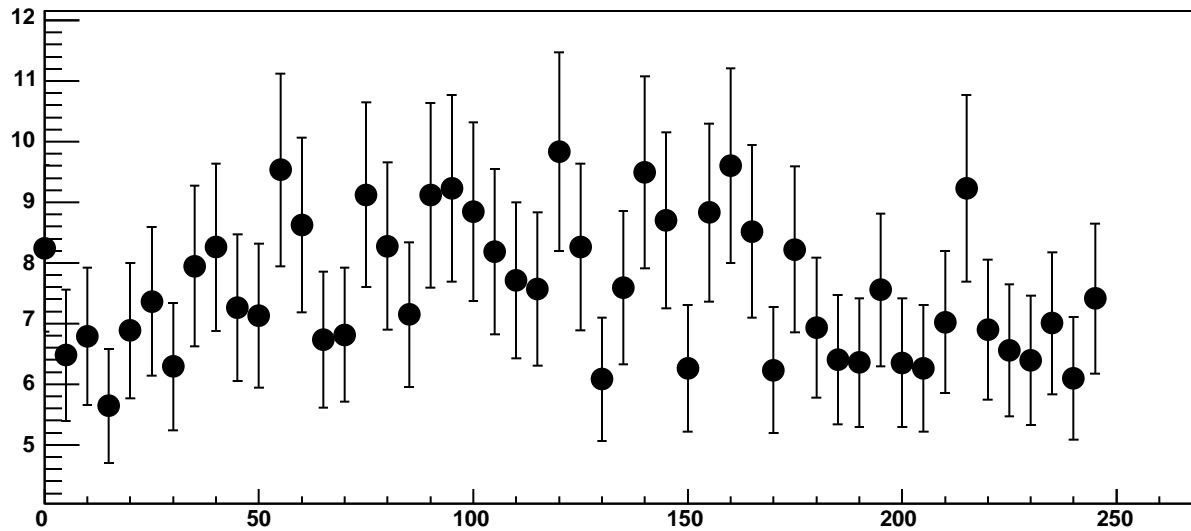


Chip 9, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

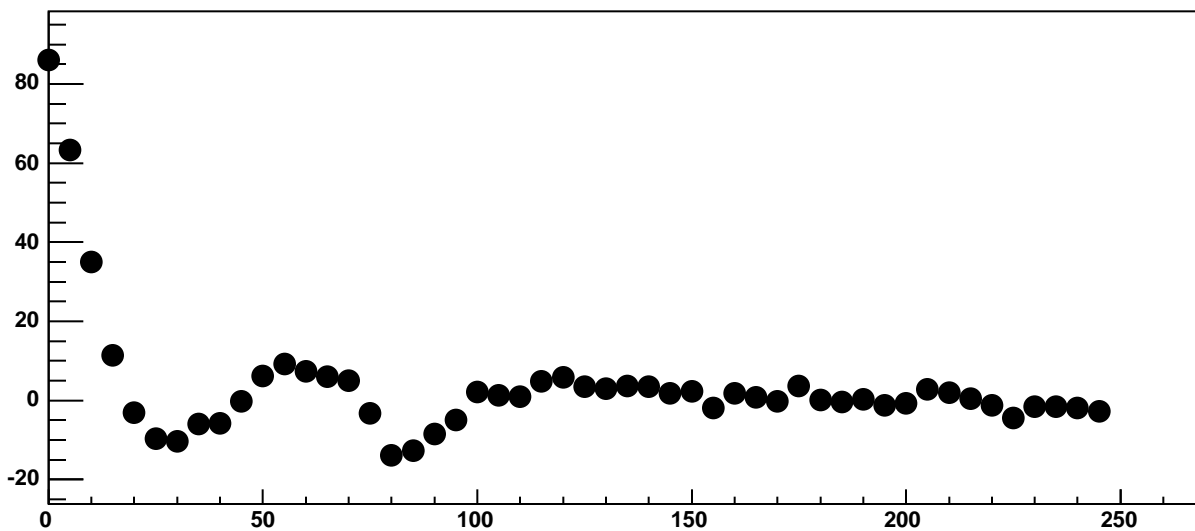


χ^2 / ndf	454.7 / 41
p0	-357.6 ± 0.6134
p1	82.38 ± 0.3895
p2	$-6.013\text{e}+07 \pm 7.96\text{e}+05$
p3	$2.07\text{e}+07 \pm 2.276\text{e}+05$
p4	2.776 ± 0.02149

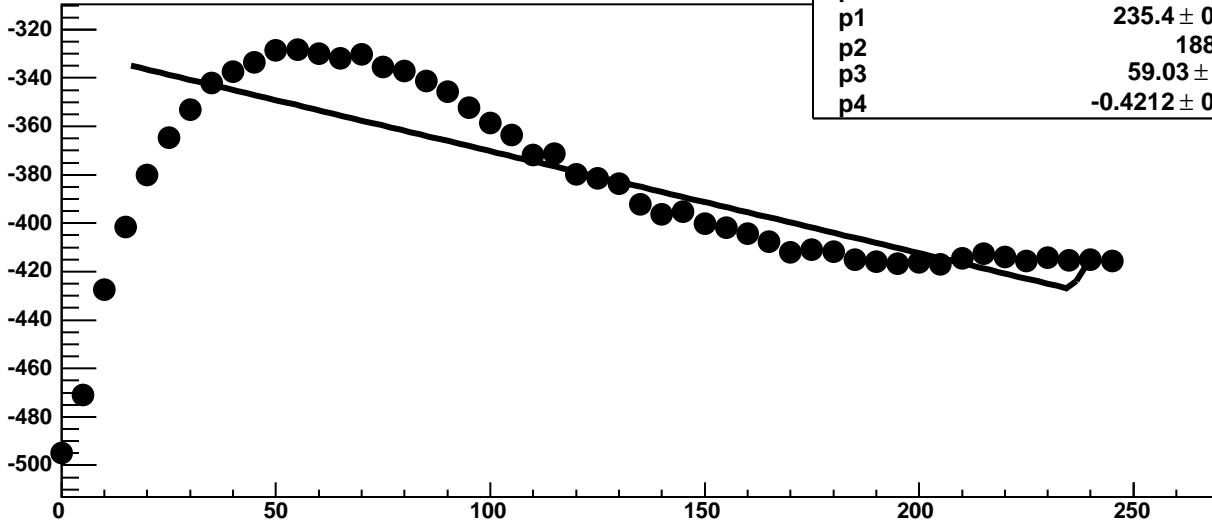
Chip 9, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

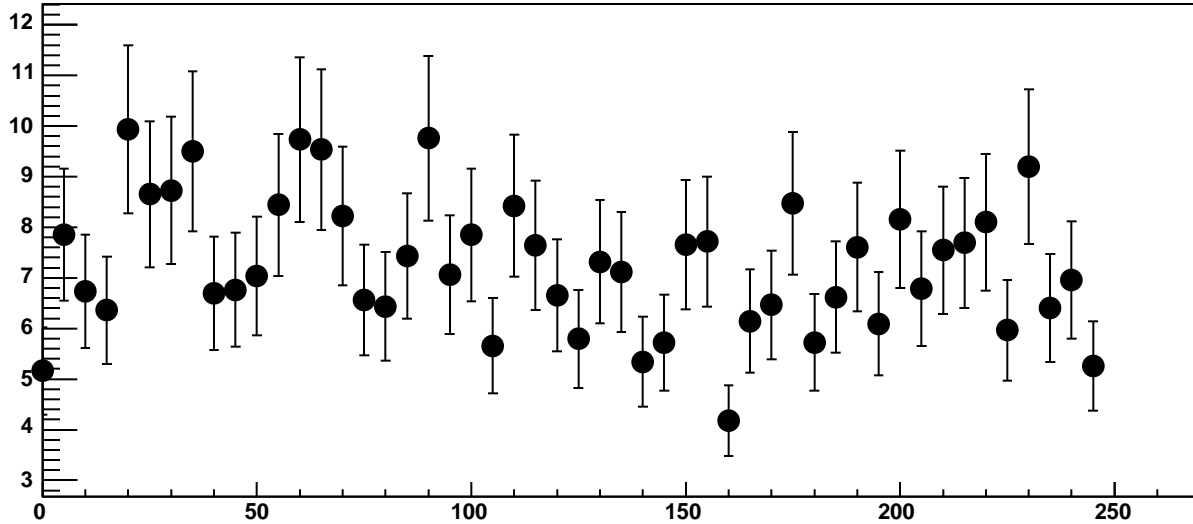


Chip 9, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold

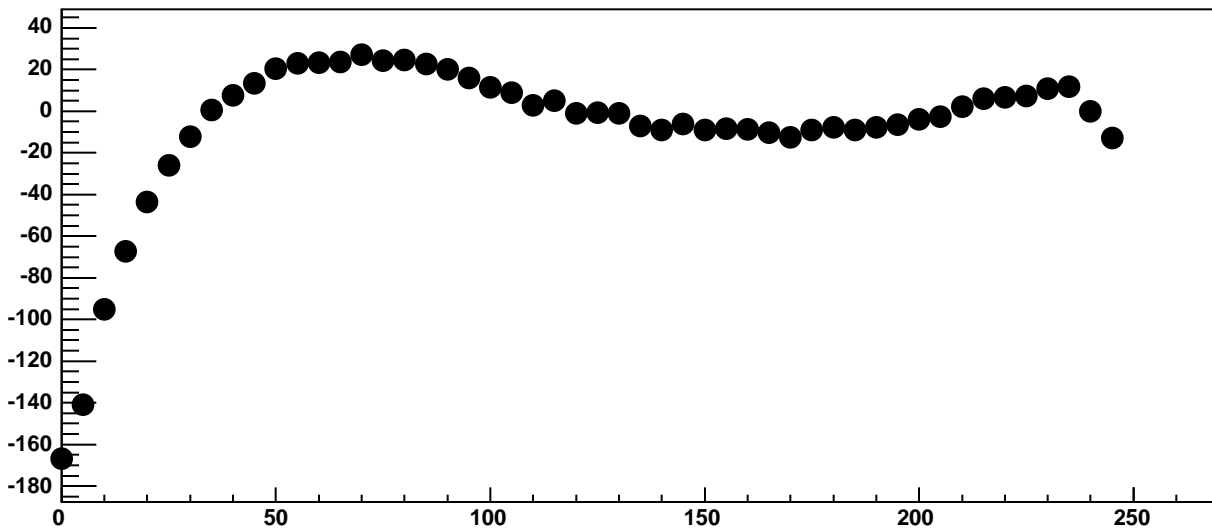


χ^2 / ndf	5161 / 41
p0	-427.2 ± 0.4692
p1	235.4 ± 0.002143
p2	188 ± 22.08
p3	59.03 ± 0.09493
p4	-0.4212 ± 0.003893

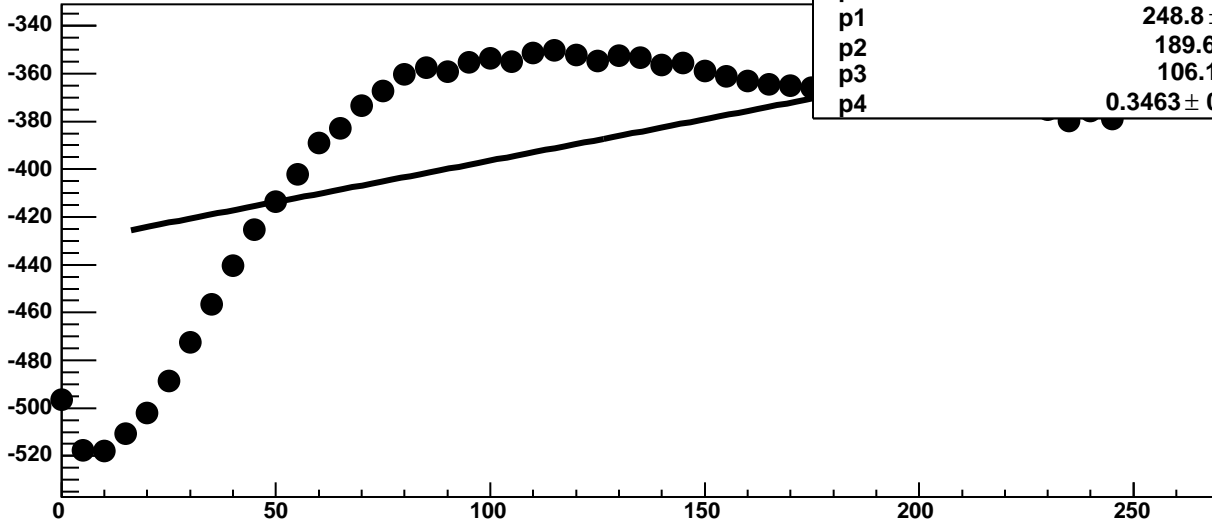
Chip 9, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold

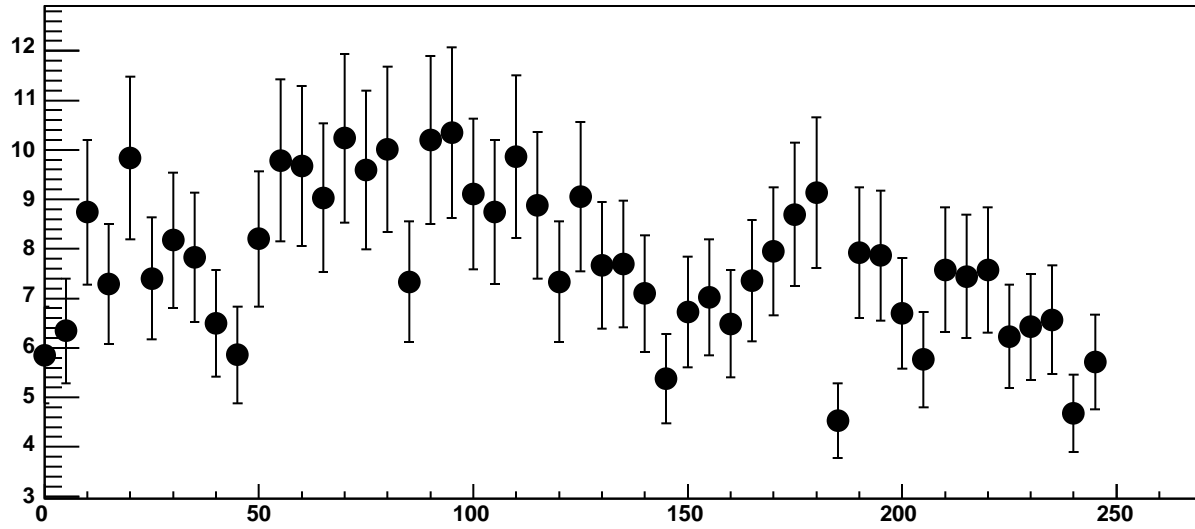


Chip 9, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

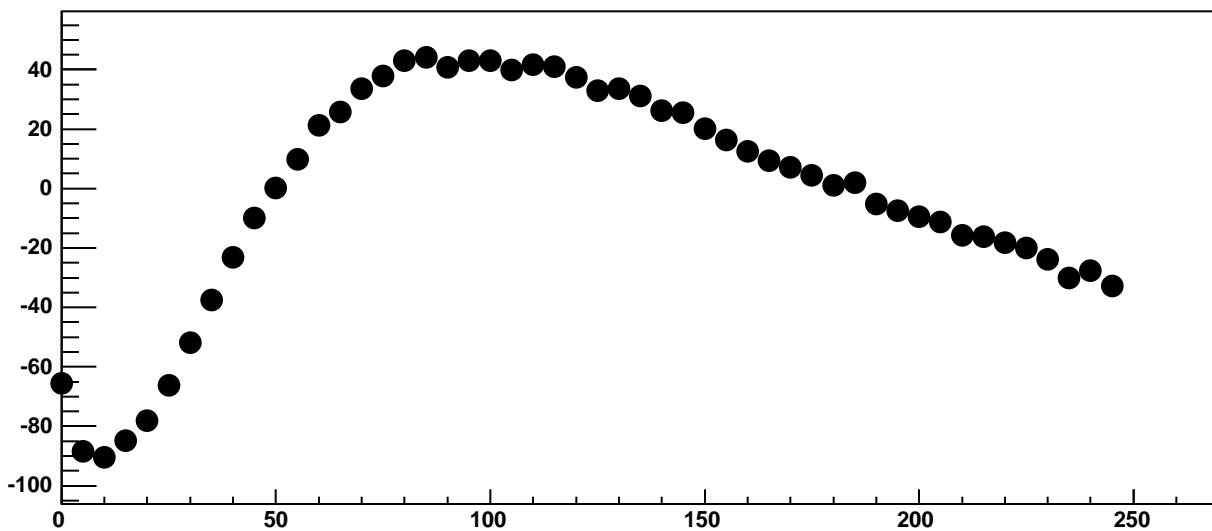


χ^2 / ndf	1.522e+04 / 41
p0	-344.9 ± 0.2494
p1	248.8 ± 0.7203
p2	189.6 ± 84.85
p3	106.1 ± 1.414
p4	0.3463 ± 0.001951

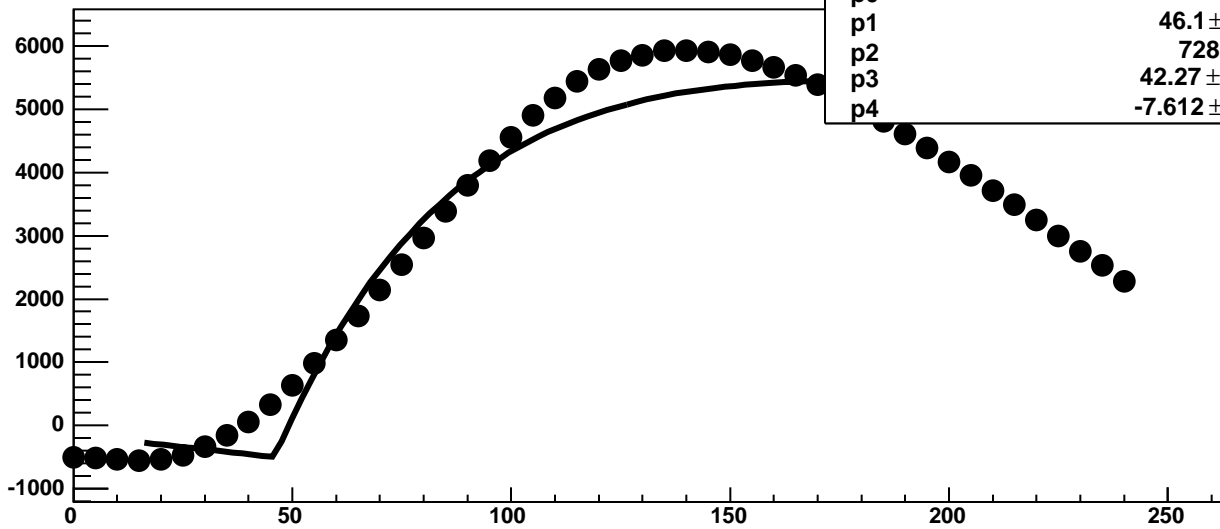
Chip 9, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



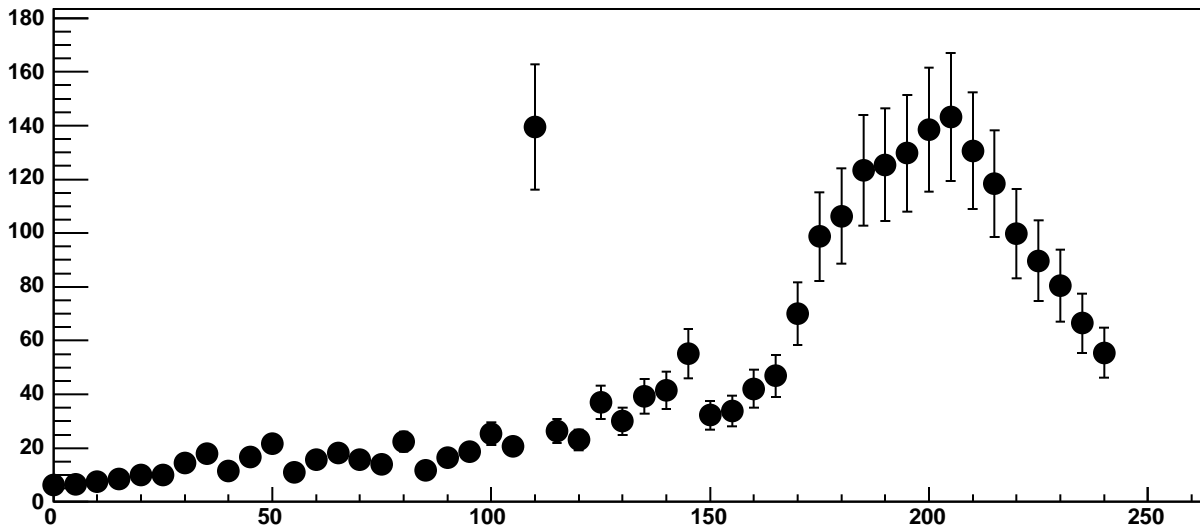
Chip 9, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold



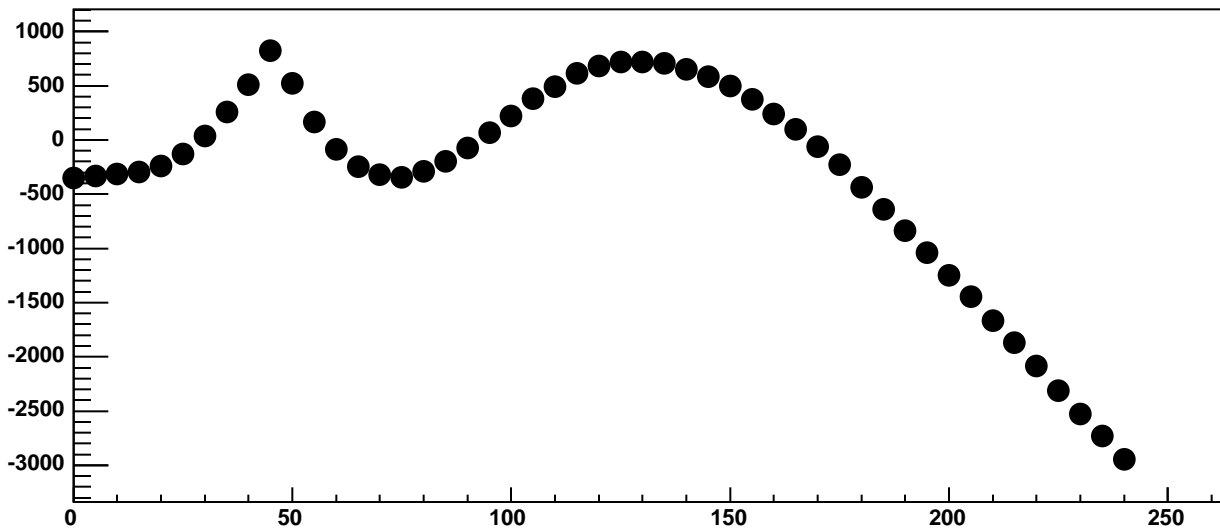
Chip 9, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold



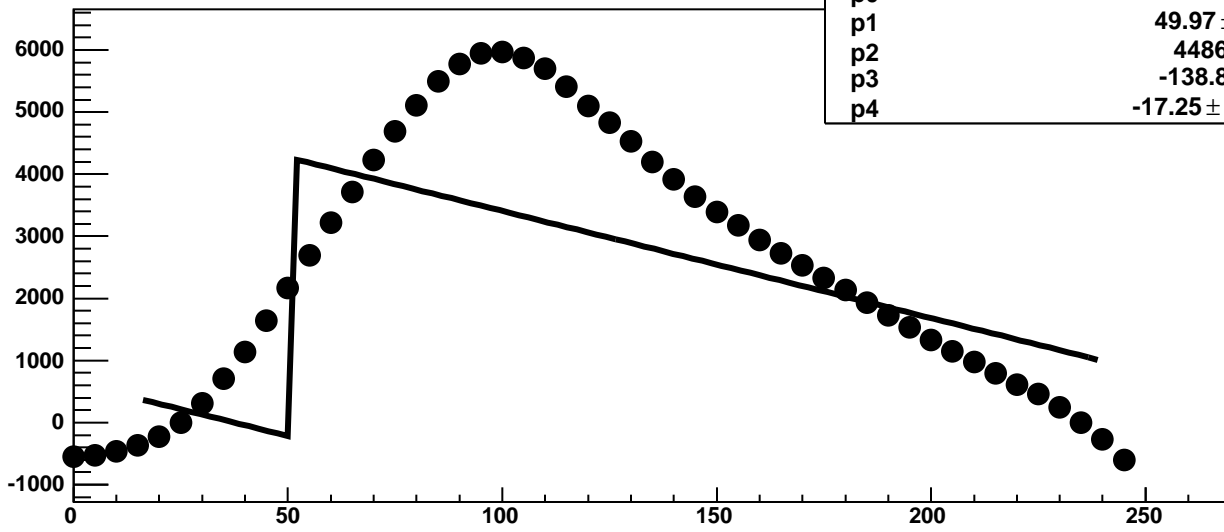
Chip 9, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

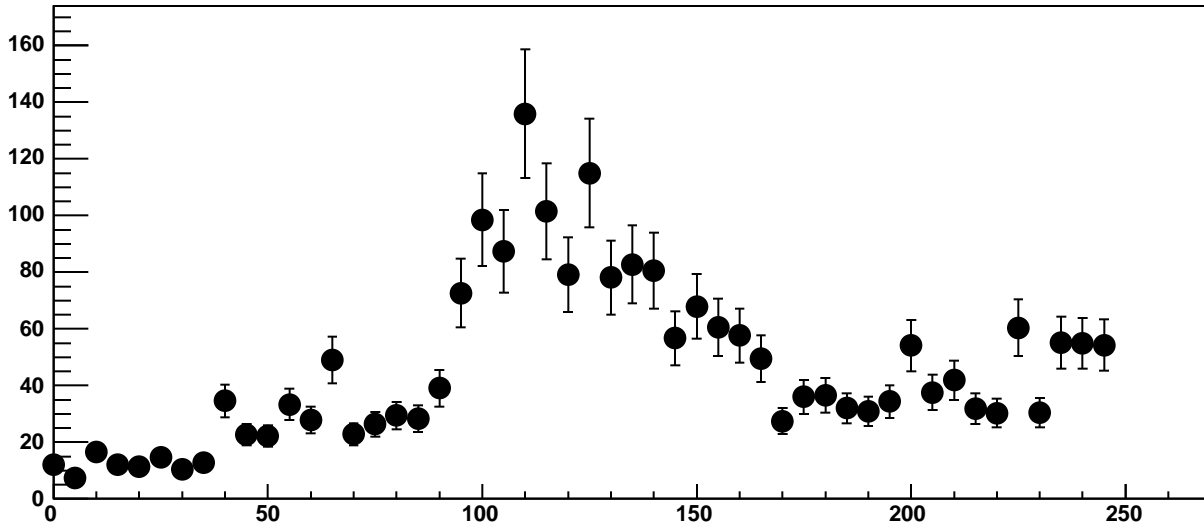


Chip 9, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold

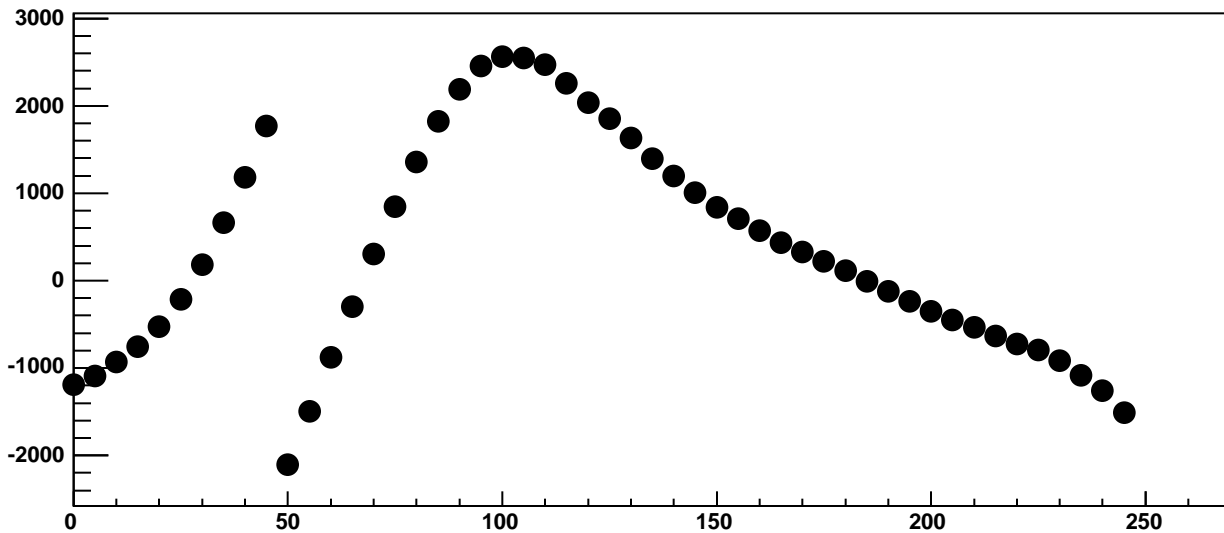


χ^2 / ndf	9.278e+05 / 41
p0	-215.4 ± 9.483
p1	49.97 ± 0.2893
p2	4486 ± 6.674
p3	-138.8 ± 2.317
p4	-17.25 ± 0.09579

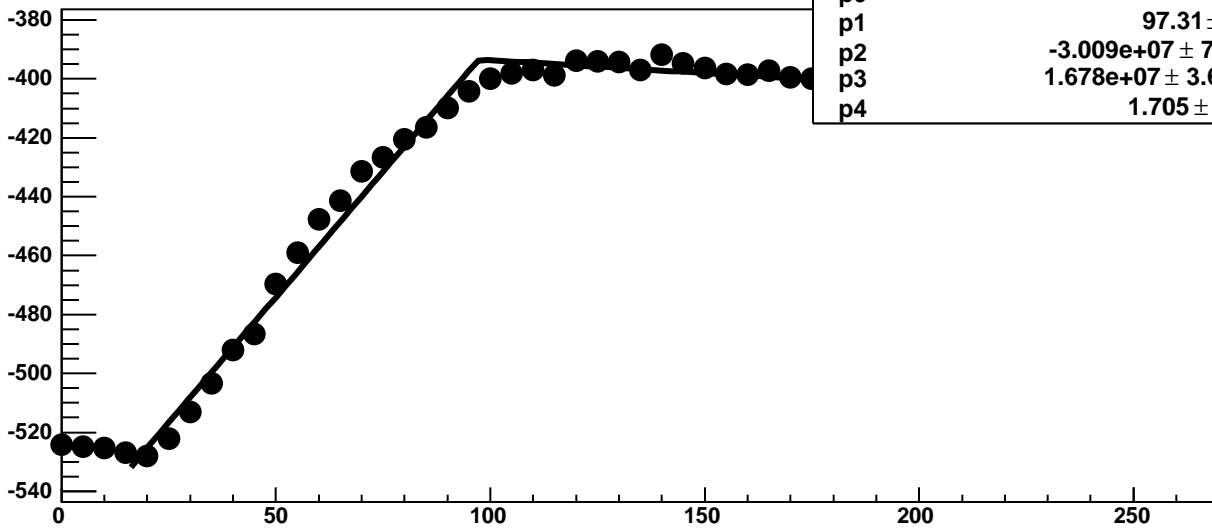
Chip 9, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold

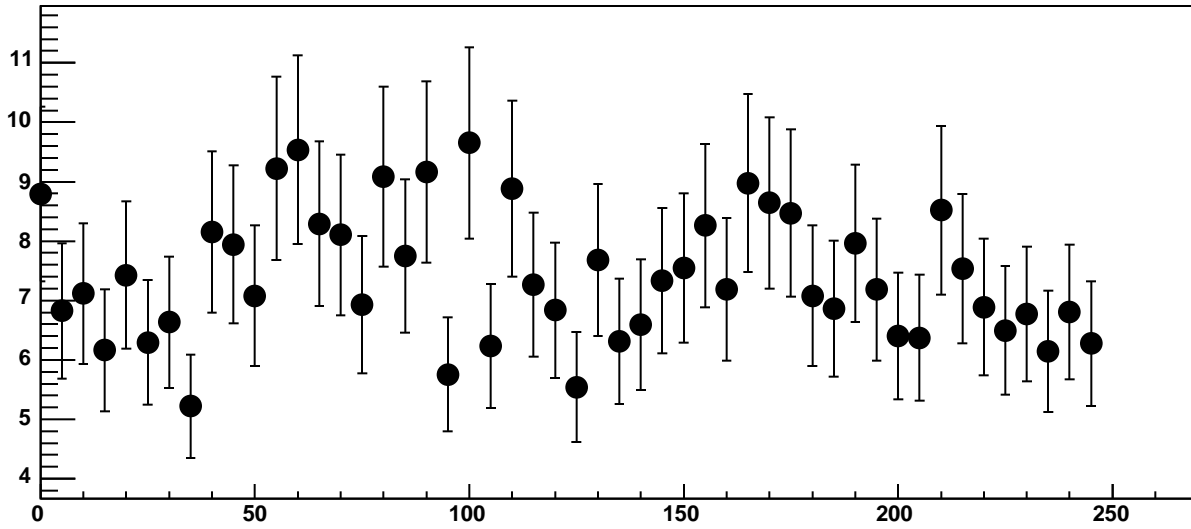


Chip 9, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

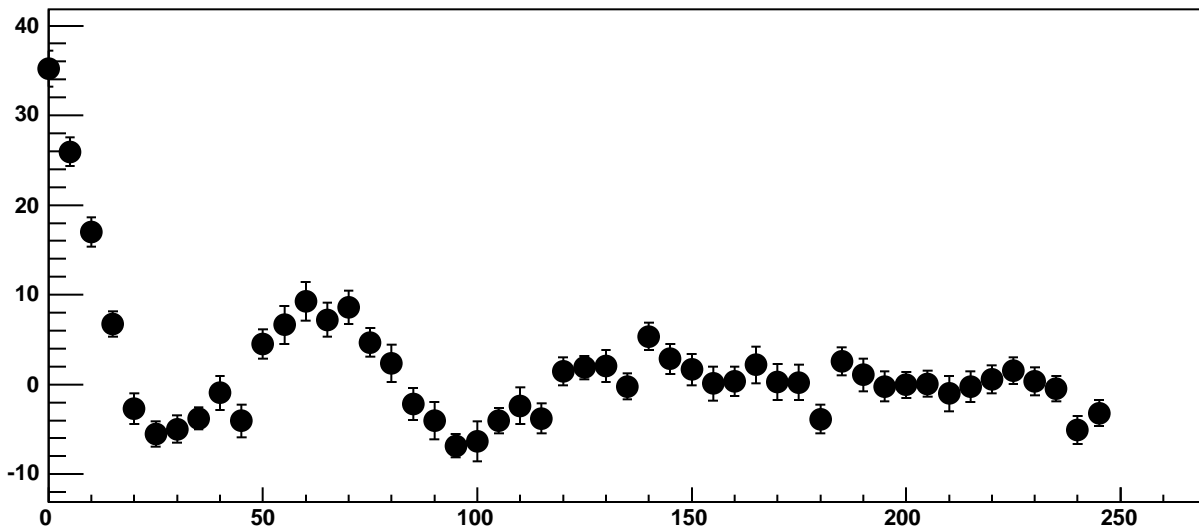


χ^2 / ndf	244.7 / 41
p0	-393.4 ± 0.5824
p1	97.31 ± 0.5656
p2	$-3.009\text{e}+07 \pm 7.31\text{e}+05$
p3	$1.678\text{e}+07 \pm 3.659\text{e}+05$
p4	1.705 ± 0.01556

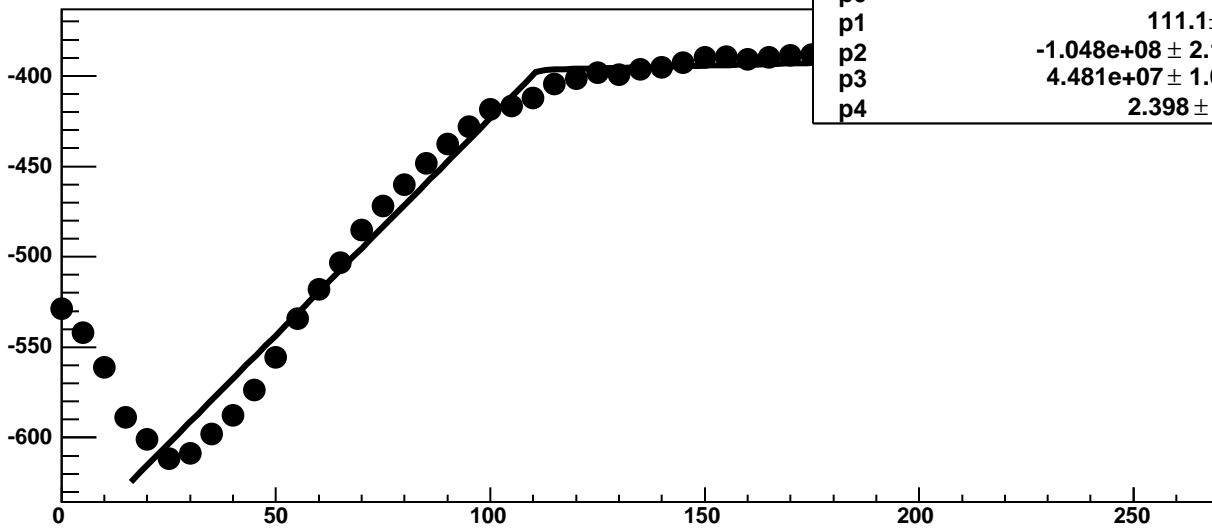
Chip 9, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold

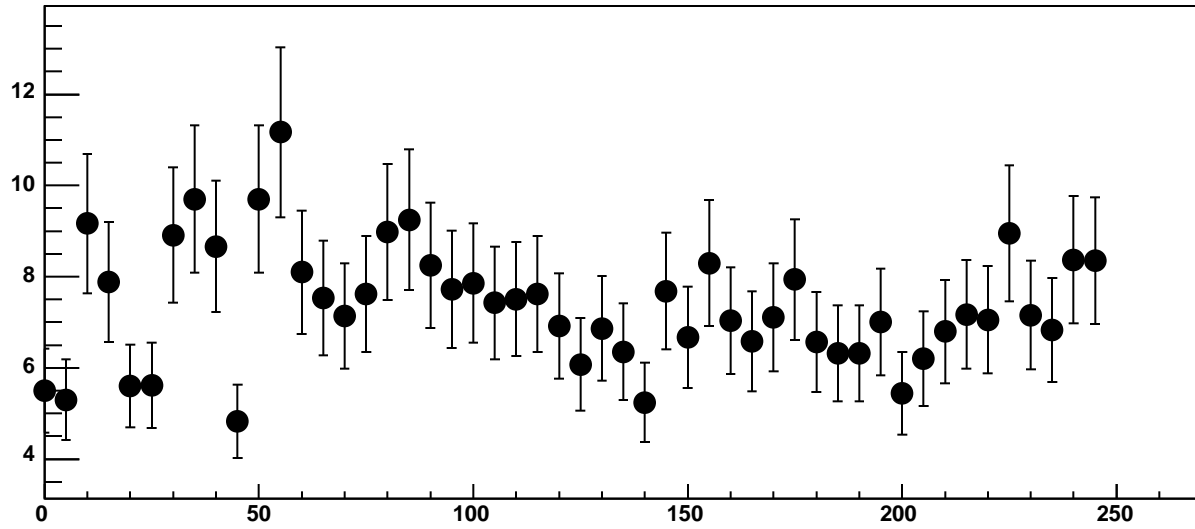


Chip 9, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

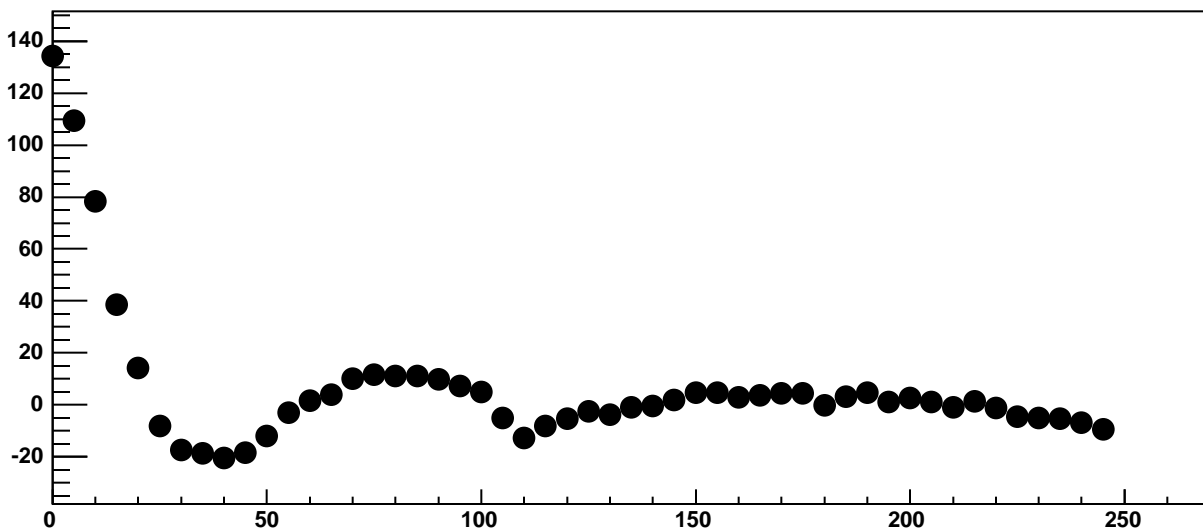


χ^2 / ndf	1582 / 41
p0	-396.7 ± 0.6153
p1	111.1 ± 0.3709
p2	$-1.048\text{e}+08 \pm 2.131\text{e}+06$
p3	$4.481\text{e}+07 \pm 1.073\text{e}+06$
p4	2.398 ± 0.01227

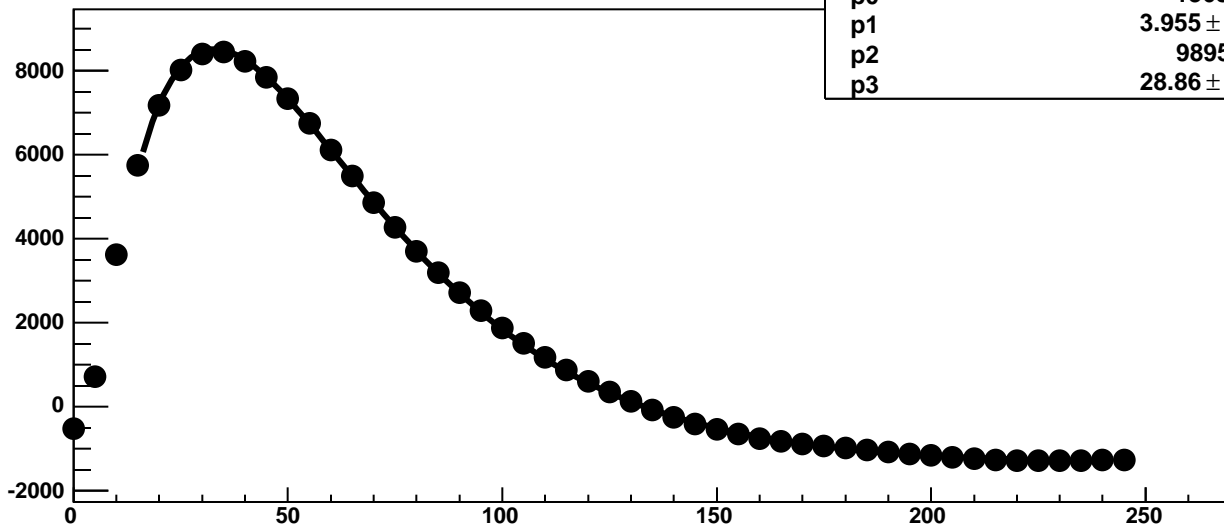
Chip 9, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

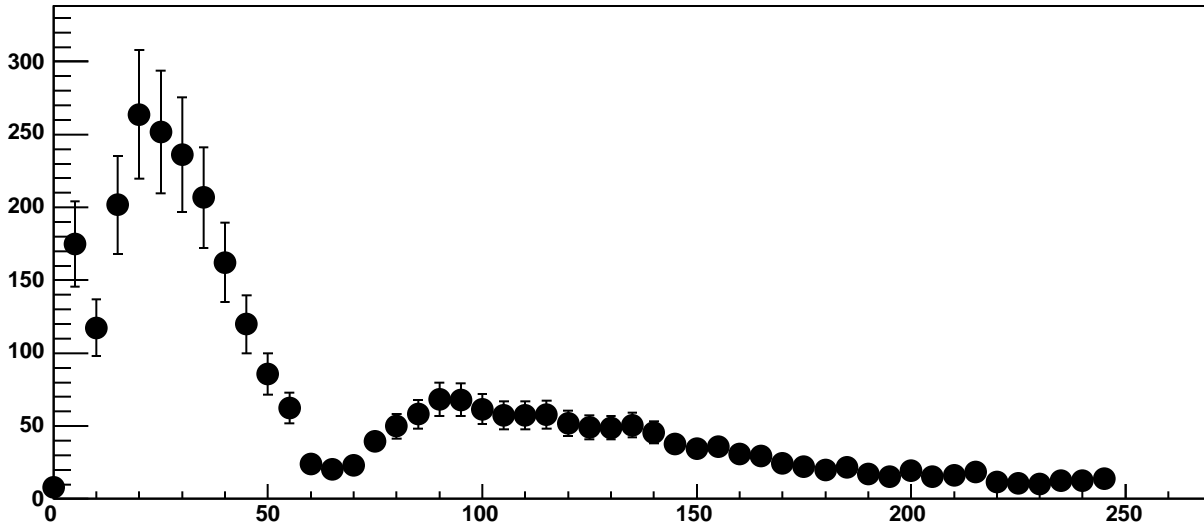


Chip 9, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold

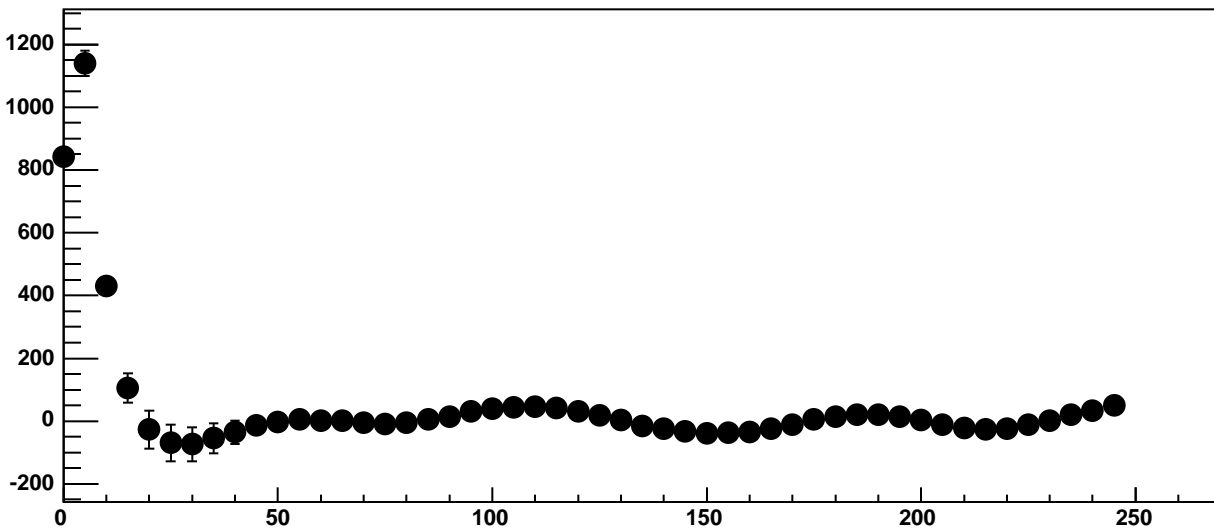


χ^2 / ndf	619.1 / 42
p0	-1368 ± 1.258
p1	3.955 ± 0.08559
p2	9895 ± 11.06
p3	28.86 ± 0.03155

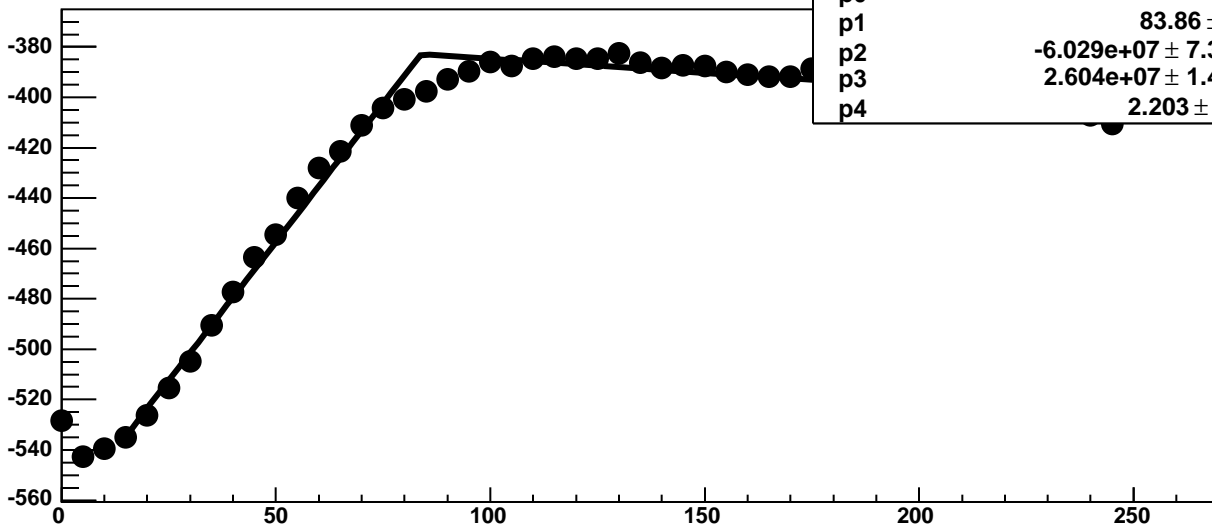
Chip 9, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold

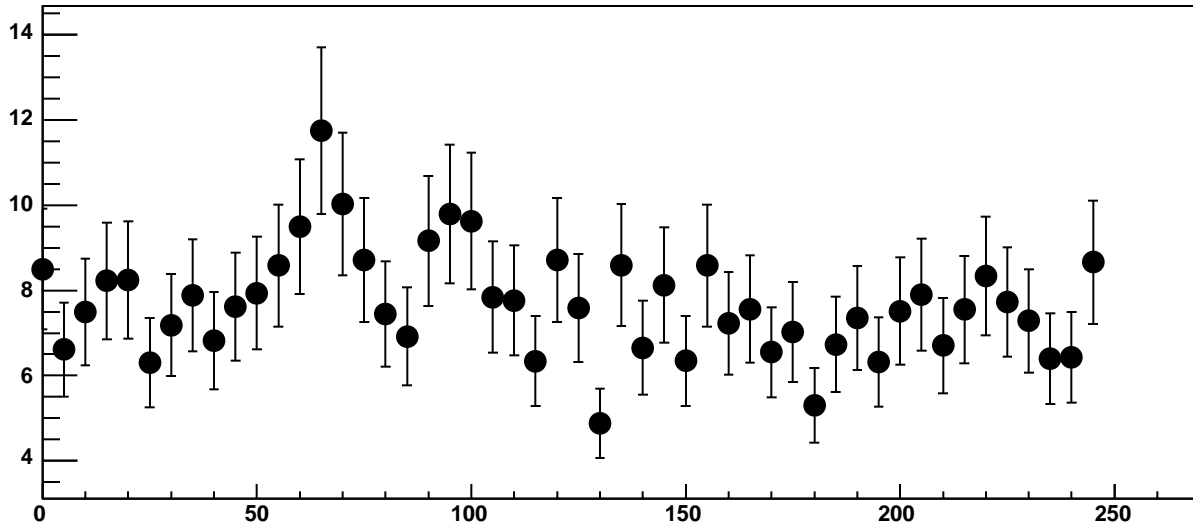


Chip 9, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

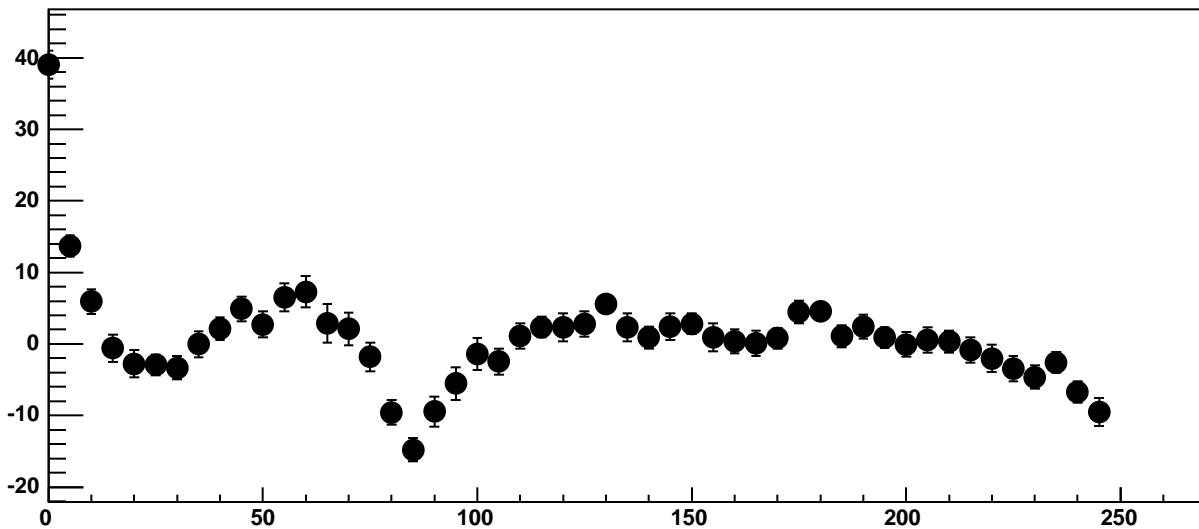


χ^2 / ndf	294.4 / 41
p0	-382.8 ± 0.5787
p1	83.86 ± 0.5354
p2	$-6.029\text{e}+07 \pm 7.325\text{e}+05$
p3	$2.604\text{e}+07 \pm 1.463\text{e}+05$
p4	2.203 ± 0.02465

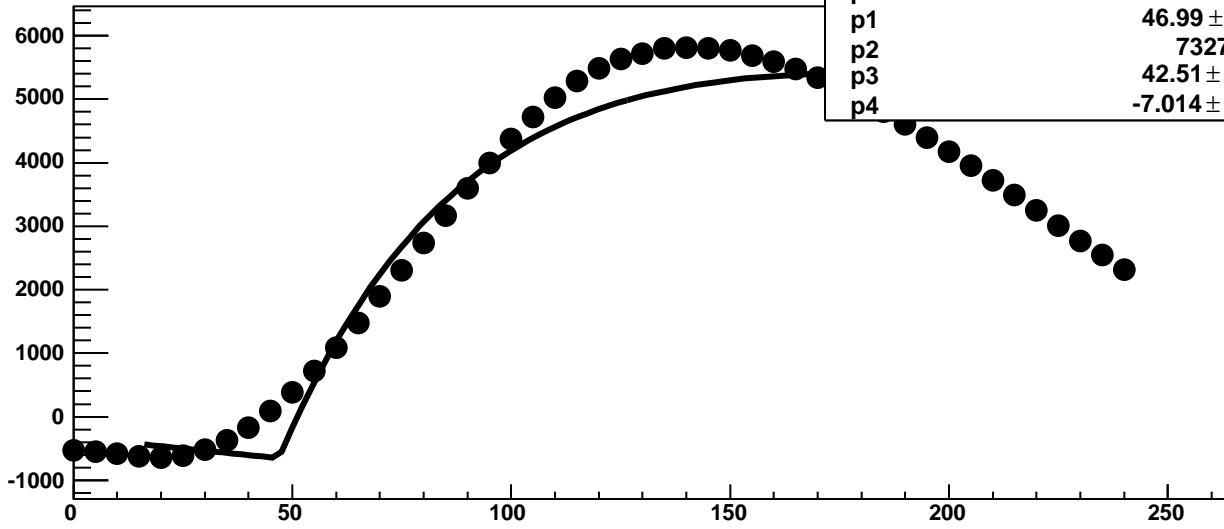
Chip 9, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

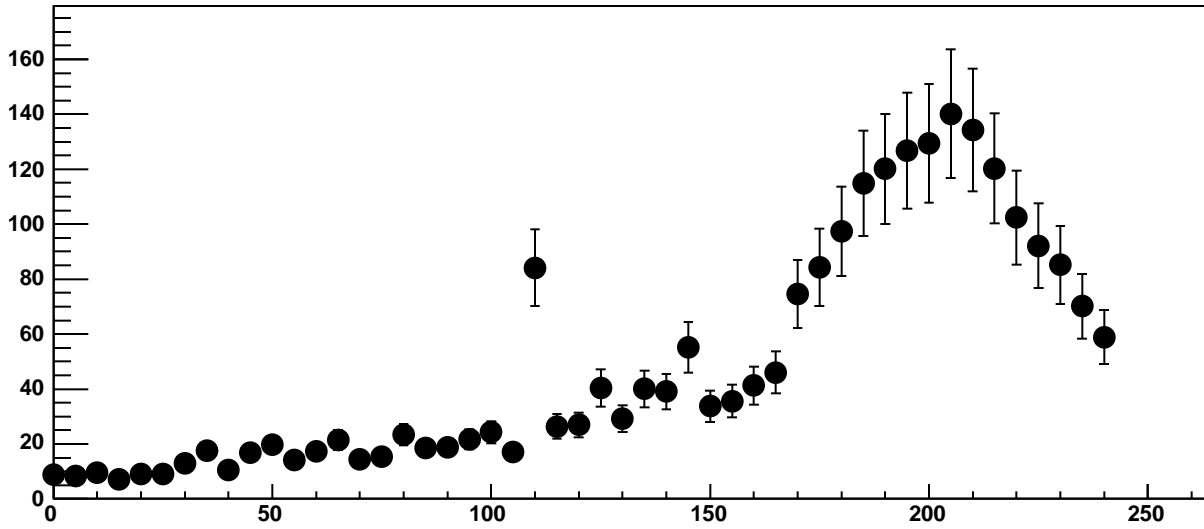


Chip 9, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold

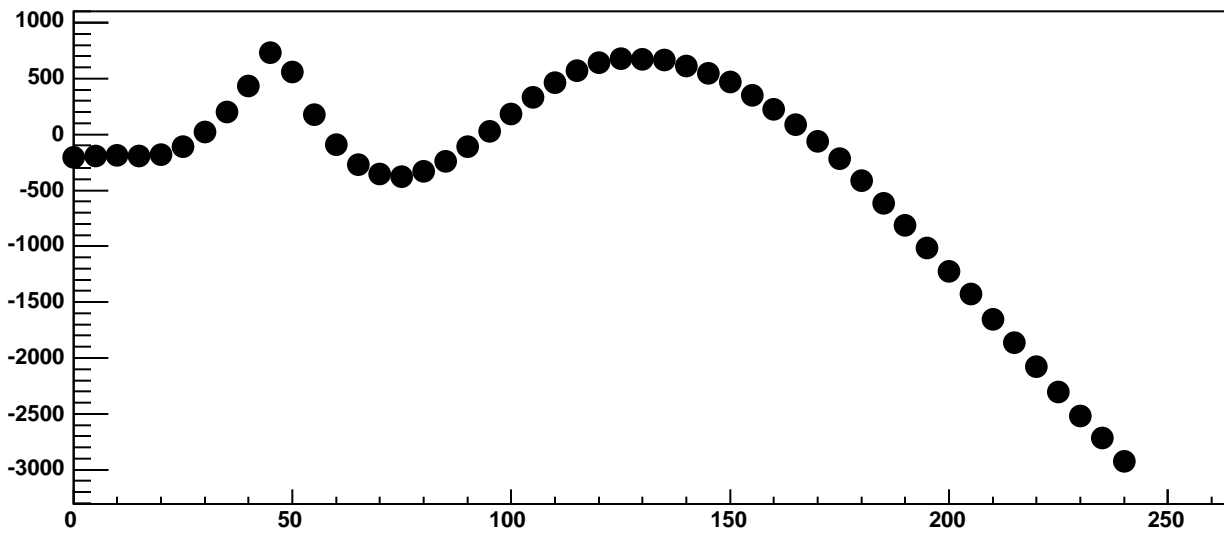


χ^2 / ndf	3.321e+05 / 41
p0	-656.4 ± 1.649
p1	46.99 ± 0.02091
p2	7327 ± 11.63
p3	42.51 ± 0.07352
p4	-7.014 ± 0.06666

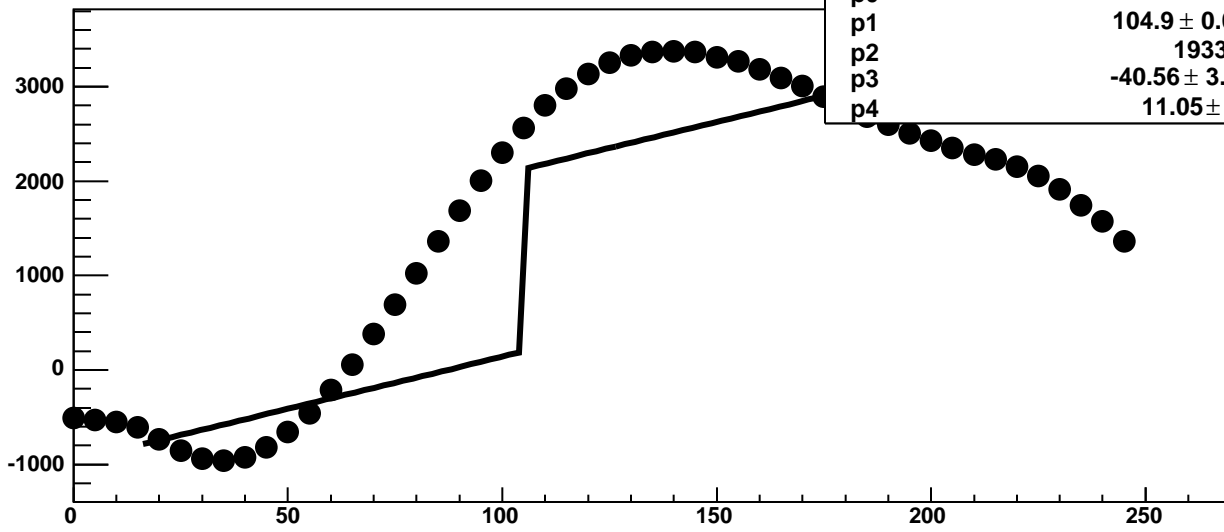
Chip 9, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

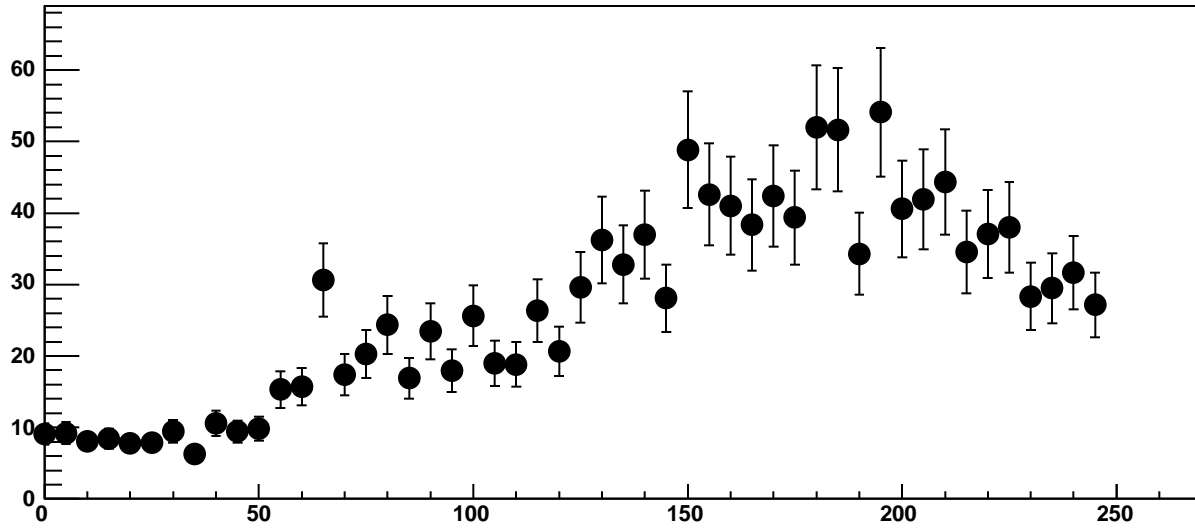


Chip 9, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

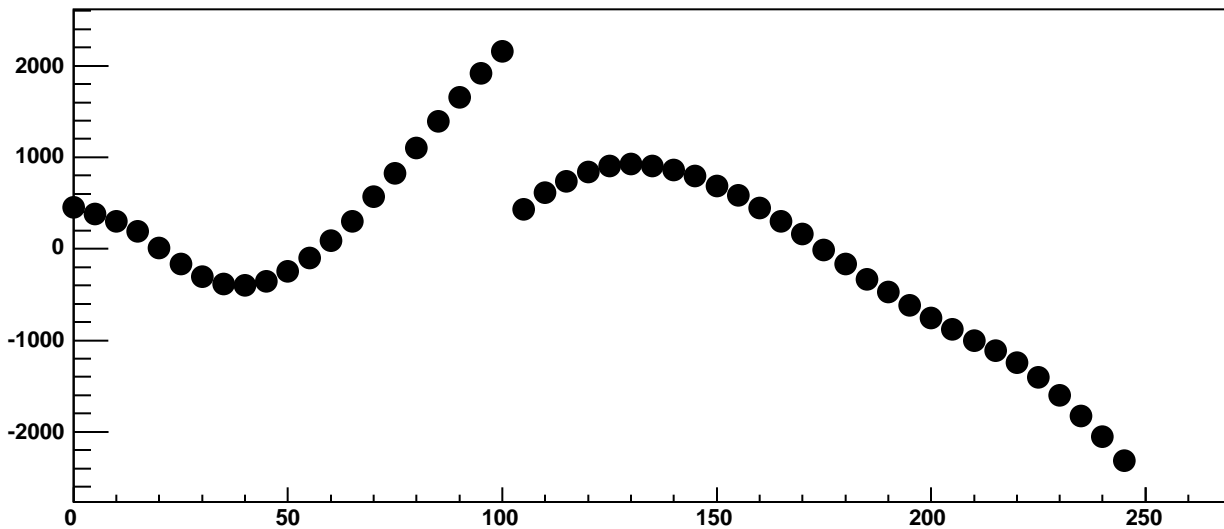


χ^2 / ndf	1.319e+06 / 41
p0	196.3 ± 1.509
p1	104.9 ± 0.0001405
p2	1933 ± 3.005
p3	-40.56 ± 3.502e-07
p4	11.05 ± 0.02104

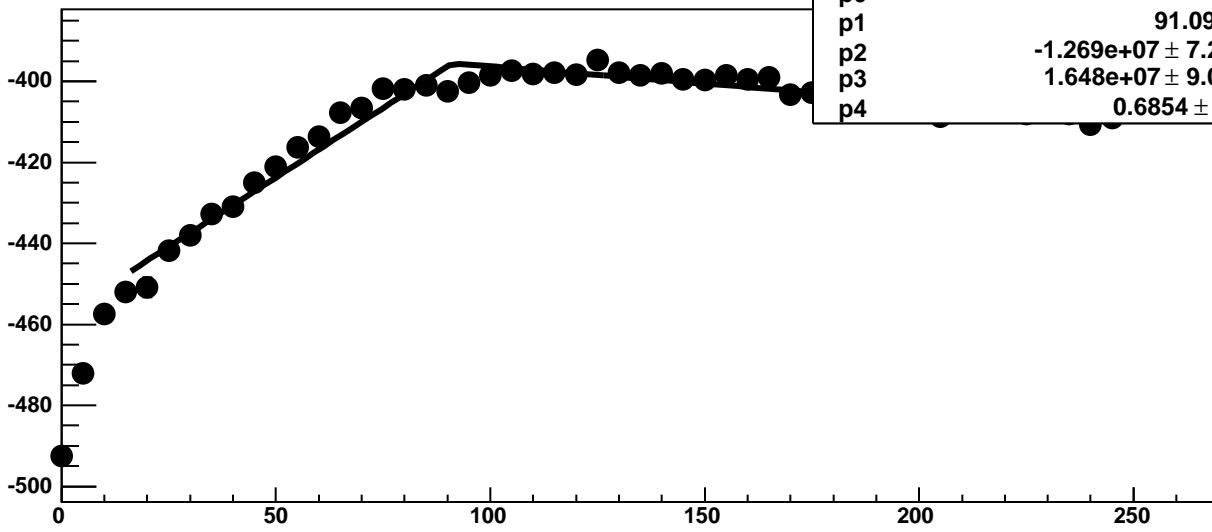
Chip 9, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

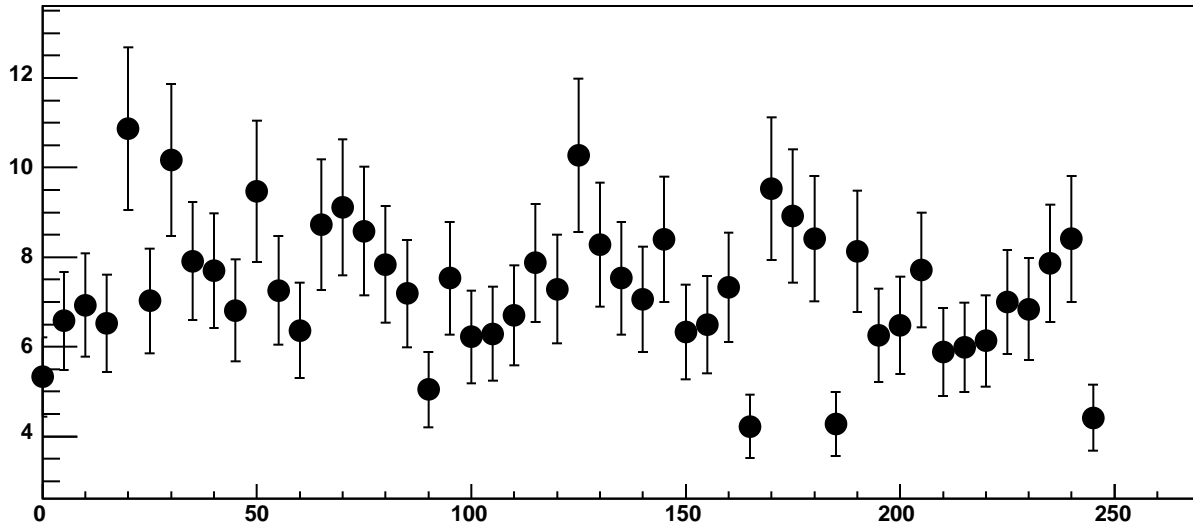


Chip 9, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold

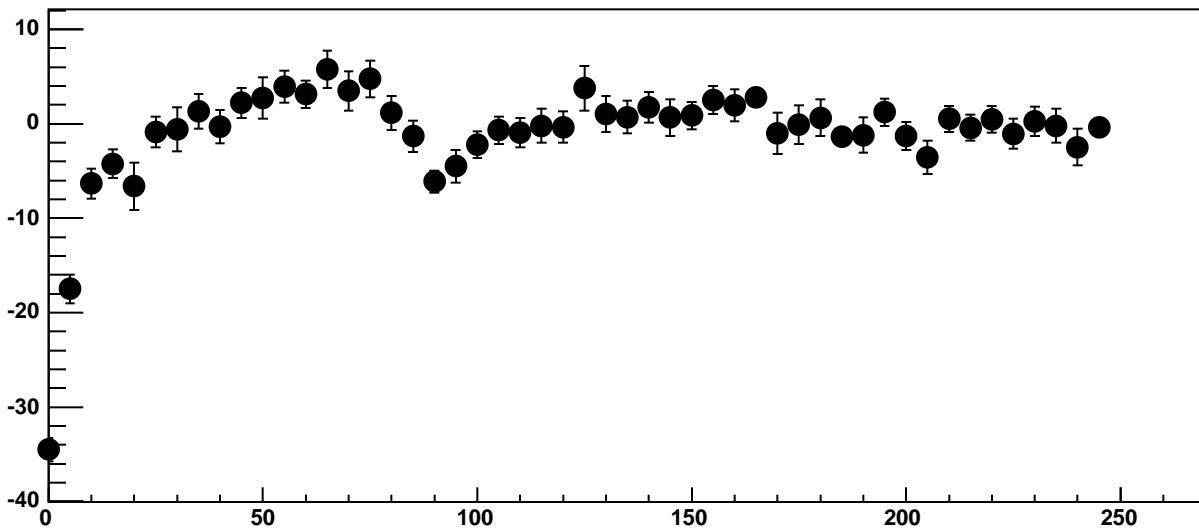


χ^2 / ndf	112.9 / 41
p0	-395.5 ± 0.5421
p1	91.09 ± 1.259
p2	$-1.269\text{e}+07 \pm 7.287\text{e}+05$
p3	$1.648\text{e}+07 \pm 9.091\text{e}+05$
p4	0.6854 ± 0.01758

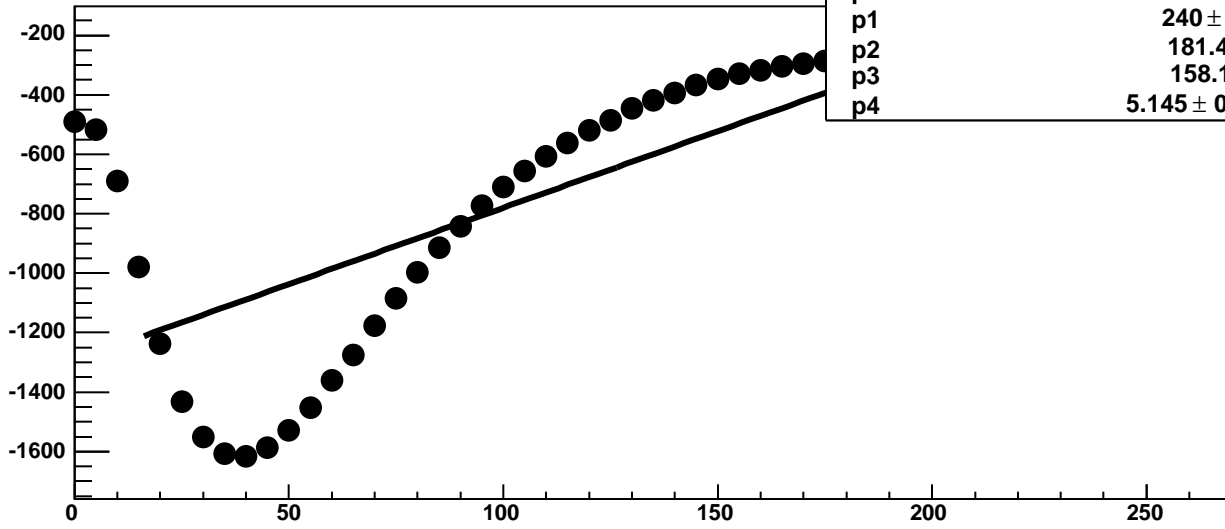
Chip 9, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold

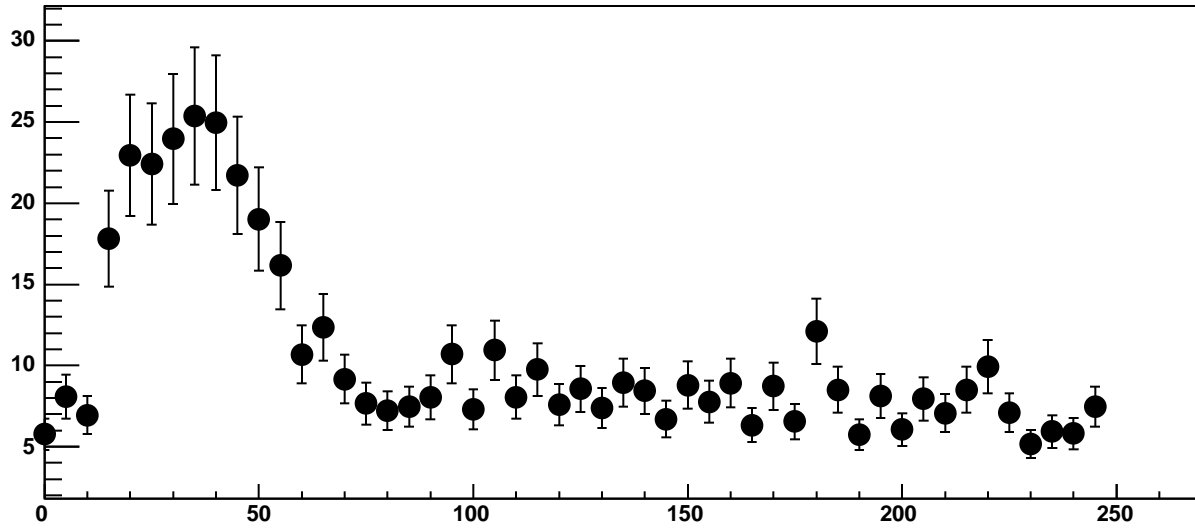


Chip 9, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

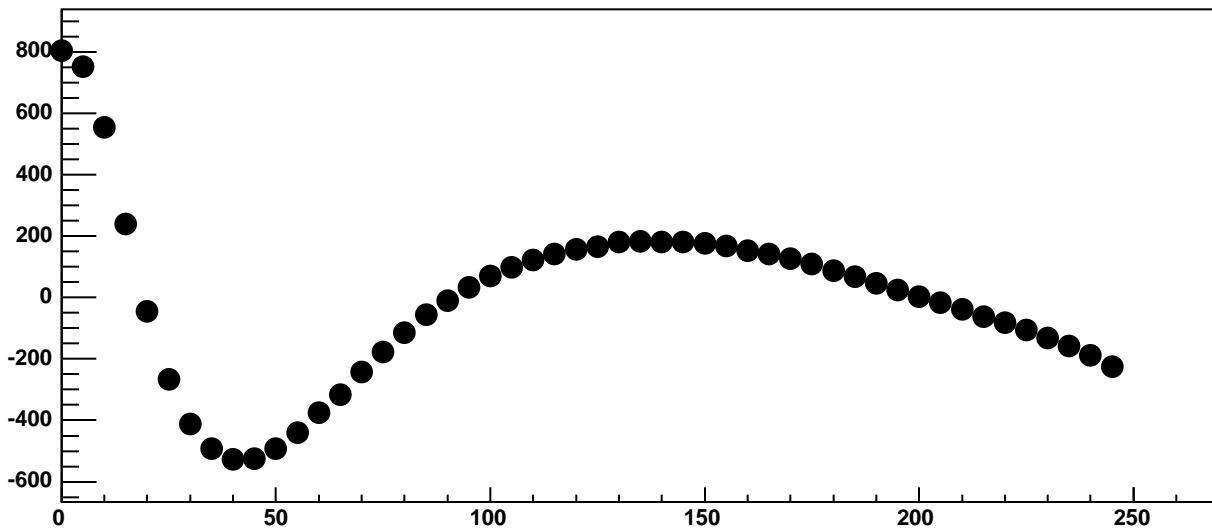


χ^2 / ndf	2.971e+05 / 41
p0	-59.27 ± 0.5244
p1	240 ± 0.03395
p2	181.4 ± 1.429
p3	158.1 ± 1.985
p4	5.145 ± 0.004957

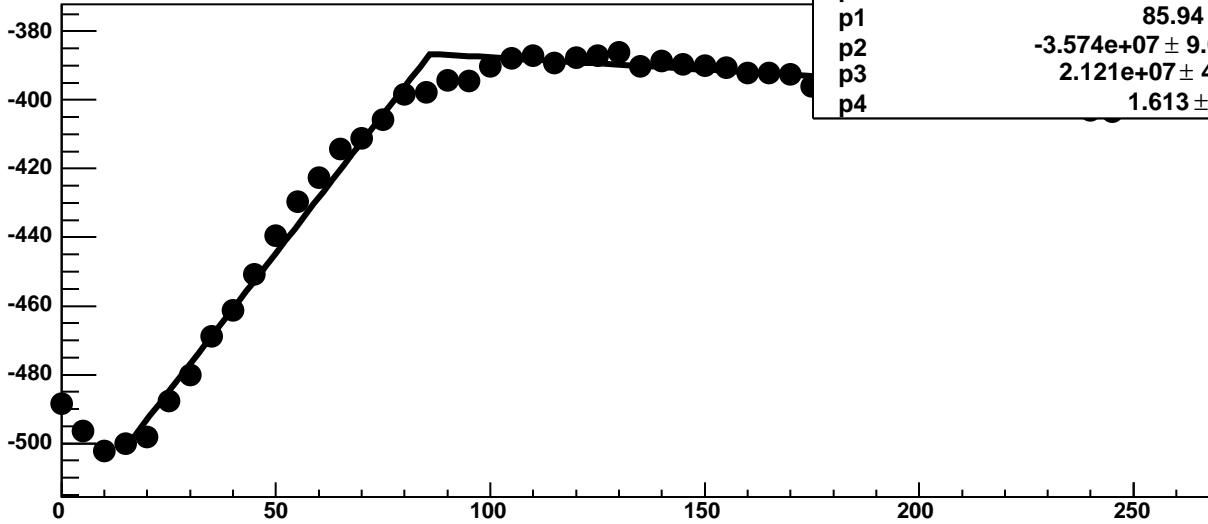
Chip 9, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

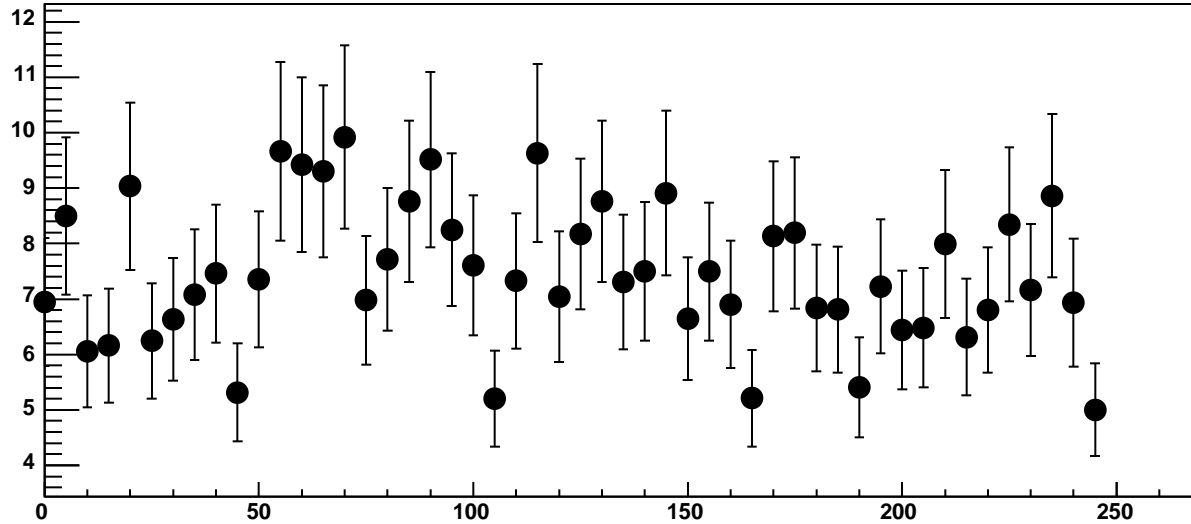


Chip 9, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

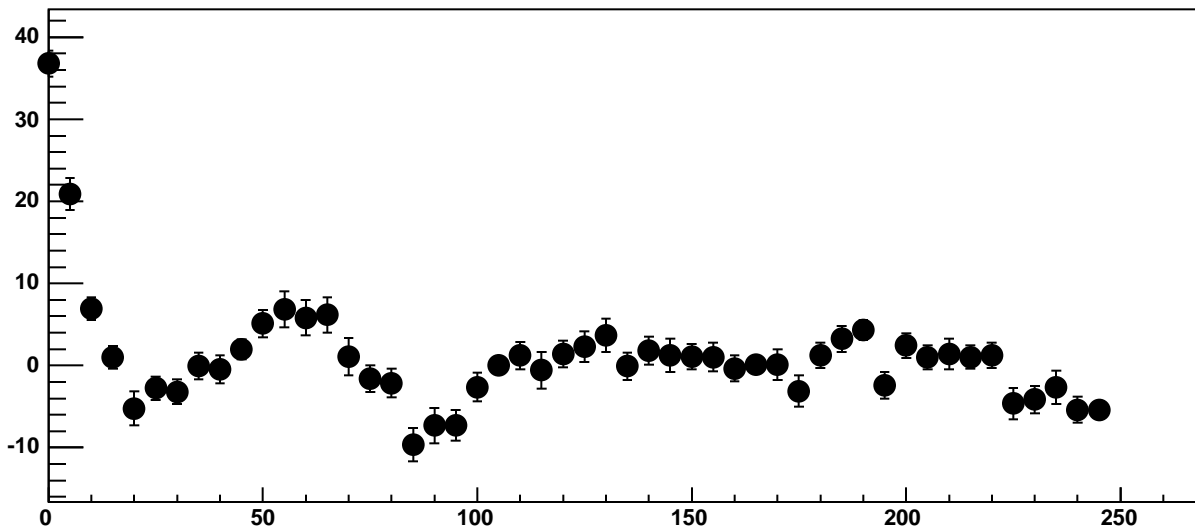


χ^2 / ndf	166.8 / 41
p0	-386.6 ± 0.5993
p1	85.94 ± 0.6658
p2	$-3.574\text{e}+07 \pm 9.041\text{e}+05$
p3	$2.121\text{e}+07 \pm 4.87\text{e}+05$
p4	1.613 ± 0.02062

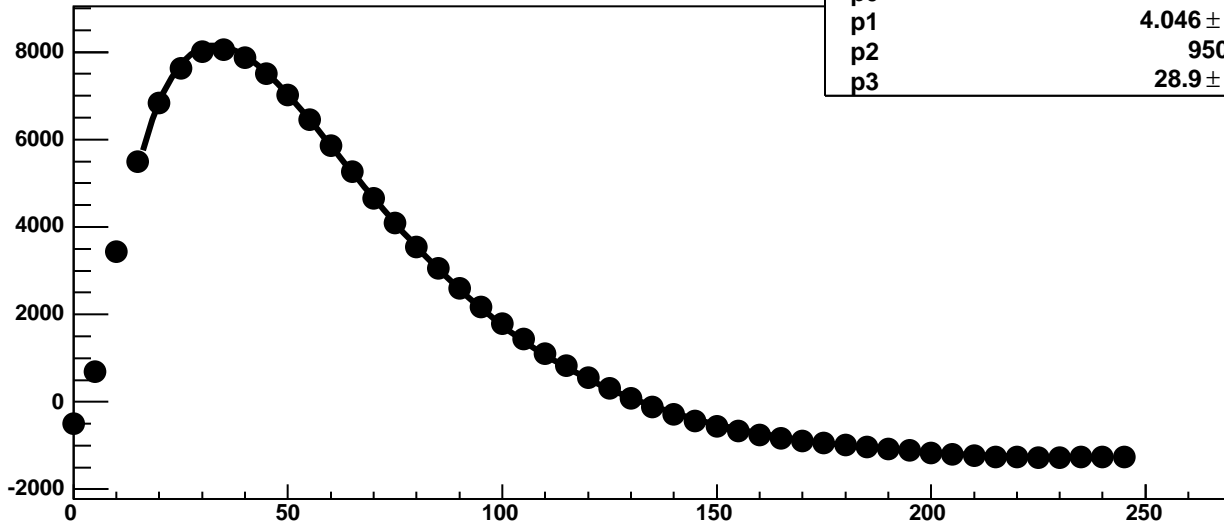
Chip 9, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

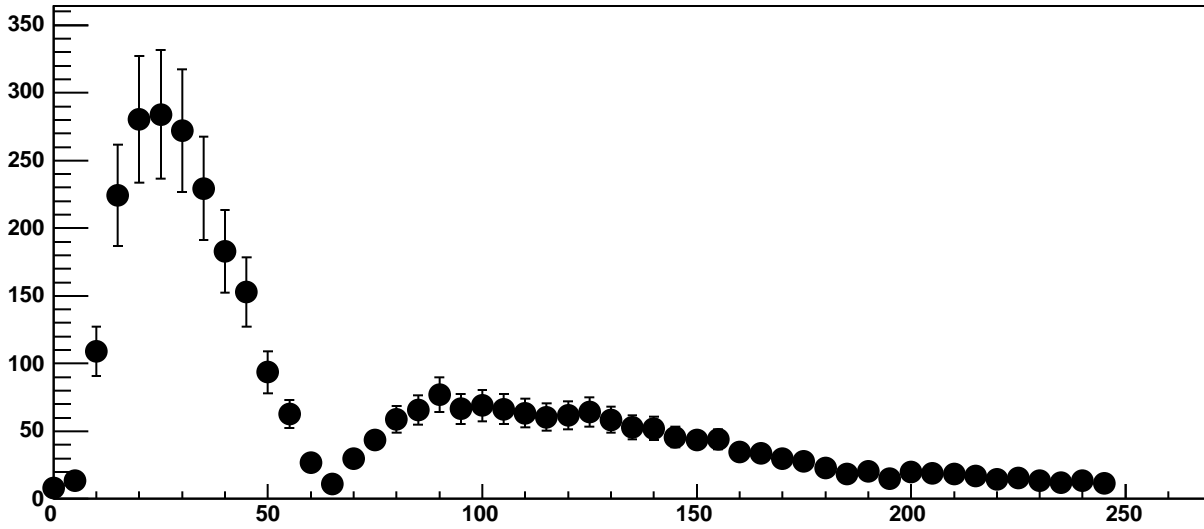


Chip 9, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold

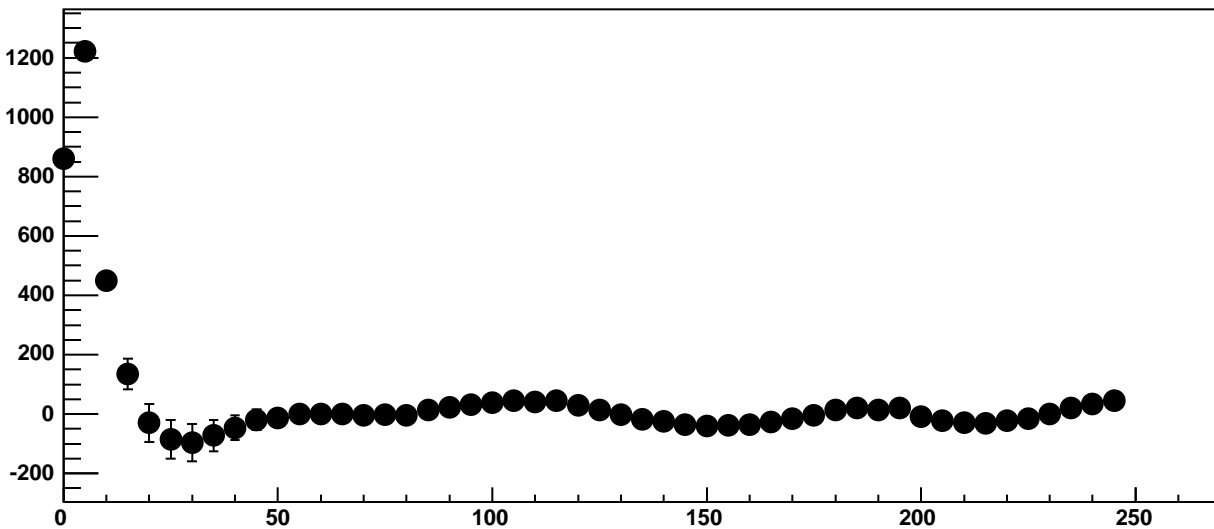


χ^2 / ndf	608 / 42
p0	-1354 ± 1.456
p1	4.046 ± 0.09744
p2	9506 ± 12
p3	28.9 ± 0.03714

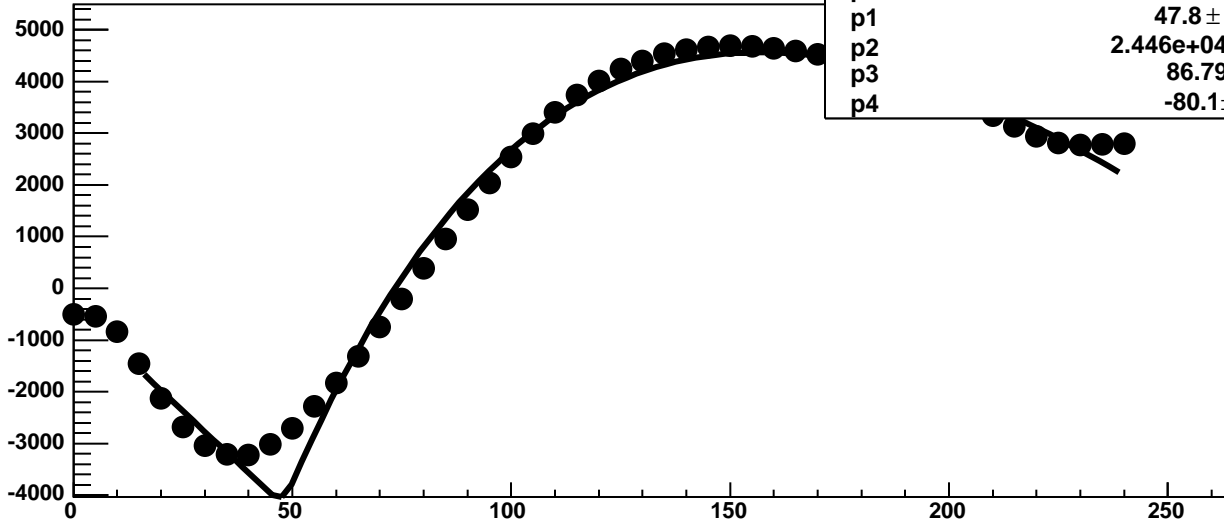
Chip 9, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold

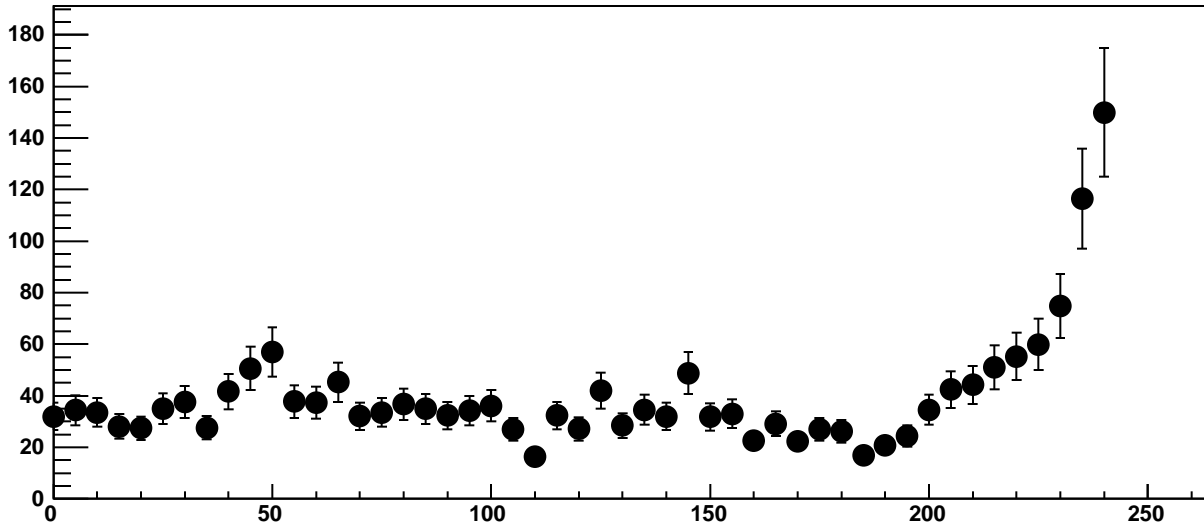


Chip 9, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

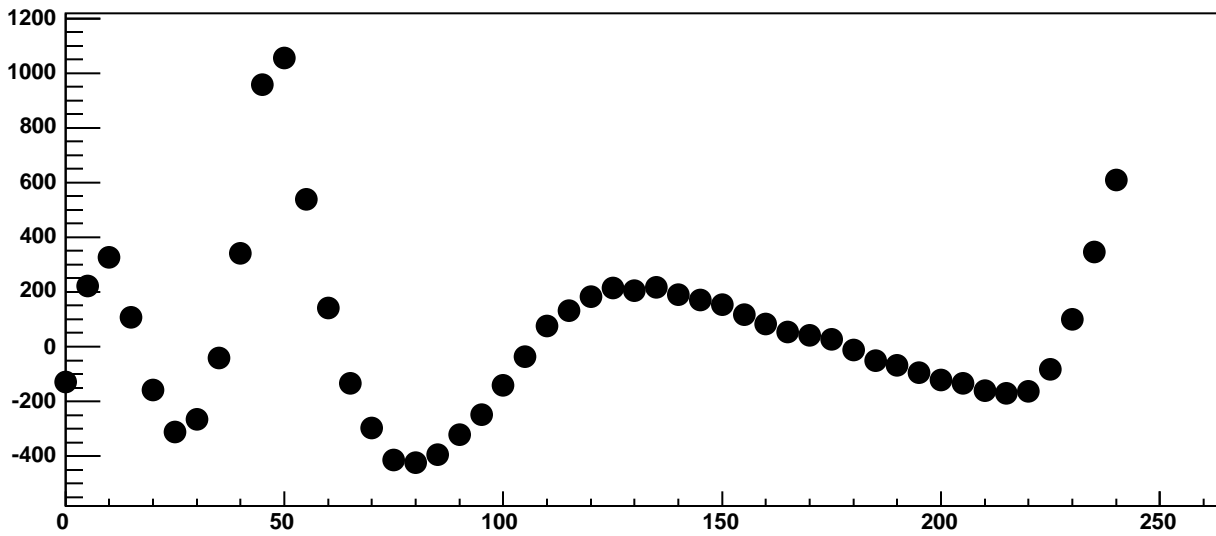


χ^2 / ndf	4.297e+04 / 41
p0	-4196 ± 4.136
p1	47.8 ± 0.03213
p2	2.446e+04 ± 68.75
p3	86.79 ± 0.198
p4	-80.1 ± 0.2449

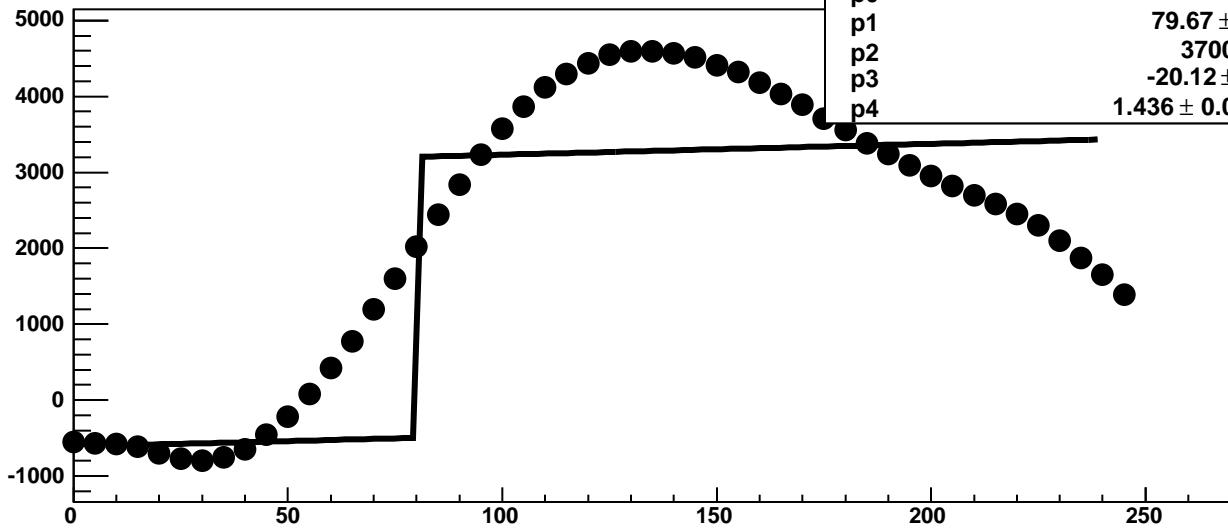
Chip 9, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold



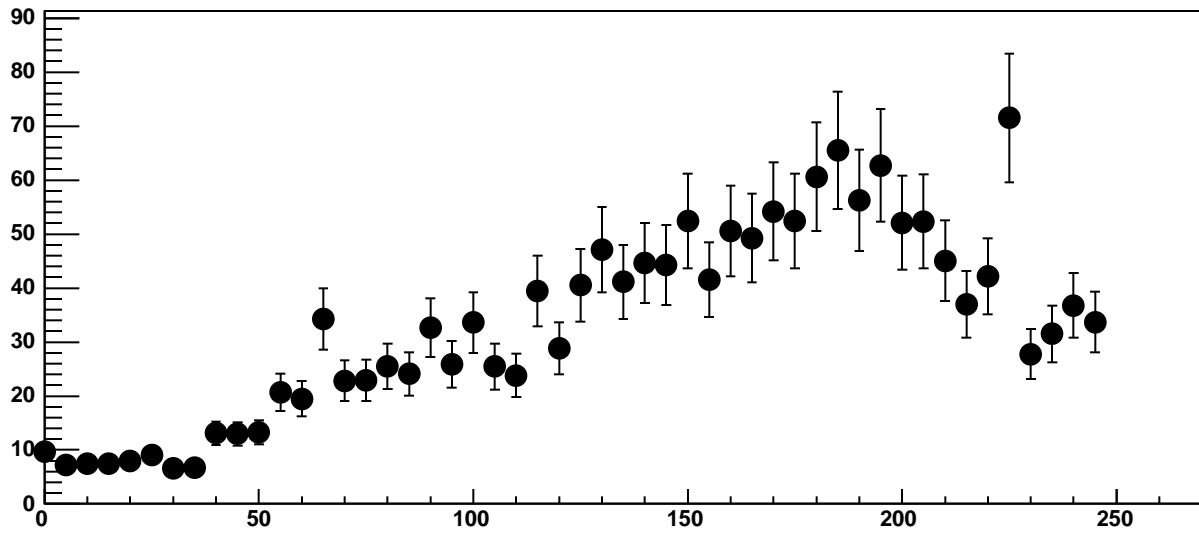
Chip 9, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold



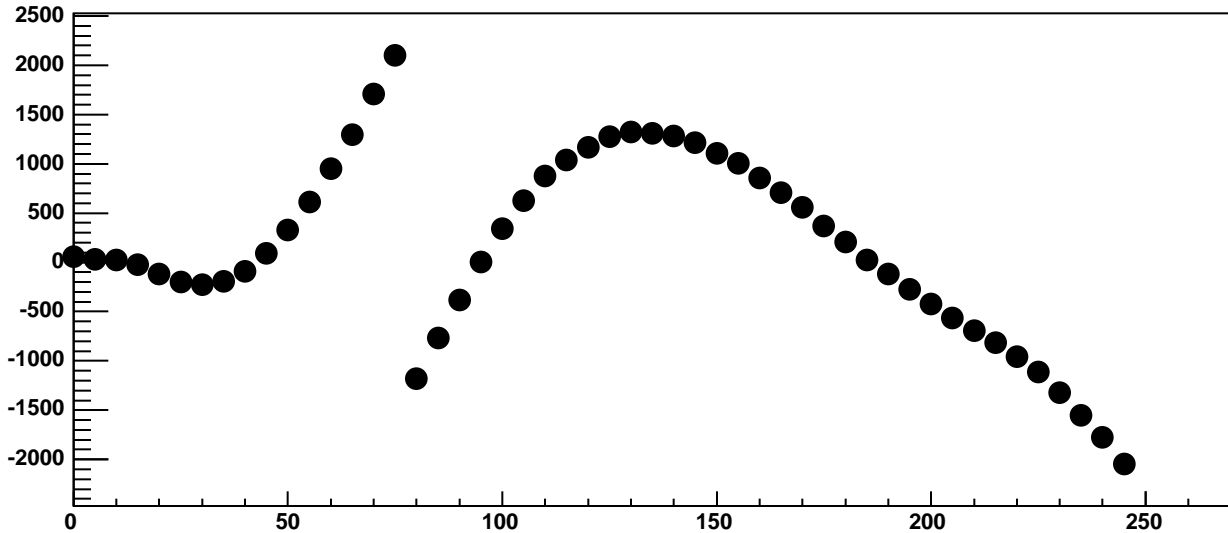
Chip 9, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold



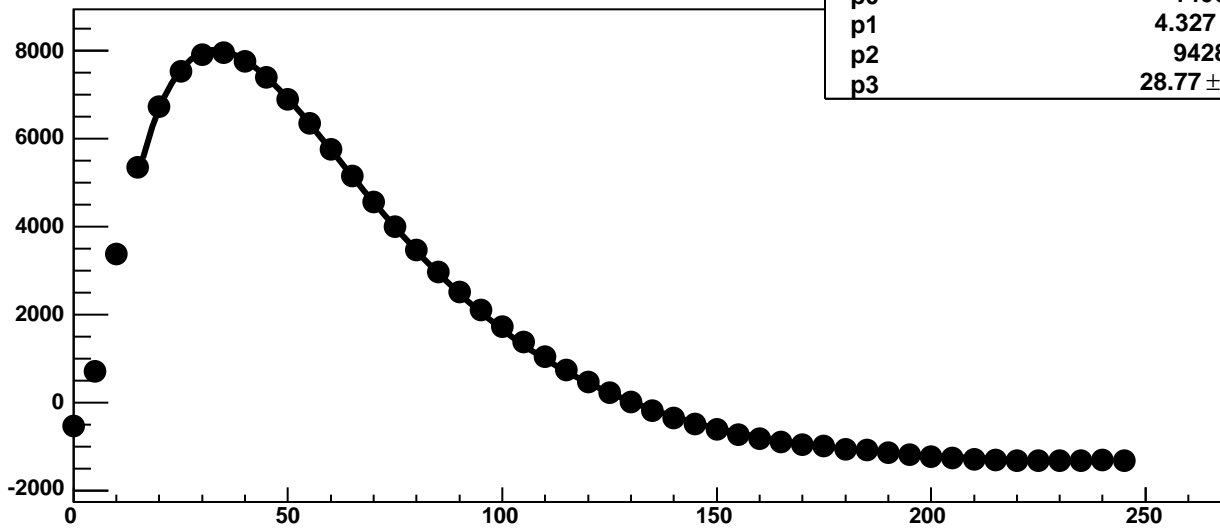
Chip 9, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold

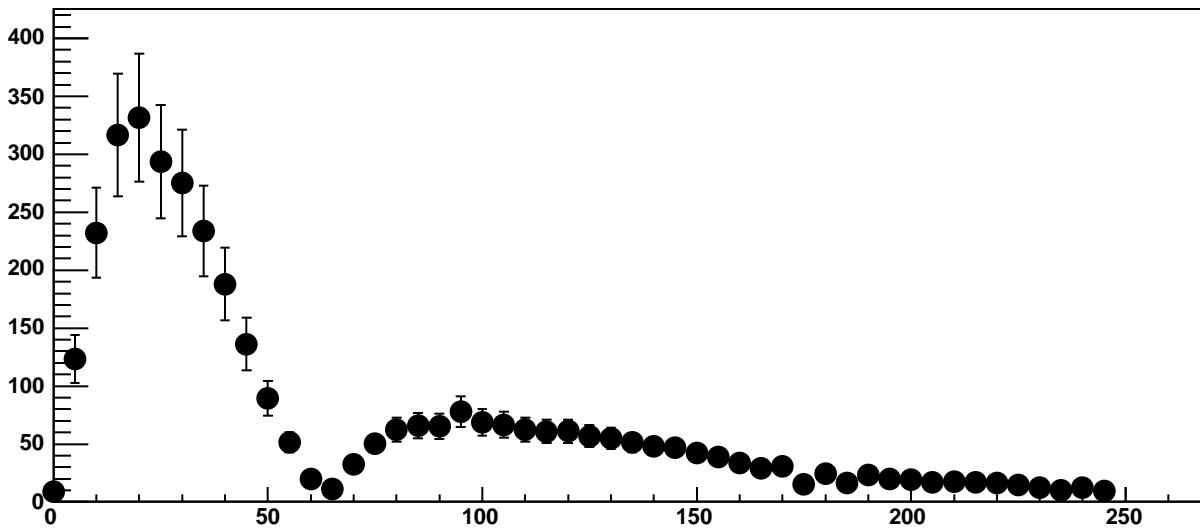


Chip 9, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold

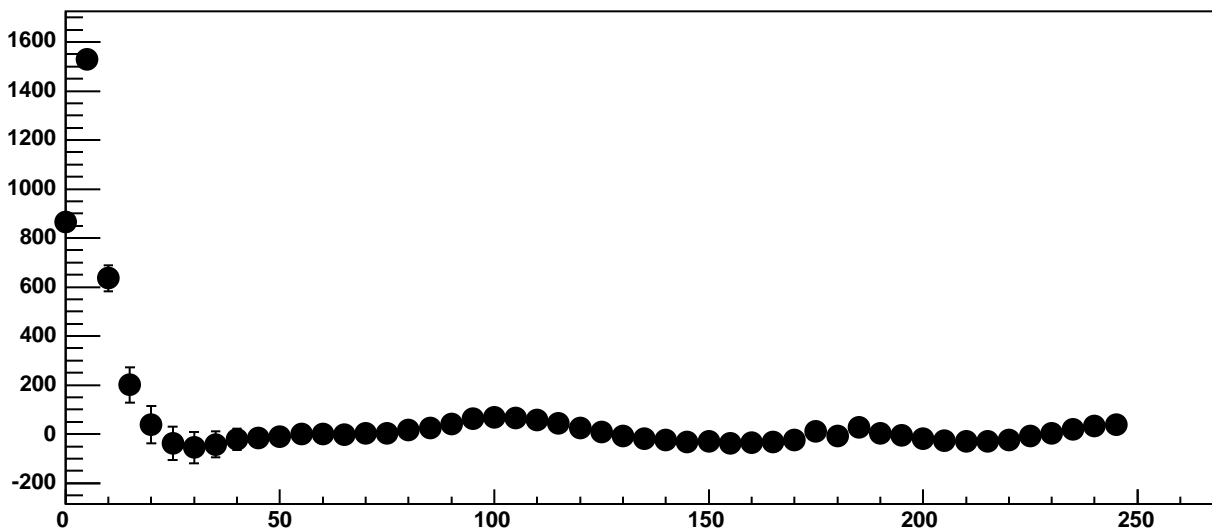


χ^2 / ndf	701.6 / 42
p0	-1406 ± 1.369
p1	4.327 ± 0.1166
p2	9428 ± 13.85
p3	28.77 ± 0.03694

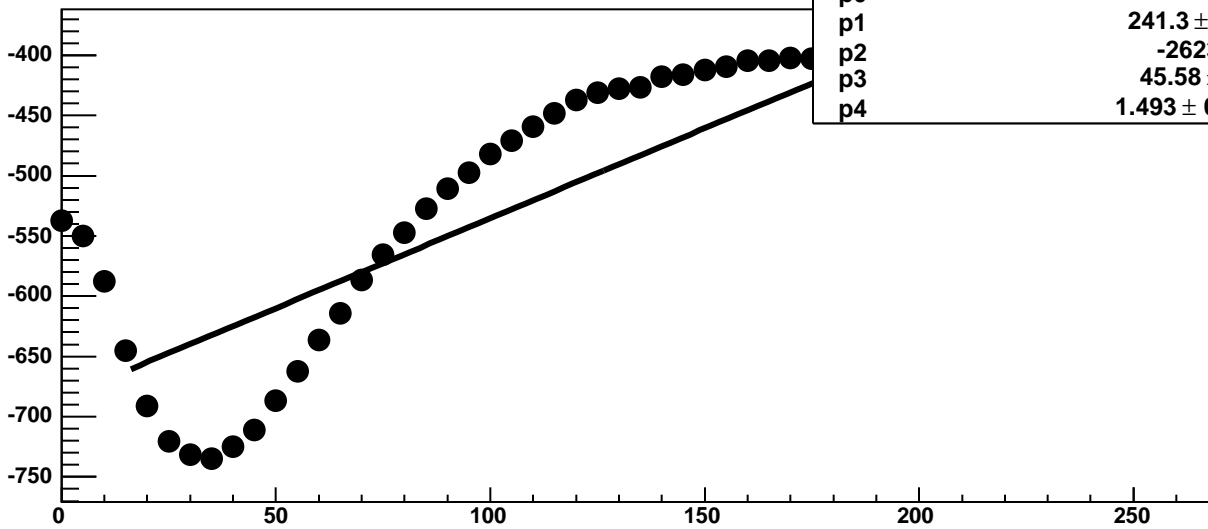
Chip 9, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold

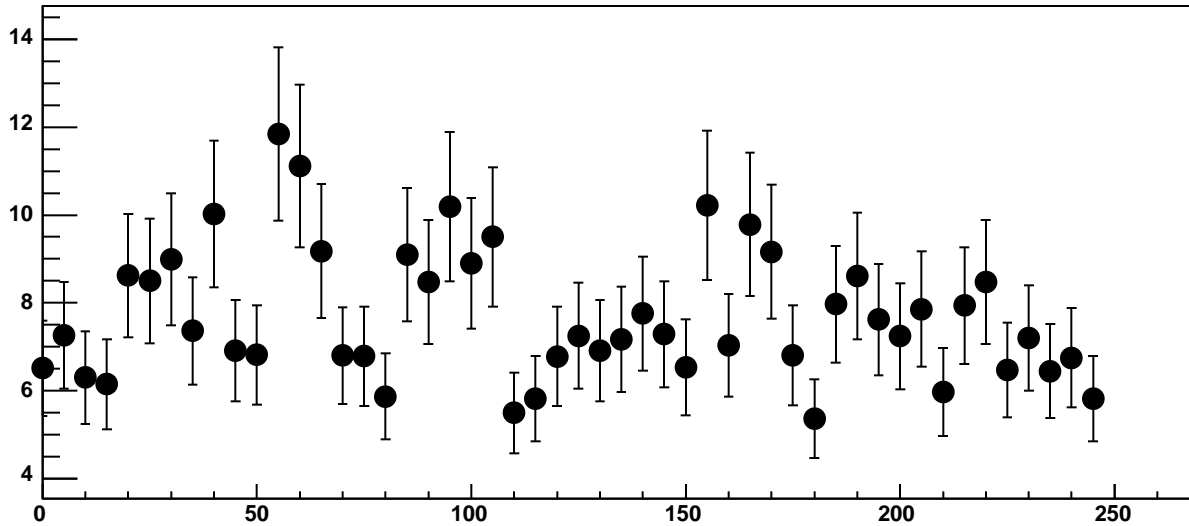


Chip 9, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

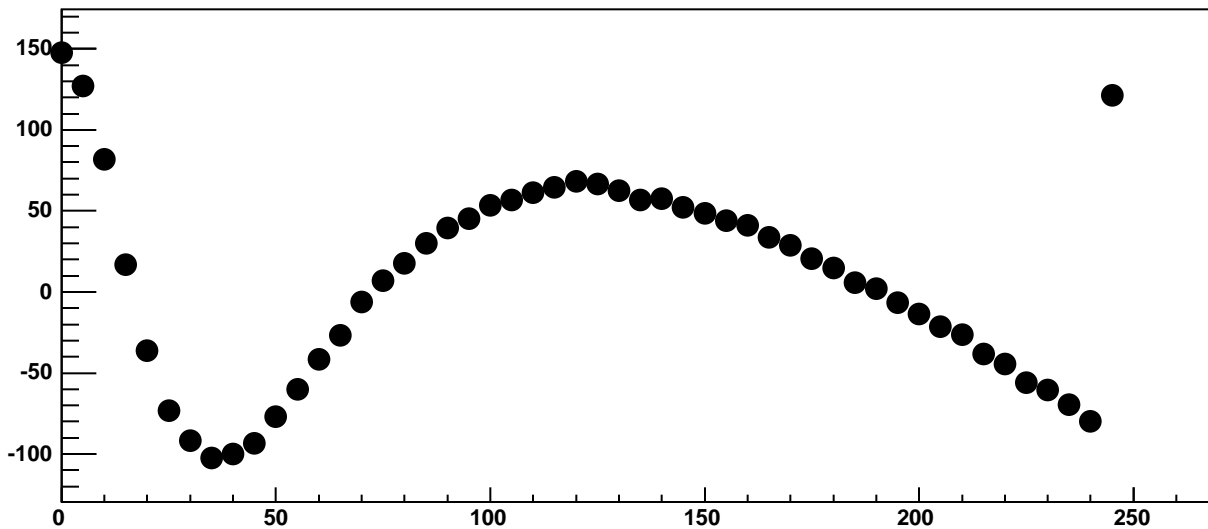


χ^2 / ndf	4.383e+04 / 41
p0	-324.4 ± 0.4665
p1	241.3 ± 0.02467
p2	-2623 ± 9.801
p3	45.58 ± 0.2154
p4	1.493 ± 0.003782

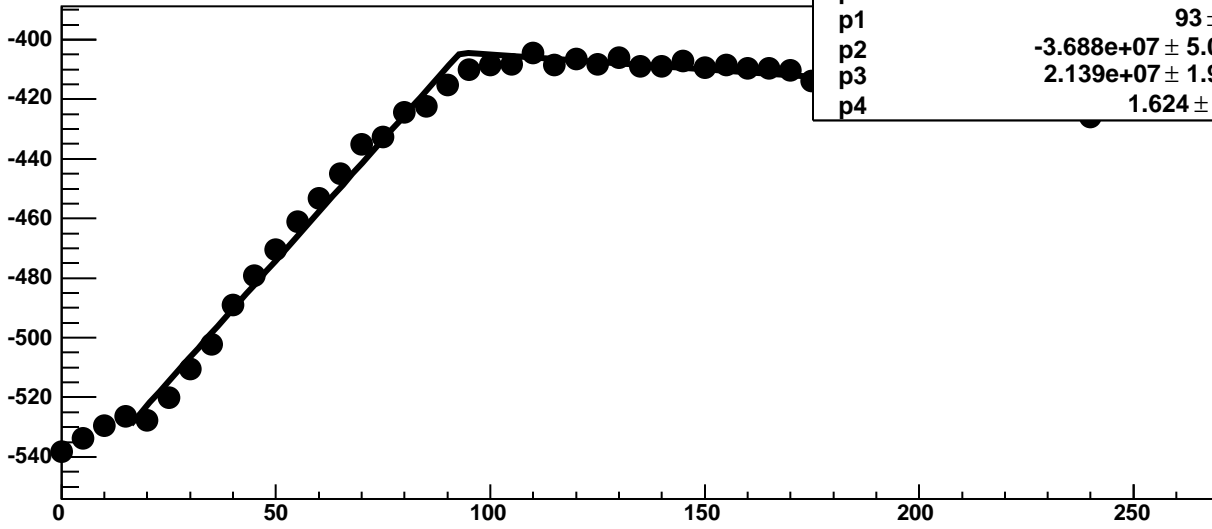
Chip 9, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

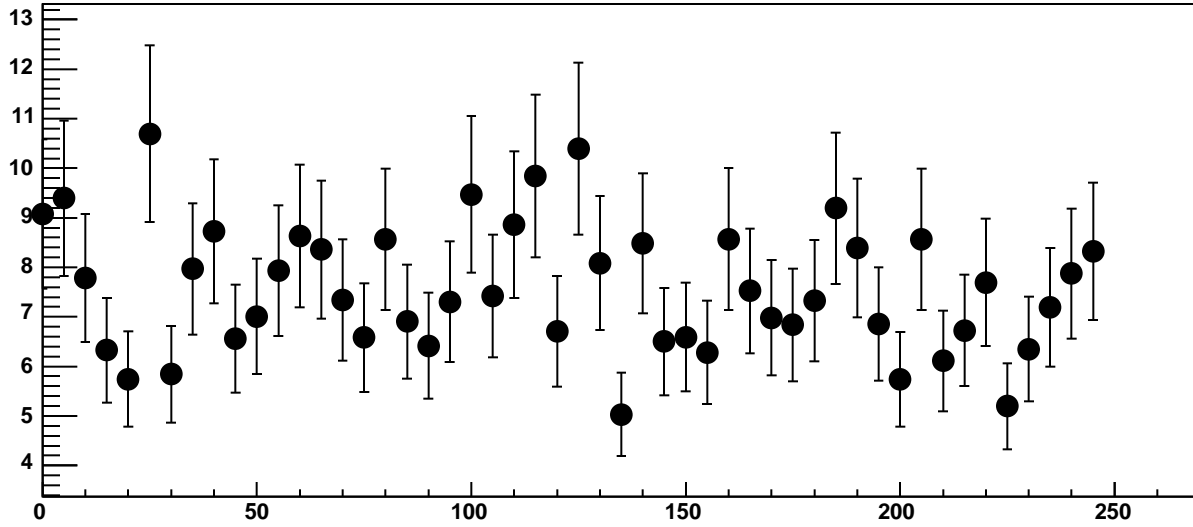


Chip 9, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

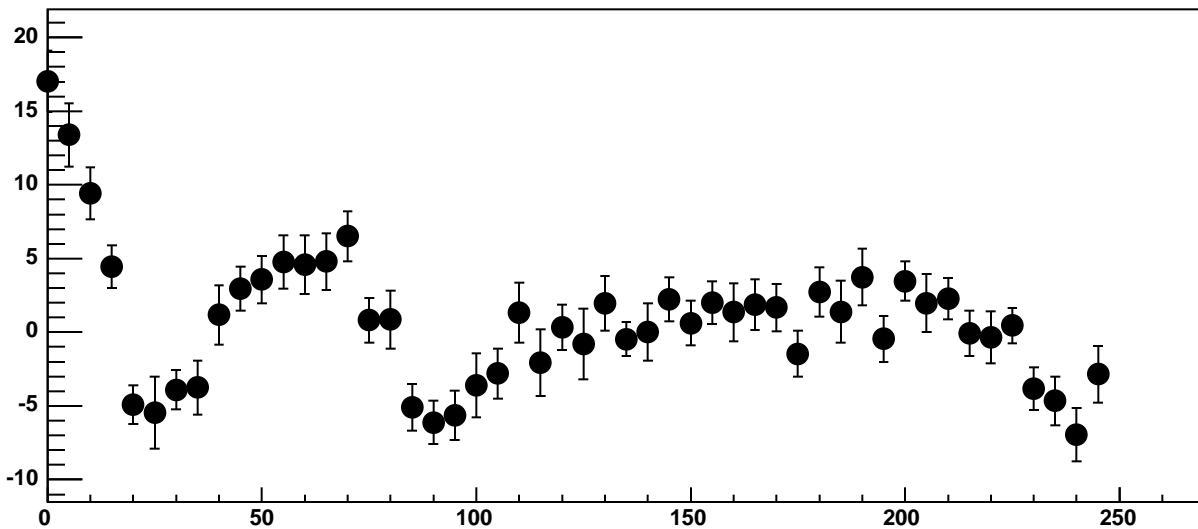


χ^2 / ndf	186.5 / 41
p0	-404.2 ± 0.5965
p1	93 ± 0.5985
p2	$-3.688e+07 \pm 5.088e+05$
p3	$2.139e+07 \pm 1.917e+05$
p4	1.624 ± 0.01693

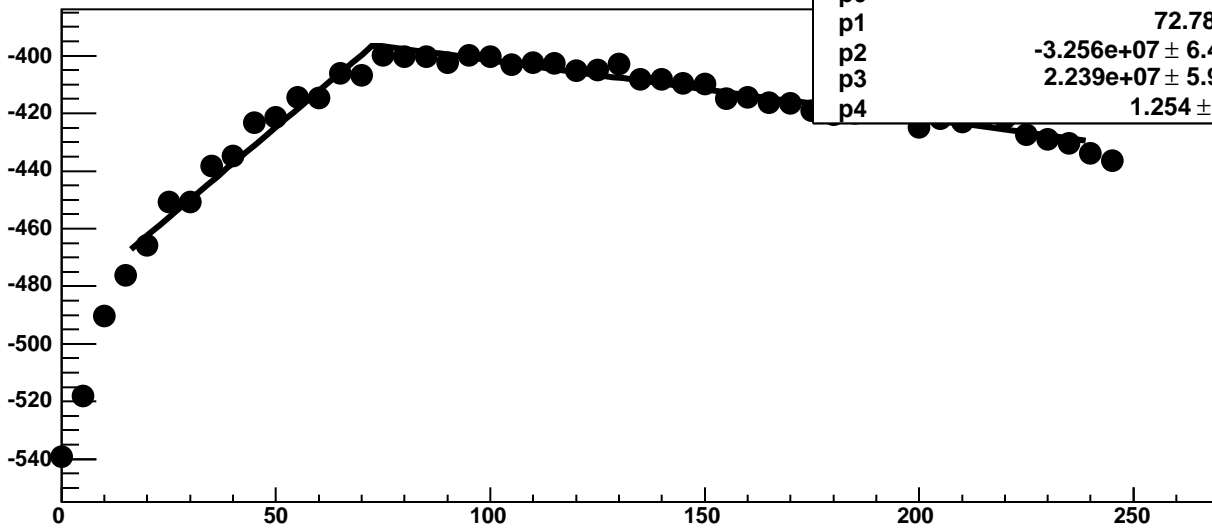
Chip 9, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



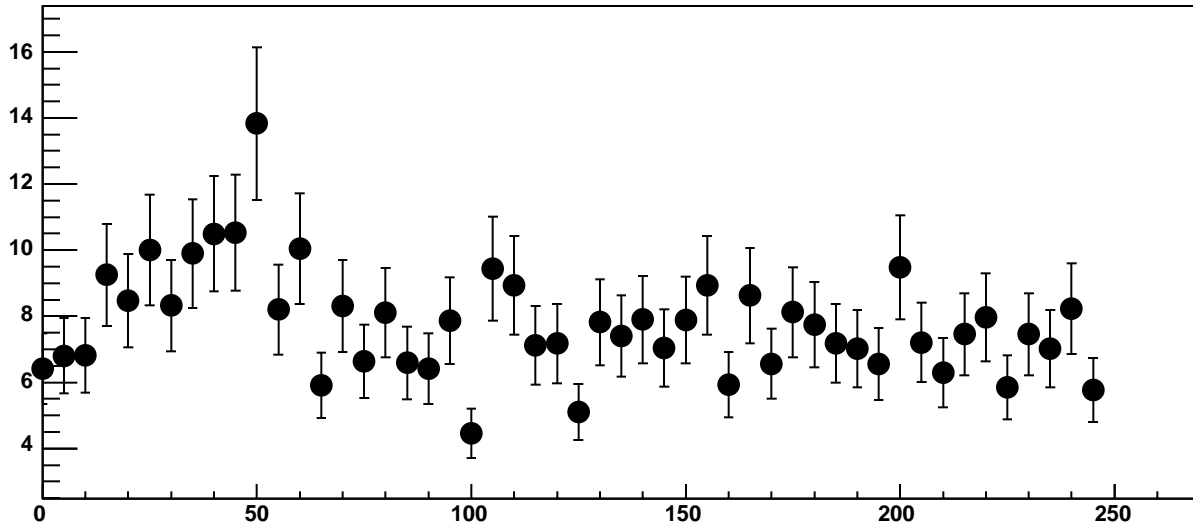
Chip 9, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold



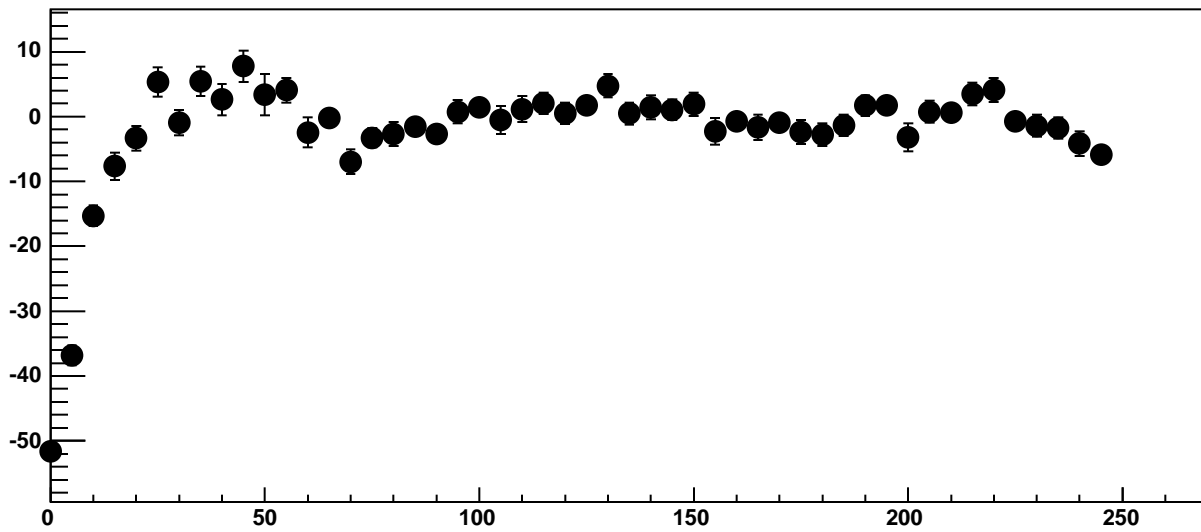
Chip 9, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold



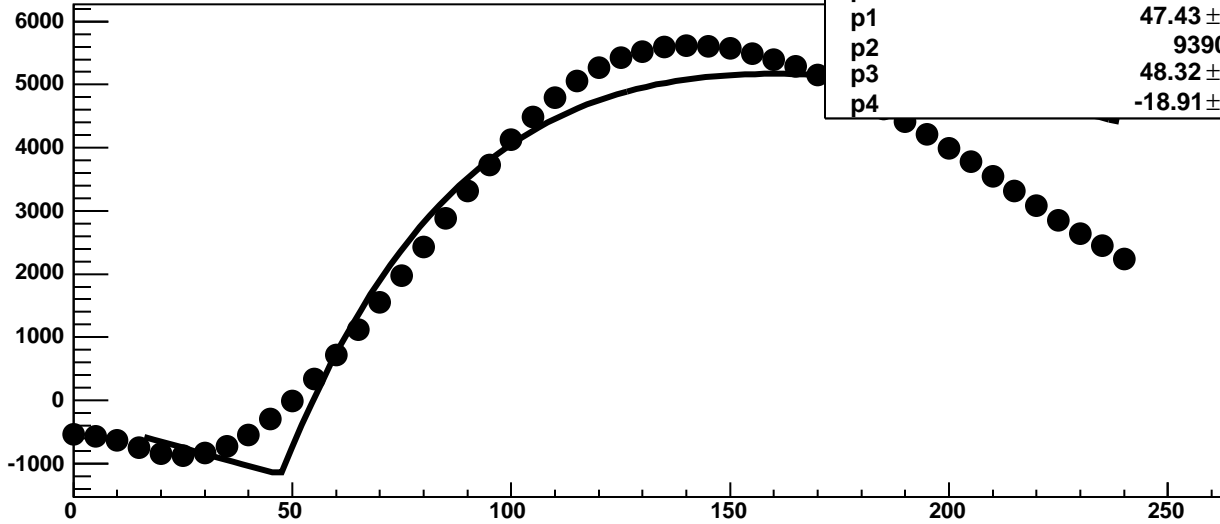
Chip 9, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold

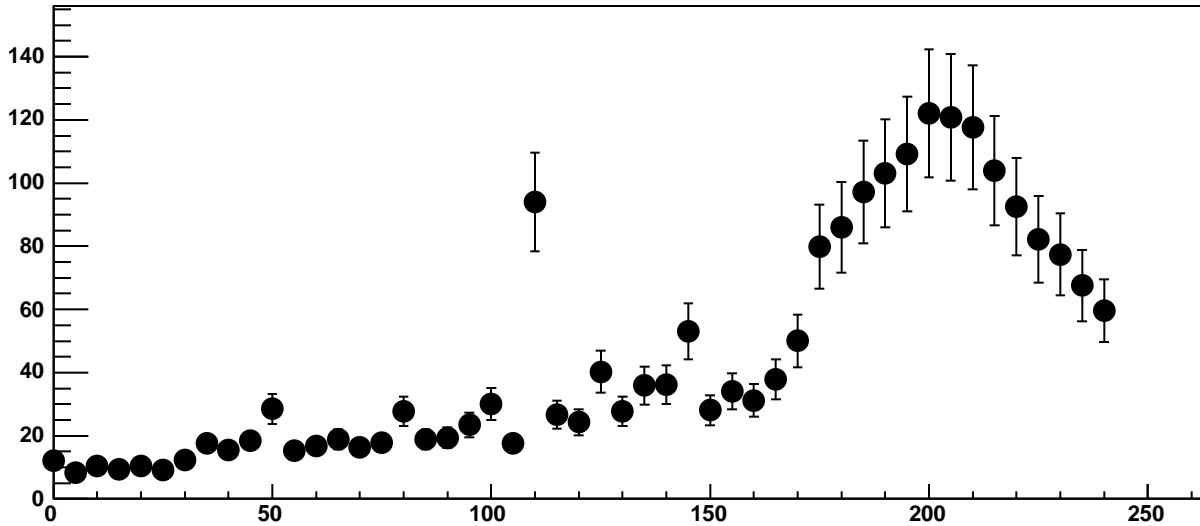


Chip 9, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

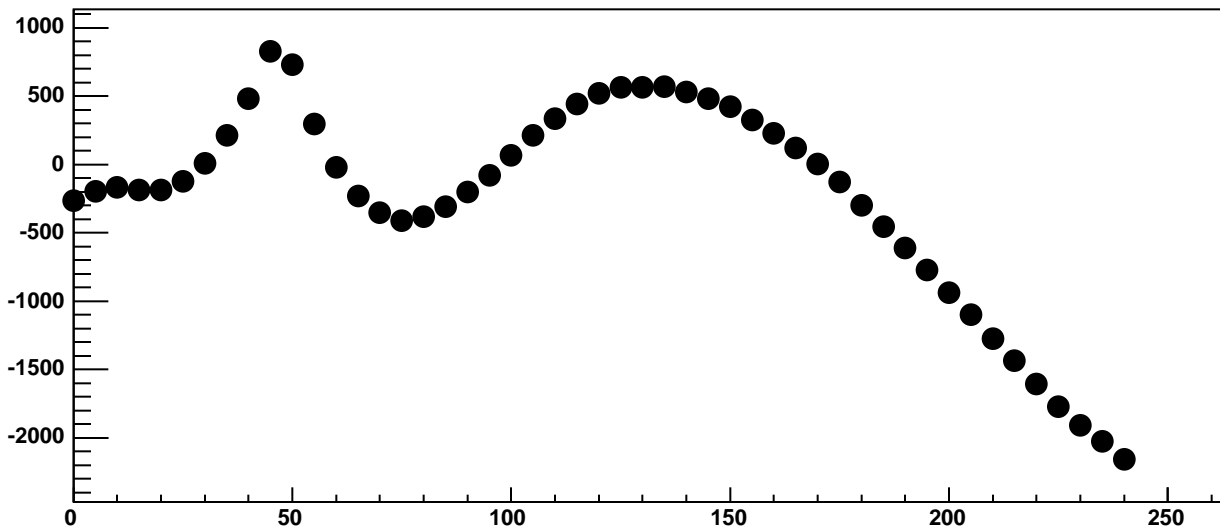


χ^2 / ndf	2.545e+05 / 41
p0	-1176 ± 1.862
p1	47.43 ± 0.02075
p2	9390 ± 15.36
p3	48.32 ± 0.08216
p4	-18.91 ± 0.08096

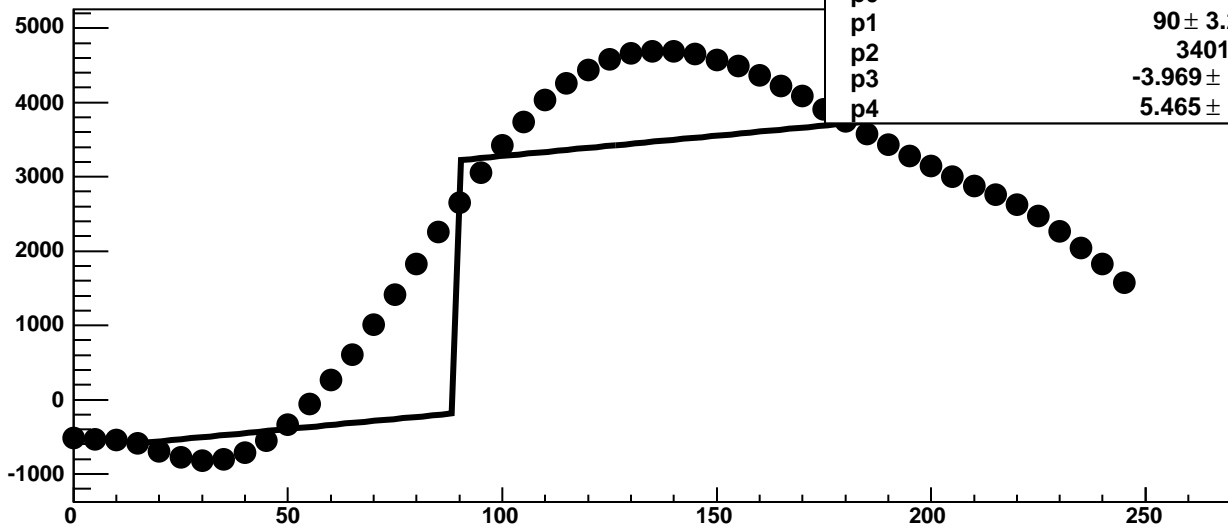
Chip 9, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold

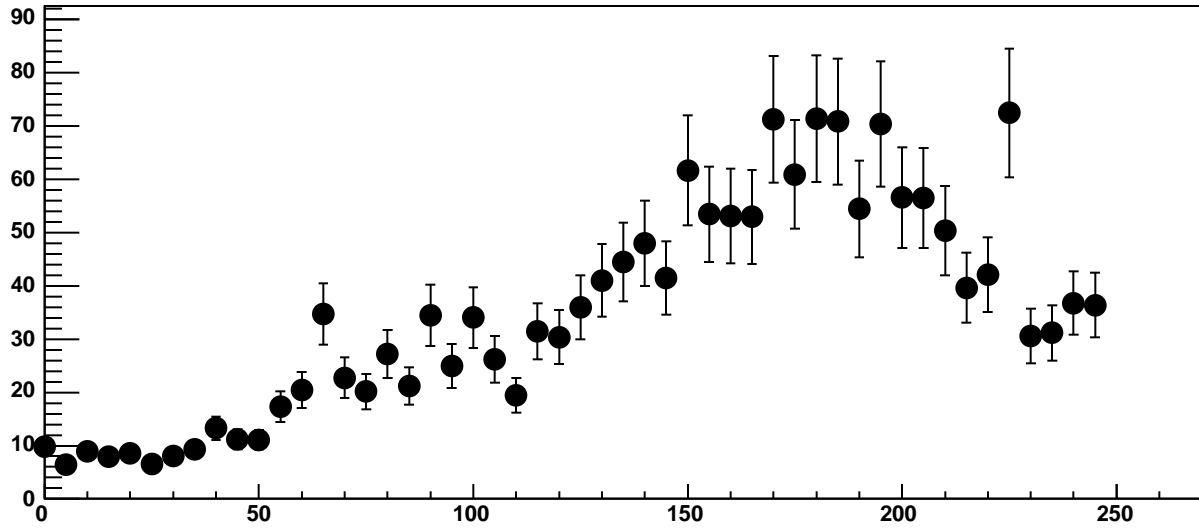


Chip 9, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

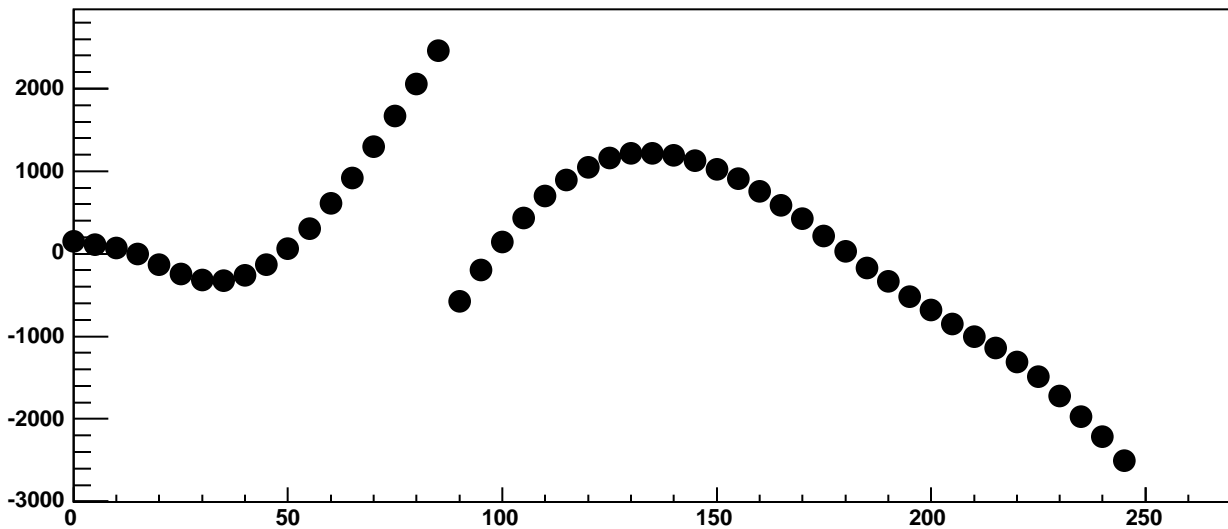


χ^2 / ndf	1.124e+06 / 41
p0	-176.3 ± 1.674
p1	90 ± 3.222e-05
p2	3401 ± 3.678
p3	-3.969 ± 0.04773
p4	5.465 ± 0.02676

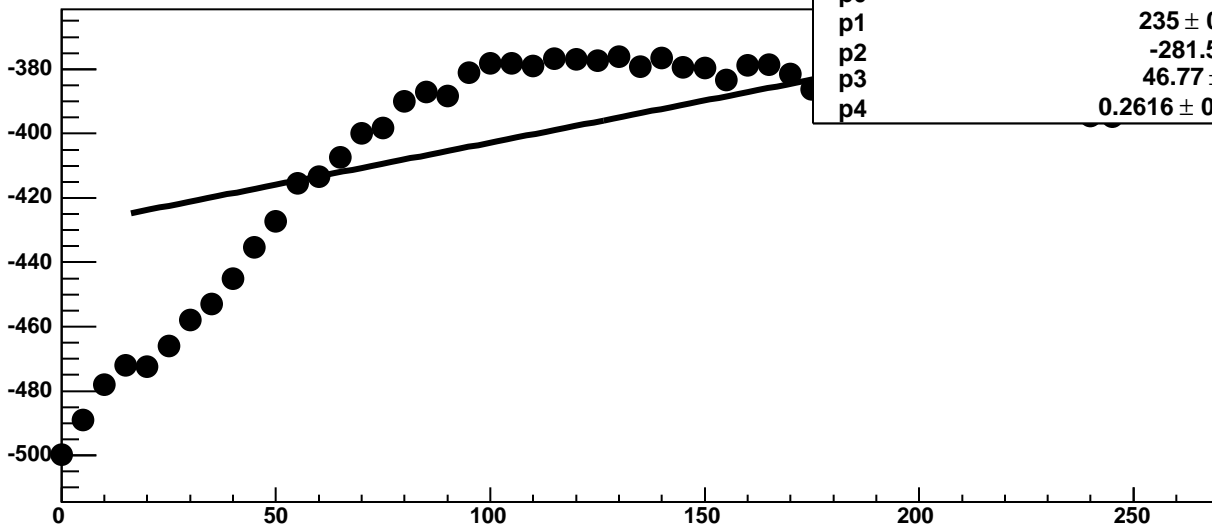
Chip 9, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

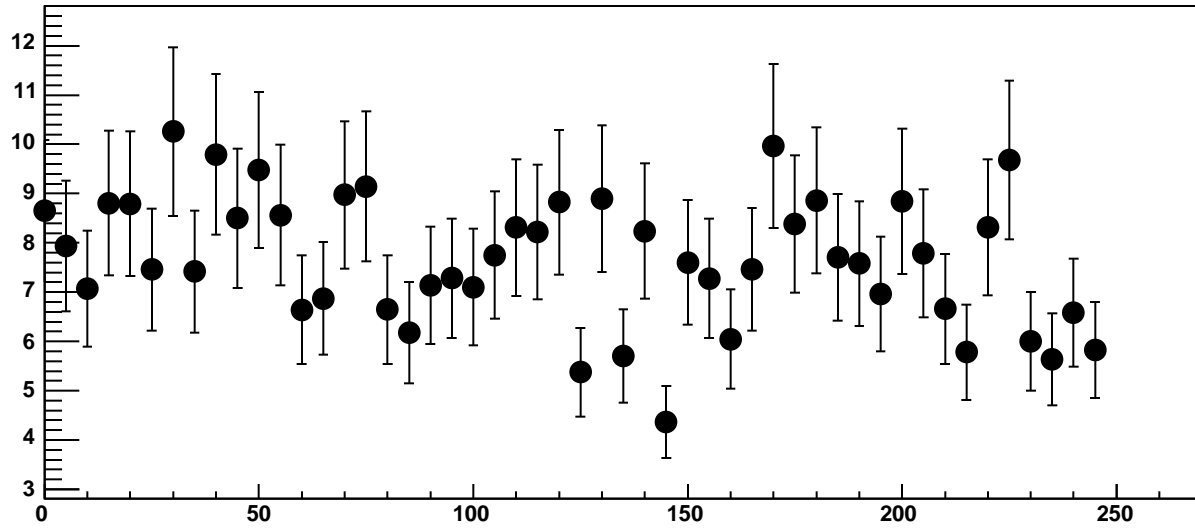


Chip 9, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold

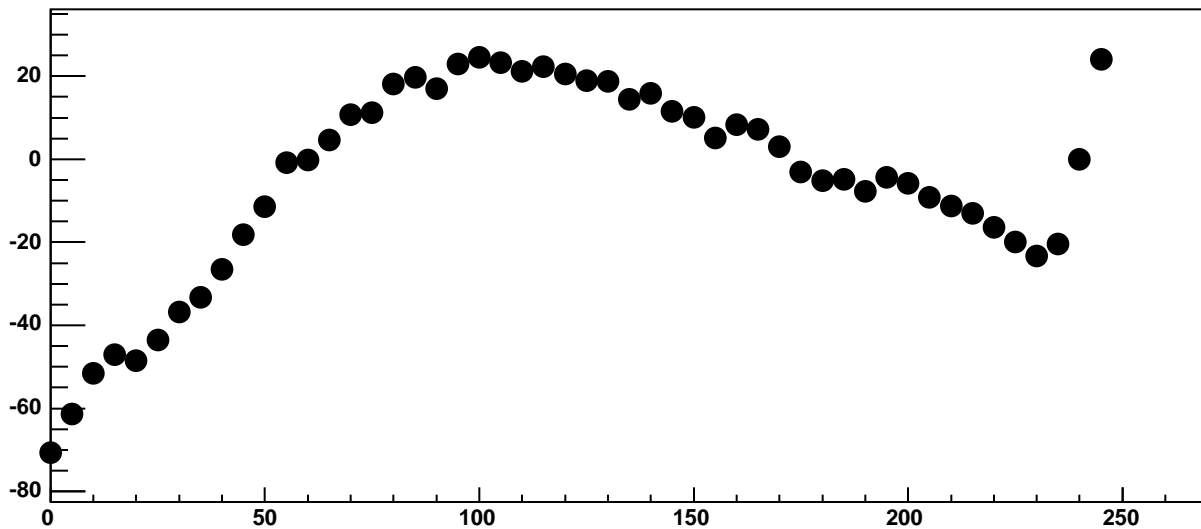


χ^2 / ndf	5773 / 41
p0	-367.5 ± 0.4808
p1	235 ± 0.001441
p2	-281.5 ± 15.91
p3	46.77 ± 0.1032
p4	0.2616 ± 0.003995

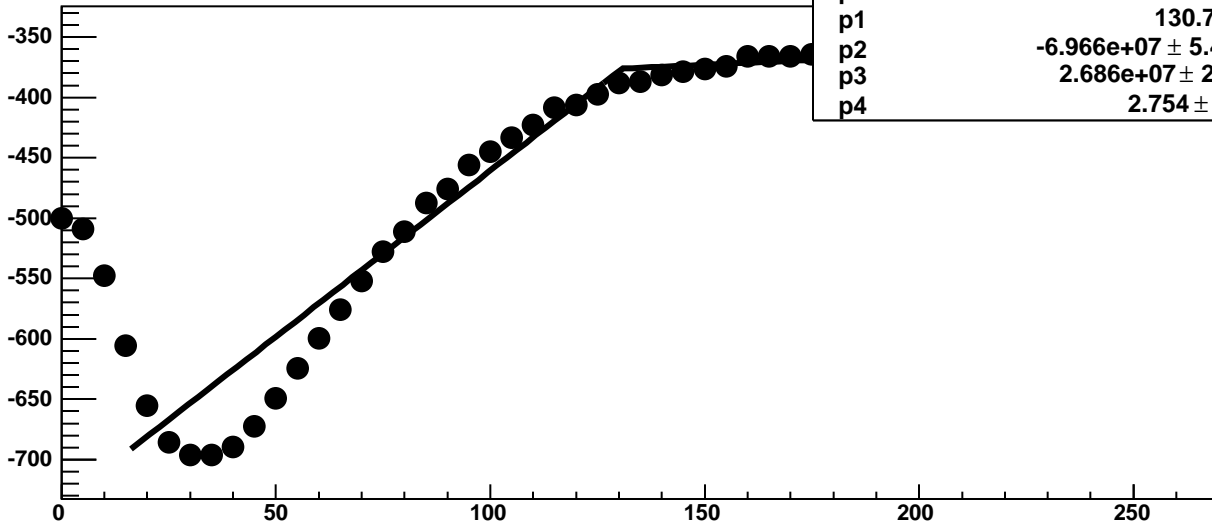
Chip 9, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold

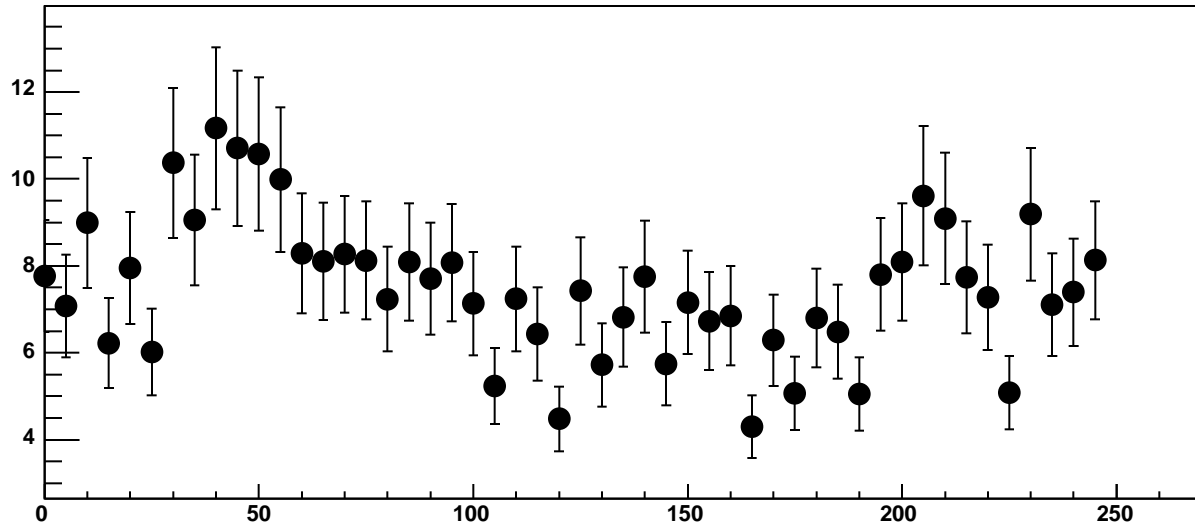


Chip 9, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

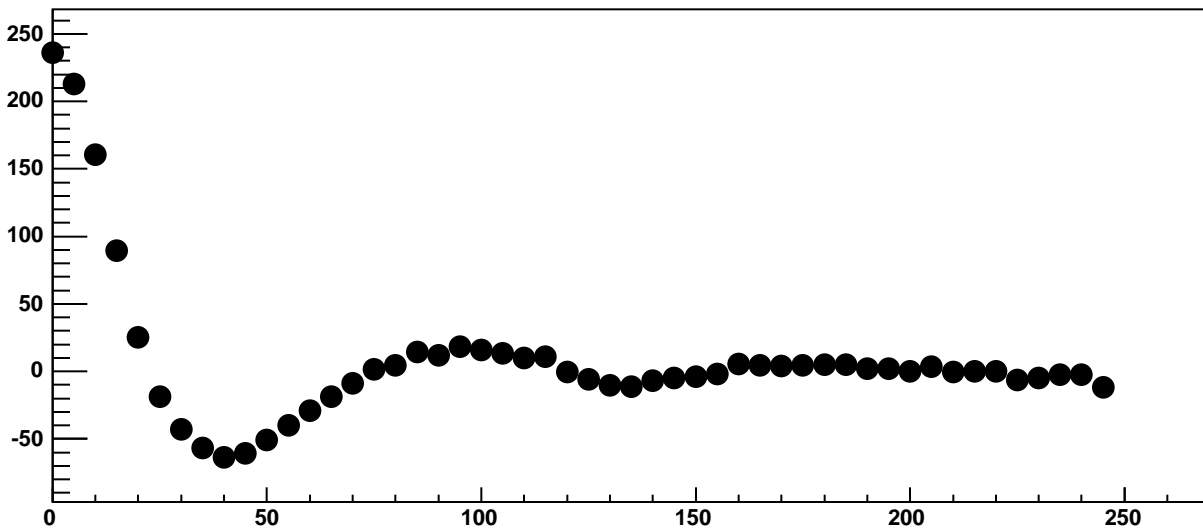


χ^2 / ndf	8506 / 41
p0	-376 ± 0.6893
p1	130.7 ± 0.333
p2	$-6.966\text{e}+07 \pm 5.434\text{e}+06$
p3	$2.686\text{e}+07 \pm 2.09\text{e}+06$
p4	2.754 ± 0.00918

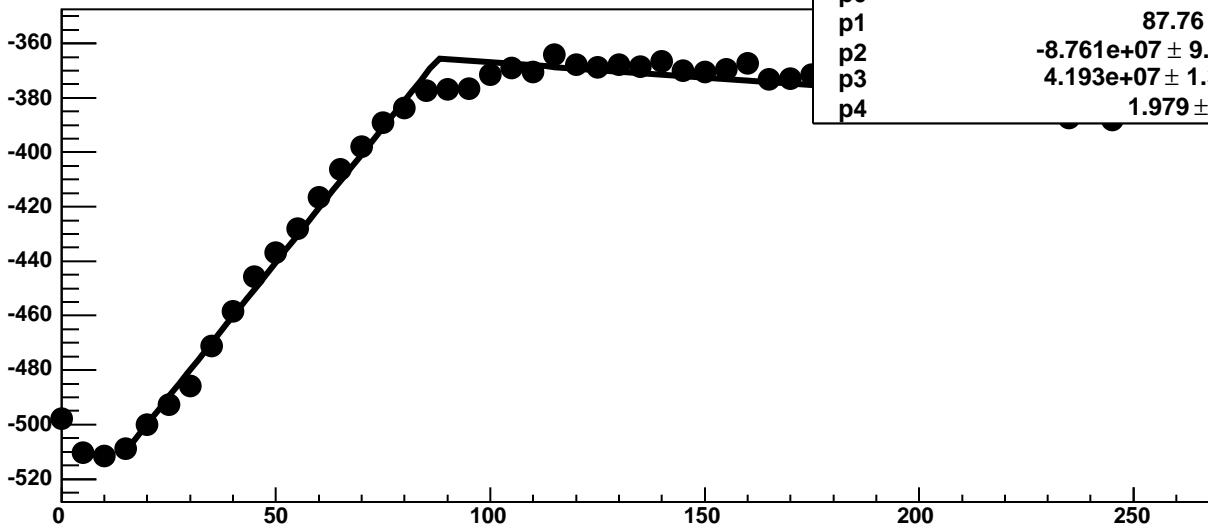
Chip 9, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

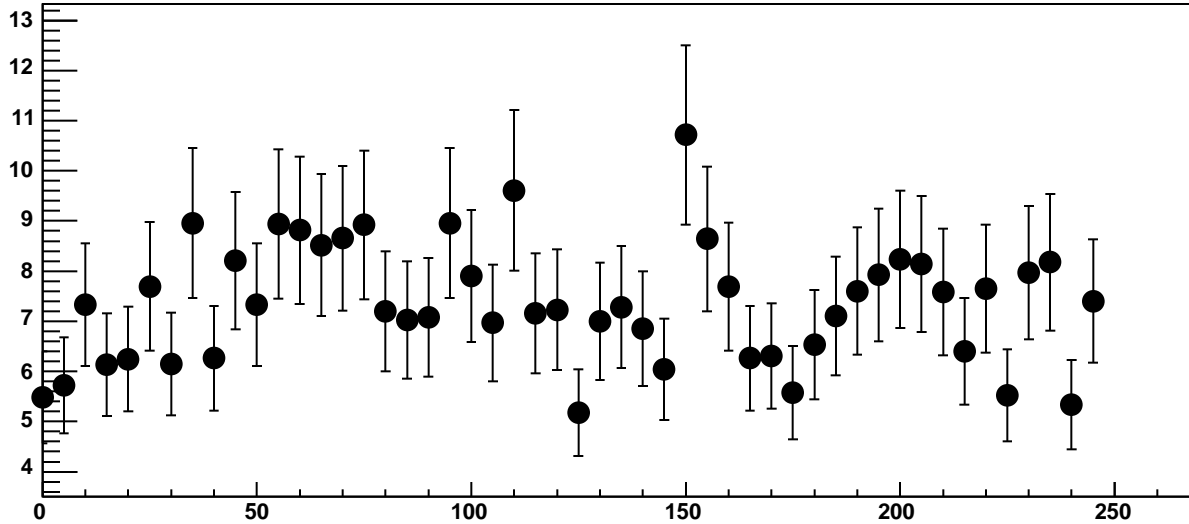


Chip 9, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

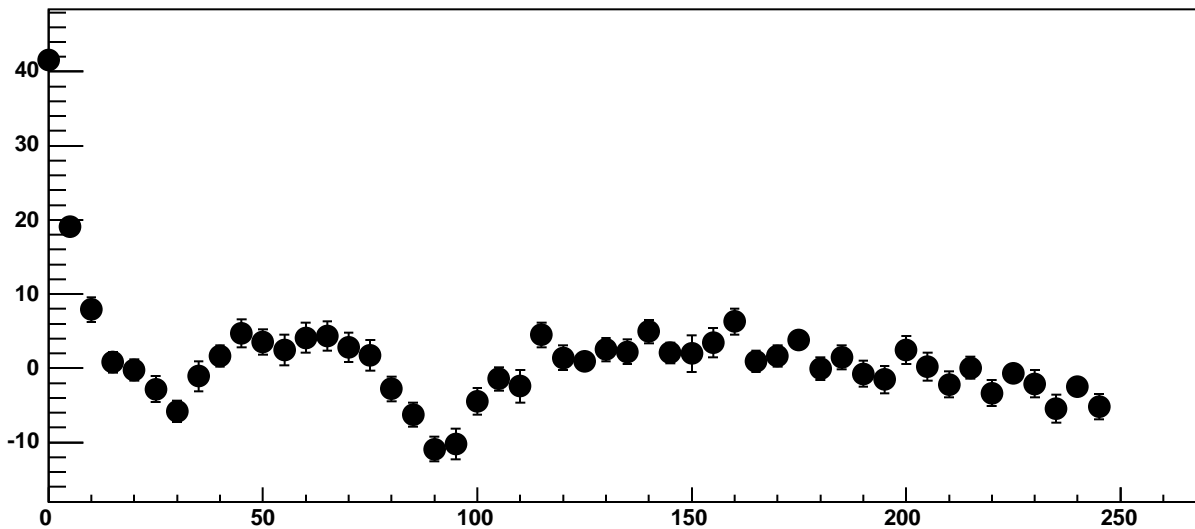


χ^2 / ndf	216.5 / 41
p0	-365.6 ± 0.5668
p1	87.76 ± 0.5198
p2	$-8.761\text{e}+07 \pm 9.011\text{e}+05$
p3	$4.193\text{e}+07 \pm 1.312\text{e}+05$
p4	1.979 ± 0.01937

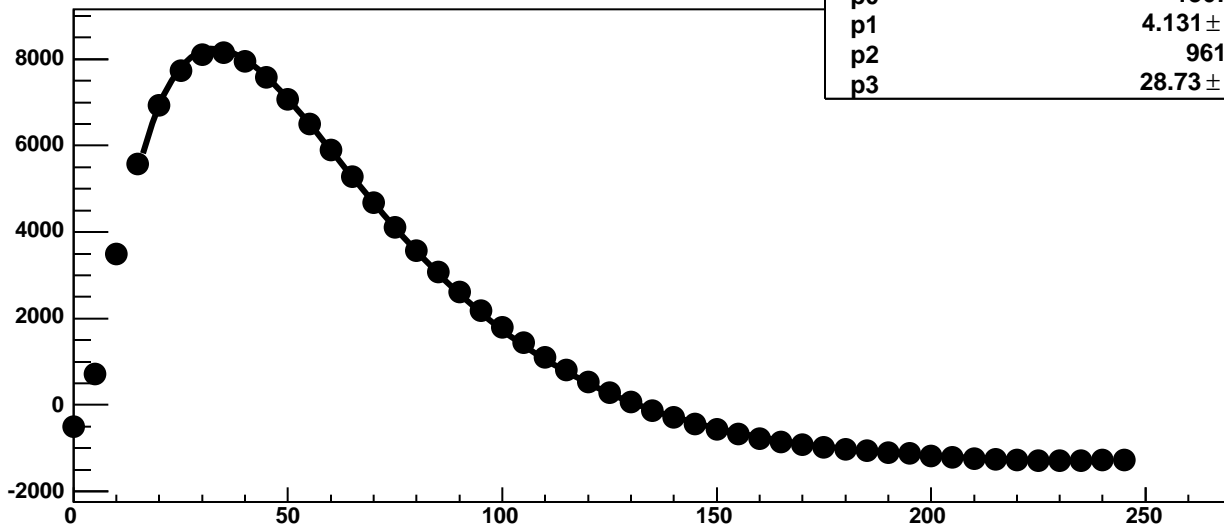
Chip 9, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

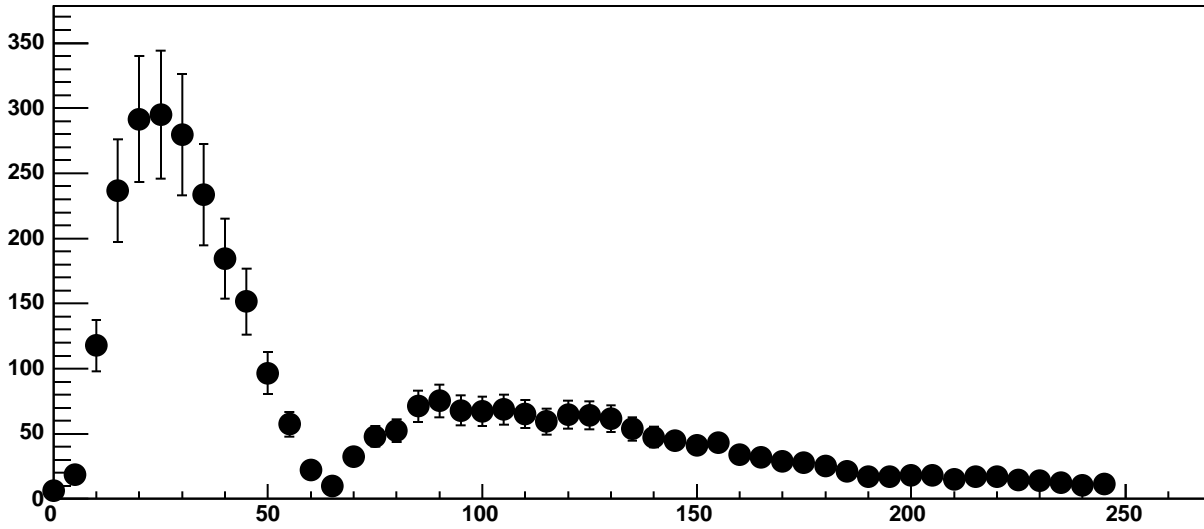


Chip 9, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold

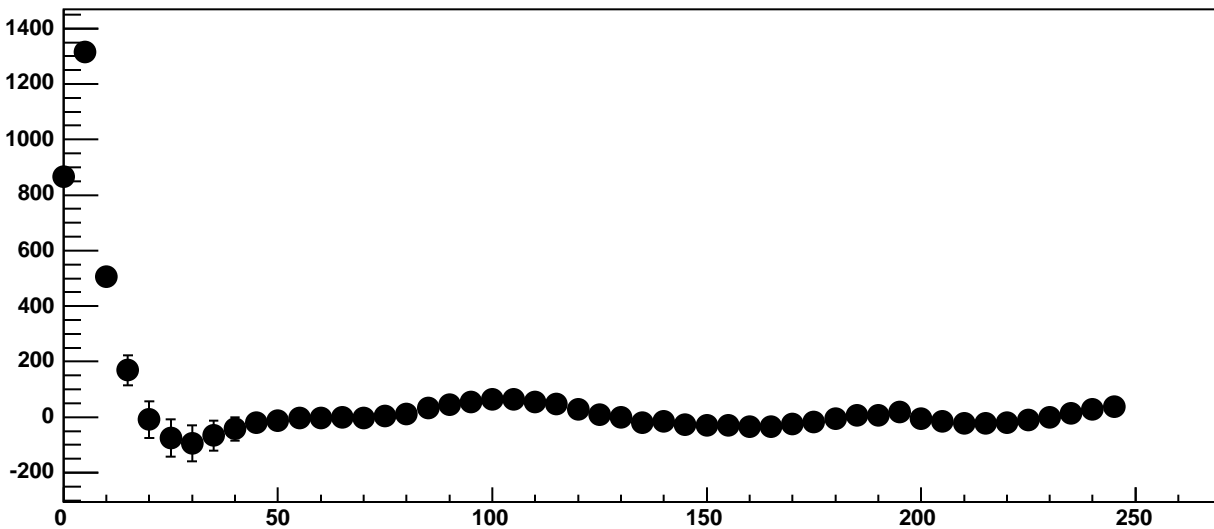


χ^2 / ndf	523.5 / 42
p0	-1367 ± 1.372
p1	4.131 ± 0.09869
p2	9611 ± 12.4
p3	28.73 ± 0.03577

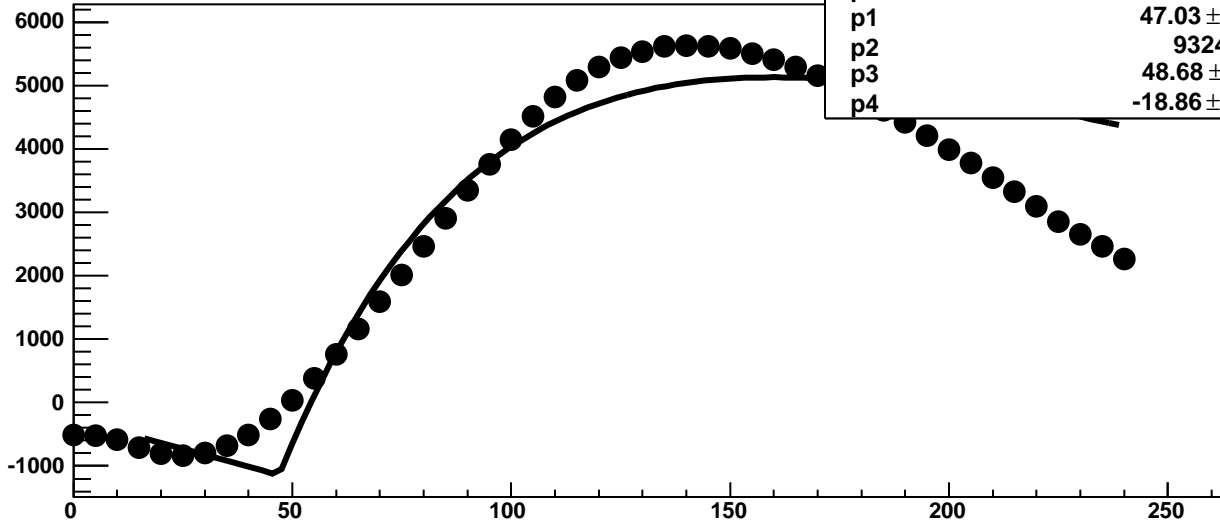
Chip 9, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold

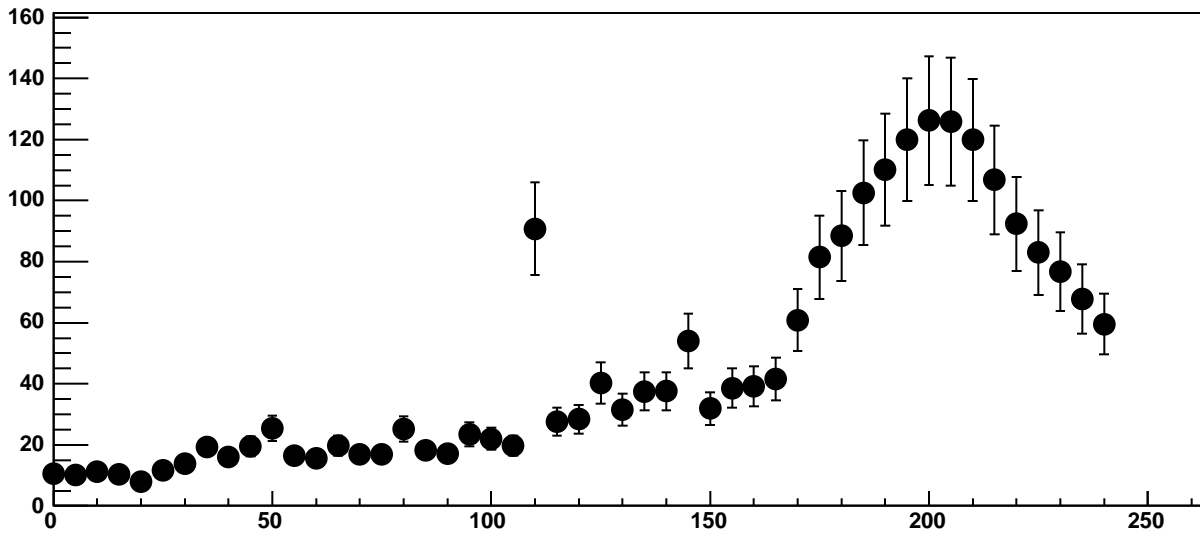


Chip 9, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

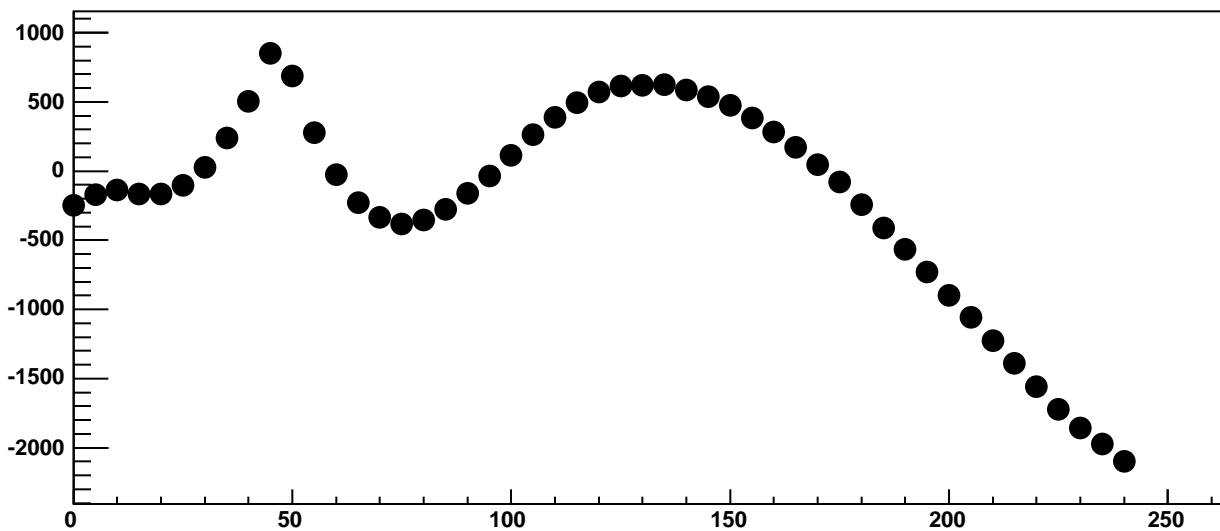


χ^2 / ndf	2.443e+05 / 41
p0	-1147 ± 1.937
p1	47.03 ± 0.02157
p2	9324 ± 15.96
p3	48.68 ± 0.08551
p4	-18.86 ± 0.08319

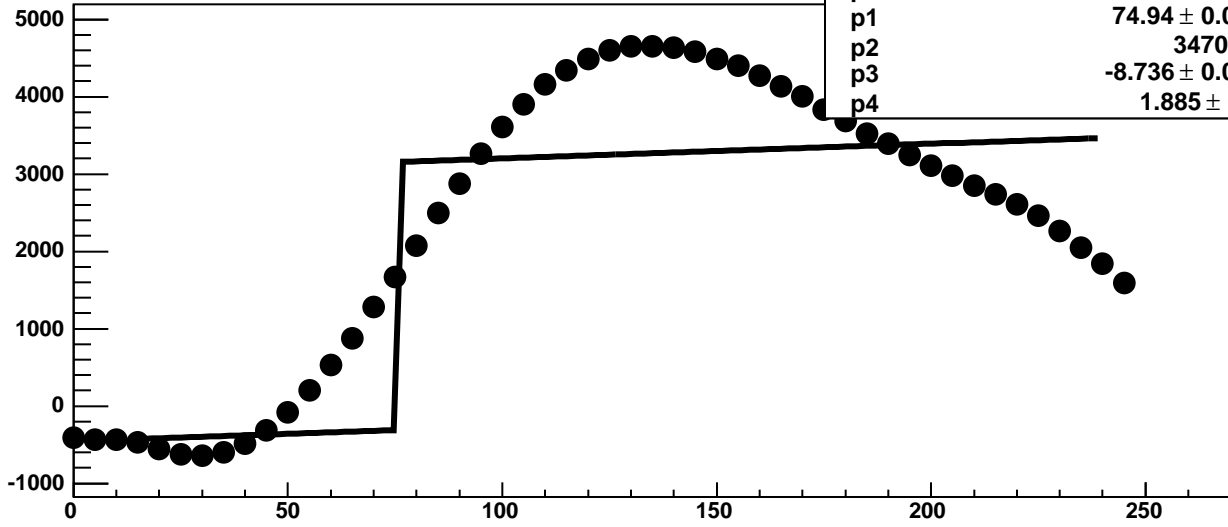
Chip 9, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

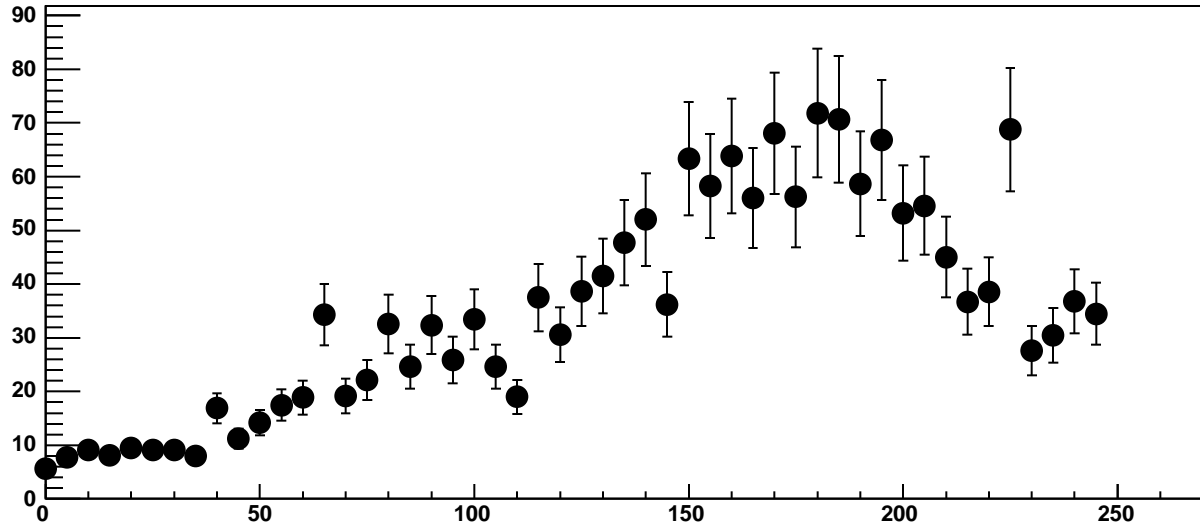


Chip 9, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

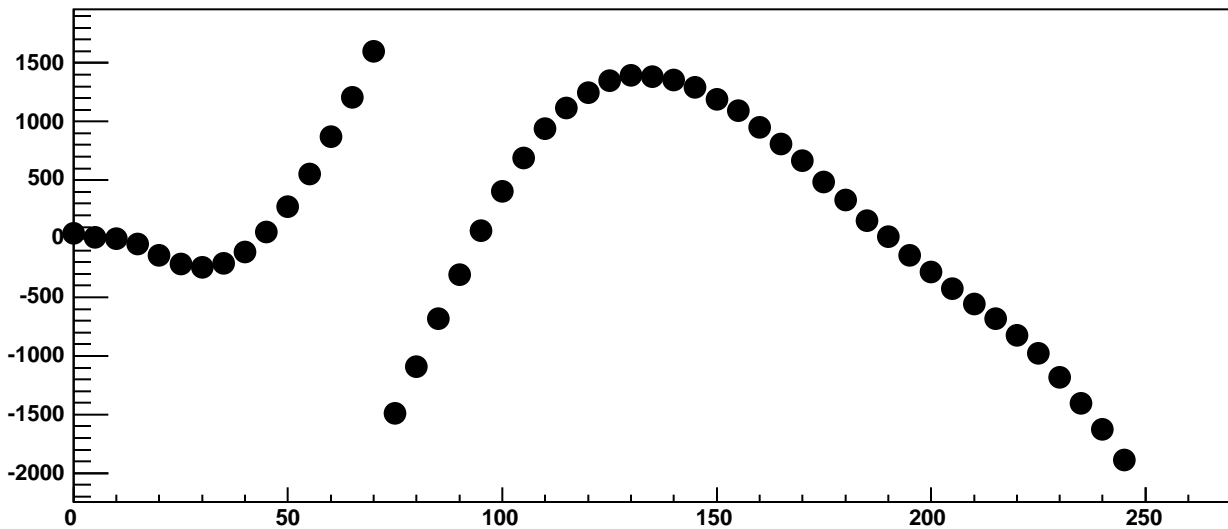


χ^2 / ndf	7.619e+05 / 41
p0	-313.4 ± 0.6371
p1	74.94 ± 0.0006213
p2	3470 ± 1.379
p3	-8.736 ± 0.0003447
p4	1.885 ± 0.01496

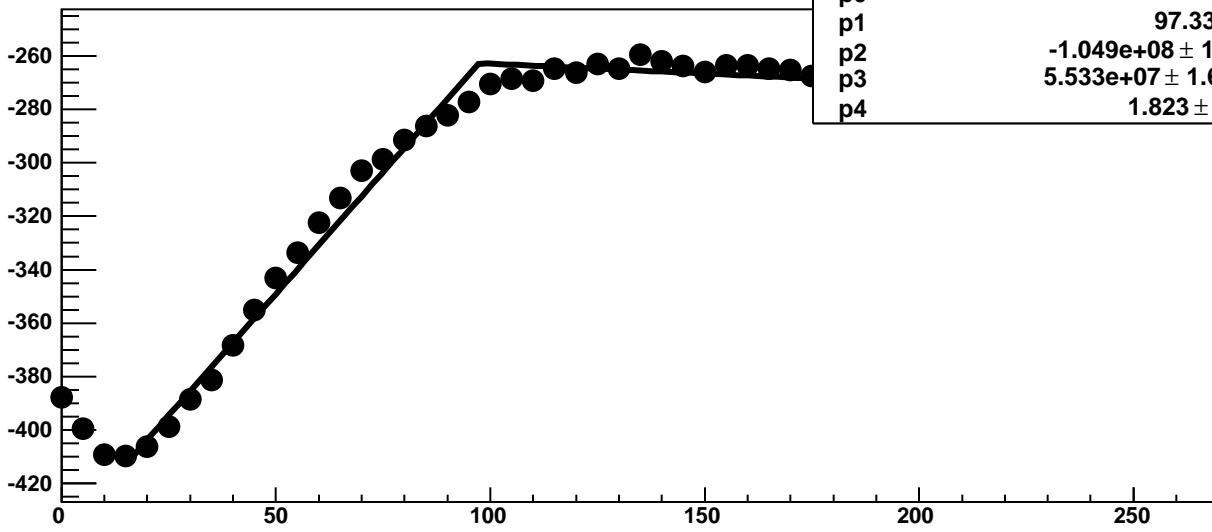
Chip 9, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold

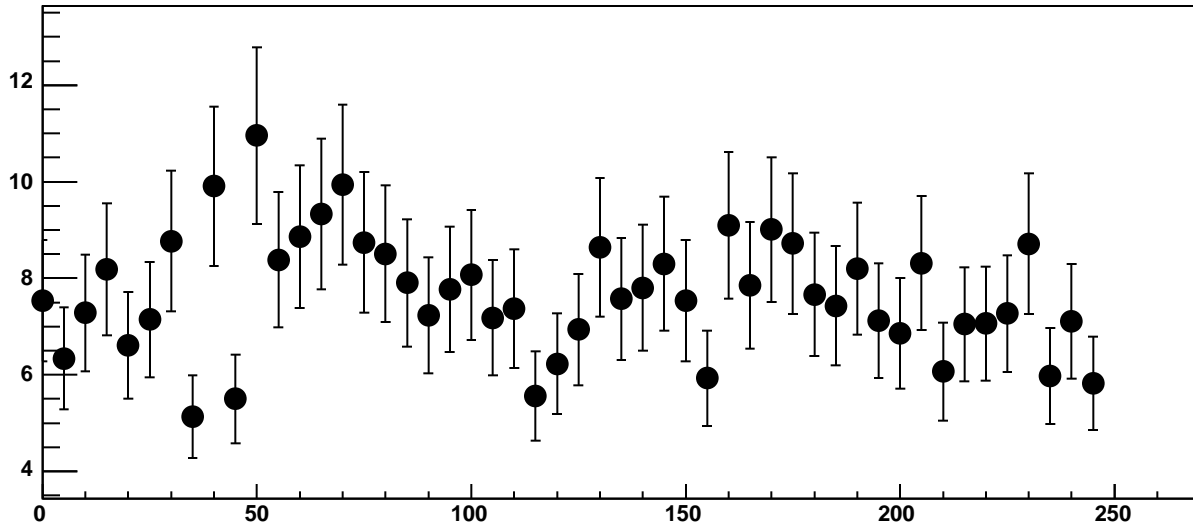


Chip 9, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

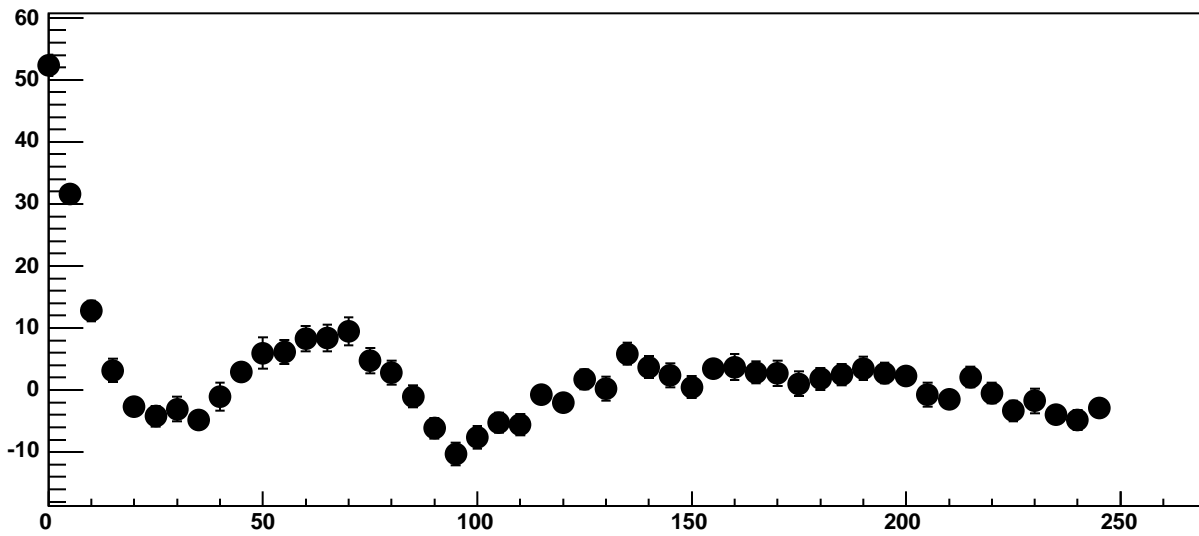


χ^2 / ndf	266.4 / 41
p0	-262.7 ± 0.5837
p1	97.33 ± 0.565
p2	$-1.049\text{e}+08 \pm 1.07\text{e}+06$
p3	$5.533\text{e}+07 \pm 1.617\text{e}+05$
p4	1.823 ± 0.01716

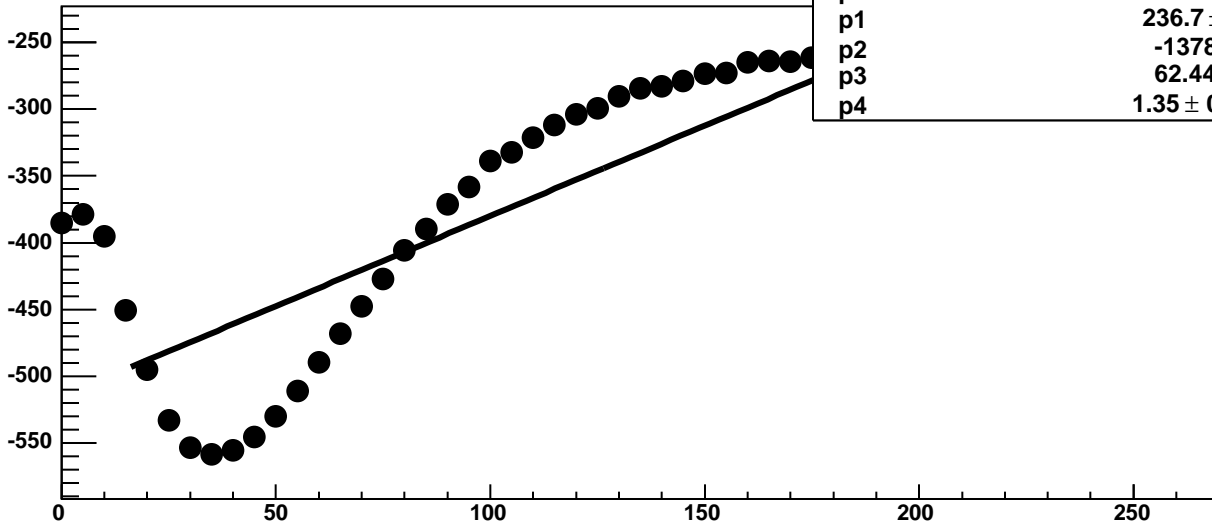
Chip 9, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

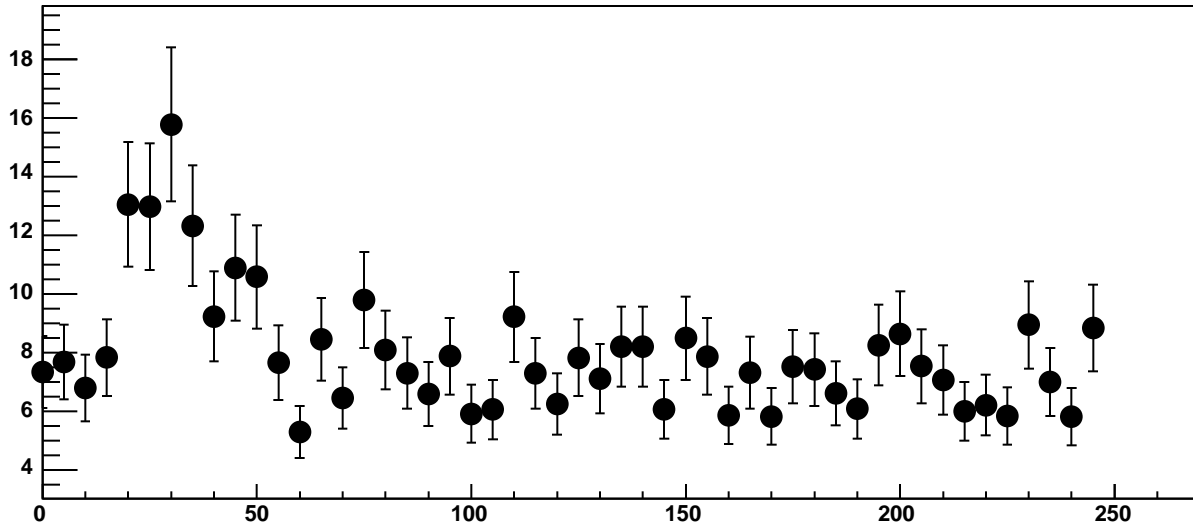


Chip 9, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold

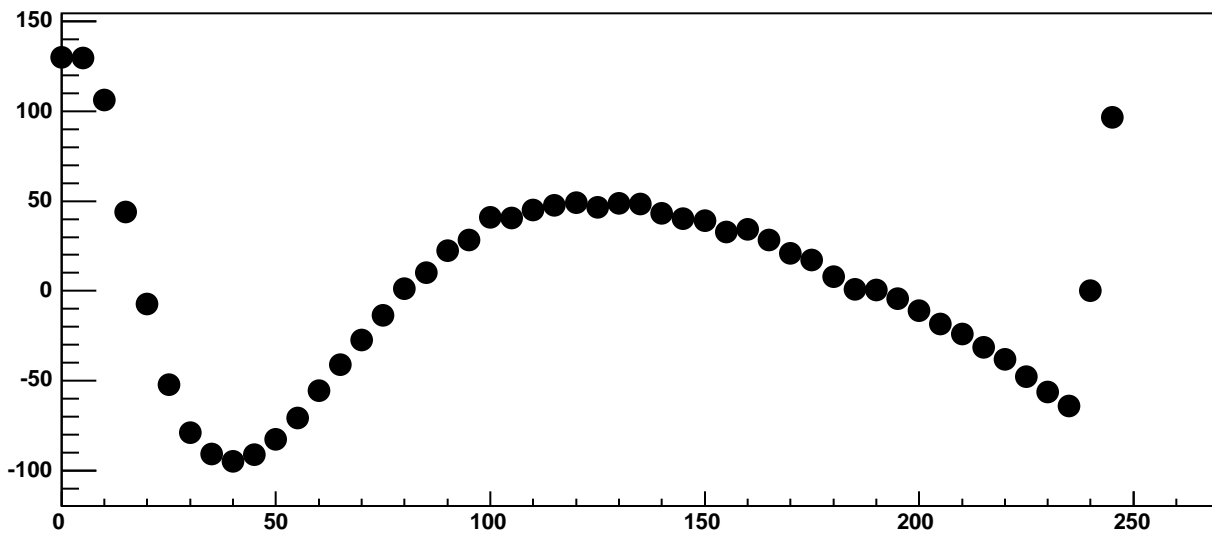


χ^2 / ndf	2.703e+04 / 41
p0	-195.3 ± 1.338
p1	236.7 ± 0.9269
p2	-1378 ± 368.8
p3	62.44 ± 14.05
p4	1.35 ± 0.004171

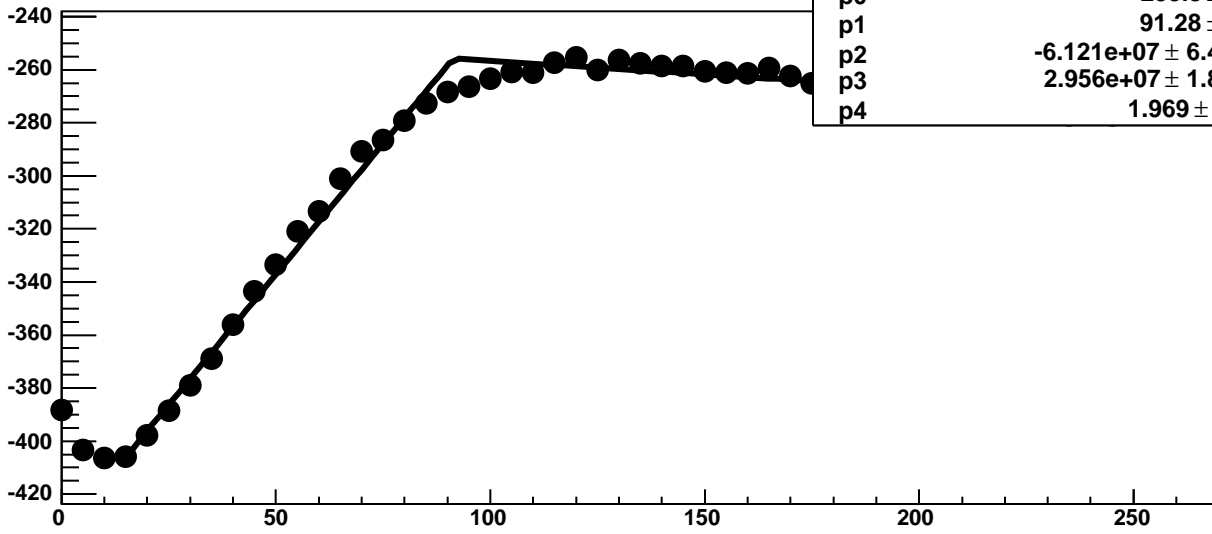
Chip 9, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold

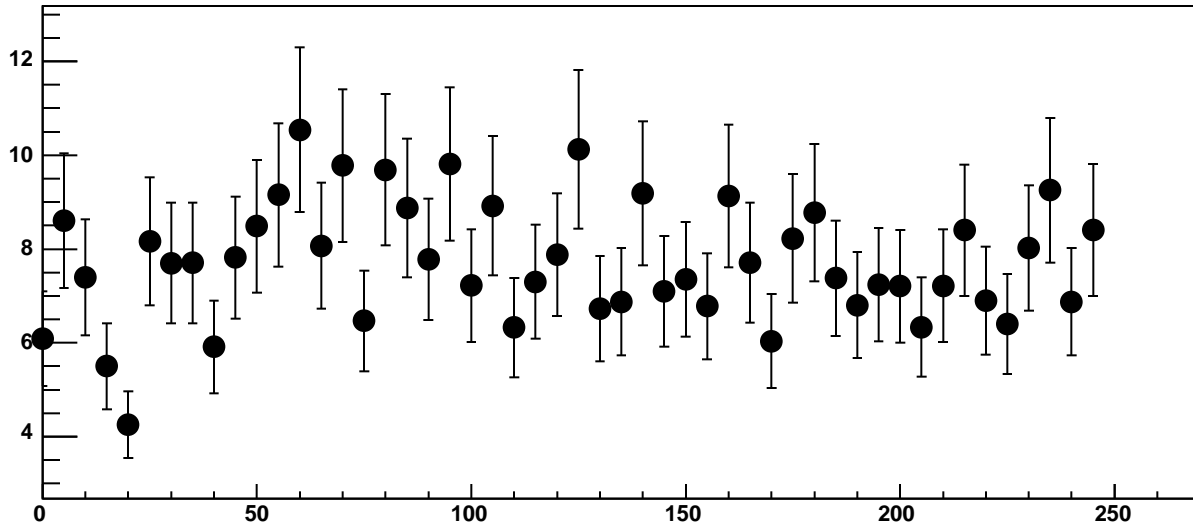


Chip 9, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

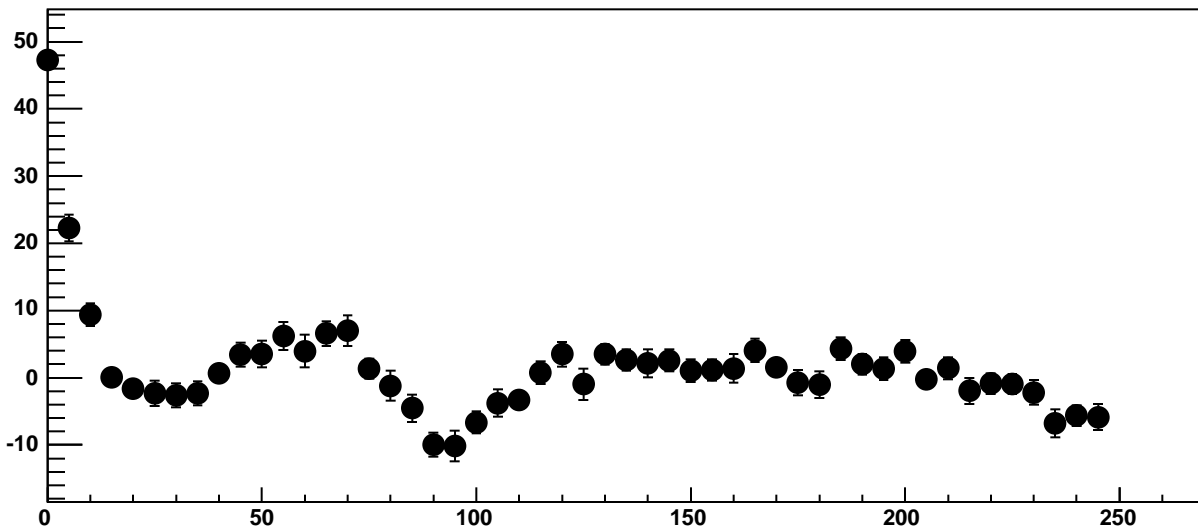


χ^2 / ndf	196.6 / 41
p0	-255.8 ± 0.6235
p1	91.28 ± 0.5354
p2	$-6.121\text{e}+07 \pm 6.494\text{e}+05$
p3	$2.956\text{e}+07 \pm 1.883\text{e}+05$
p4	1.969 ± 0.01703

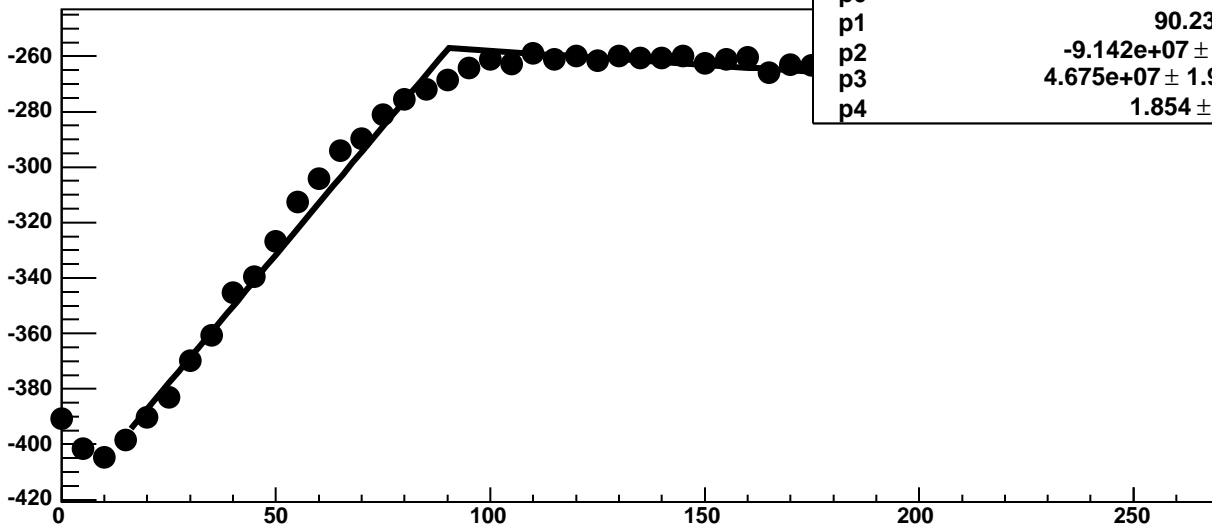
Chip 9, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

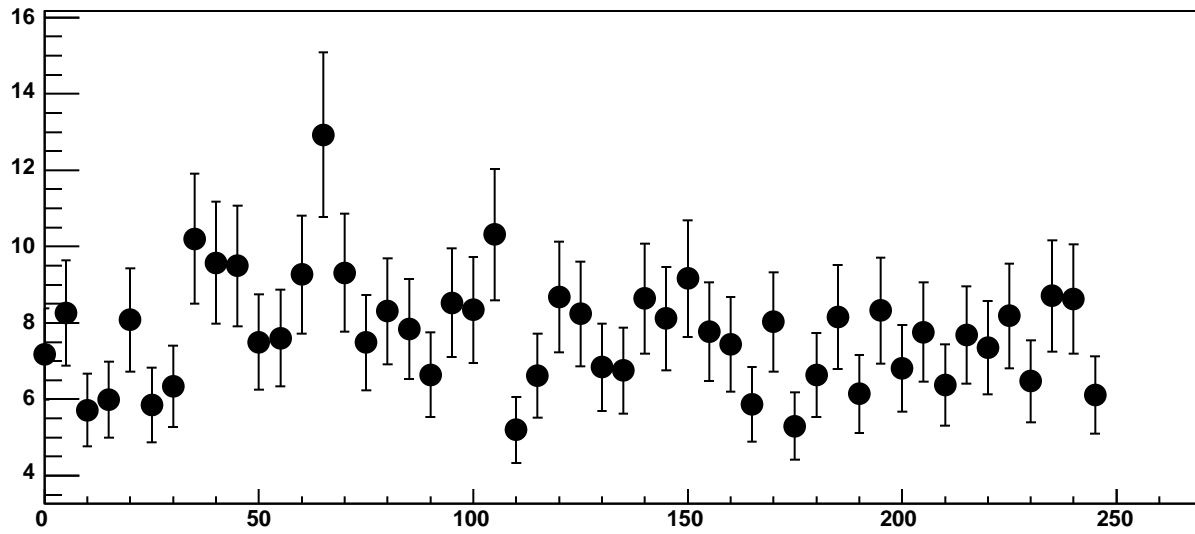


Chip 9, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

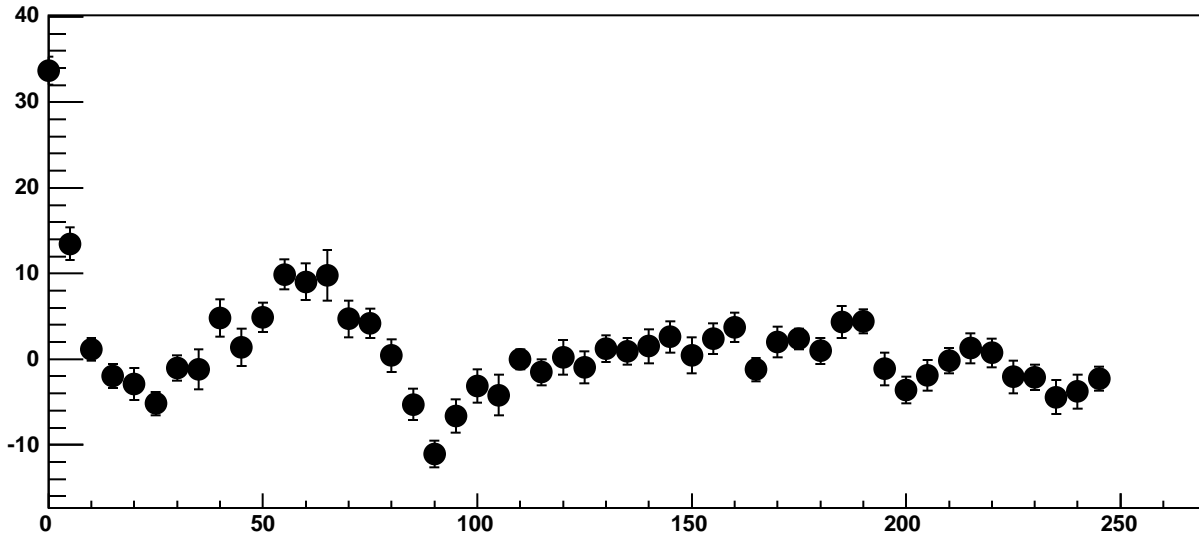


χ^2 / ndf	236 / 41
p0	-257 ± 0.6127
p1	90.23 ± 0.543
p2	$-9.142\text{e}+07 \pm 9.7\text{e}+05$
p3	$4.675\text{e}+07 \pm 1.994\text{e}+05$
p4	1.854 ± 0.01771

Chip 9, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold

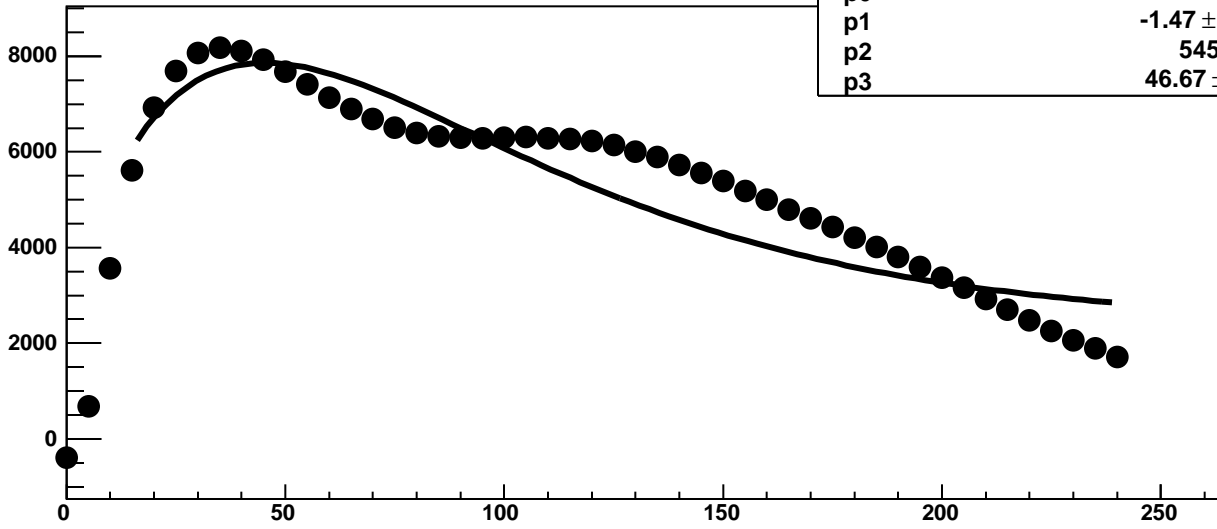


Chip 9, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

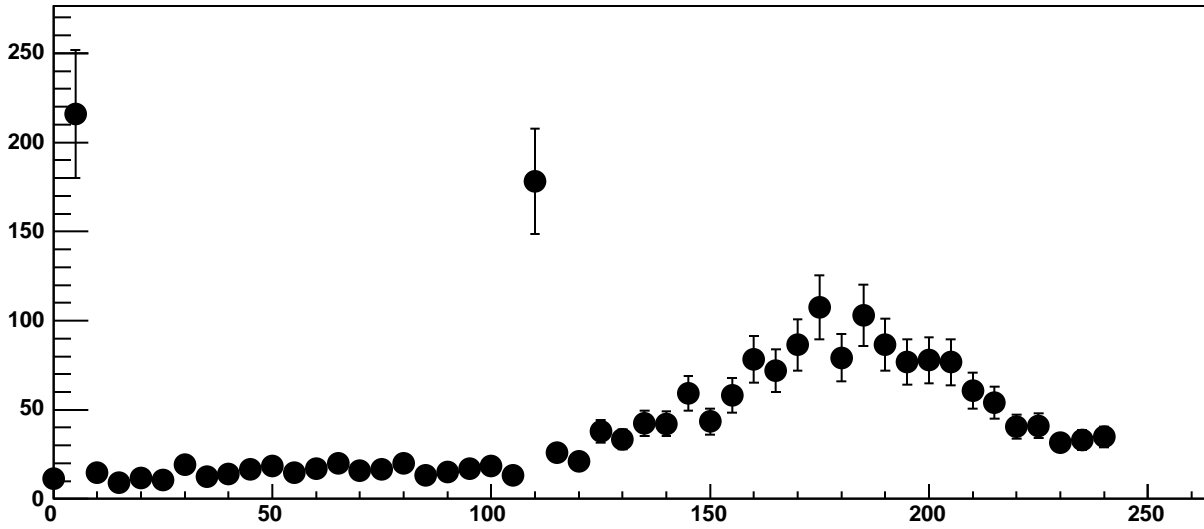


Chip 9, Channel 15, Enable 5!, DAC=4000, ADC Mean vs Hold

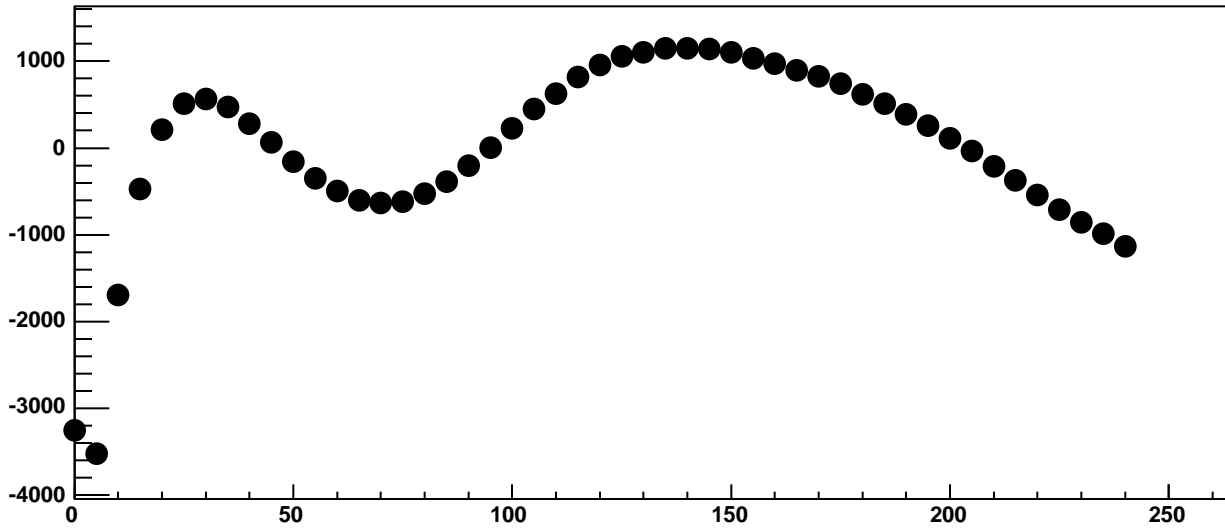
χ^2 / ndf	5.32e+05 / 42
p0	2411 ± 5.855
p1	-1.47 ± 0.04471
p2	5453 ± 5.4
p3	46.67 ± 0.0566



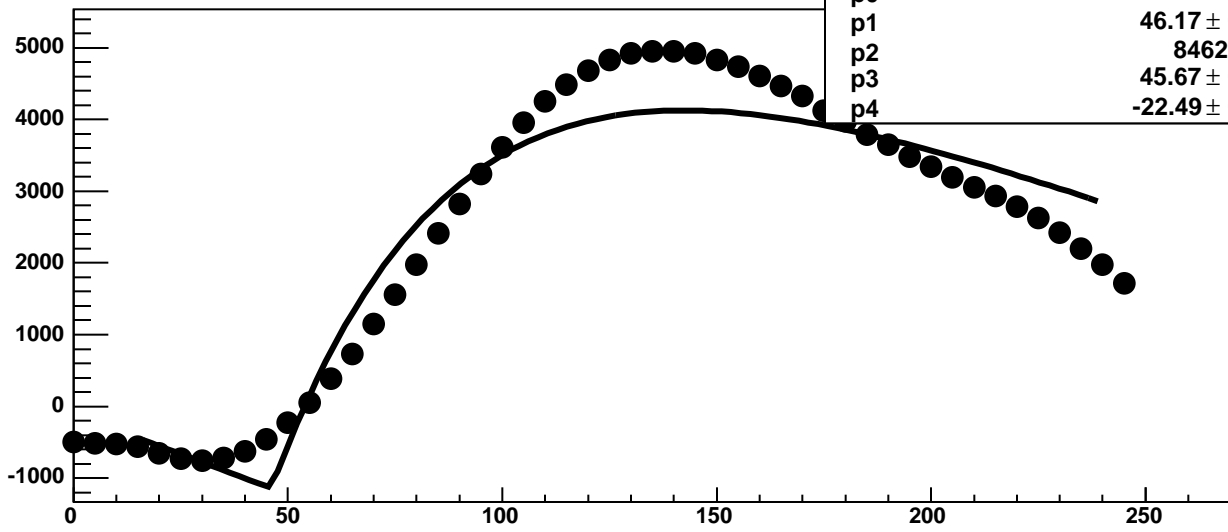
Chip 9, Channel 15, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 15, Enable 5!, DAC=4000, ADC Residuals vs Hold

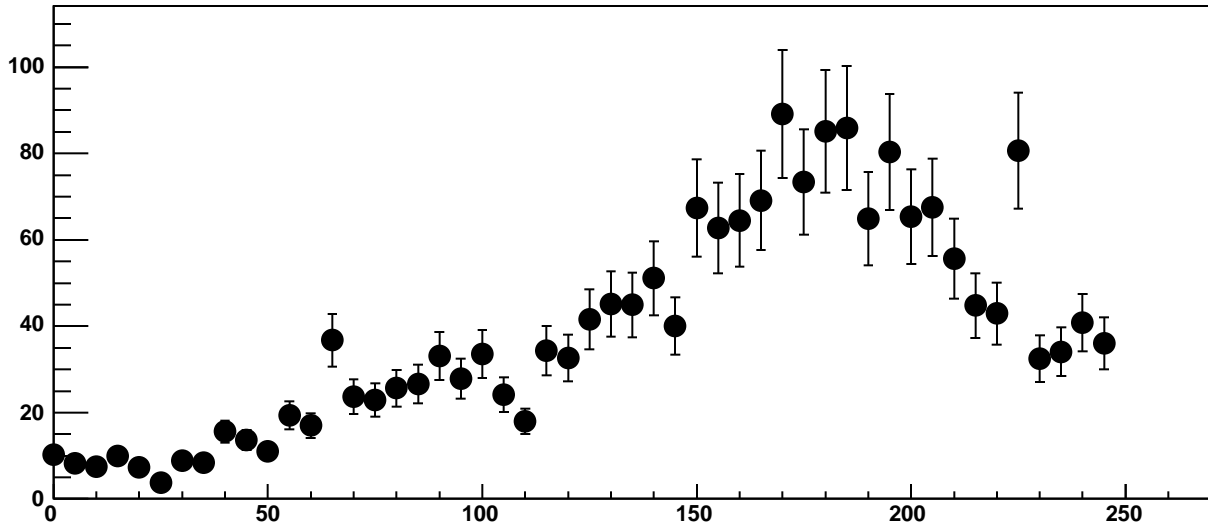


Chip 9, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

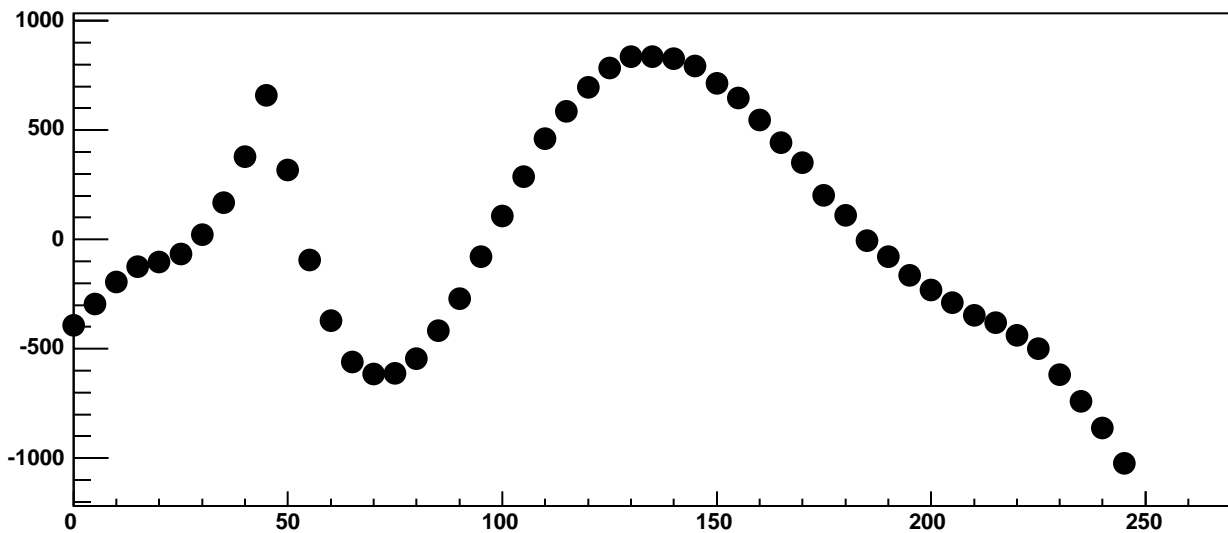


χ^2 / ndf	2.453e+05 / 41
p0	-1141 ± 1.229
p1	46.17 ± 0.01647
p2	8462 ± 12.53
p3	45.67 ± 0.07602
p4	-22.49 ± 0.06184

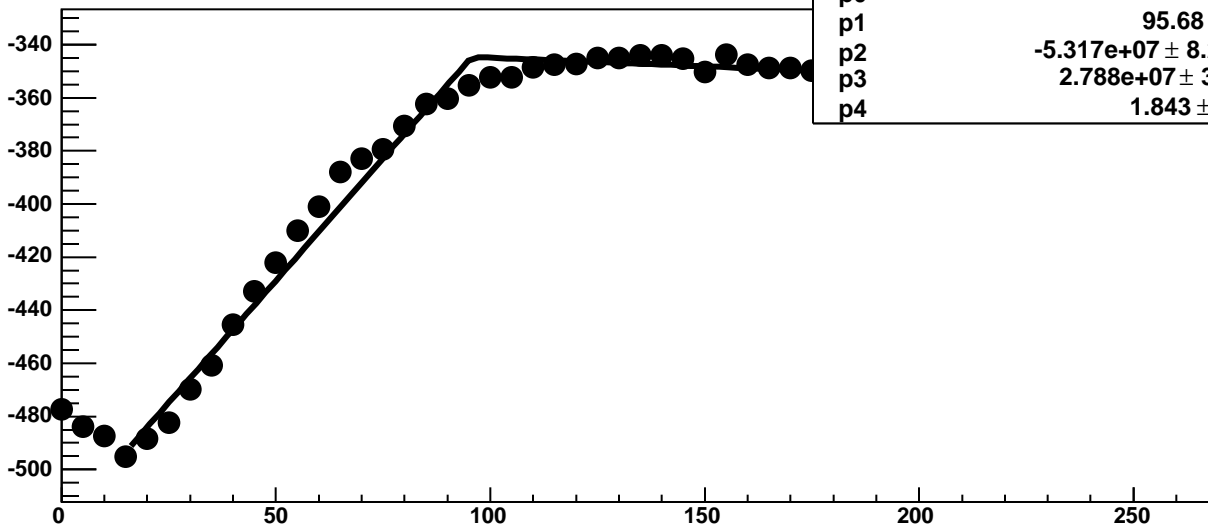
Chip 9, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

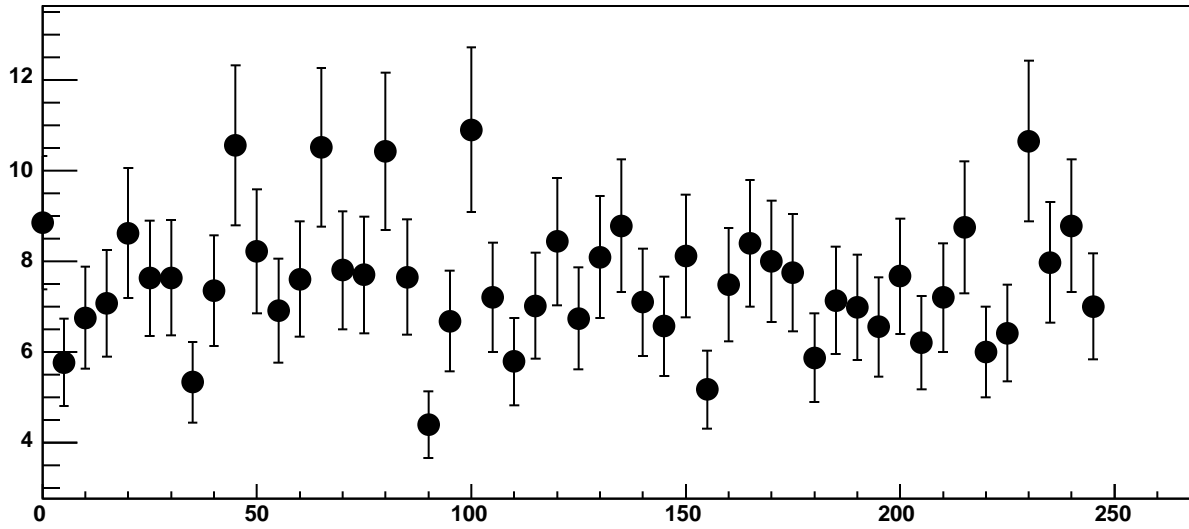


Chip 9, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold

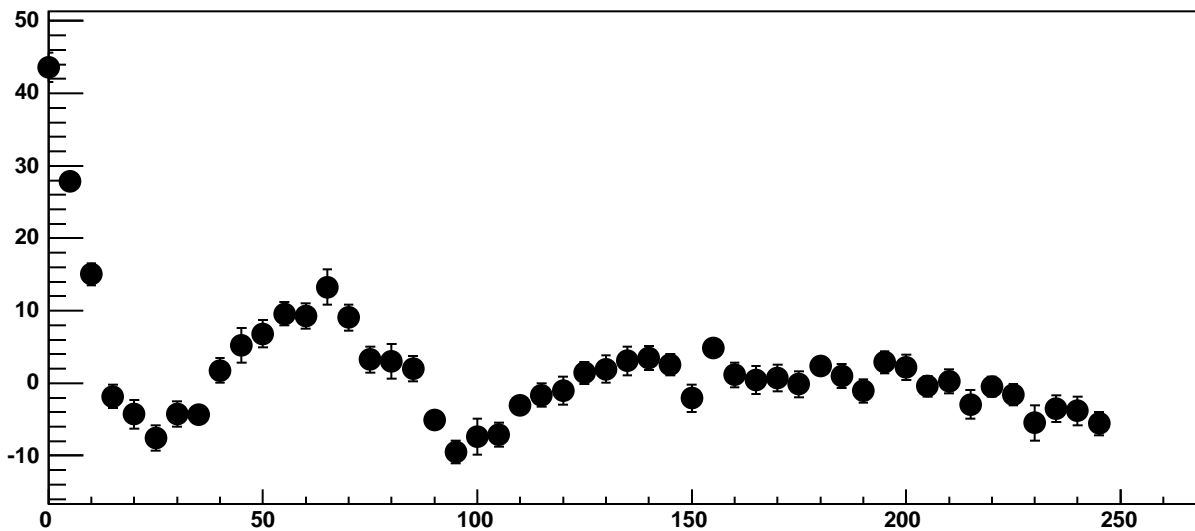


χ^2 / ndf	341.3 / 41
p0	-344.6 ± 0.622
p1	95.68 ± 0.5012
p2	$-5.317\text{e}+07 \pm 8.244\text{e}+05$
p3	$2.788\text{e}+07 \pm 3.53\text{e}+05$
p4	1.843 ± 0.01531

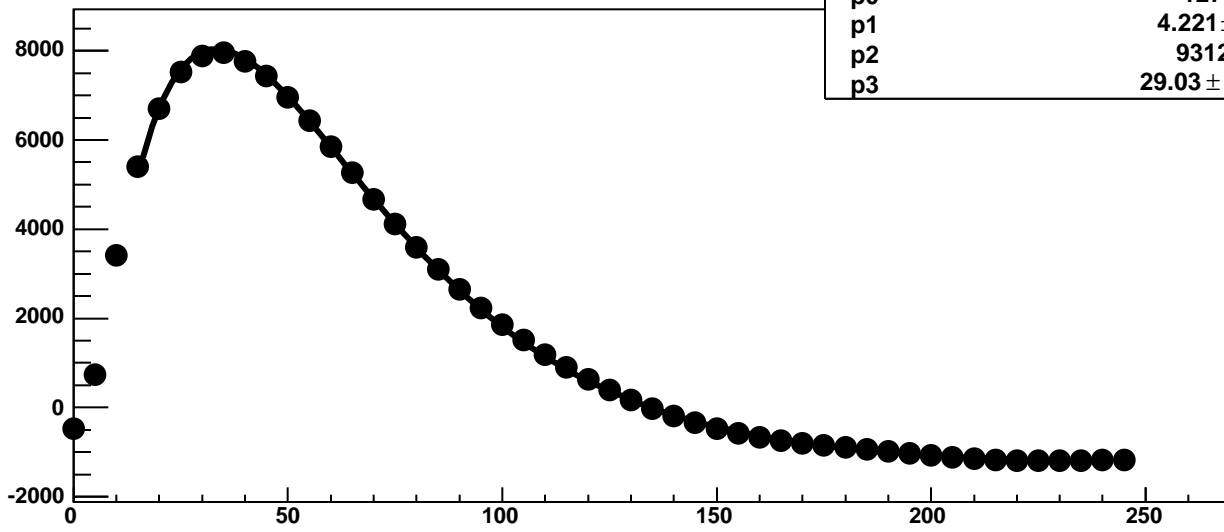
Chip 9, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



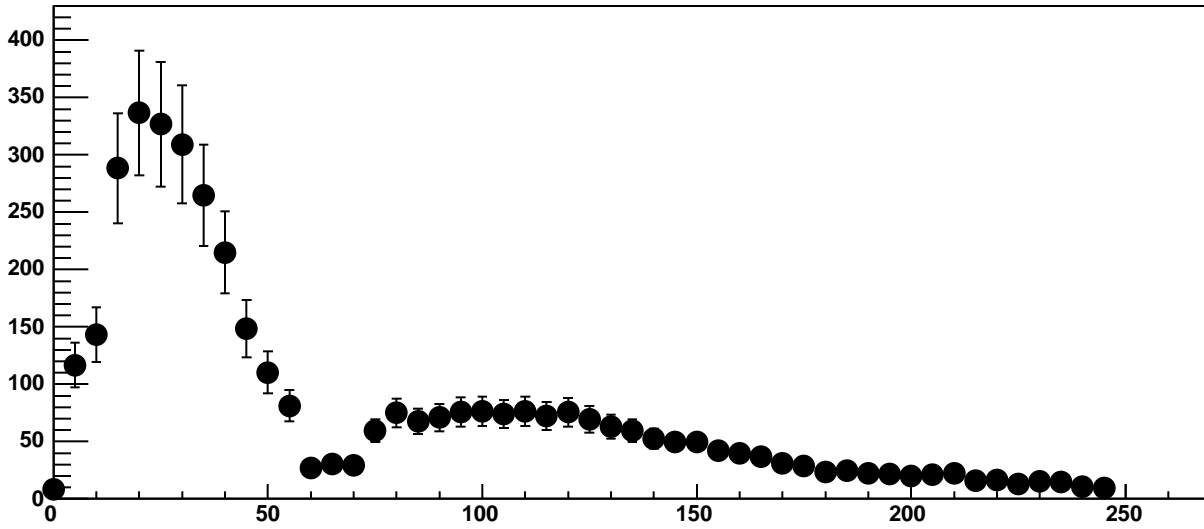
Chip 9, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold



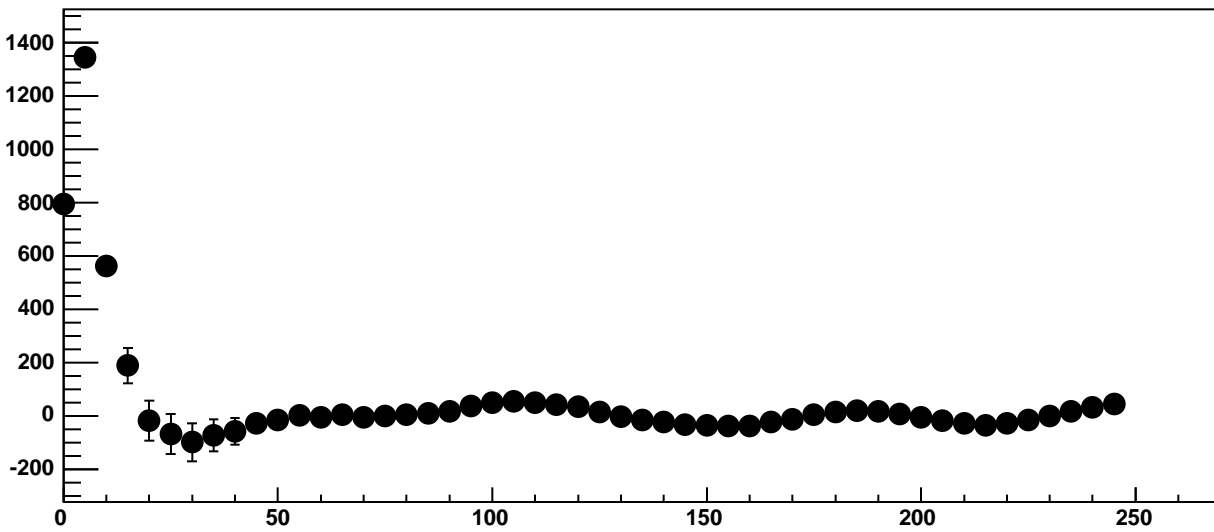
Chip 9, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold



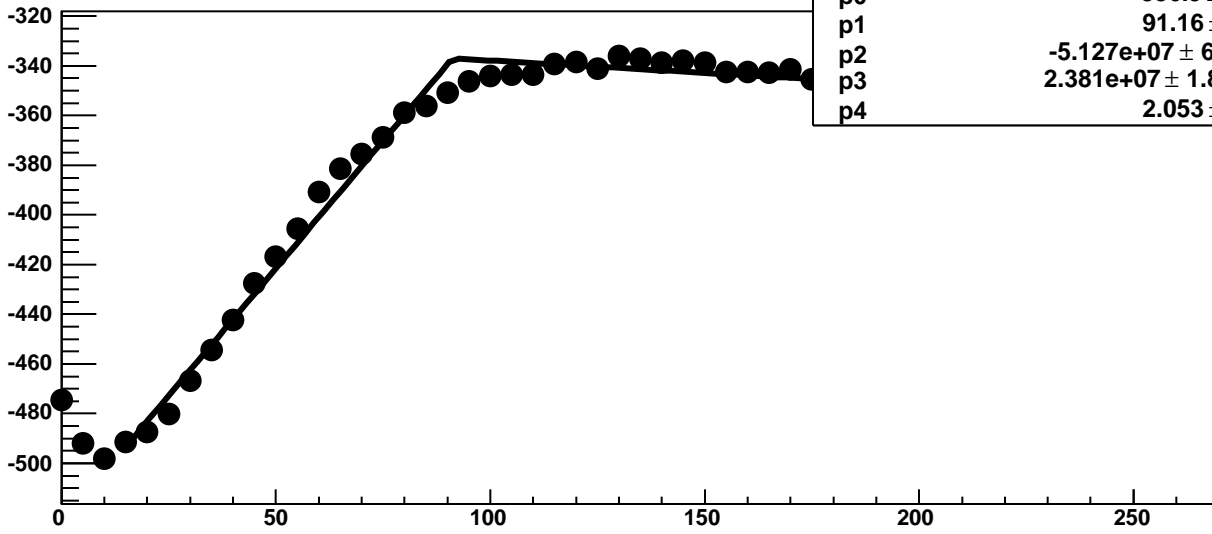
Chip 9, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold

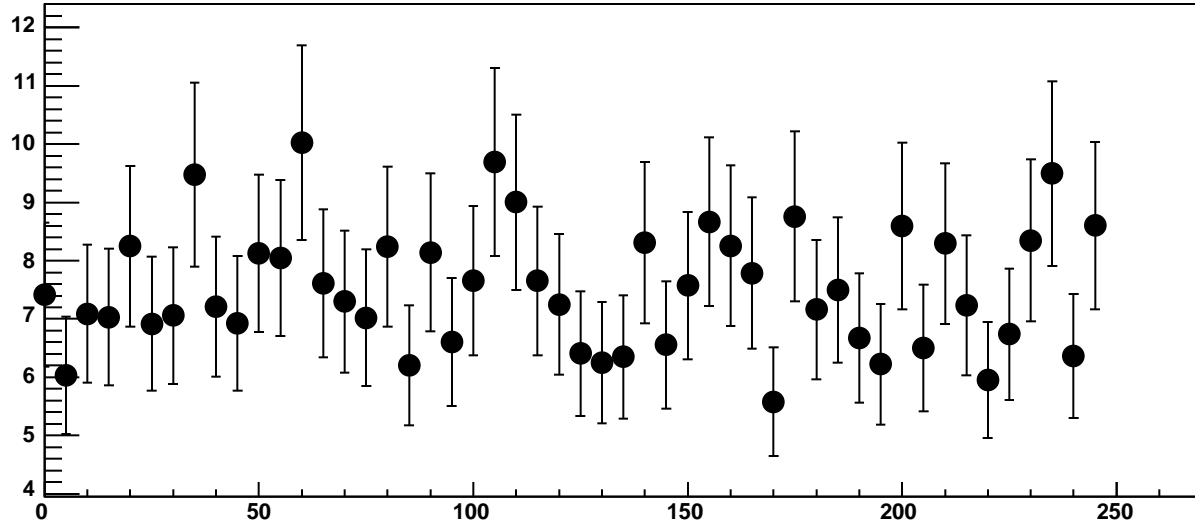


Chip 9, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

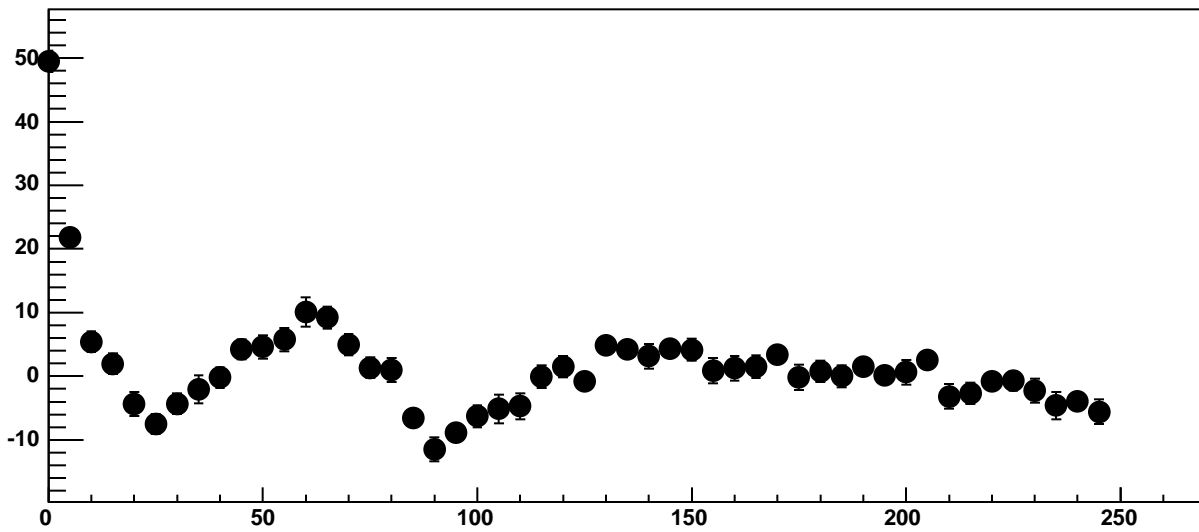


χ^2 / ndf	303.2 / 41
p0	-336.9 ± 0.5976
p1	91.16 ± 0.4807
p2	$-5.127\text{e}+07 \pm 6.14\text{e}+05$
p3	$2.381\text{e}+07 \pm 1.885\text{e}+05$
p4	2.053 ± 0.0183

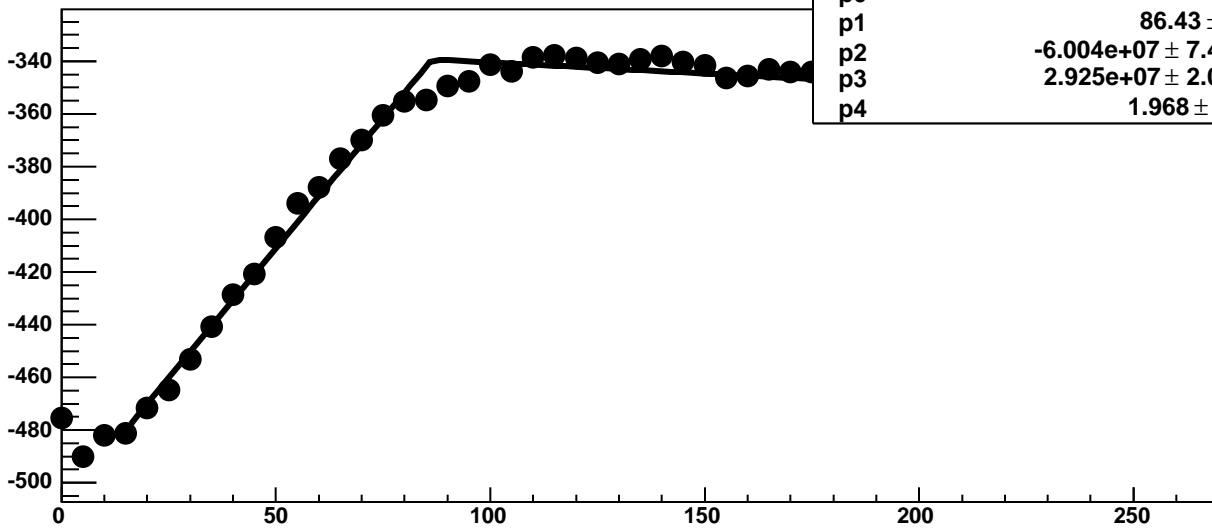
Chip 9, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

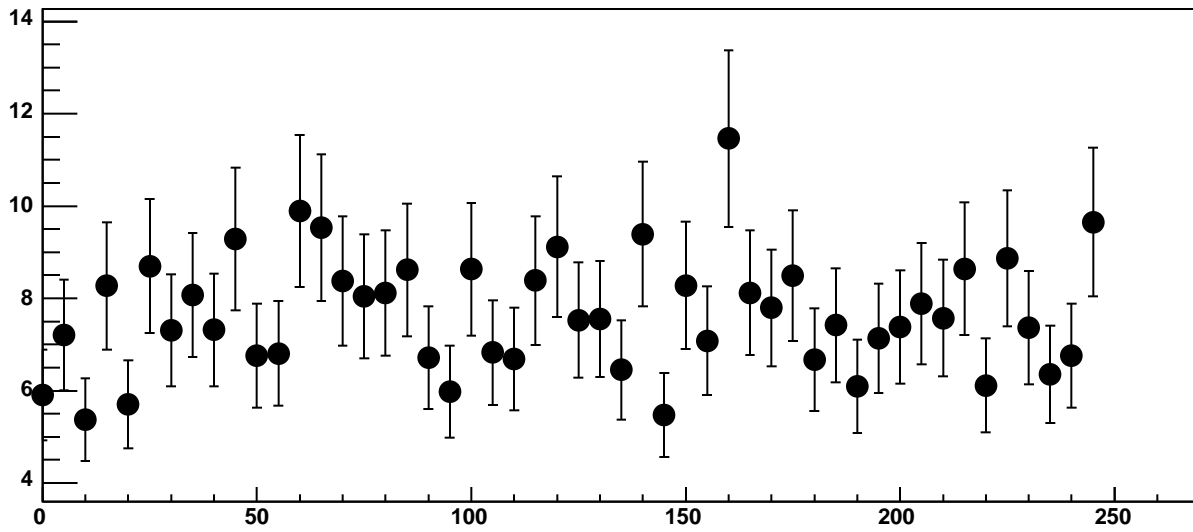


Chip 9, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

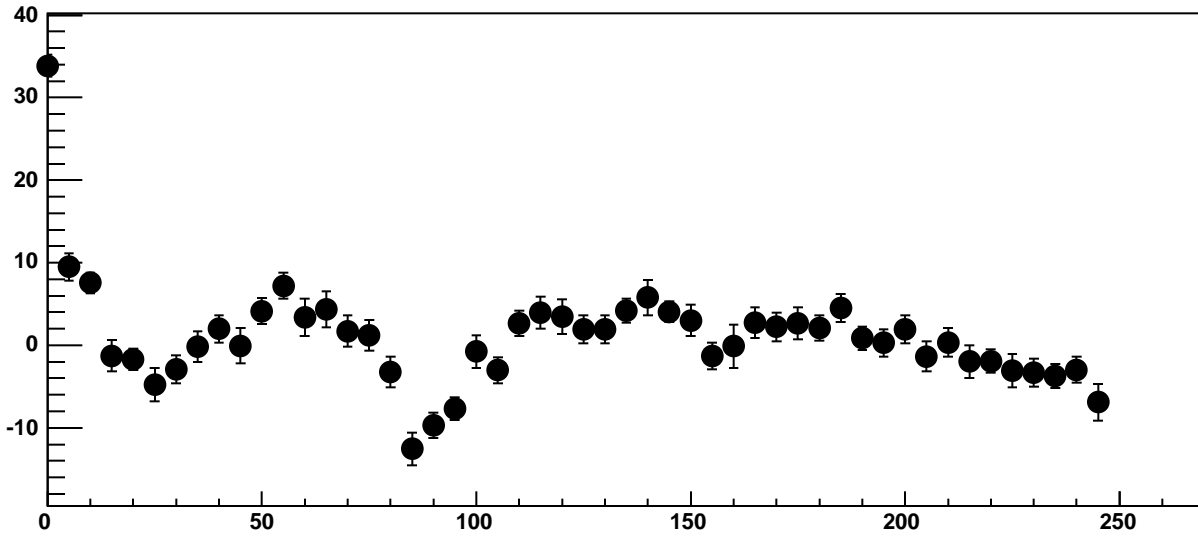


χ^2 / ndf	243.6 / 41
p0	-339.2 ± 0.5686
p1	86.43 ± 0.5386
p2	$-6.004\text{e}+07 \pm 7.404\text{e}+05$
p3	$2.925\text{e}+07 \pm 2.096\text{e}+05$
p4	1.968 ± 0.02114

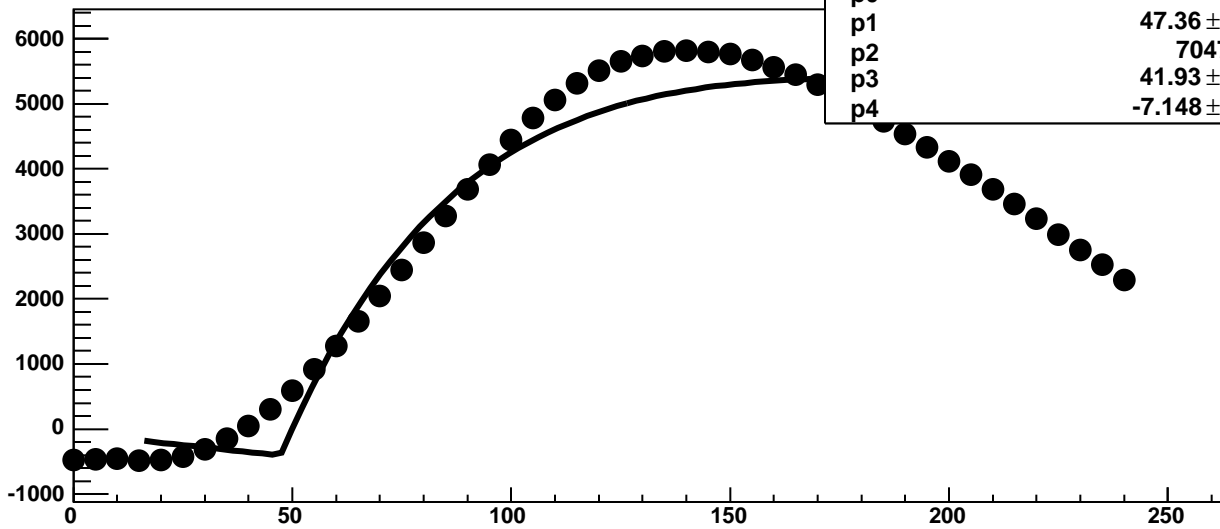
Chip 9, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

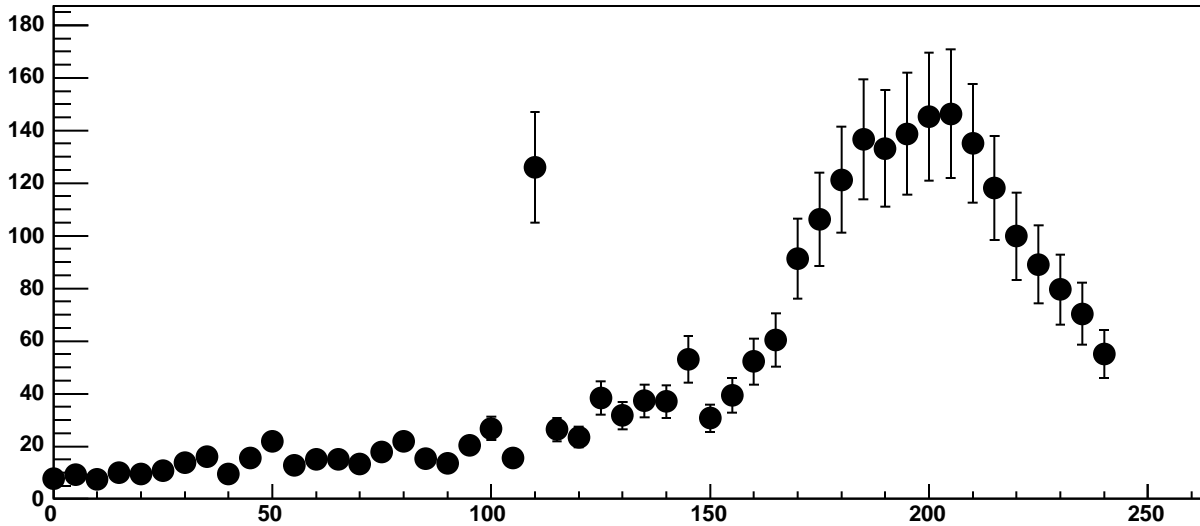


Chip 9, Channel 16, Enable 5, DAC=4000, ADC Mean vs Hold

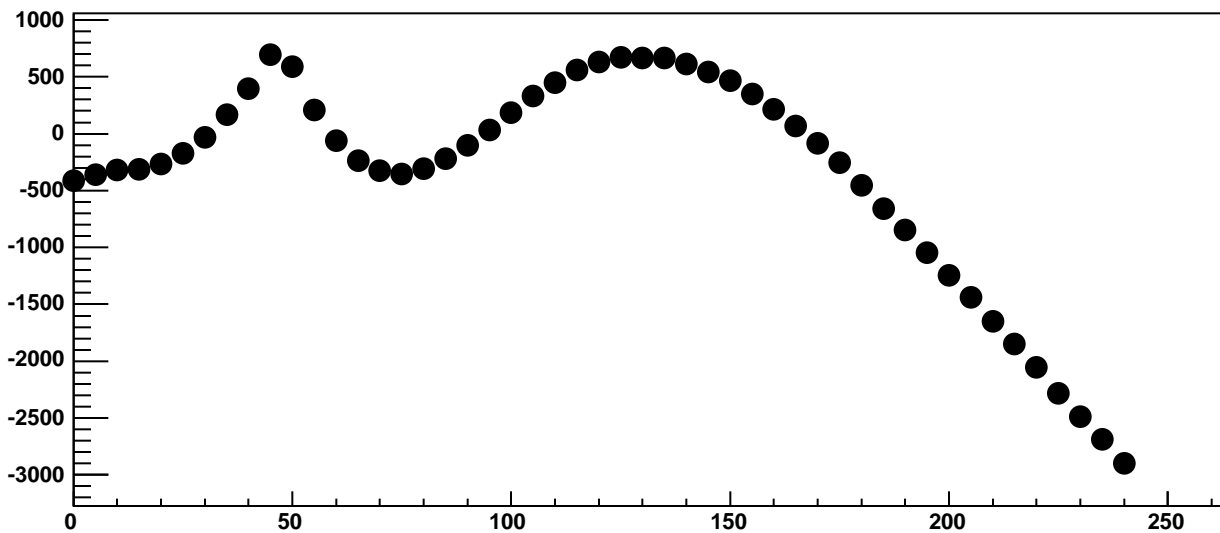


χ^2 / ndf	3.617e+05 / 41
p0	-406.4 ± 1.567
p1	47.36 ± 0.02023
p2	7047 ± 11.32
p3	41.93 ± 0.07358
p4	-7.148 ± 0.06667

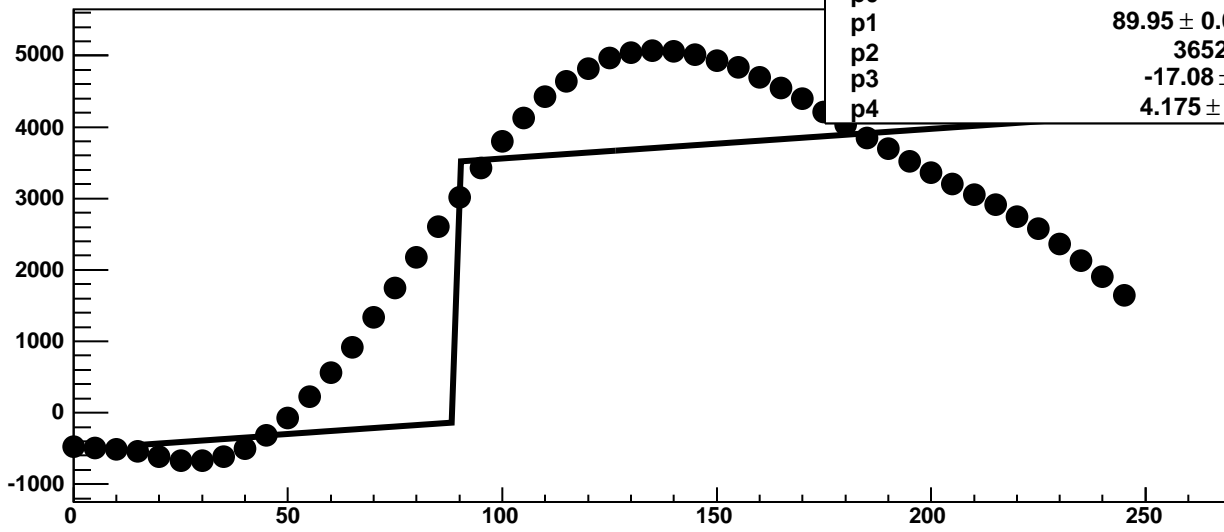
Chip 9, Channel 16, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 16, Enable 5, DAC=4000, ADC Residuals vs Hold

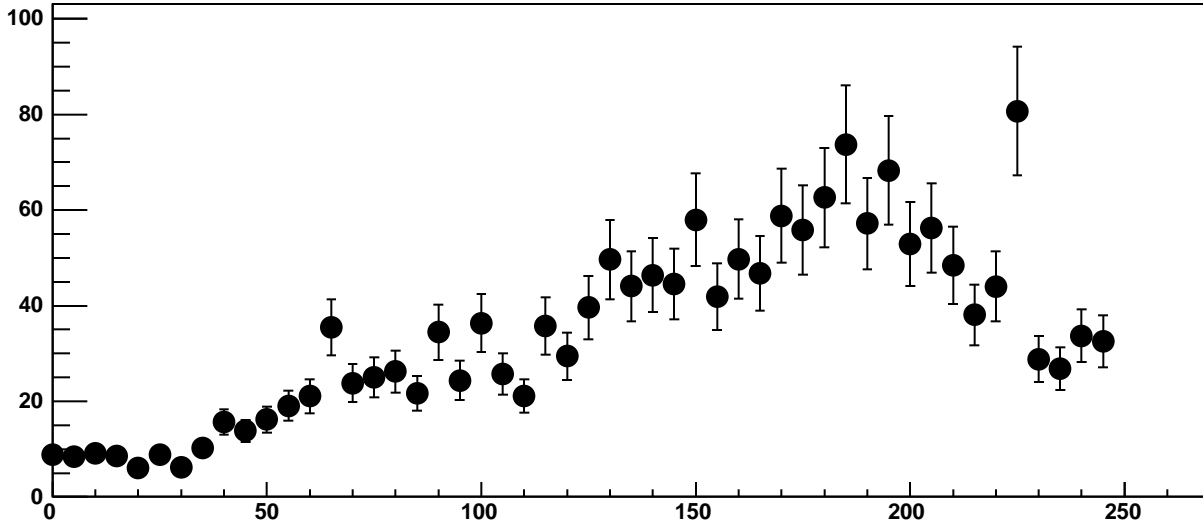


Chip 9, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

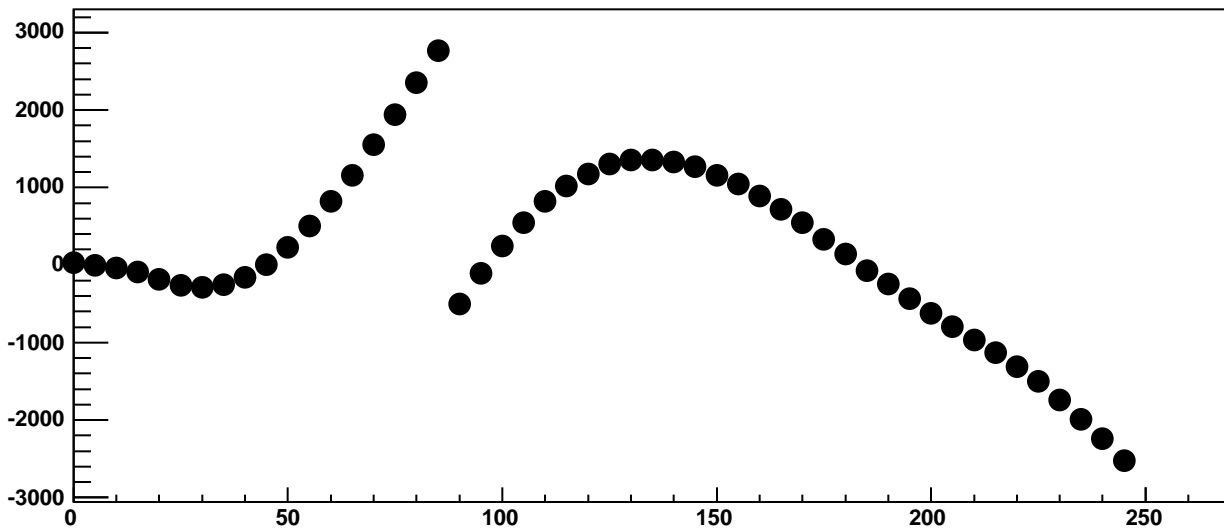


χ^2 / ndf	1.334e+06 / 41
p0	-134.5 ± 1.422
p1	89.95 ± 0.0001427
p2	3652 ± 3.399
p3	-17.08 ± 0.0104
p4	4.175 ± 0.02224

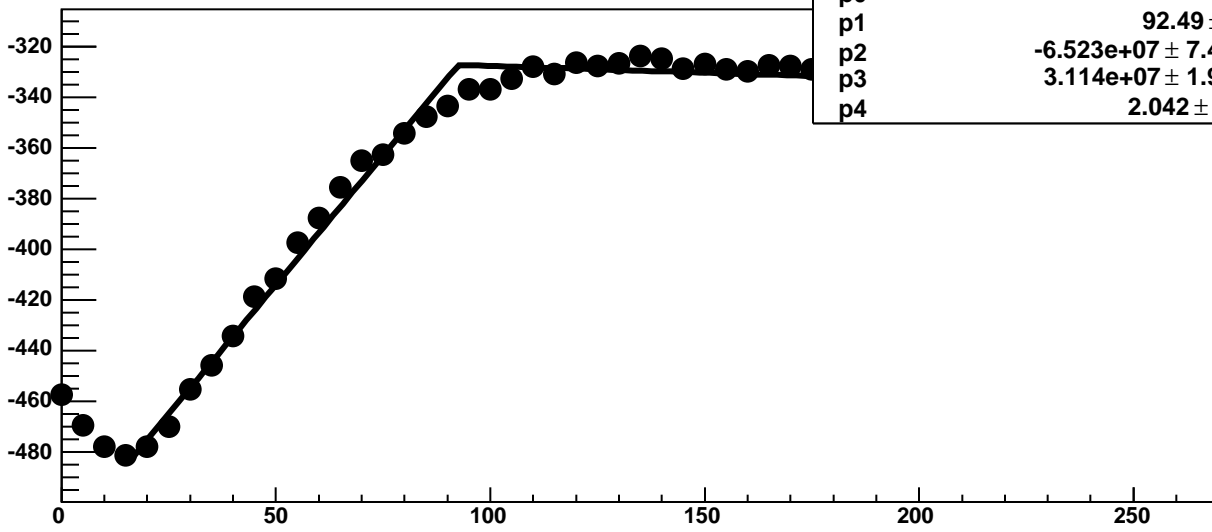
Chip 9, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

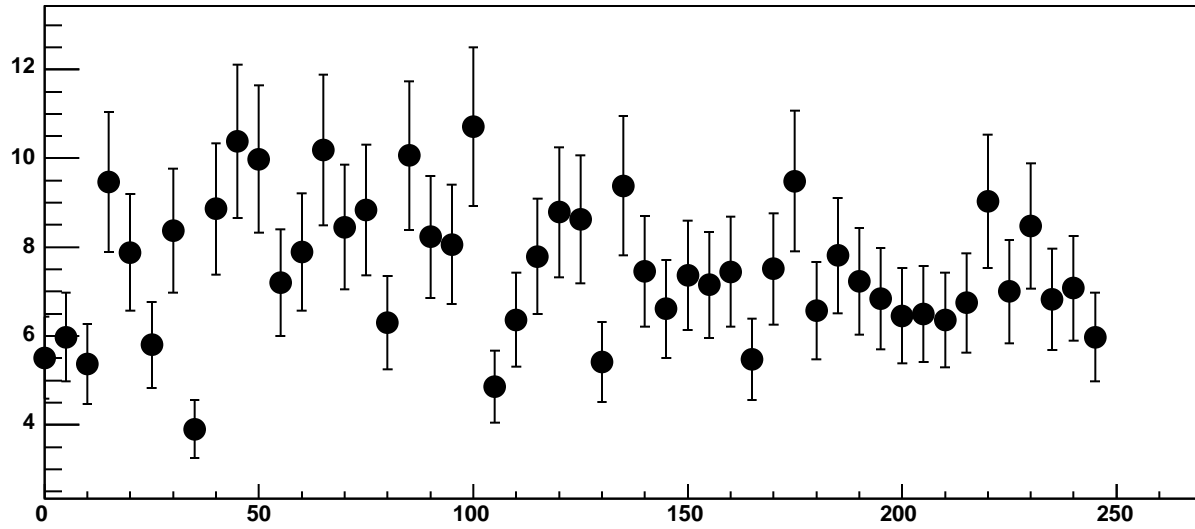


Chip 9, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

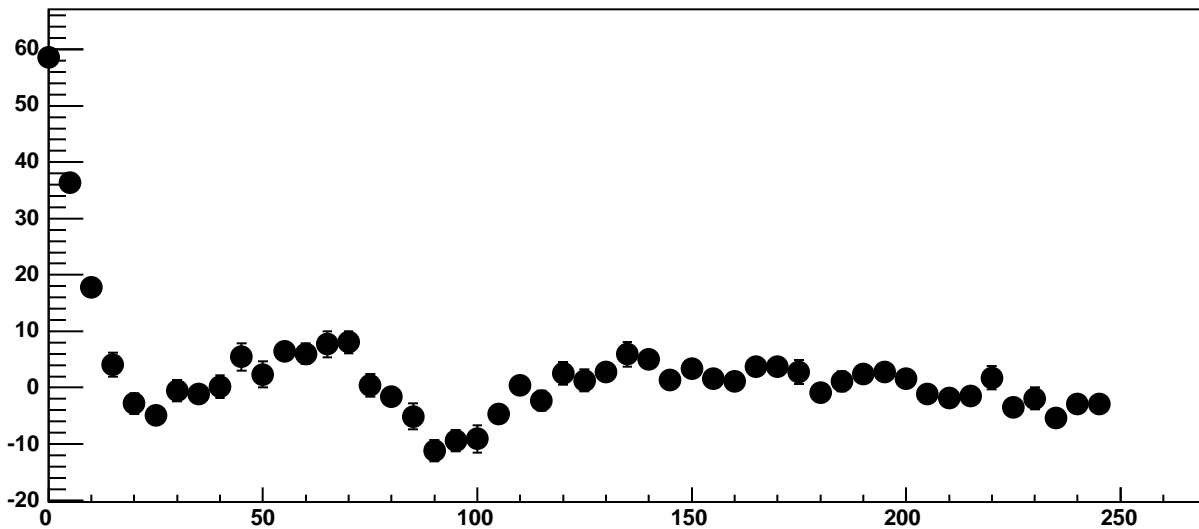


χ^2 / ndf	257.2 / 41
p0	-327.2 ± 0.5726
p1	92.49 ± 0.5129
p2	$-6.523\text{e}+07 \pm 7.407\text{e}+05$
p3	$3.114\text{e}+07 \pm 1.907\text{e}+05$
p4	2.042 ± 0.01869

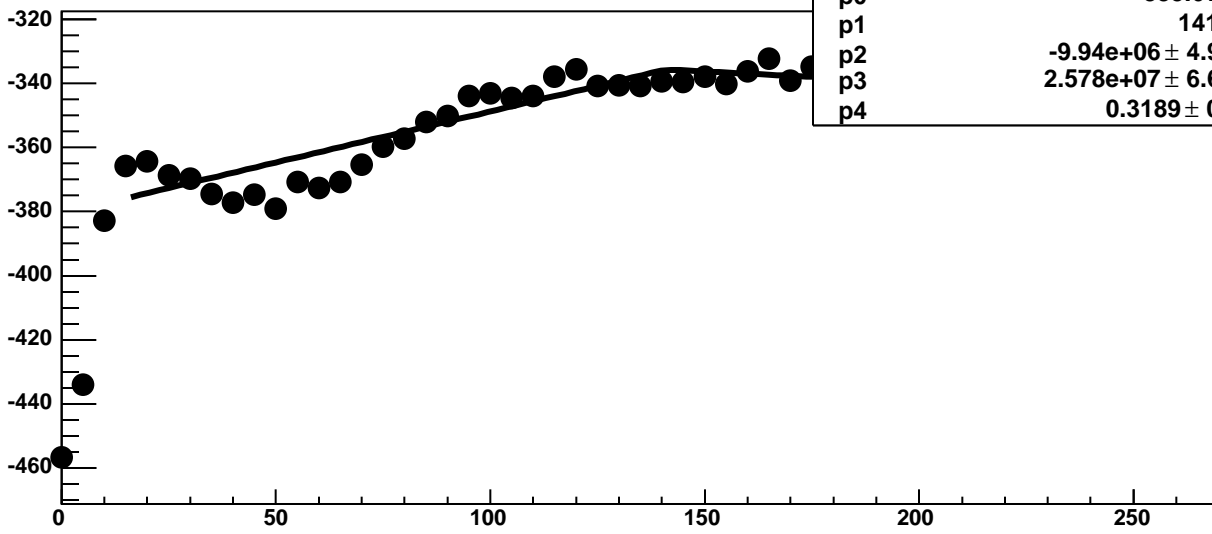
Chip 9, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

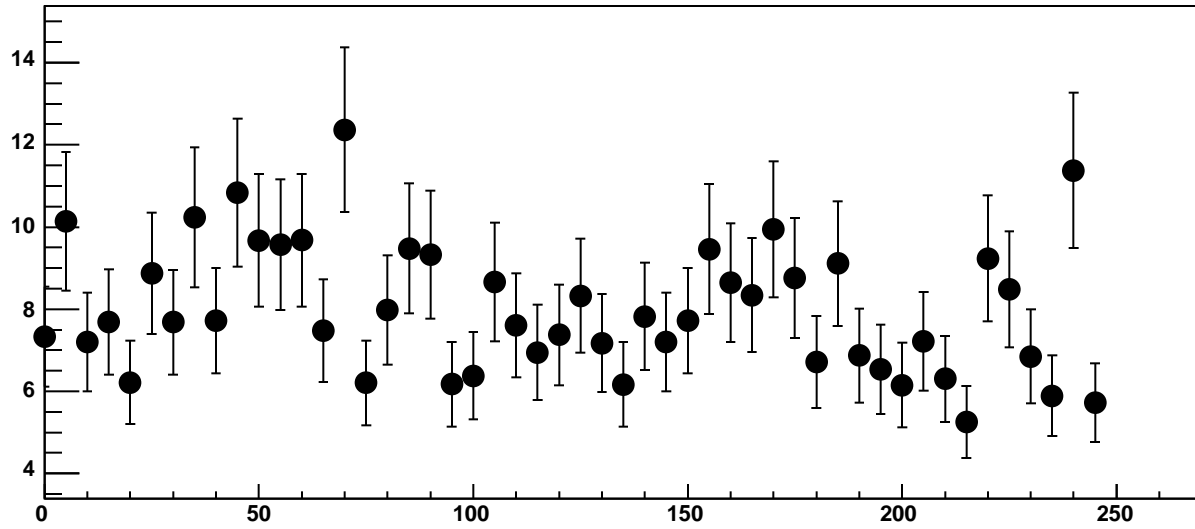


Chip 9, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold

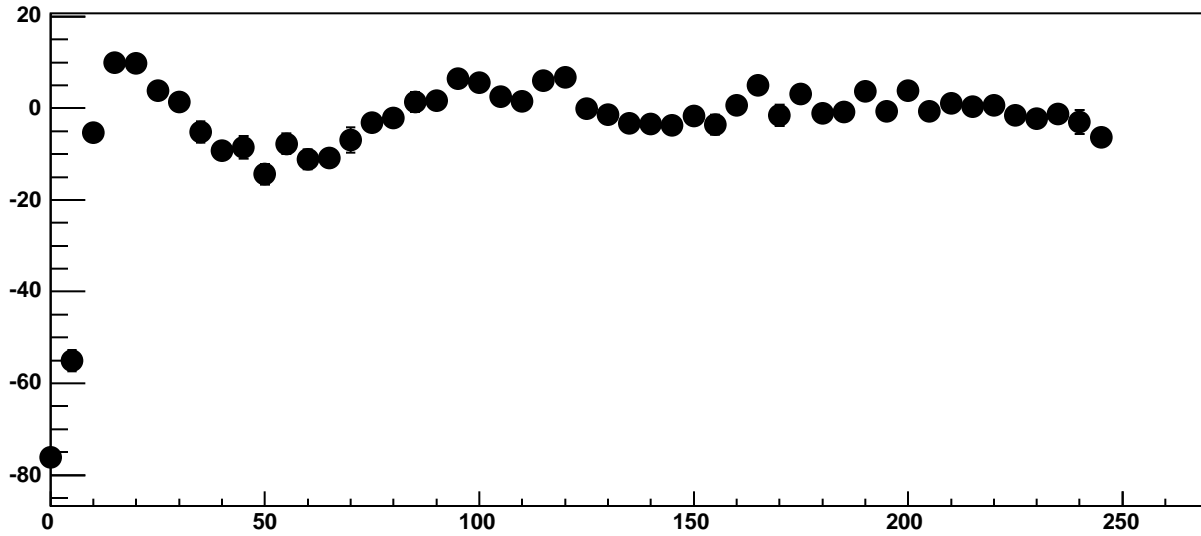


χ^2 / ndf	380.4 / 41
p0	-335.6 ± 0.7059
p1	141 ± 2.758
p2	$-9.94\text{e}+06 \pm 4.987\text{e}+05$
p3	$2.578\text{e}+07 \pm 6.679\text{e}+05$
p4	0.3189 ± 0.009041

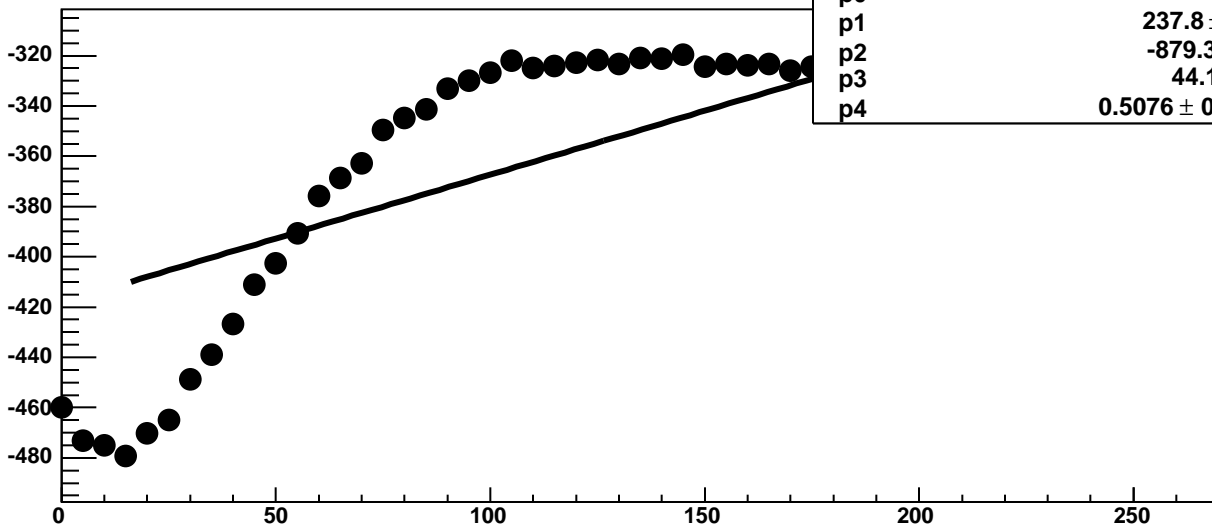
Chip 9, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold

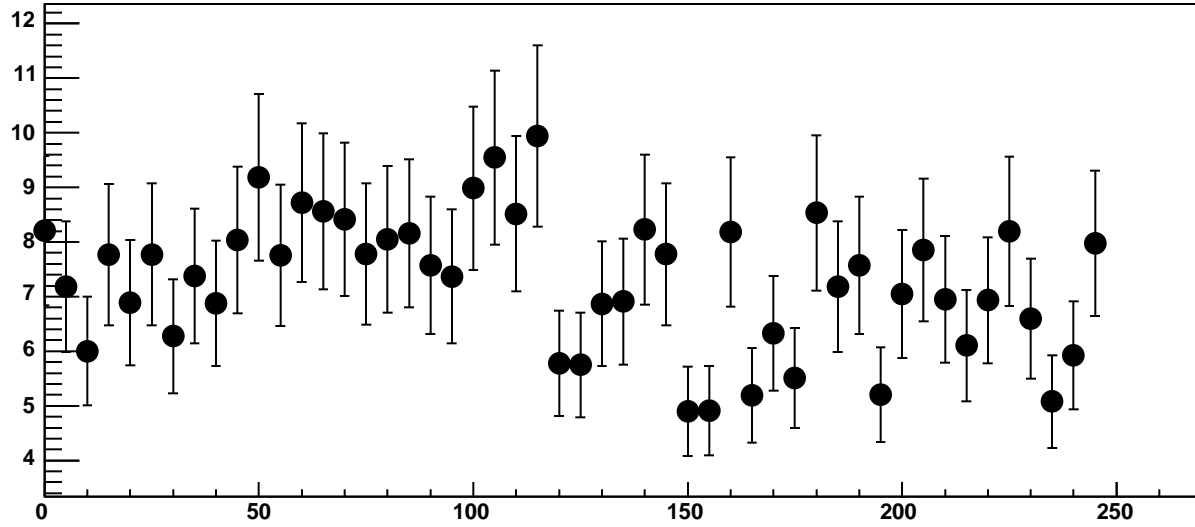


Chip 9, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold

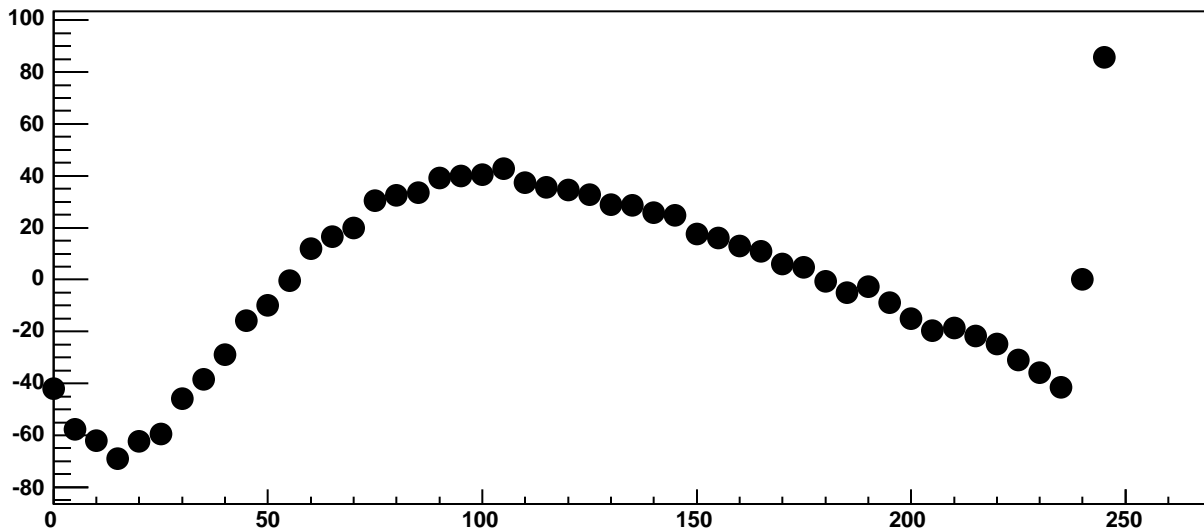


χ^2 / ndf	1.565e+04 / 41
p0	-297.4 ± 0.6386
p1	237.8 ± 0.9069
p2	-879.3 ± 348.3
p3	44.1 ± 14.72
p4	0.5076 ± 0.003684

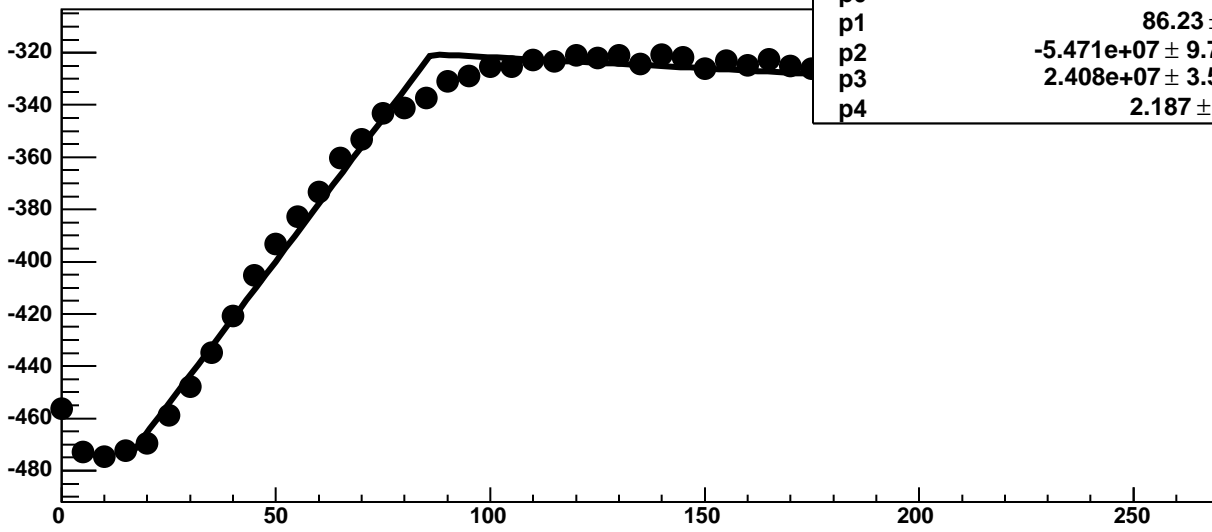
Chip 9, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 9, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold

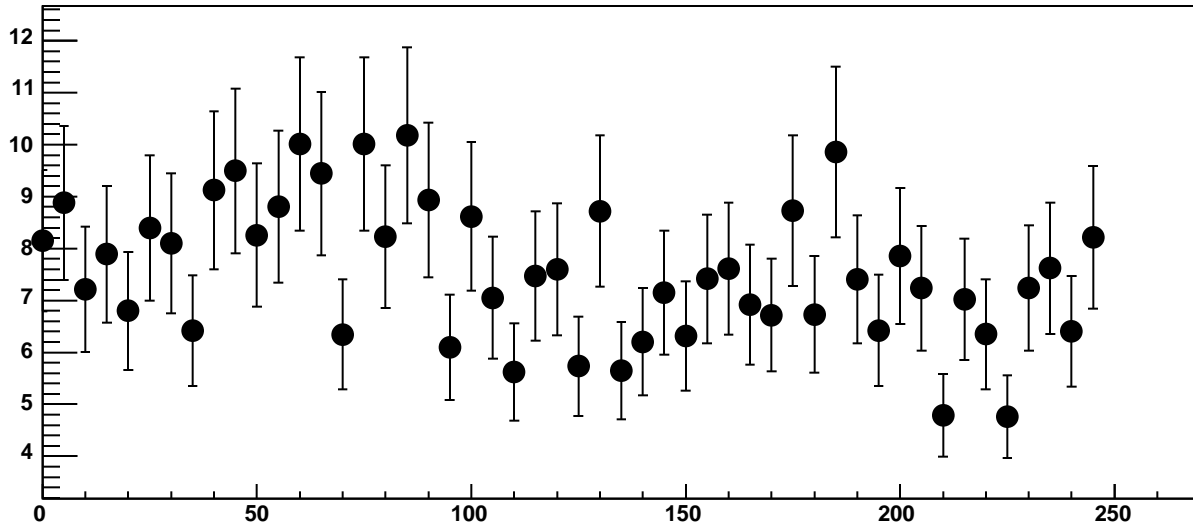


Chip 9, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold

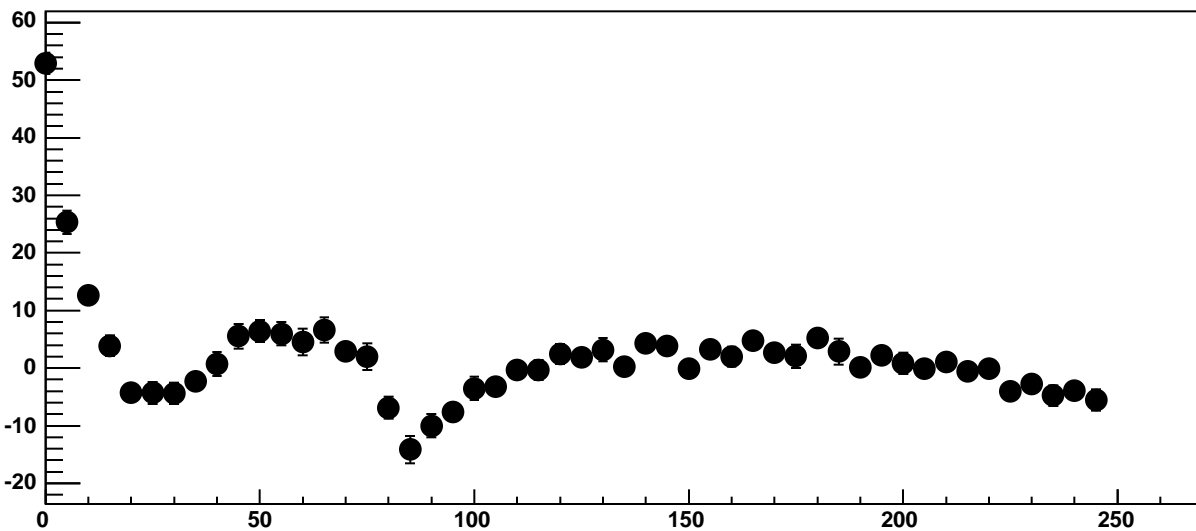


χ^2 / ndf	268 / 41
p0	-320.5 ± 0.5528
p1	86.23 ± 0.5053
p2	$-5.471\text{e}+07 \pm 9.744\text{e}+05$
p3	$2.408\text{e}+07 \pm 3.508\text{e}+05$
p4	2.187 ± 0.02241

Chip 9, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold

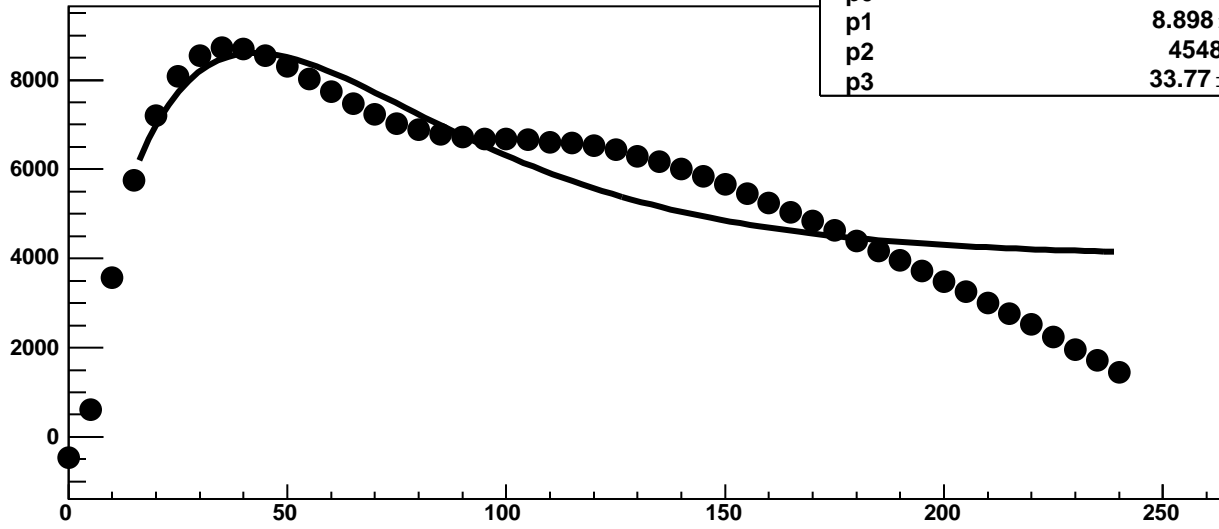


Chip 9, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

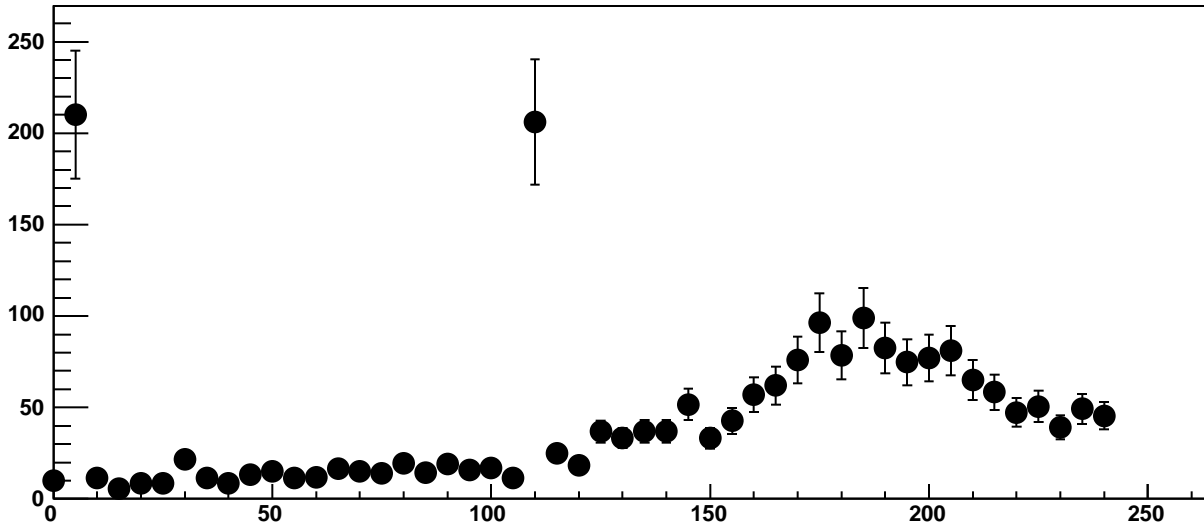


Chip 9, Channel 17, Enable 5!, DAC=4000, ADC Mean vs Hold

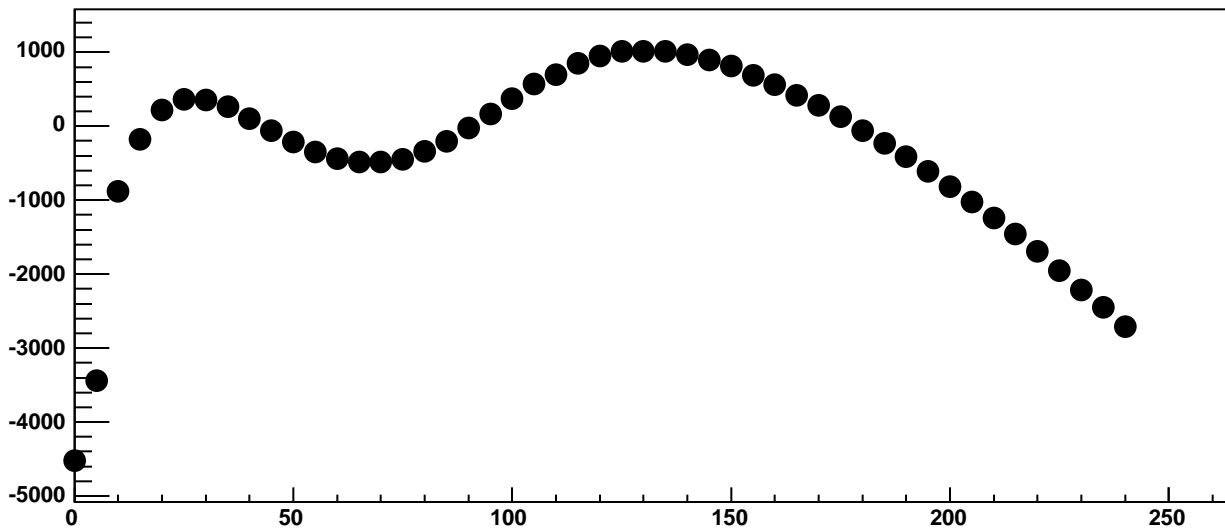
χ^2 / ndf	6.669e+05 / 42
p0	4059 \pm 4.676
p1	8.898 \pm 0.0201
p2	4548 \pm 4.512
p3	33.77 \pm 0.0348



Chip 9, Channel 17, Enable 5!, DAC=4000, ADC Noise vs Hold

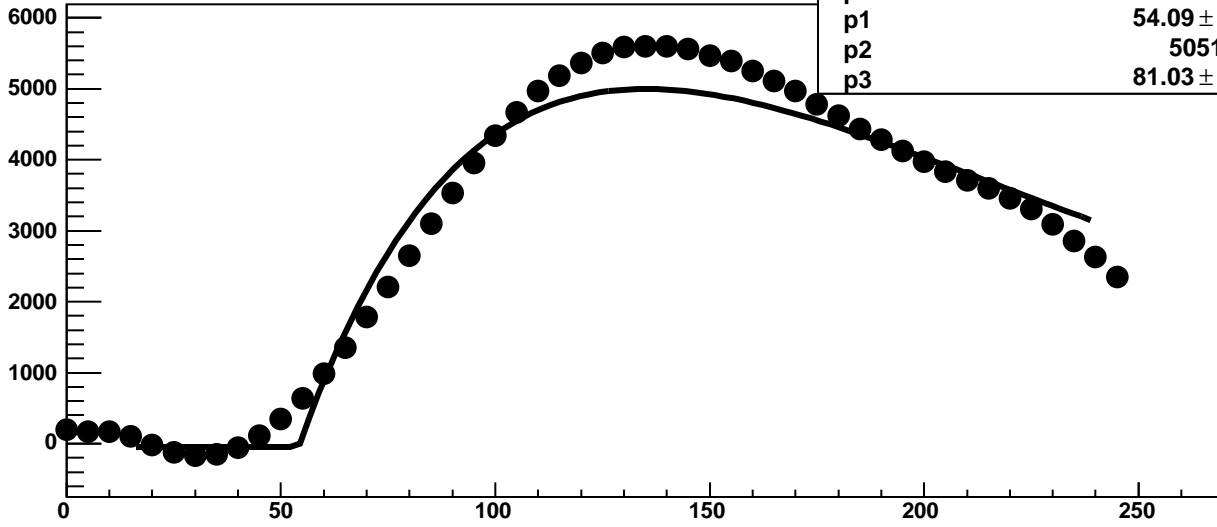


Chip 9, Channel 17, Enable 5!, DAC=4000, ADC Residuals vs Hold

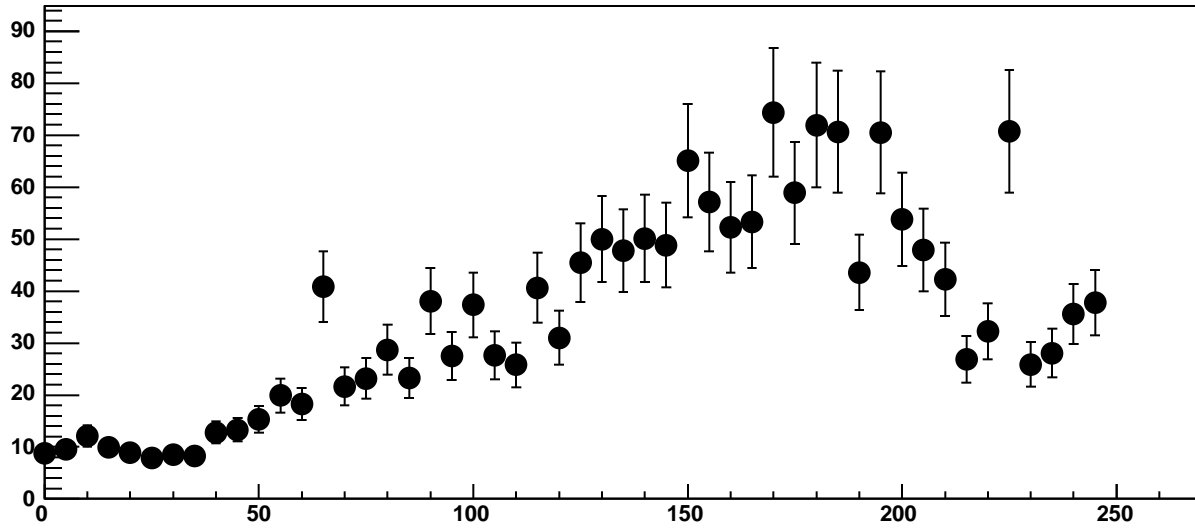


Chip 10, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold

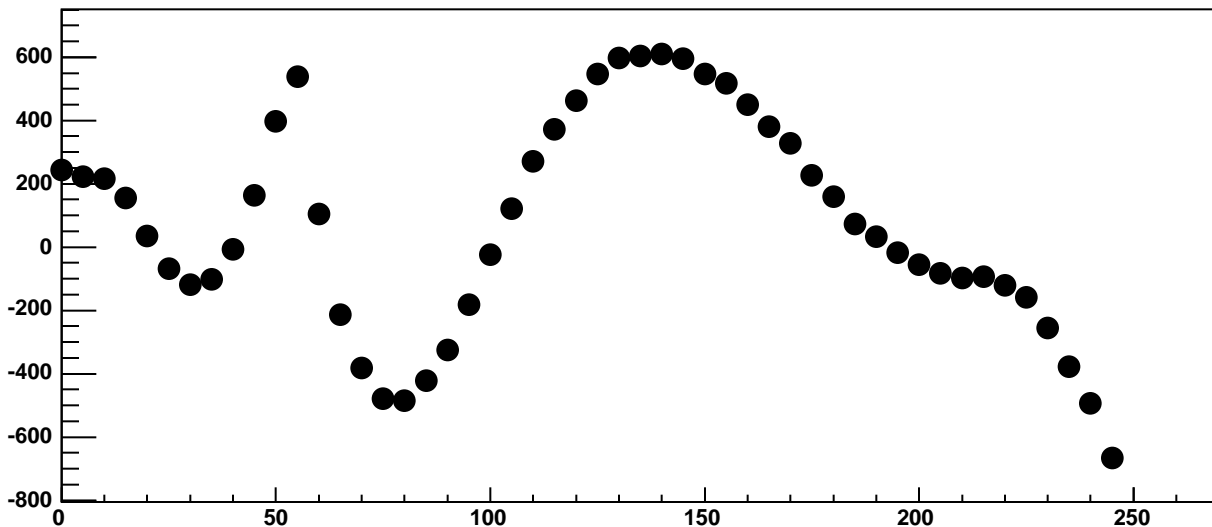
χ^2 / ndf	1.099e+05 / 42
p0	-50.86 ± 0.7945
p1	54.09 ± 0.01956
p2	5051 ± 2.106
p3	81.03 ± 0.05095



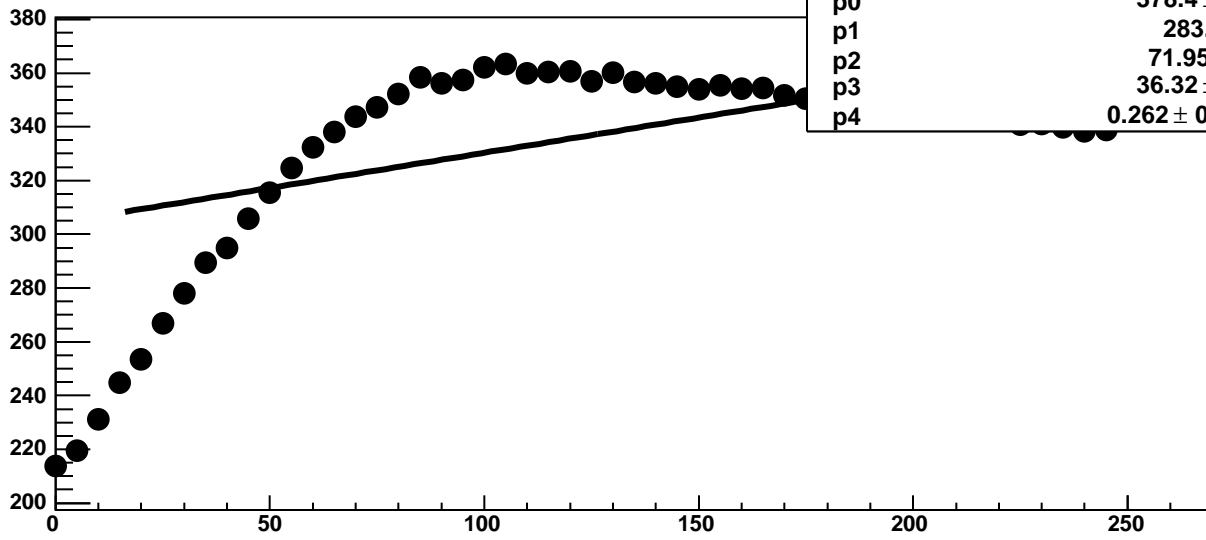
Chip 10, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold

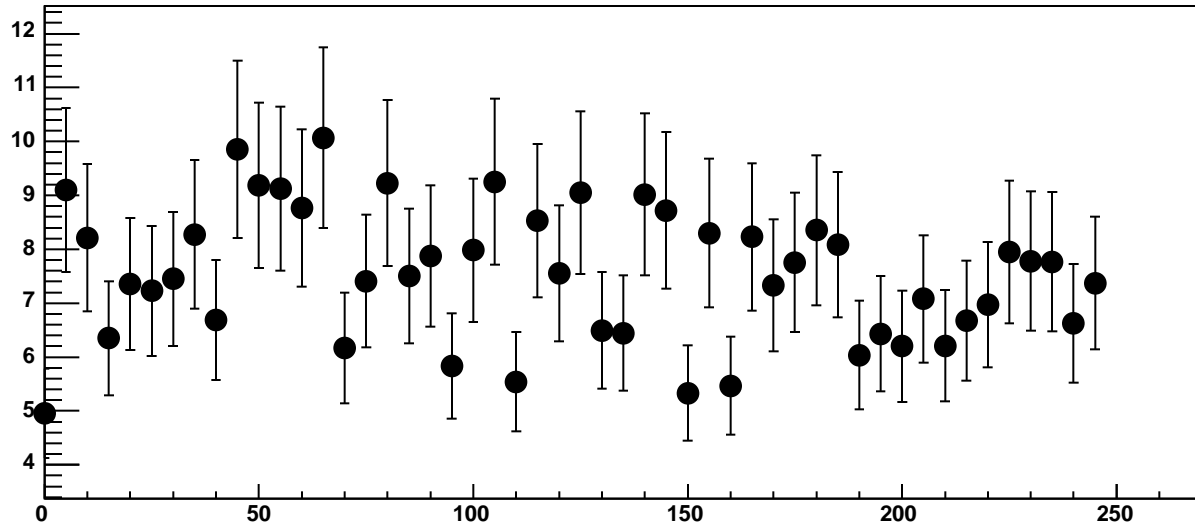


Chip 10, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

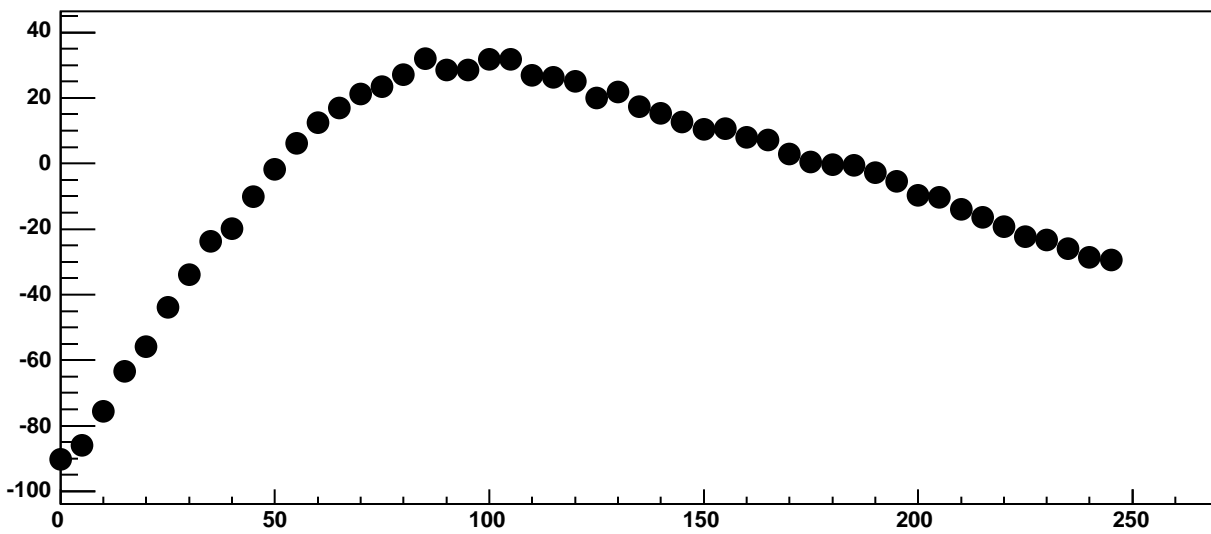


χ^2 / ndf	9557 / 41
p0	378.4 ± 0.6019
p1	283.6 ± 0.5
p2	71.95 ± 2.175
p3	36.32 ± 0.2027
p4	0.262 ± 0.003717

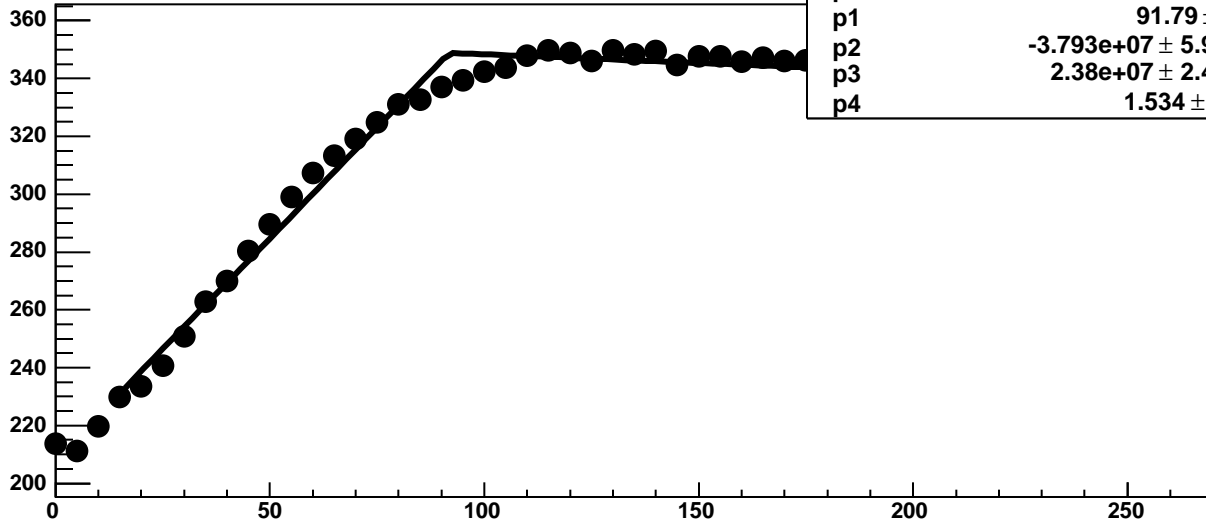
Chip 10, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold

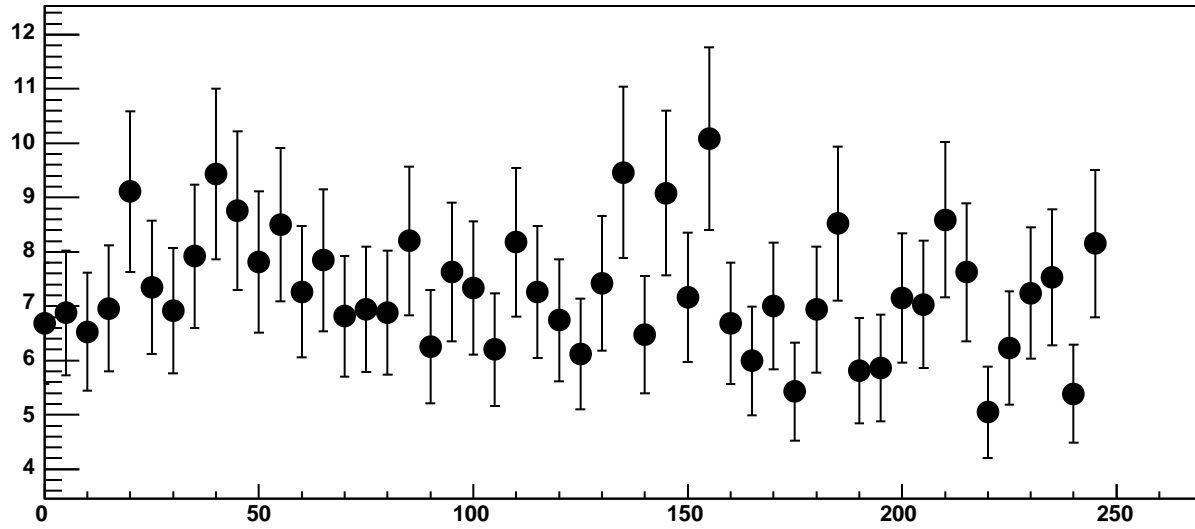


Chip 10, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

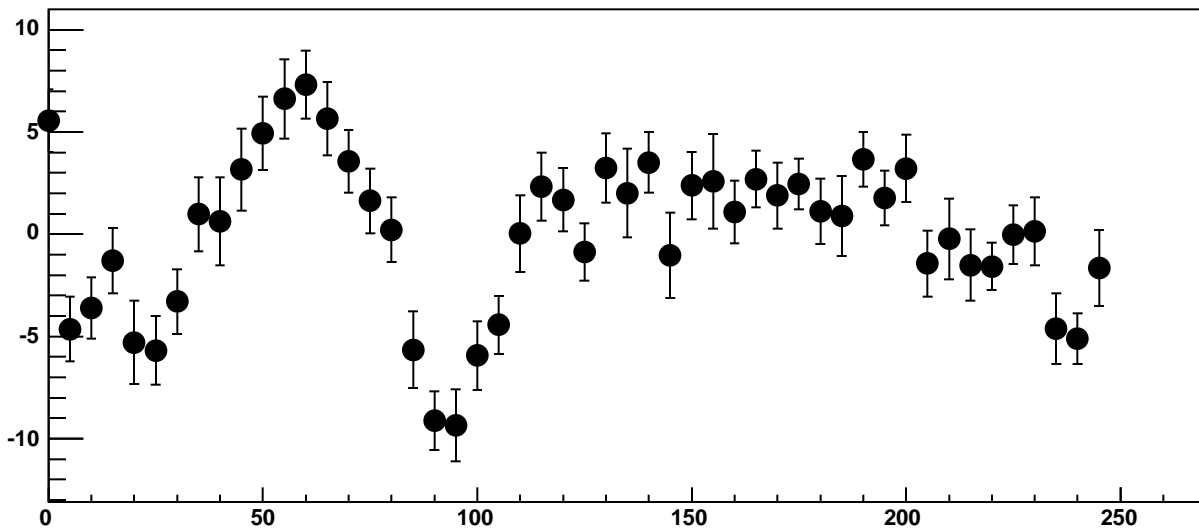


χ^2 / ndf	249.1 / 41
p0	348.9 ± 0.5748
p1	91.79 ± 0.6257
p2	$-3.793\text{e}+07 \pm 5.966\text{e}+05$
p3	$2.38\text{e}+07 \pm 2.473\text{e}+05$
p4	1.534 ± 0.01801

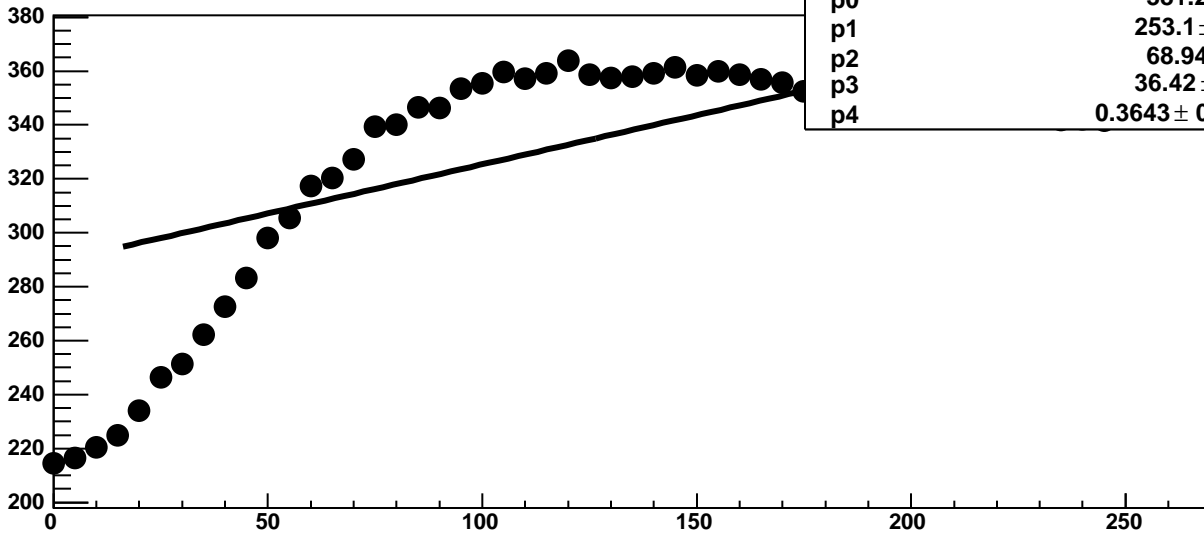
Chip 10, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

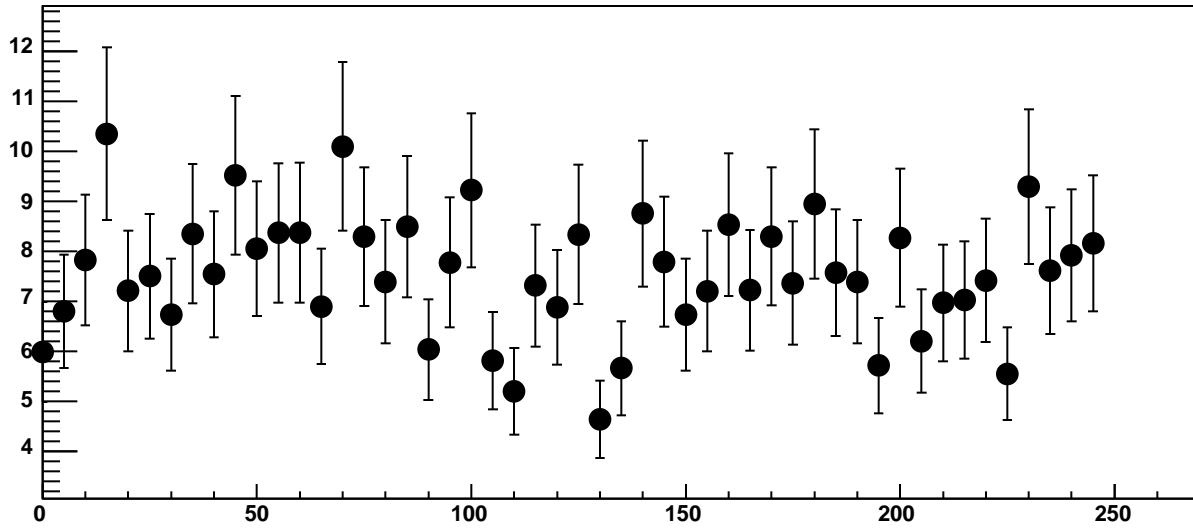


Chip 10, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold

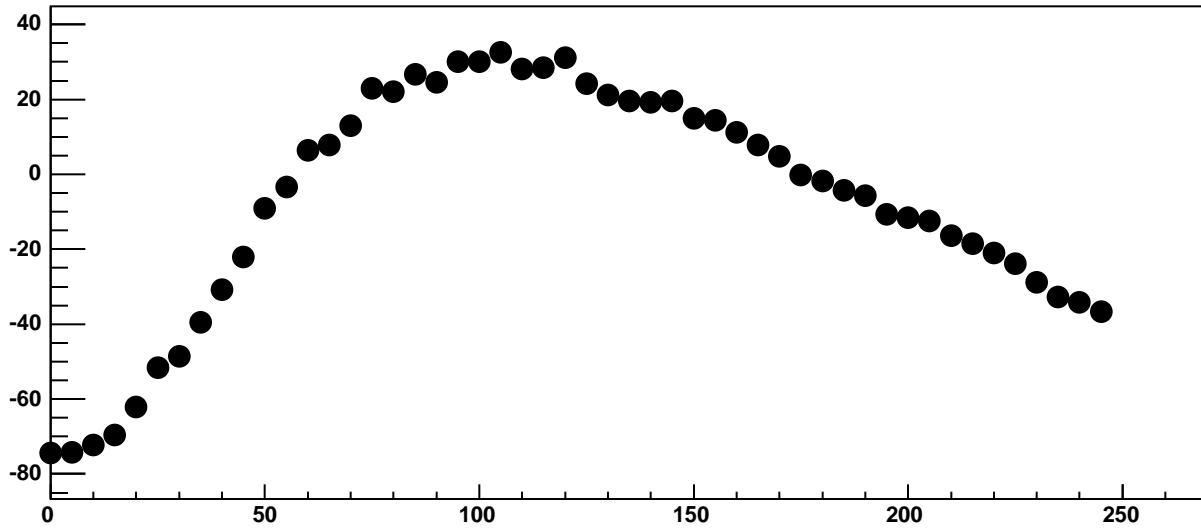


χ^2 / ndf	1.128e+04 / 41
p0	381.2 ± 0.455
p1	253.1 ± 0.5134
p2	68.94 ± 2.007
p3	36.42 ± 0.1979
p4	0.3643 ± 0.003842

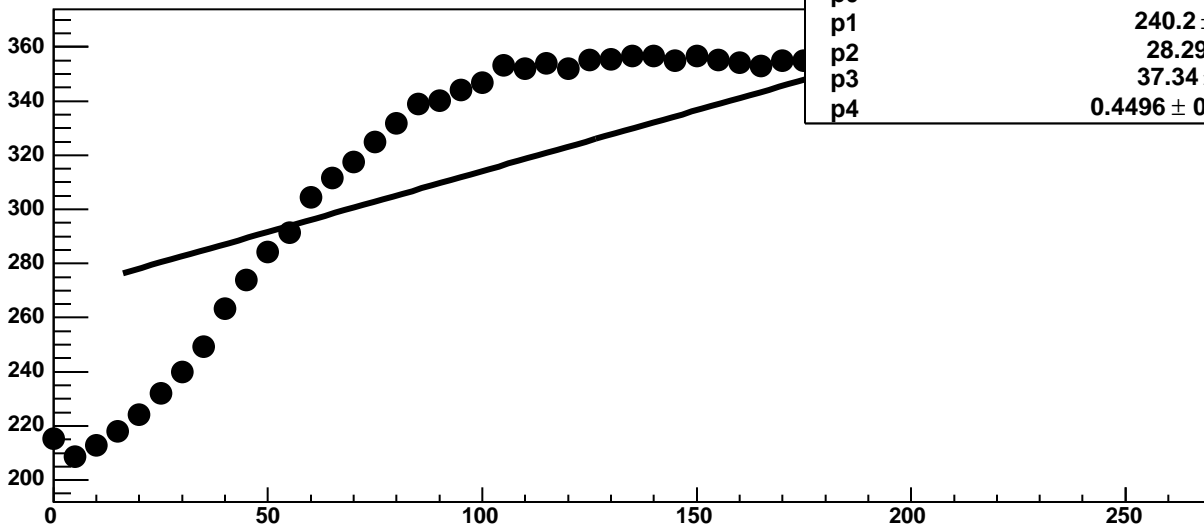
Chip 10, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold

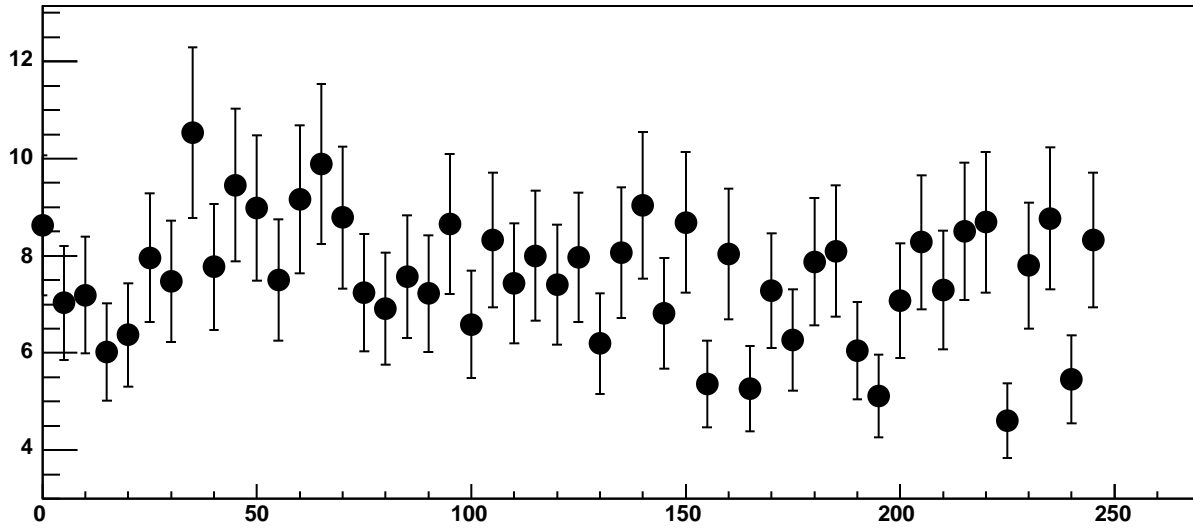


Chip 10, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

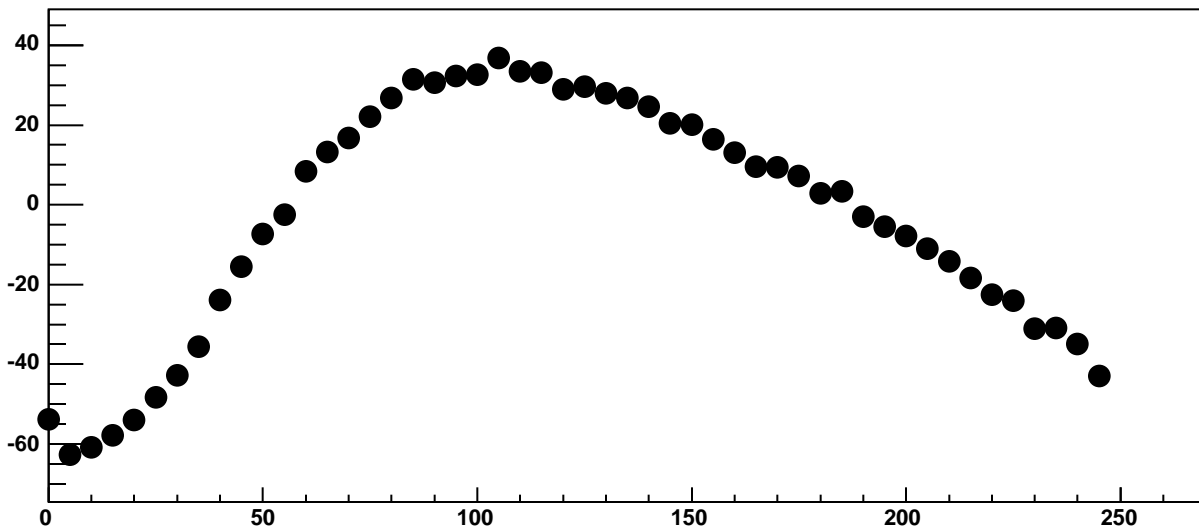


χ^2 / ndf	1.208e+04 / 41
p0	377.2 ± 0.4528
p1	240.2 ± 0.1308
p2	28.29 ± 2.199
p3	37.34 ± 0.1991
p4	0.4496 ± 0.003633

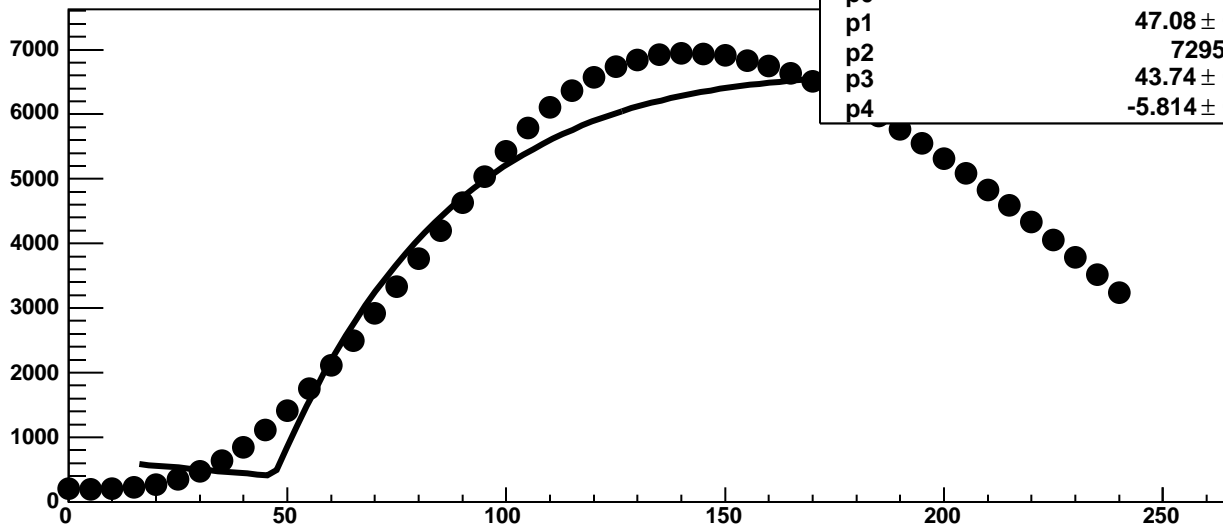
Chip 10, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

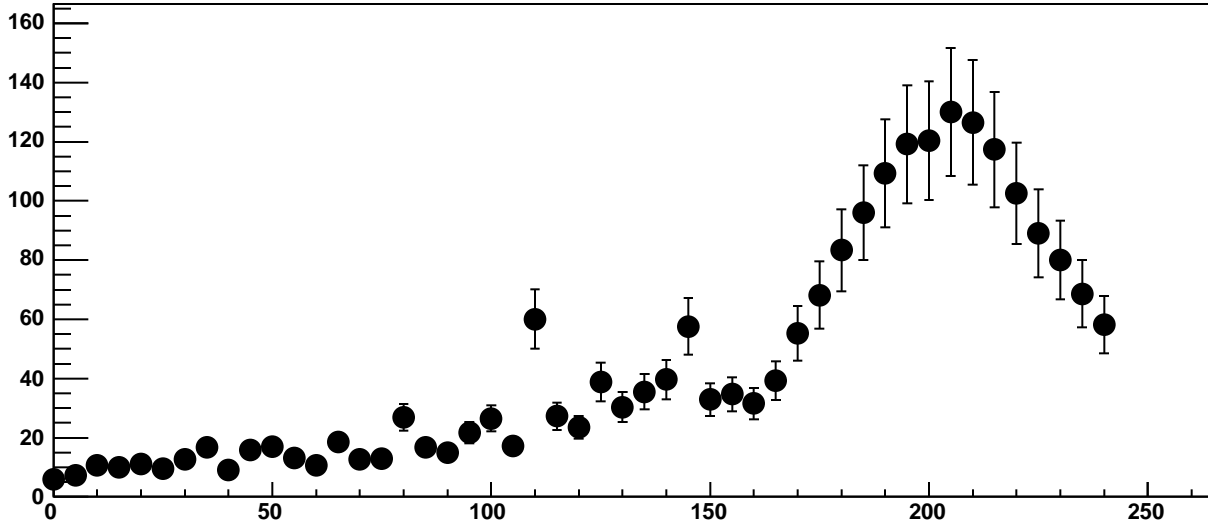


Chip 10, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold

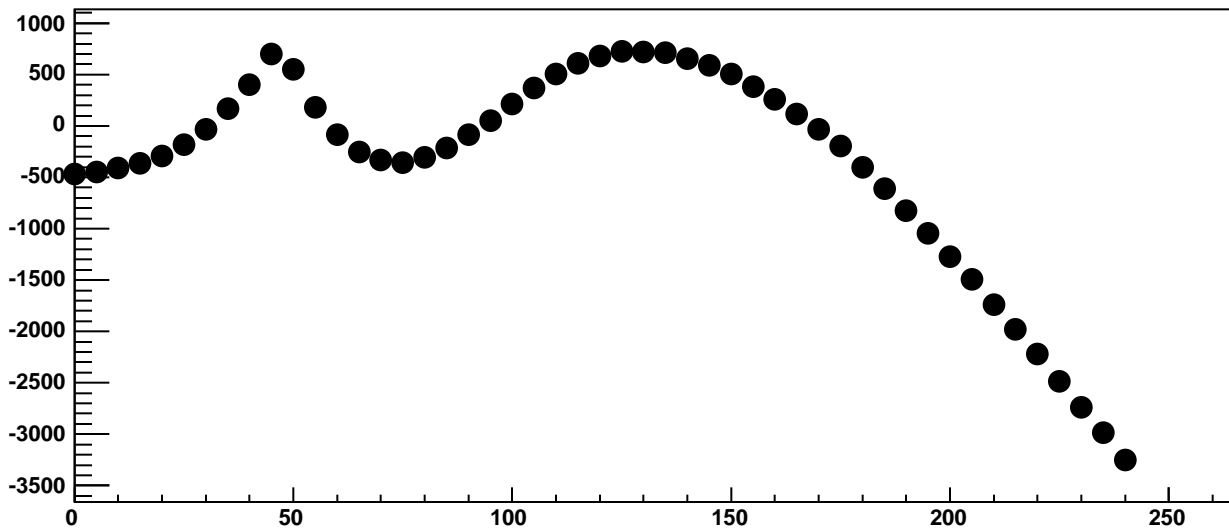


χ^2 / ndf	4.192e+05 / 41
p0	401.9 ± 1.534
p1	47.08 ± 0.01856
p2	7295 ± 11.48
p3	43.74 ± 0.07177
p4	-5.814 ± 0.06708

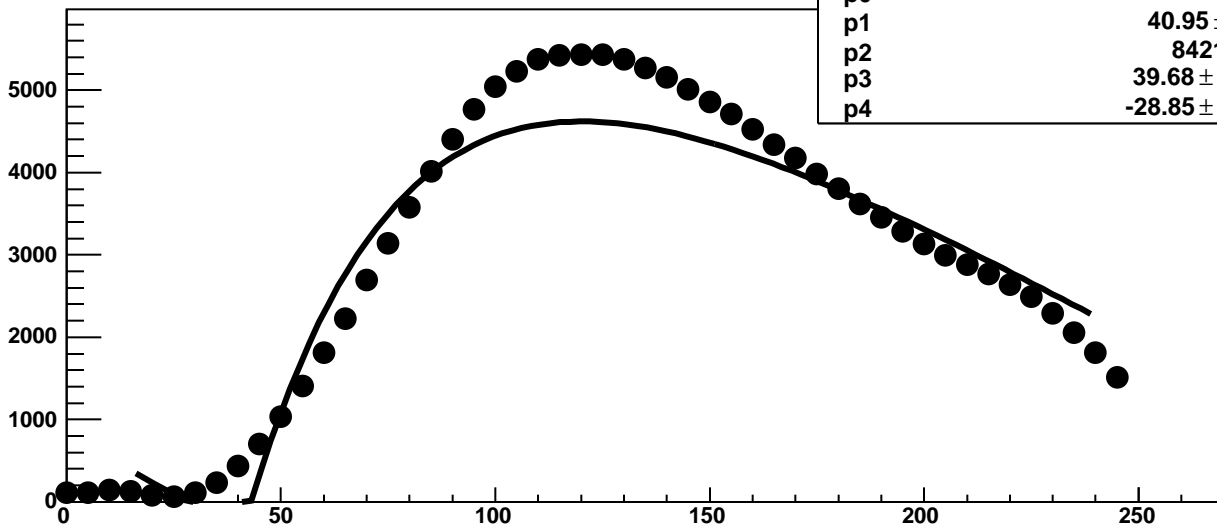
Chip 10, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold

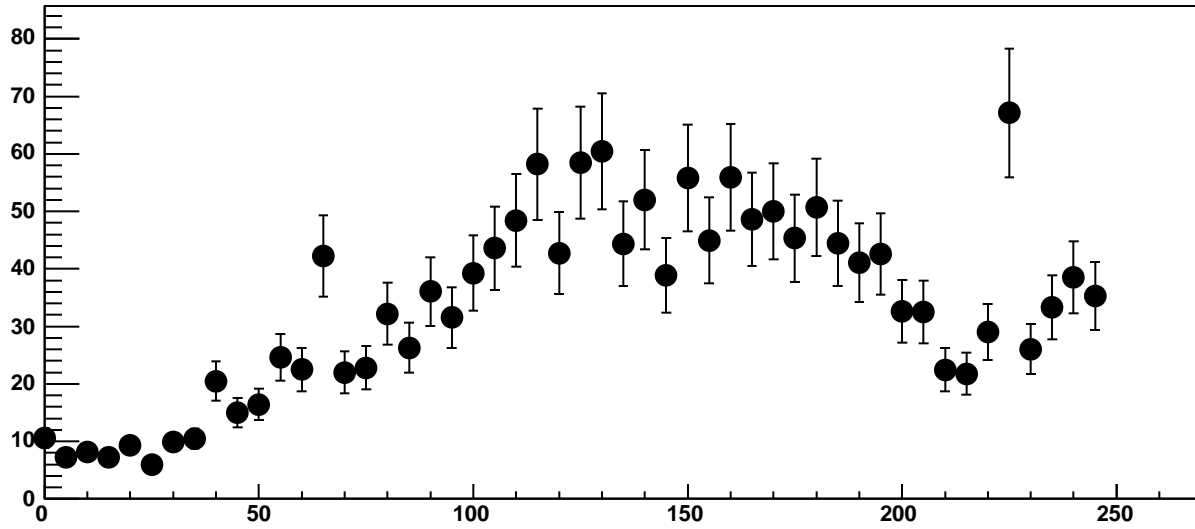


Chip 10, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold

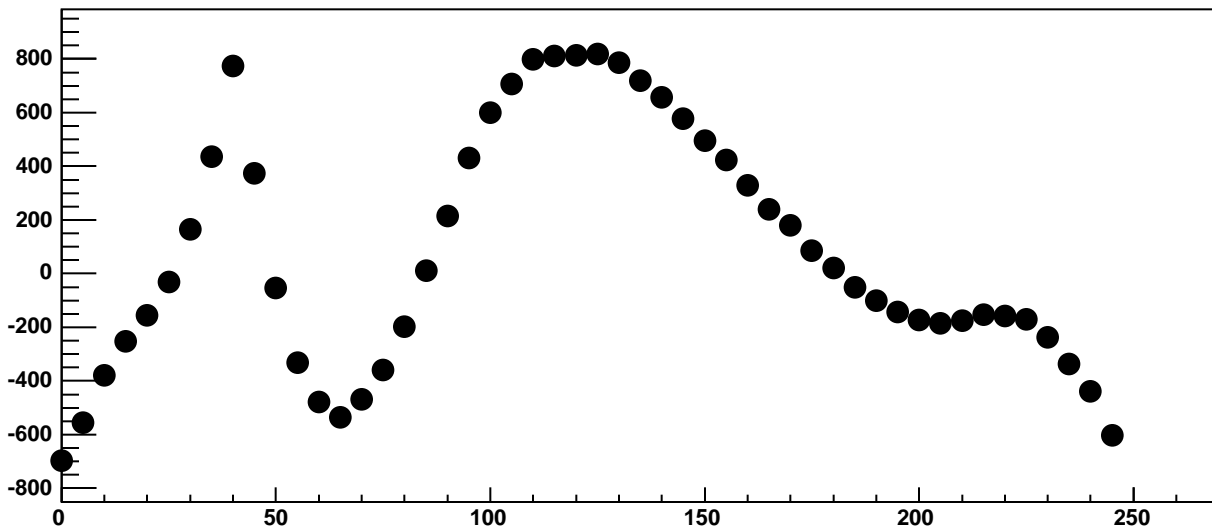


χ^2 / ndf	1.989e+05 / 41
p0	-370.4 ± 1.178
p1	40.95 ± 0.0179
p2	8421 ± 12.01
p3	39.68 ± 0.07052
p4	-28.85 ± 0.06104

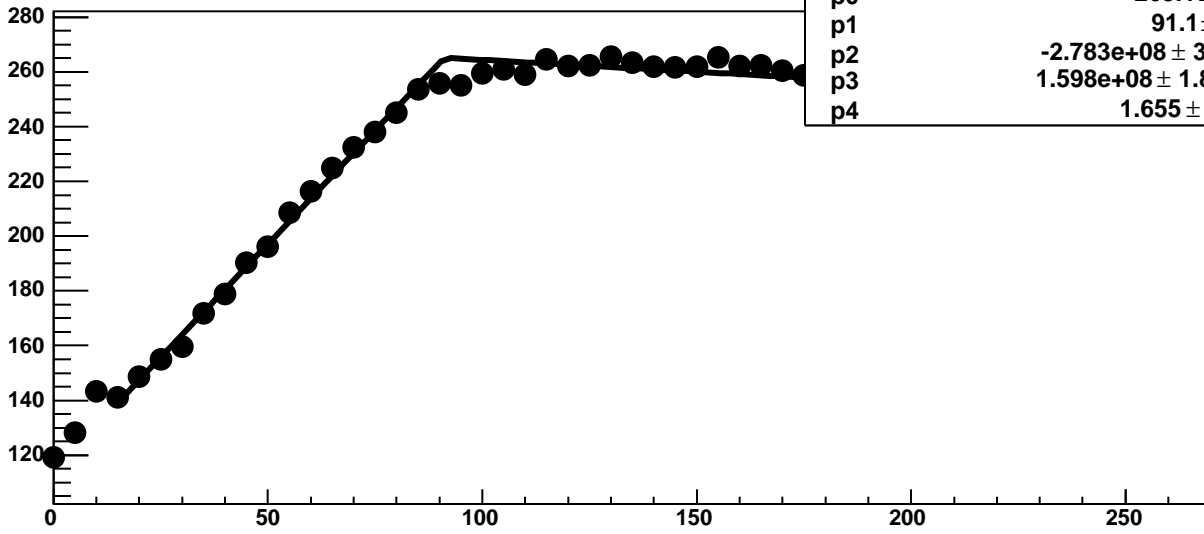
Chip 10, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold

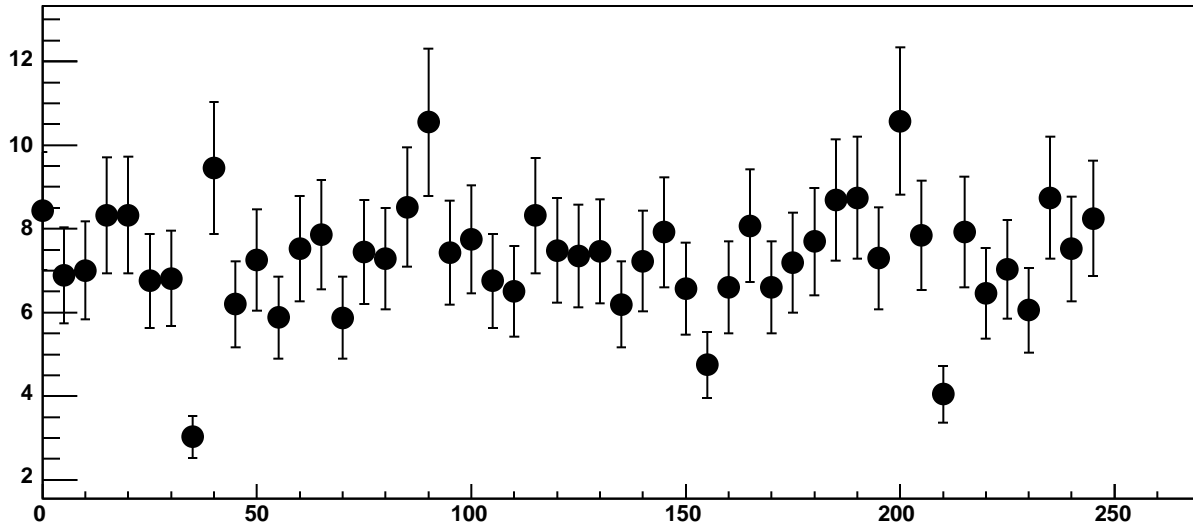


Chip 10, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

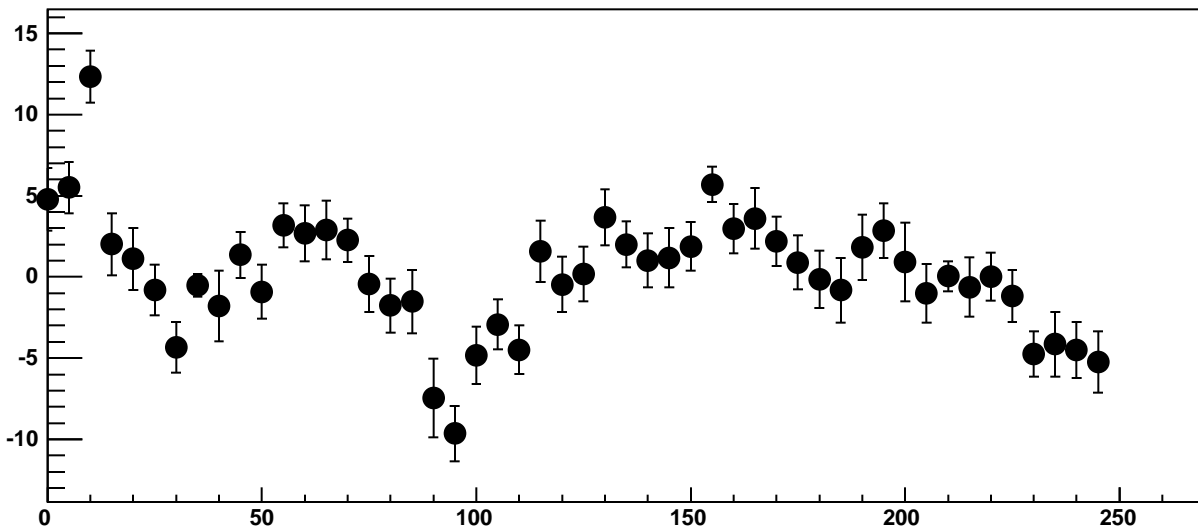


χ^2 / ndf	163.2 / 41
p0	265.1 ± 0.5746
p1	91.1 ± 0.6087
p2	$-2.783\text{e}+08 \pm 3.12\text{e}+06$
p3	$1.598\text{e}+08 \pm 1.885\text{e}+05$
p4	1.655 ± 0.01836

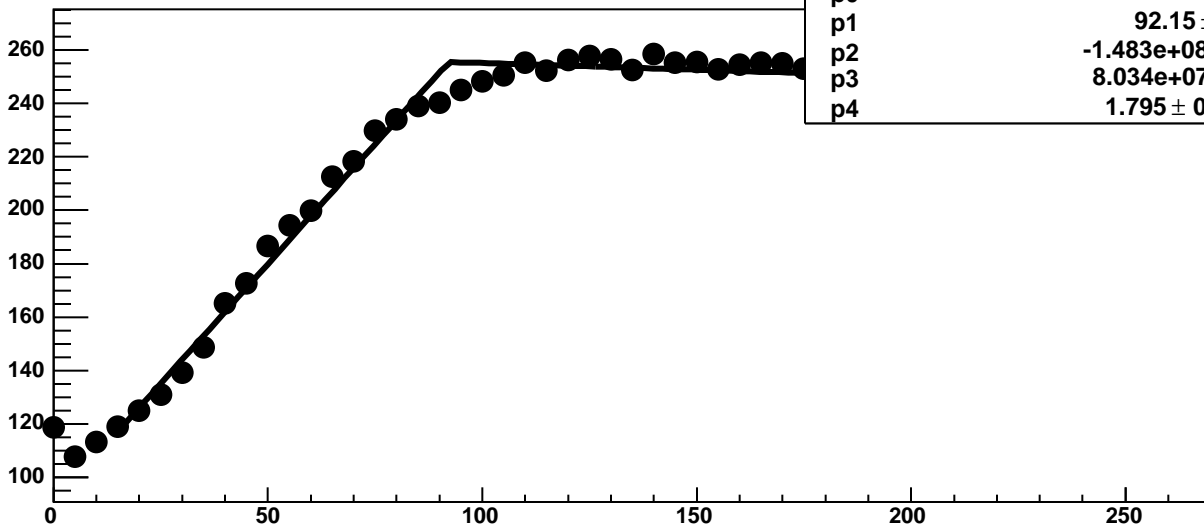
Chip 10, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

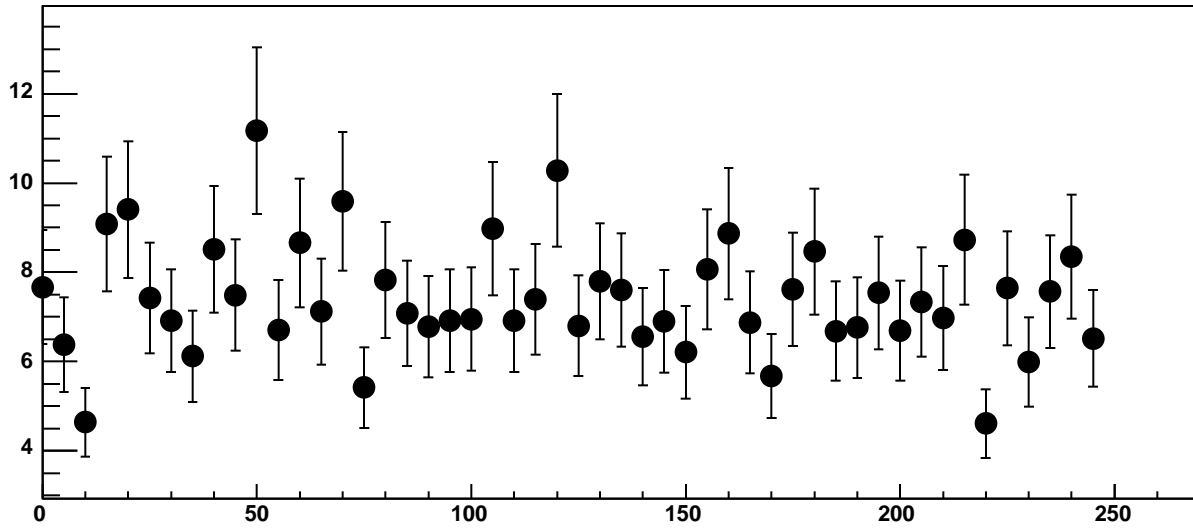


Chip 10, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

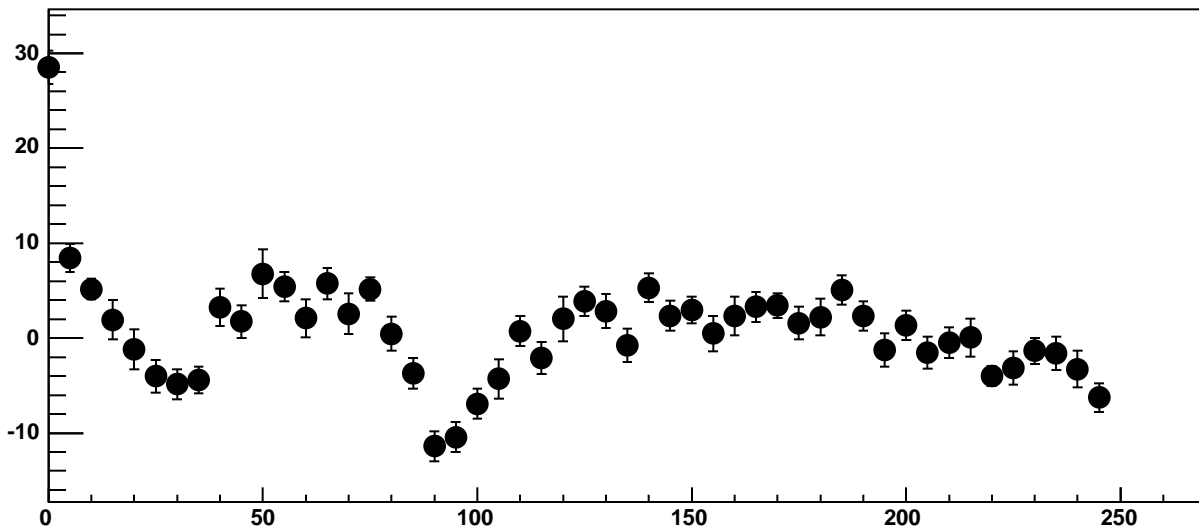


χ^2 / ndf	289.6 / 41
p0	255.6 ± 0.5597
p1	92.15 ± 0.4907
p2	$-1.483\text{e}+08 \pm 1.414$
p3	$8.034\text{e}+07 \pm 1.414$
p4	1.795 ± 0.006434

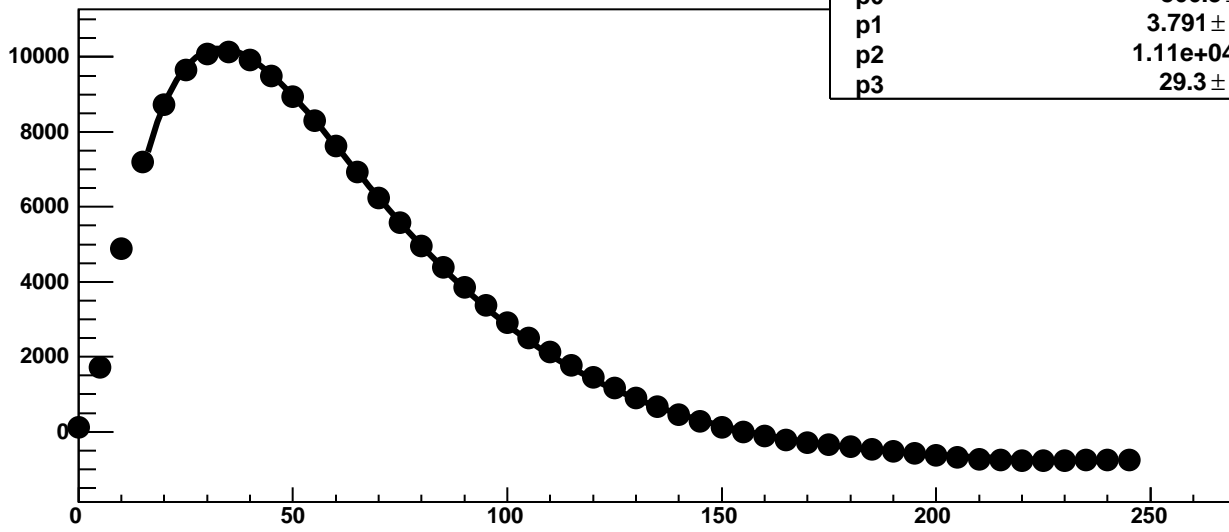
Chip 10, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

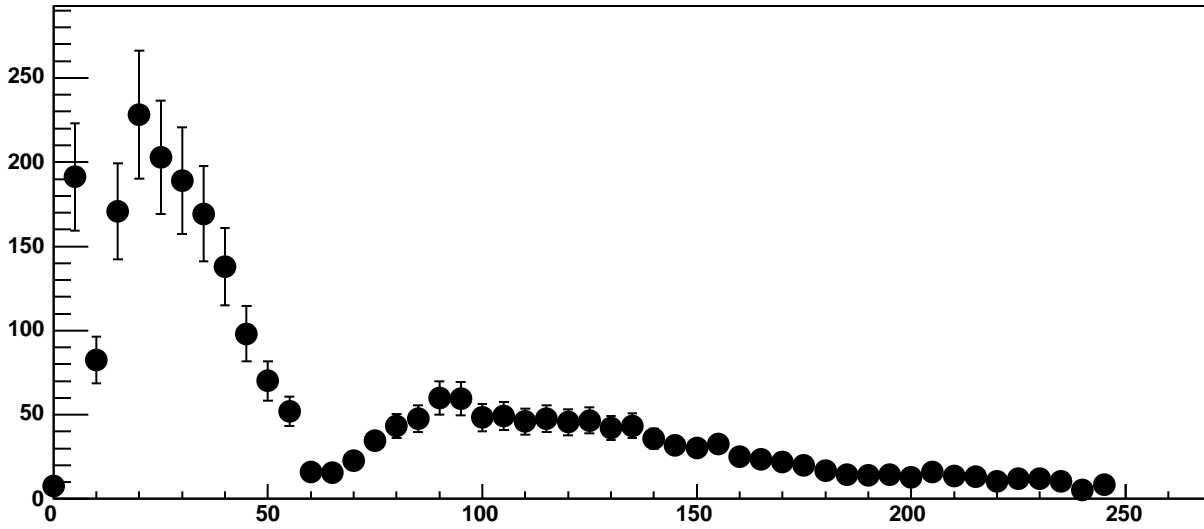


Chip 10, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold

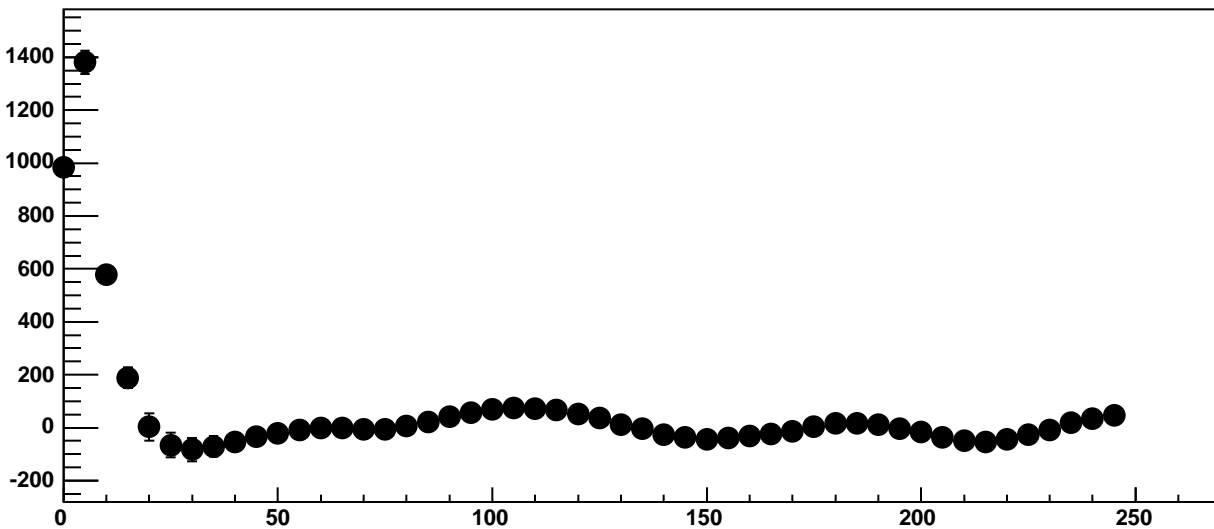


χ^2 / ndf	2407 / 42
p0	-866.3 ± 0.9758
p1	3.791 ± 0.06343
p2	1.11e+04 ± 8.867
p3	29.3 ± 0.02264

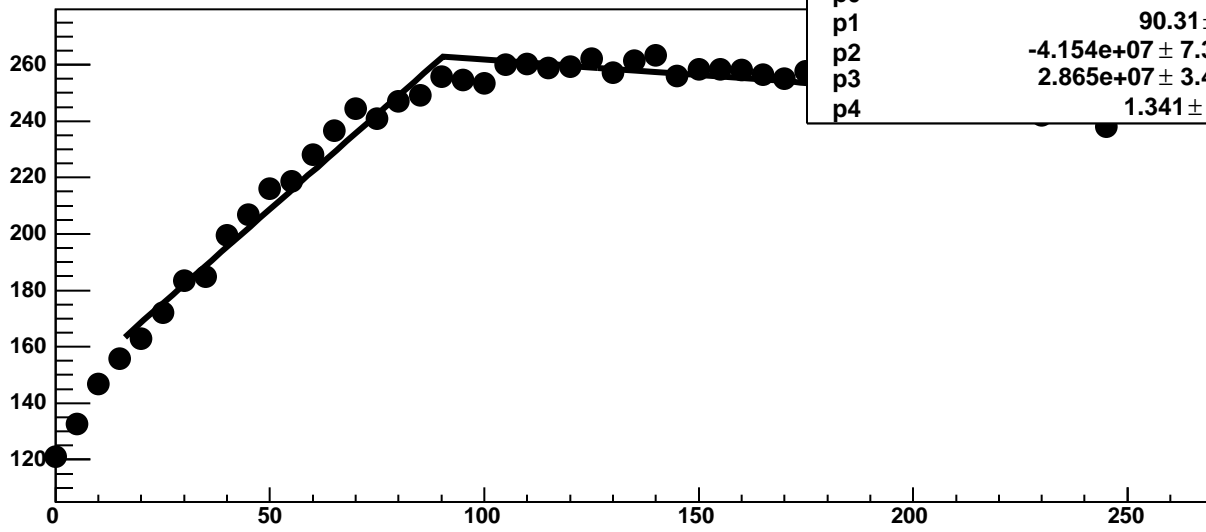
Chip 10, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold

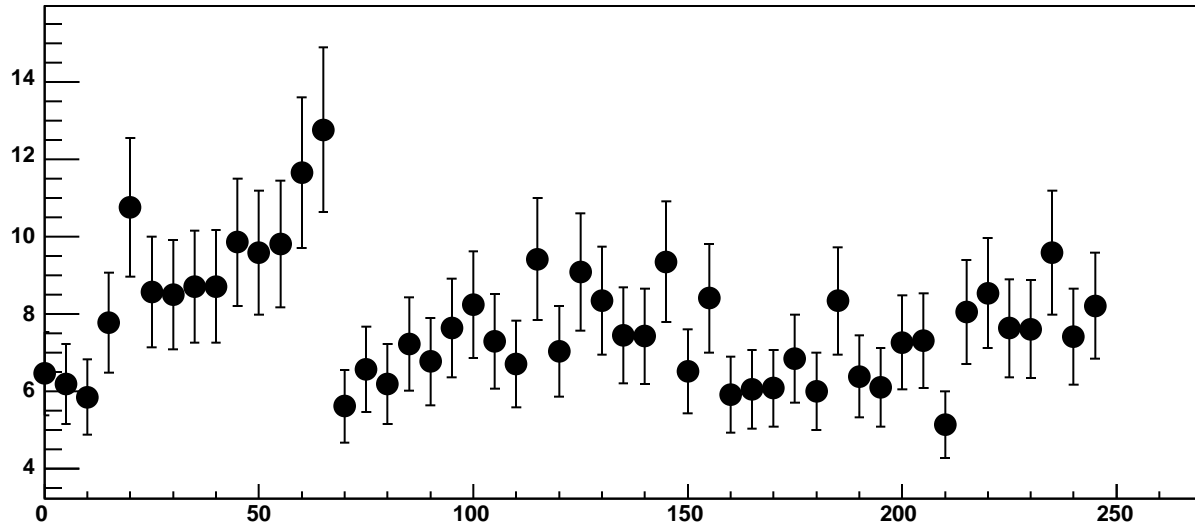


Chip 10, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

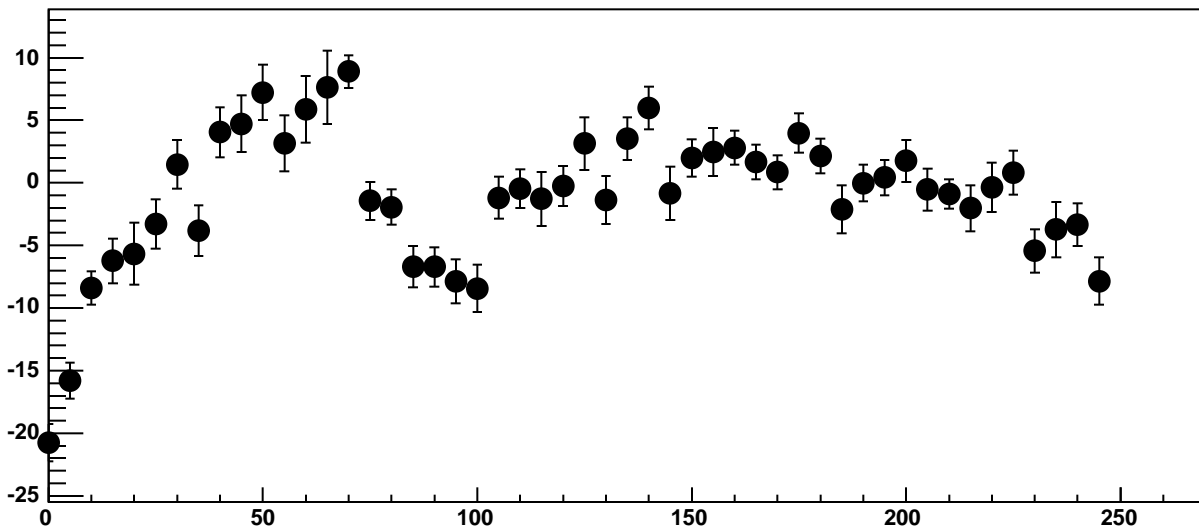


χ^2 / ndf	242.6 / 41
p0	262.9 ± 0.6092
p1	90.31 ± 0.7012
p2	$-4.154\text{e}+07 \pm 7.357\text{e}+05$
p3	$2.865\text{e}+07 \pm 3.445\text{e}+05$
p4	1.341 ± 0.01916

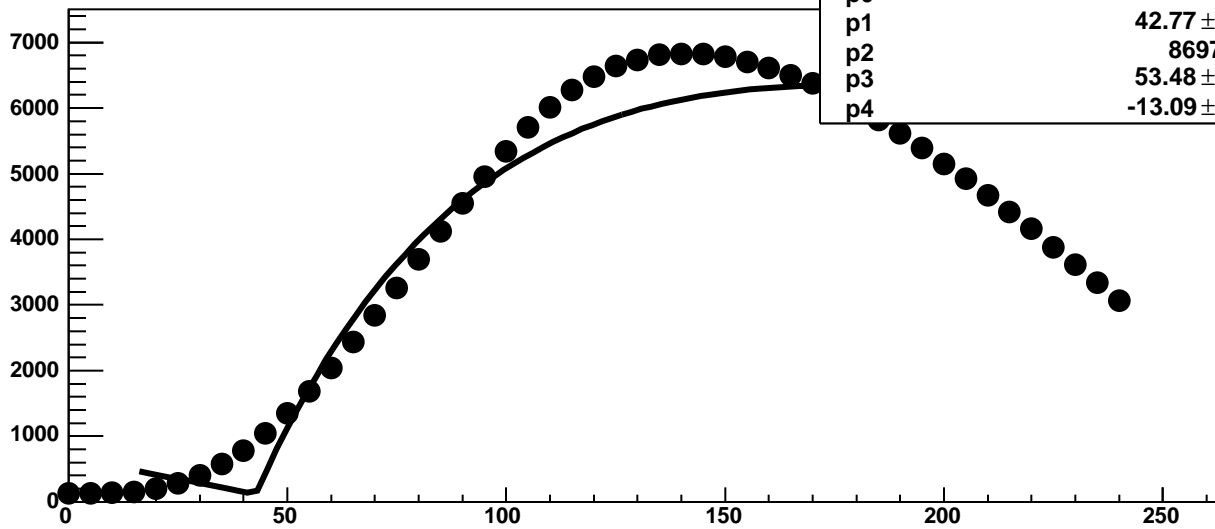
Chip 10, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

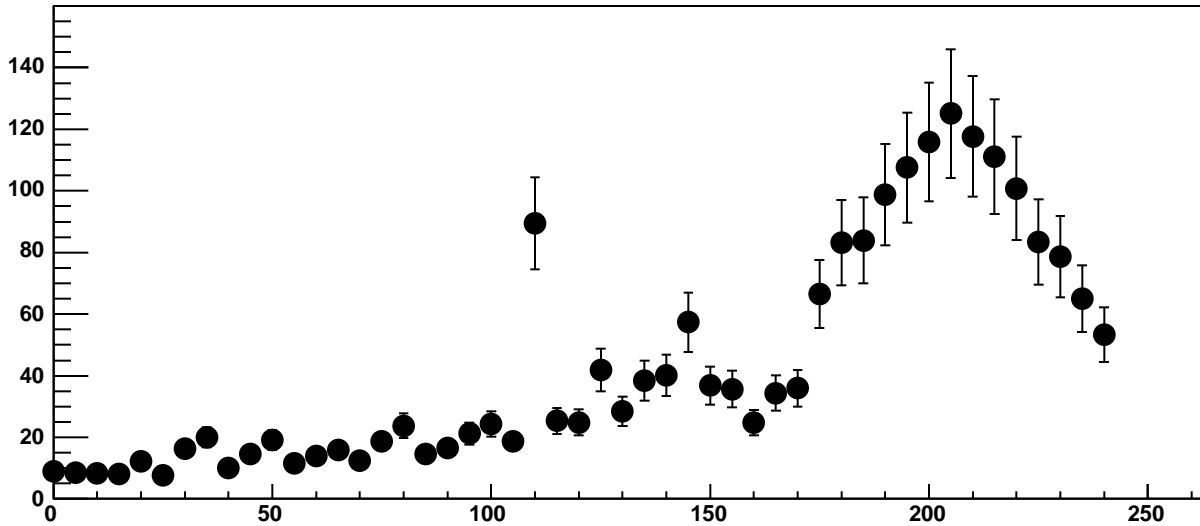


Chip 10, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

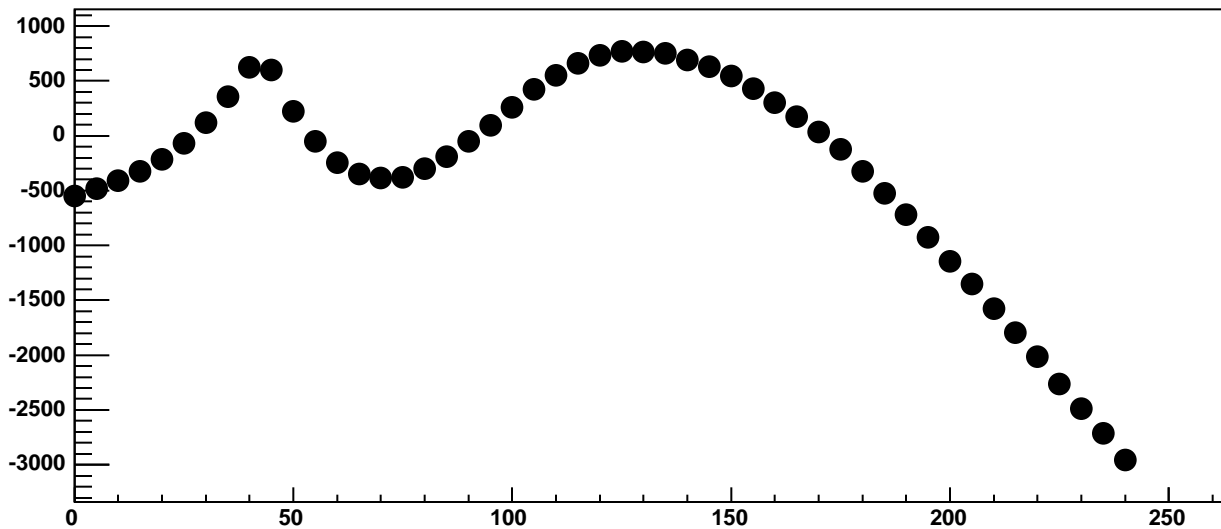


χ^2 / ndf	4.357e+05 / 41
p0	117.2 ± 1.522
p1	42.77 ± 0.01805
p2	8697 ± 14.39
p3	53.48 ± 0.08329
p4	-13.09 ± 0.07482

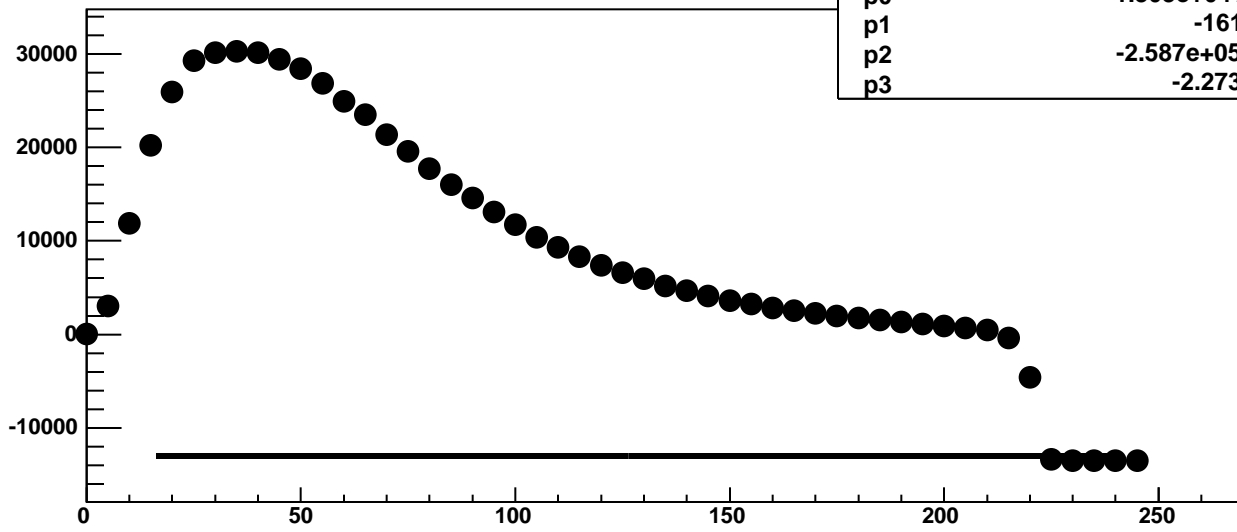
Chip 10, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold

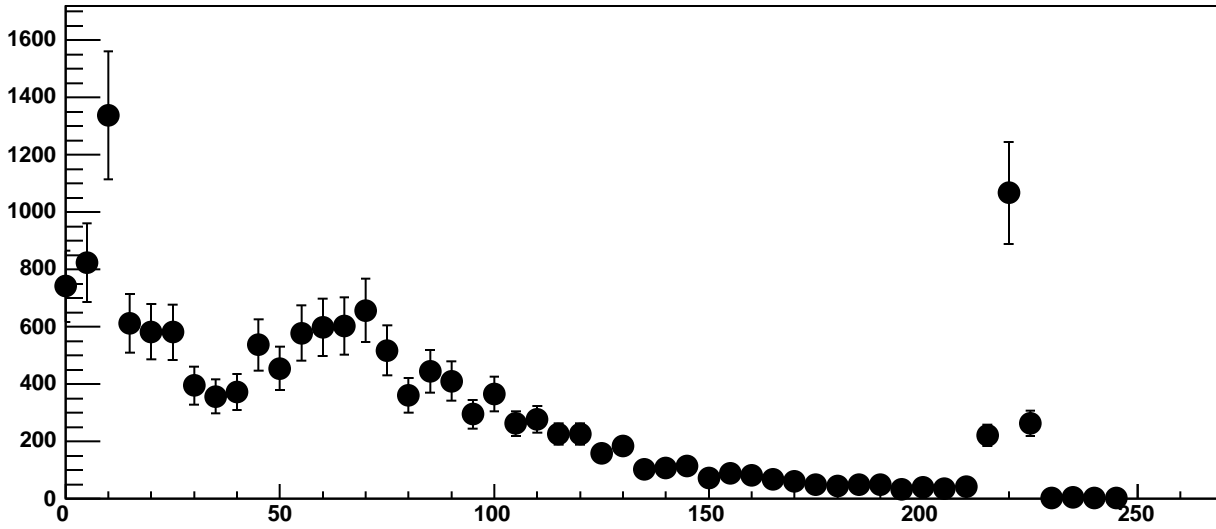


Chip 10, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold

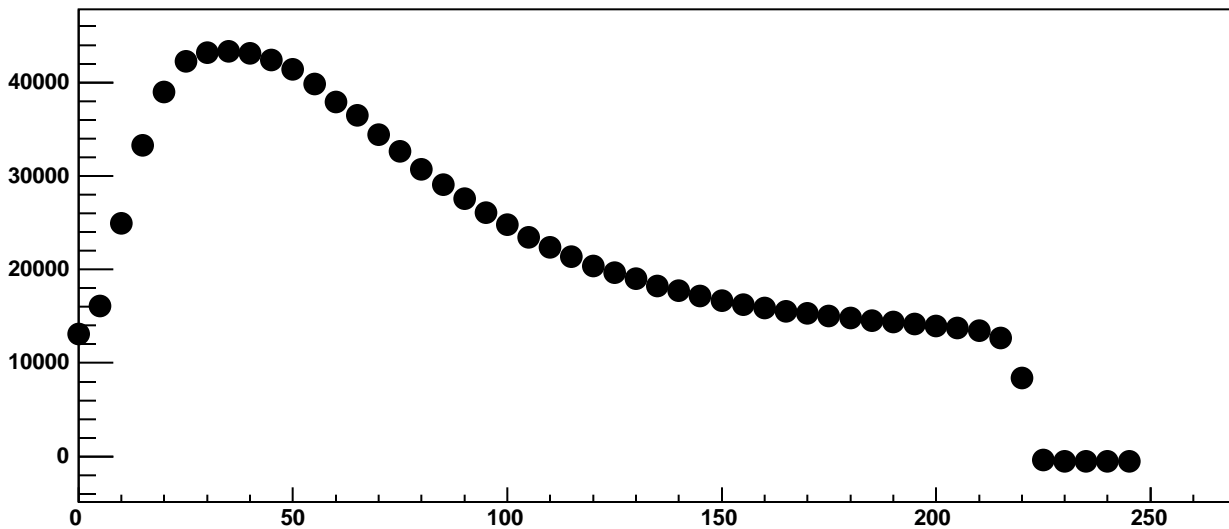


χ^2 / ndf	2.748e+07 / 42
p0	-1.303e+04 \pm 0.5251
p1	-1619 \pm 2.7
p2	-2.587e+05 \pm 1.414
p3	-2.273 \pm 1.414

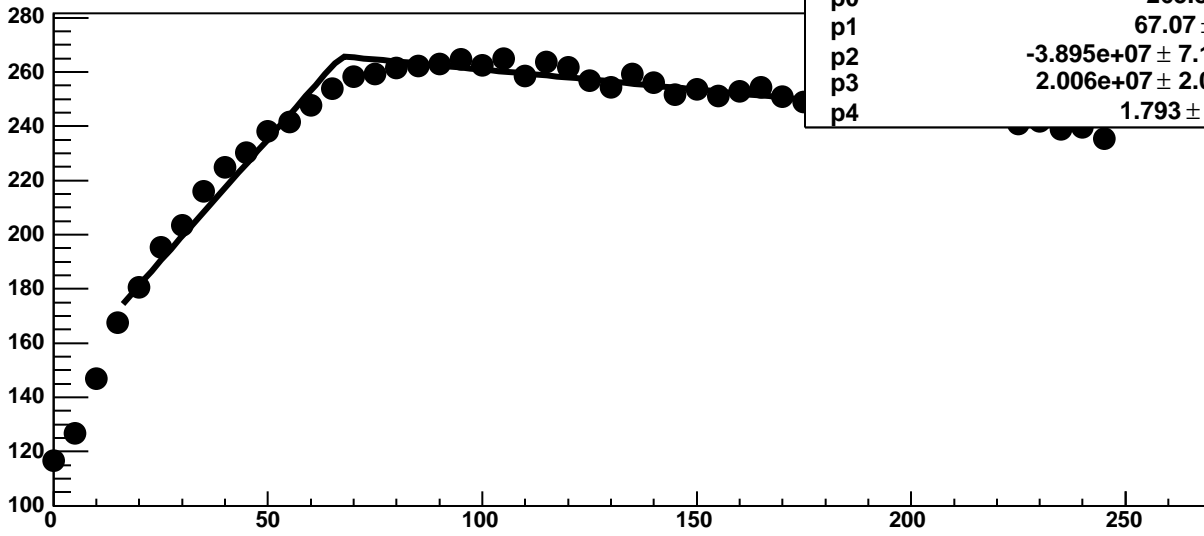
Chip 10, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold

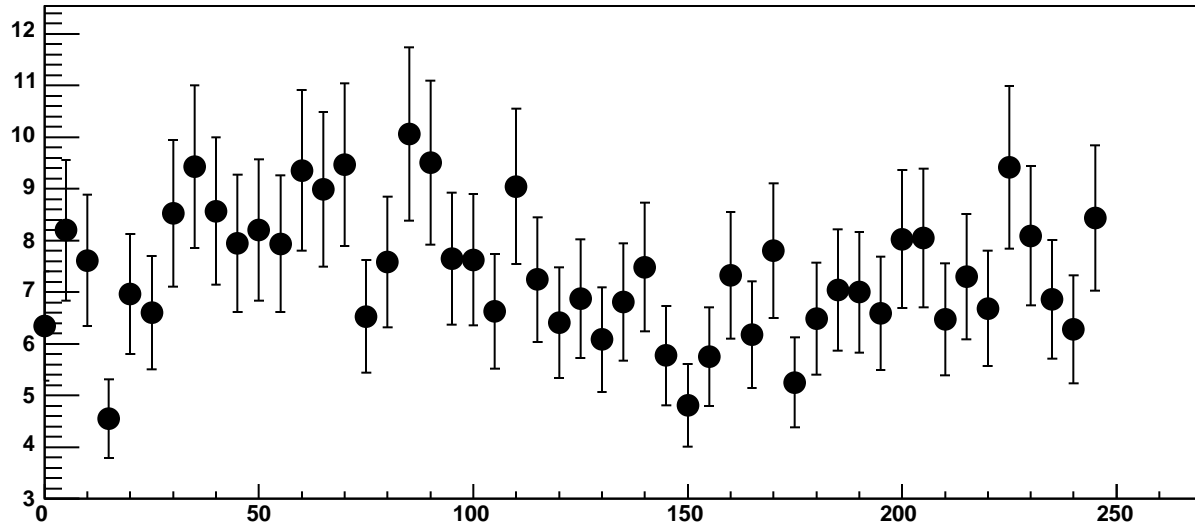


Chip 10, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

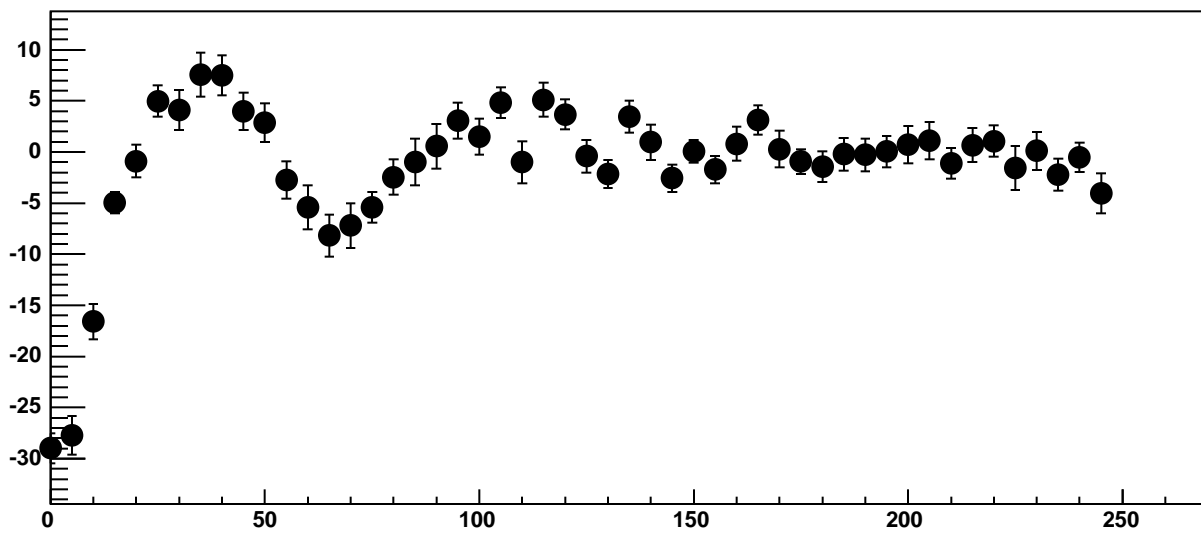


χ^2 / ndf	176.9 / 41
p0	265.8 ± 0.548
p1	67.07 ± 0.6538
p2	$-3.895\text{e}+07 \pm 7.186\text{e}+05$
p3	$2.006\text{e}+07 \pm 2.029\text{e}+05$
p4	1.793 ± 0.03056

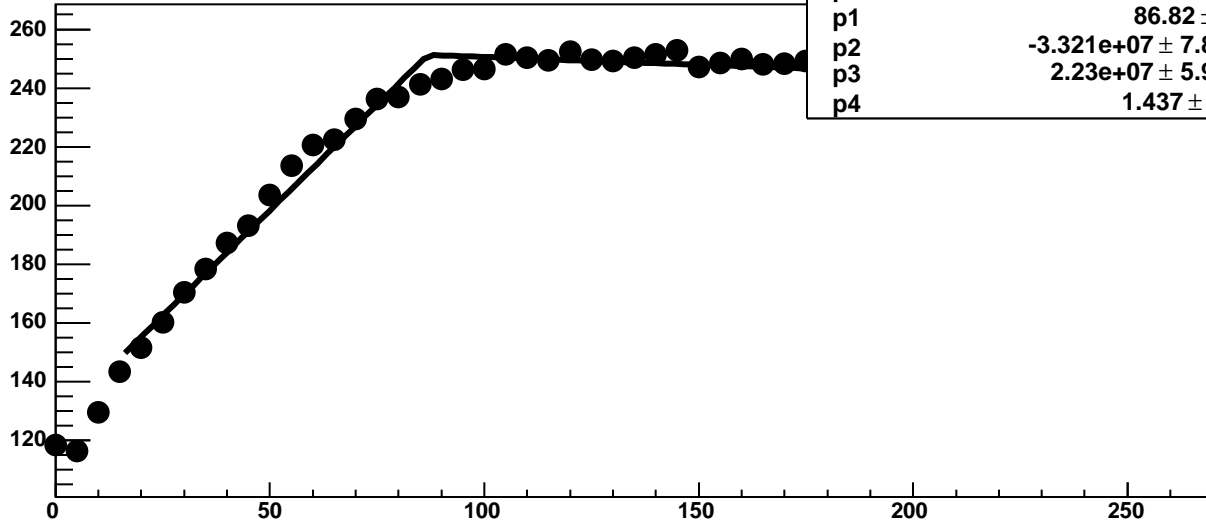
Chip 10, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold

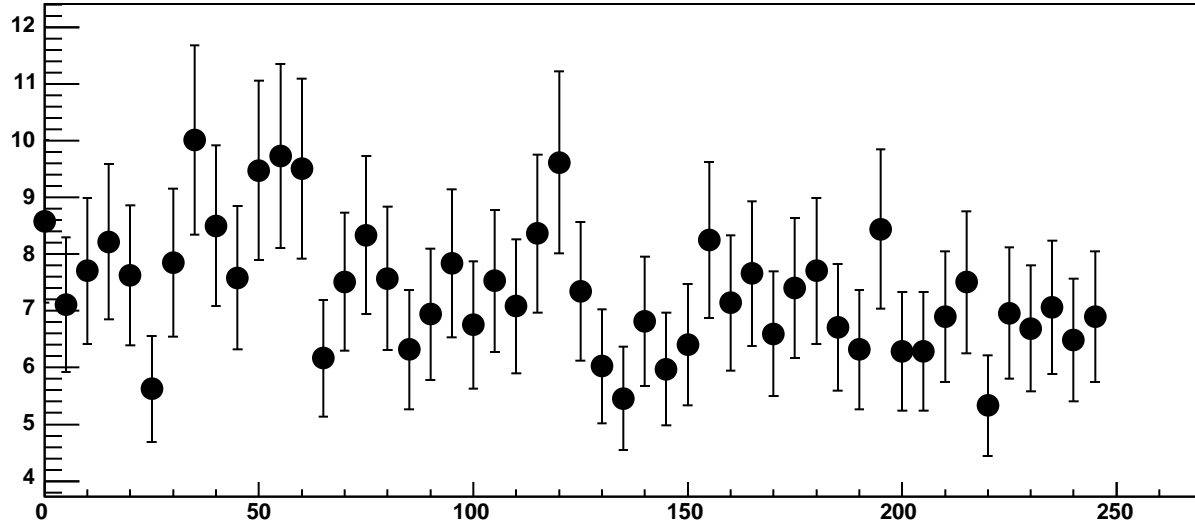


Chip 10, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

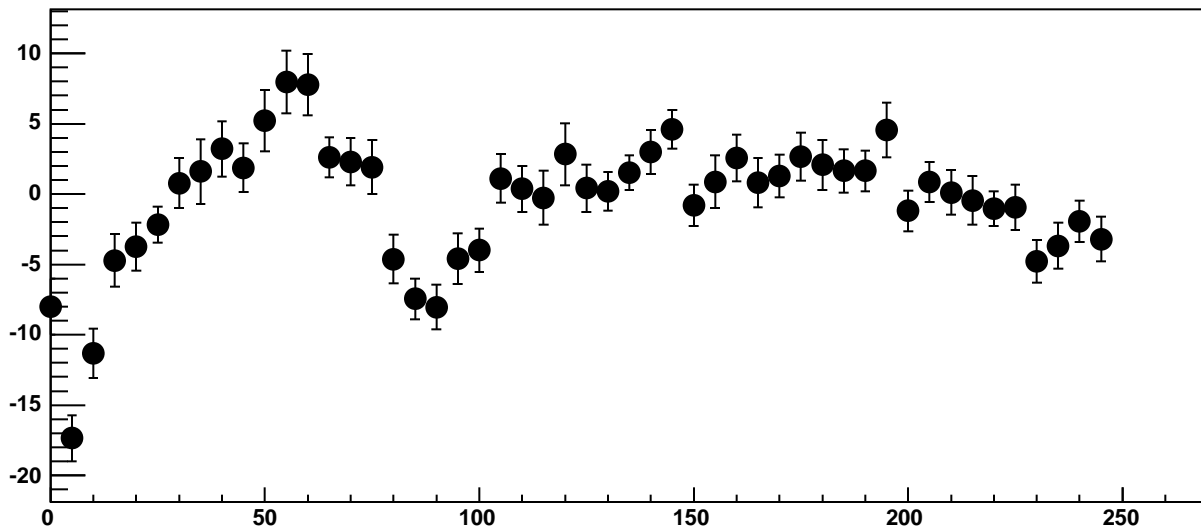


χ^2 / ndf	181.2 / 41
p0	251.3 ± 0.5514
p1	86.82 ± 0.6335
p2	$-3.321\text{e}+07 \pm 7.829\text{e}+05$
p3	$2.23\text{e}+07 \pm 5.953\text{e}+05$
p4	1.437 ± 0.01696

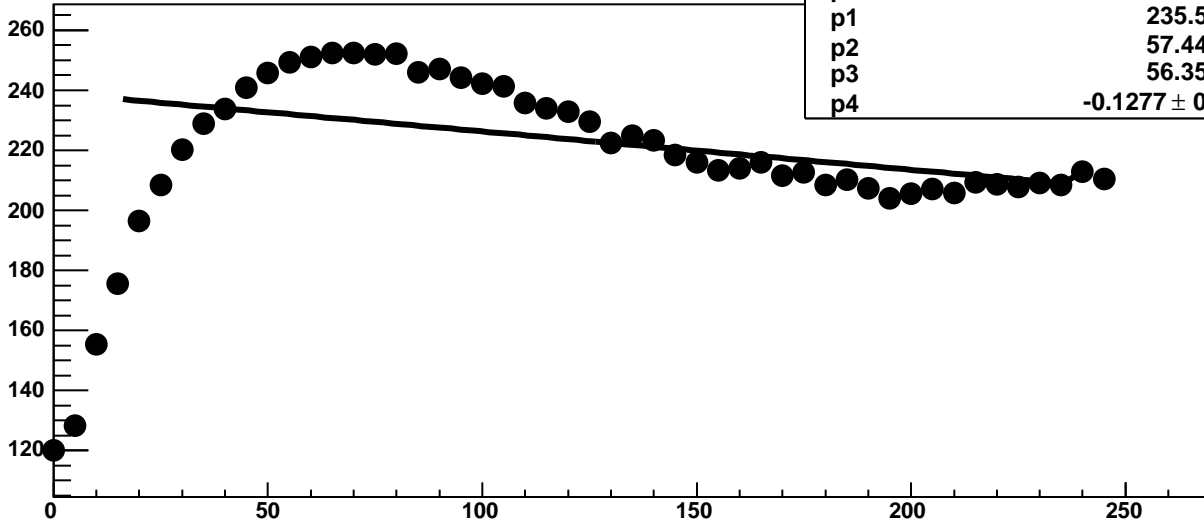
Chip 10, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

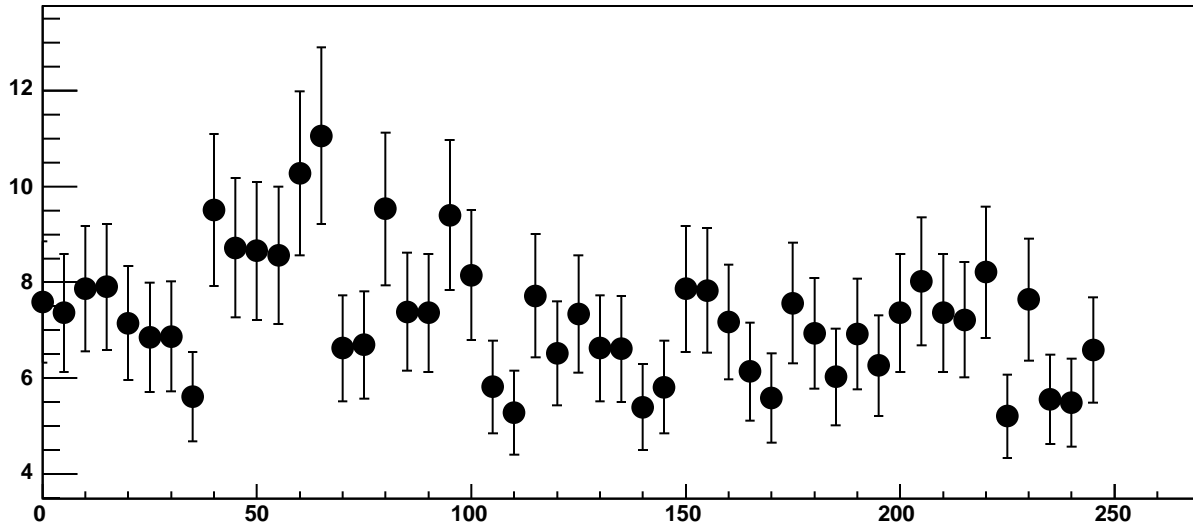


Chip 10, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold

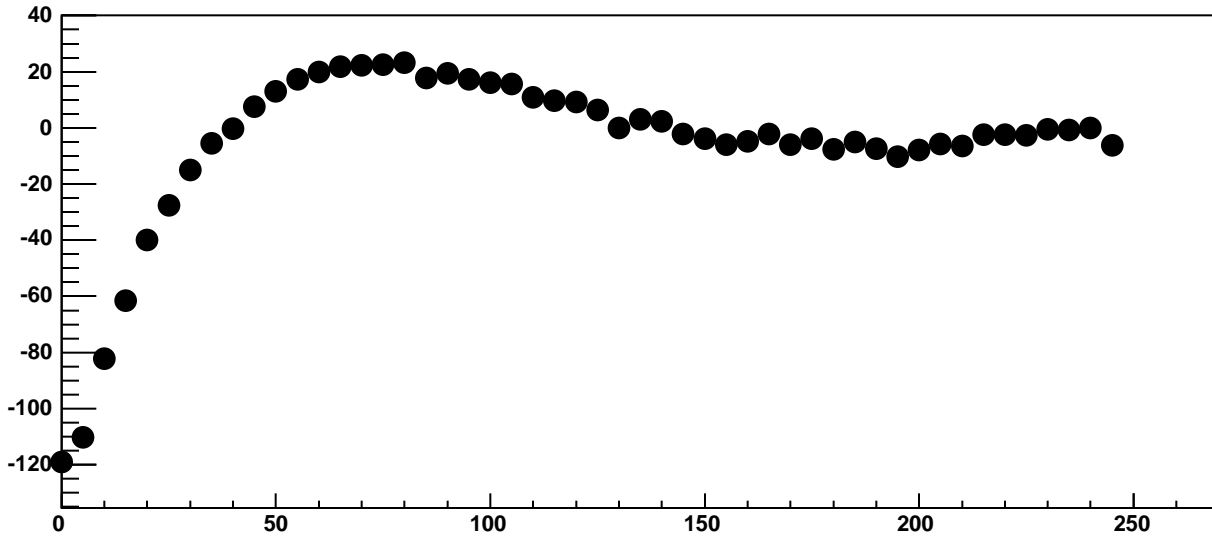


χ^2 / ndf	3890 / 41
p0	209 ± 1.655
p1	235.5 ± 12.88
p2	57.44 ± 227.5
p3	56.35 ± 109.3
p4	-0.1277 ± 0.003654

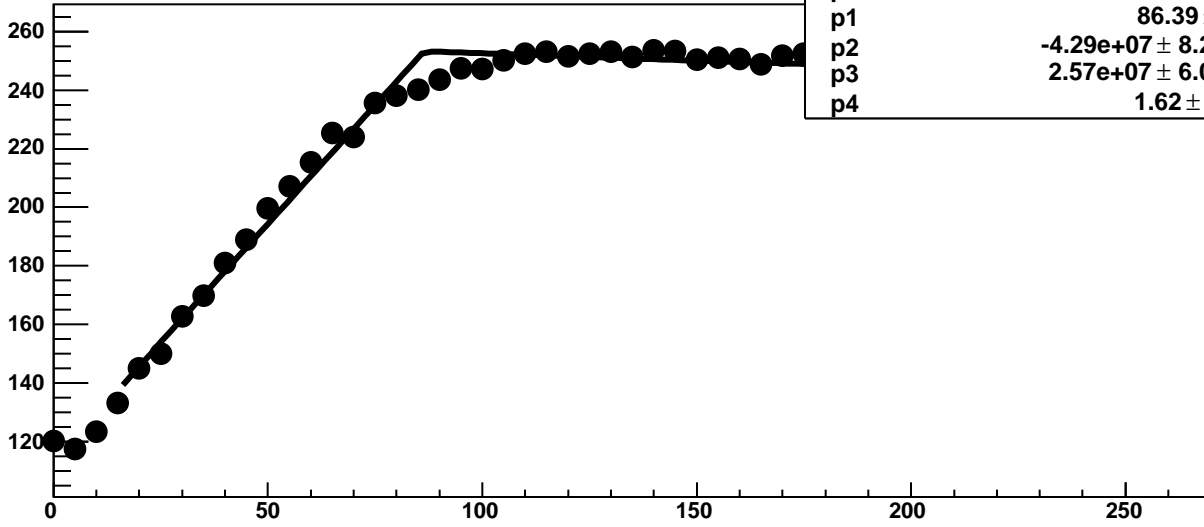
Chip 10, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold

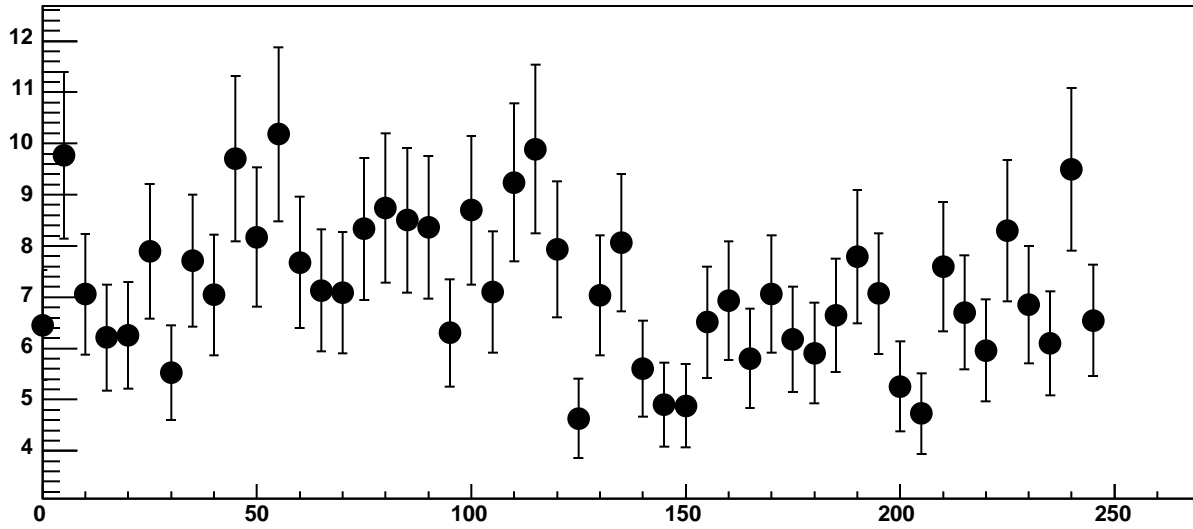


Chip 10, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold

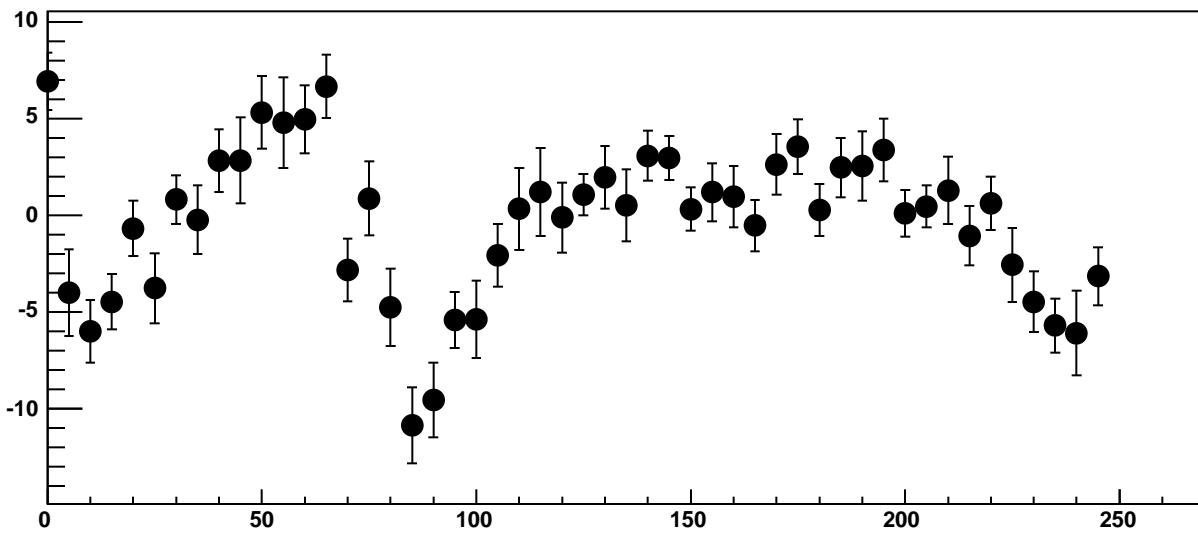


χ^2 / ndf	214.1 / 41
p0	253.3 ± 0.5463
p1	86.39 ± 0.5791
p2	$-4.29\text{e}+07 \pm 8.202\text{e}+05$
p3	$2.57\text{e}+07 \pm 6.027\text{e}+05$
p4	1.62 ± 0.01685

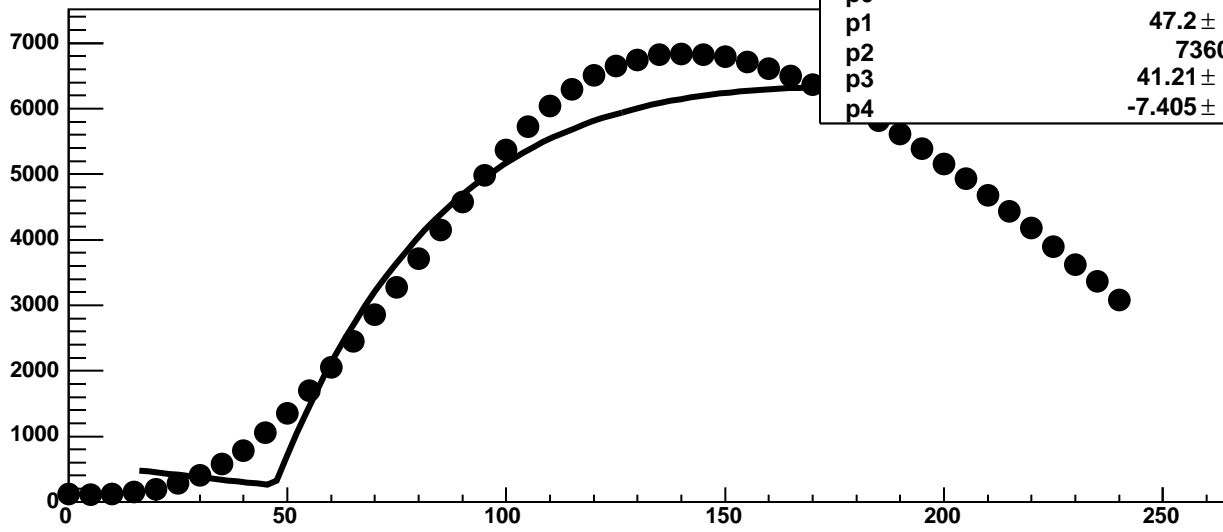
Chip 10, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

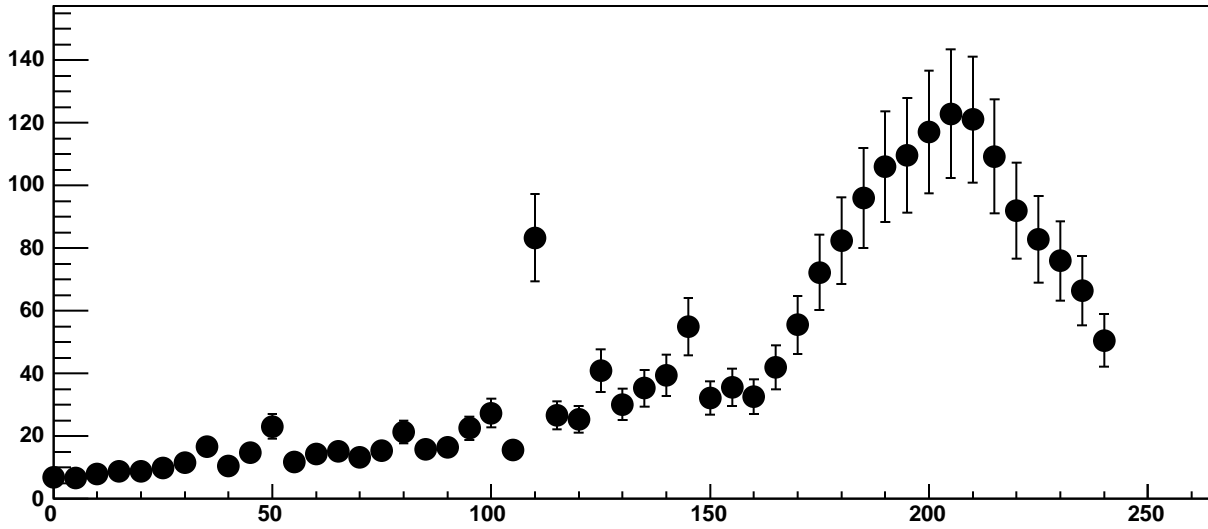


Chip 10, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

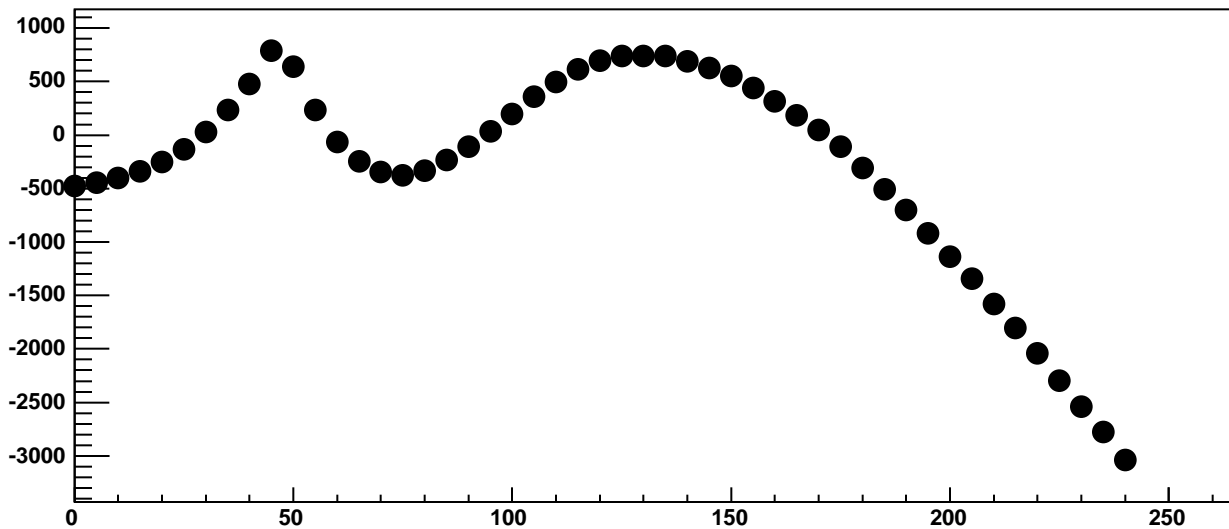


χ^2 / ndf	4.488e+05 / 41
p0	248.3 \pm 1.527
p1	47.2 \pm 0.01887
p2	7360 \pm 10.8
p3	41.21 \pm 0.06614
p4	-7.405 \pm 0.06323

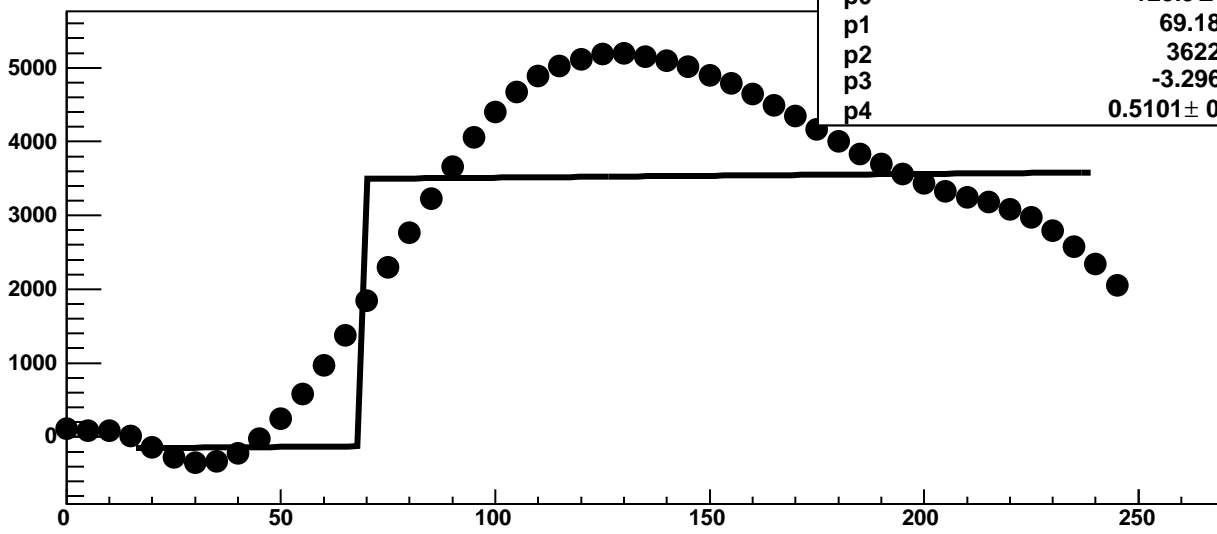
Chip 10, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold



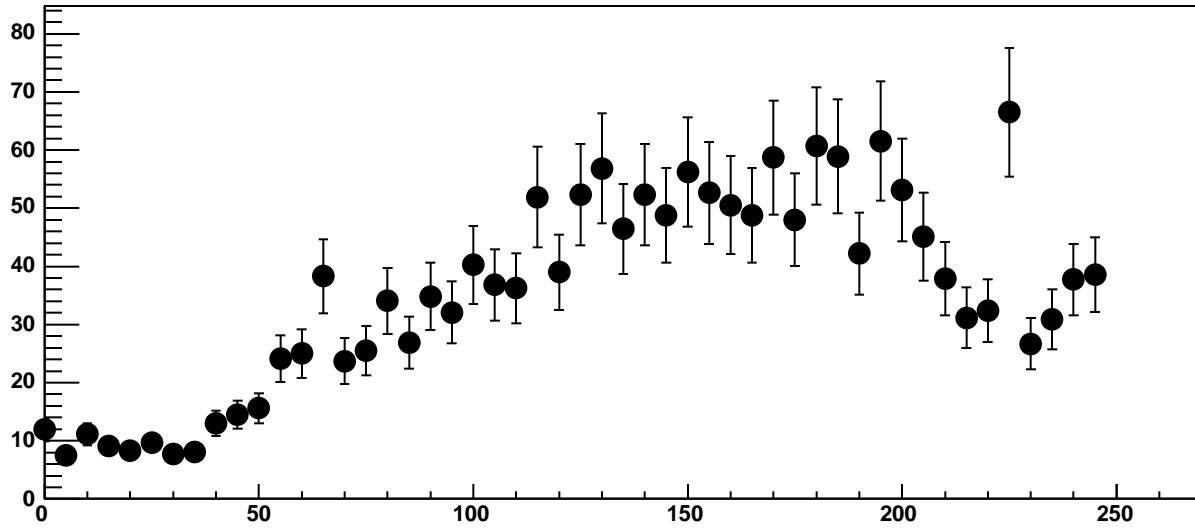
Chip 10, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold



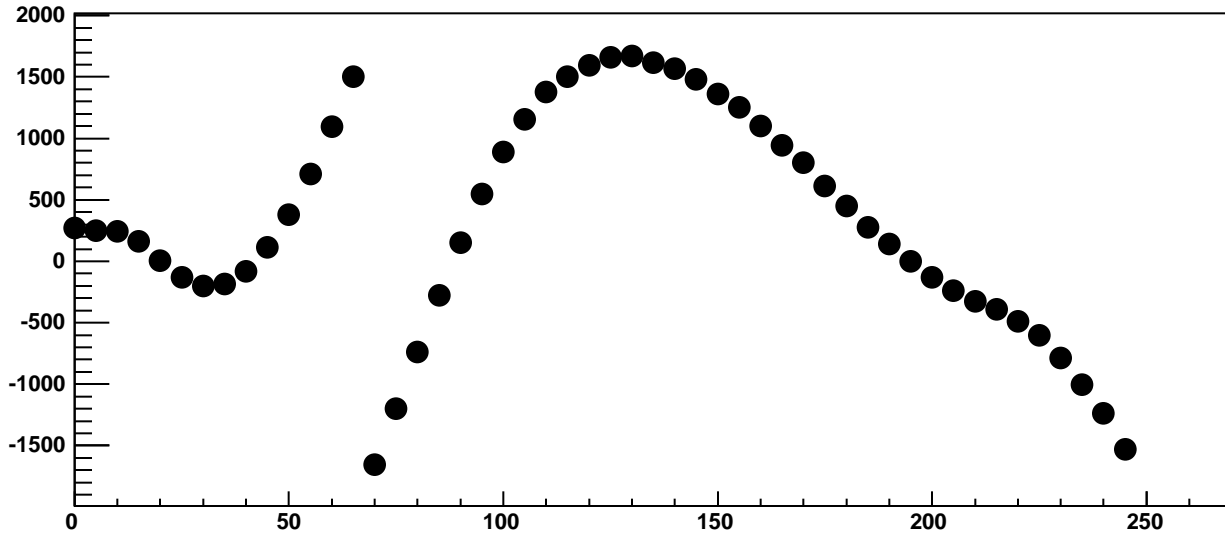
Chip 10, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold



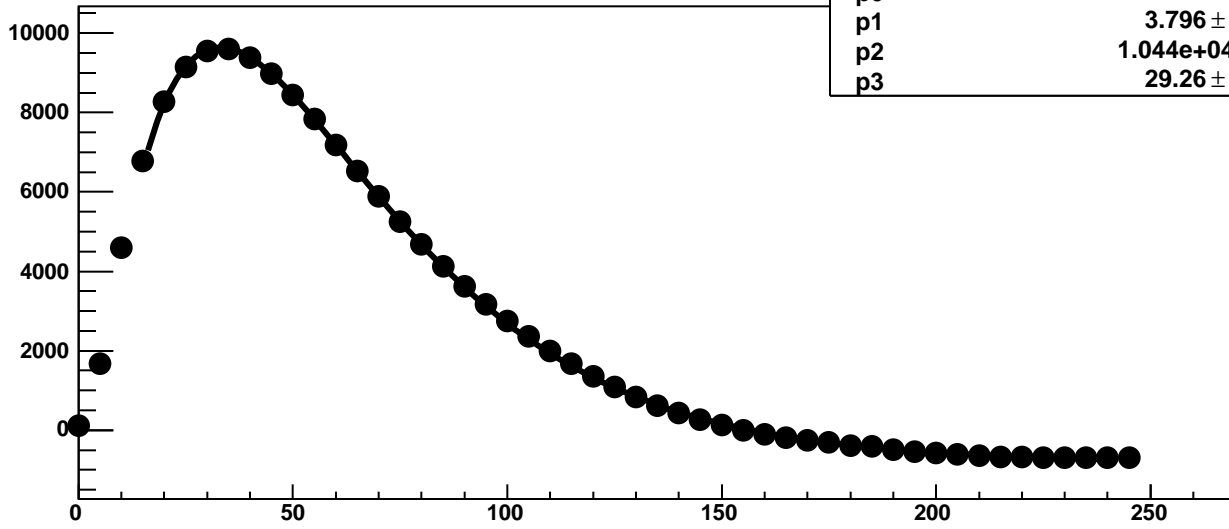
Chip 10, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

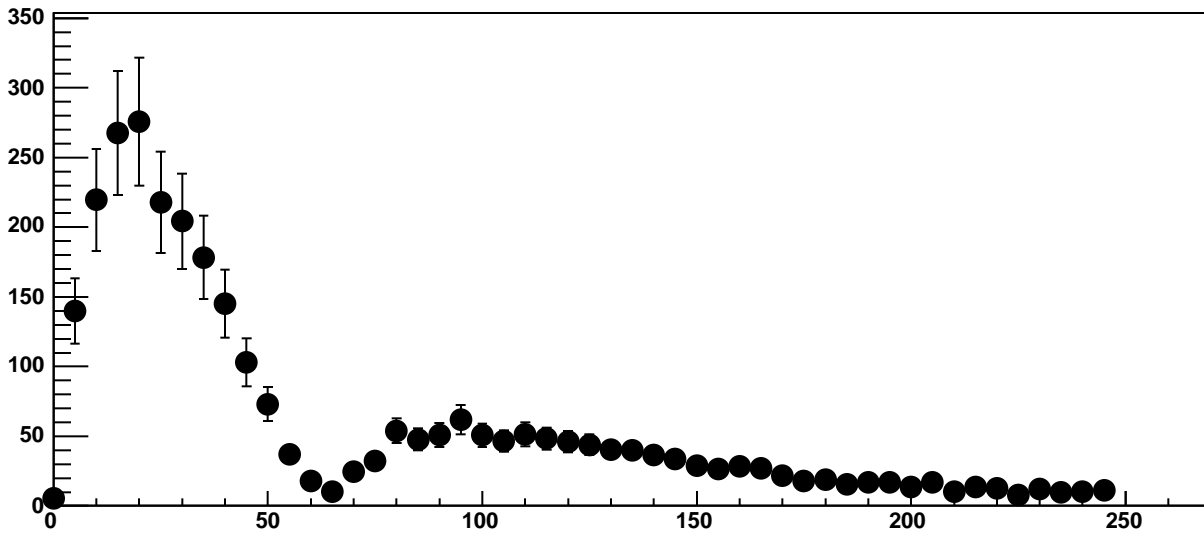


Chip 10, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold

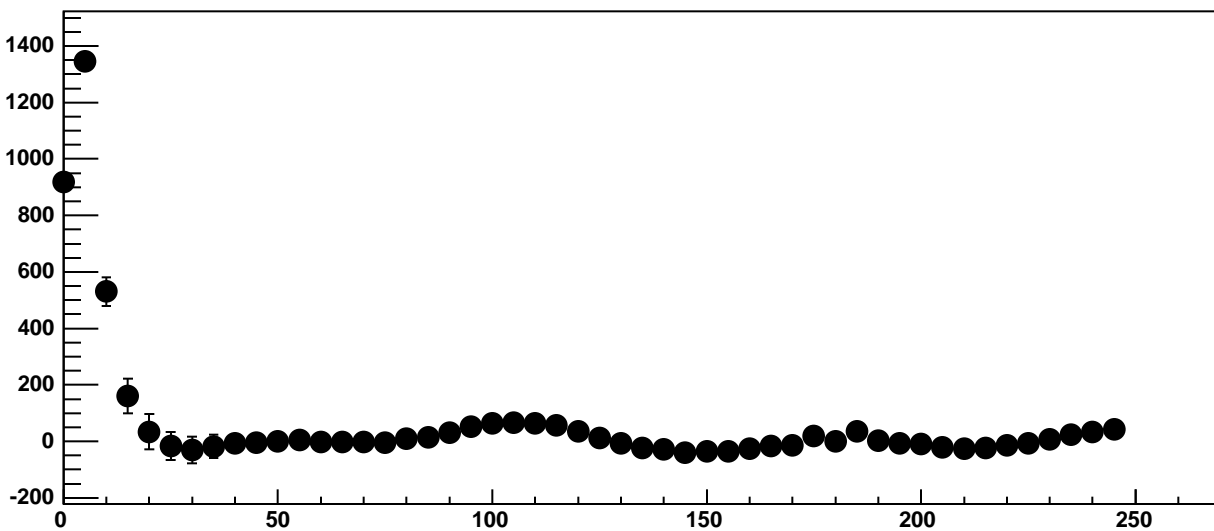


χ^2 / ndf	1013 / 42
p0	-796.4 \pm 1.109
p1	3.796 \pm 0.08857
p2	1.044e+04 \pm 11.17
p3	29.26 \pm 0.02694

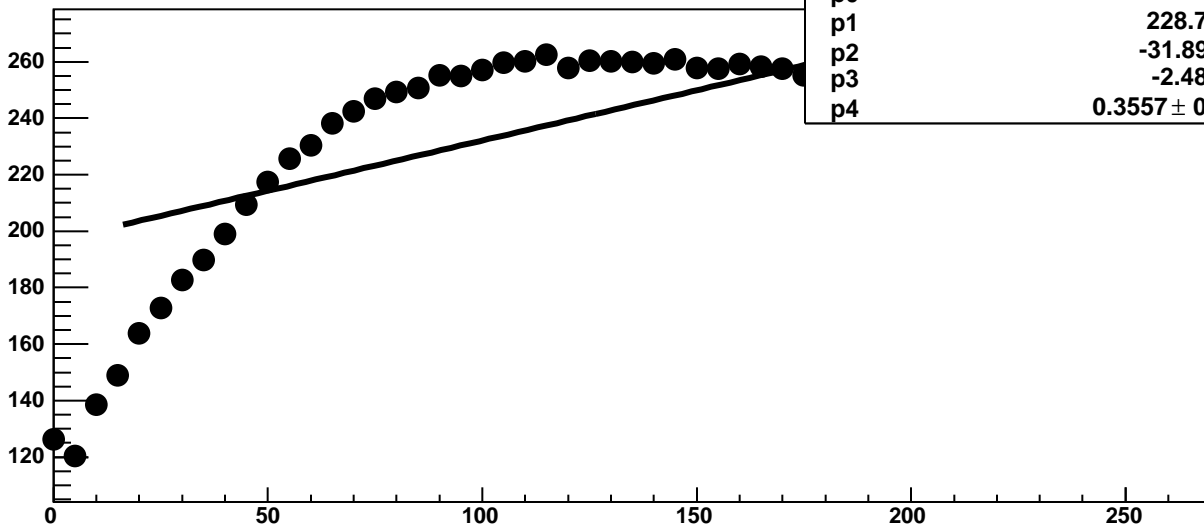
Chip 10, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold

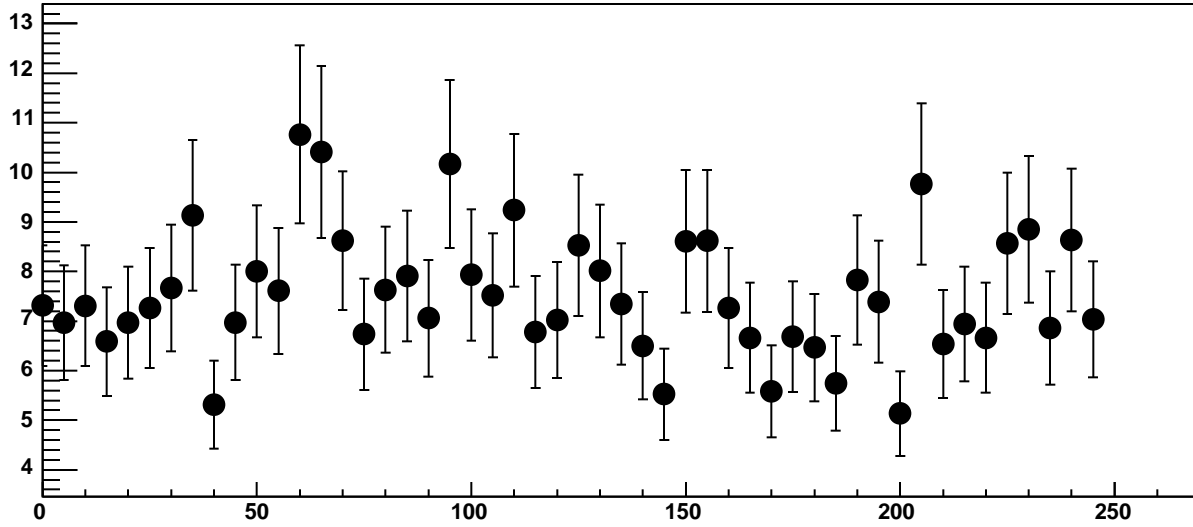


Chip 10, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

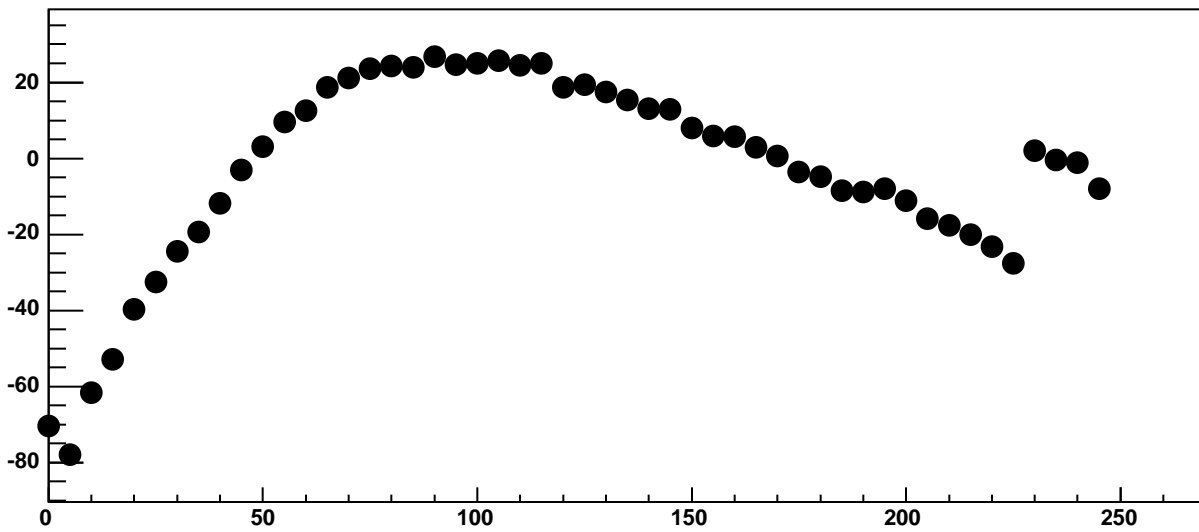


χ^2 / ndf	6261 / 41
p0	277.9 ± 0.8729
p1	228.7 ± 7.408
p2	-31.89 ± 2.542
p3	-2.48 ± 8.235
p4	0.3557 ± 0.004003

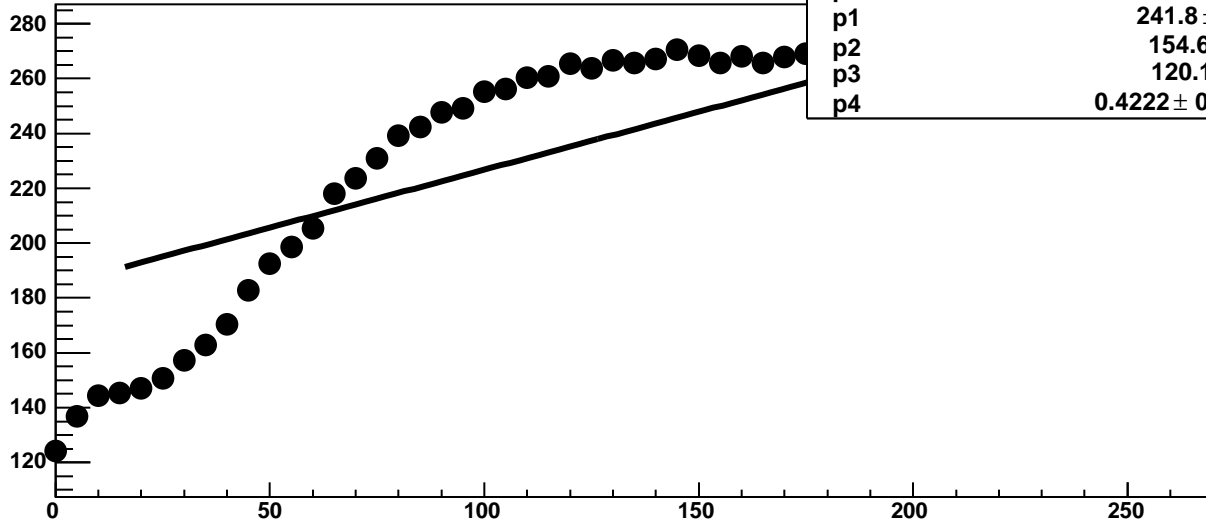
Chip 10, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

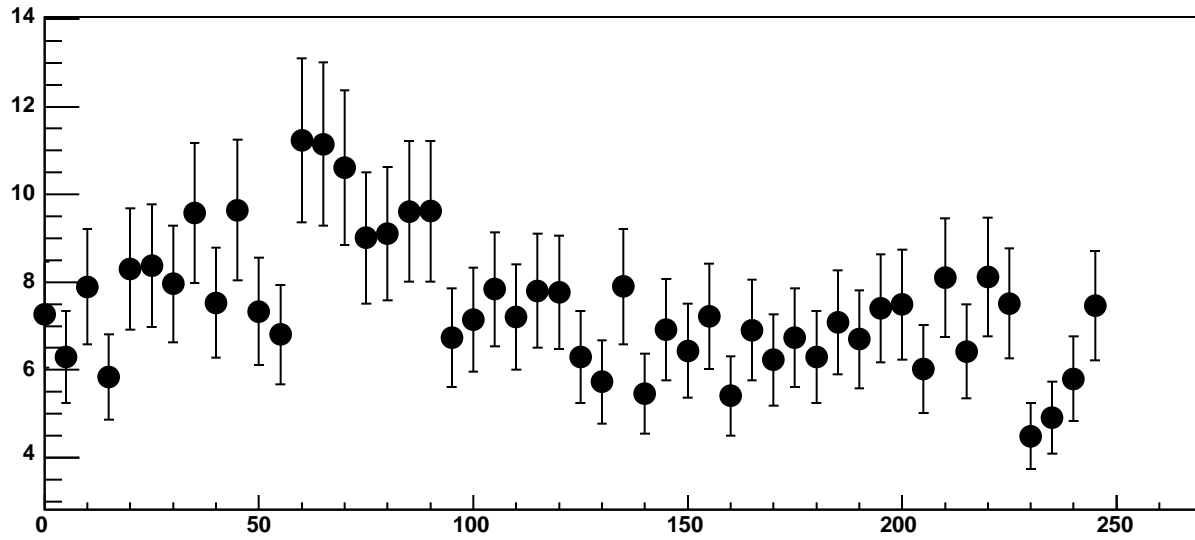


Chip 10, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

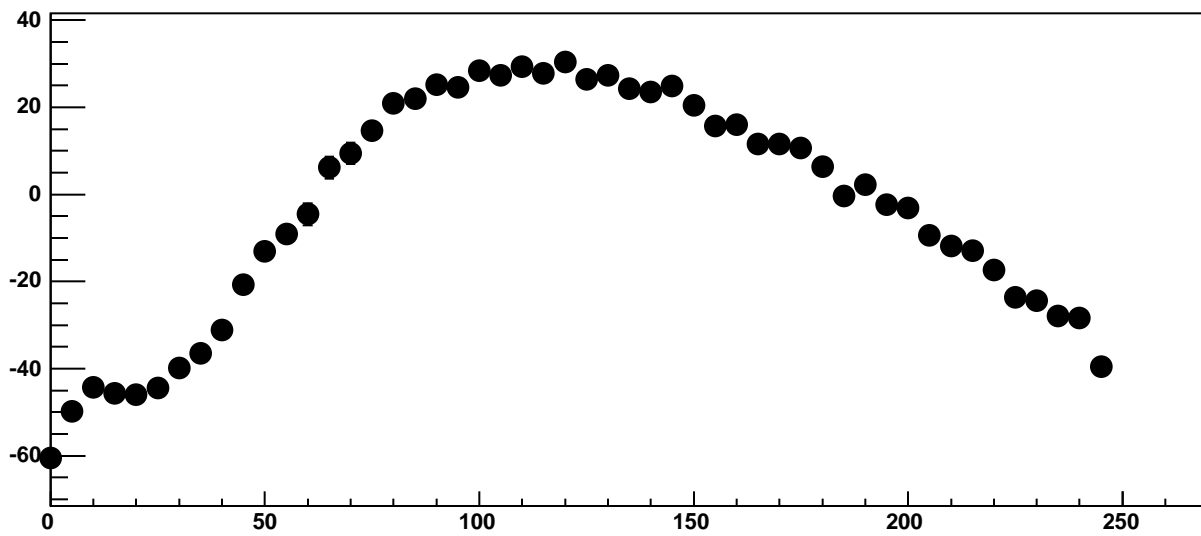


χ^2 / ndf	9815 / 41
p0	286.6 ± 0.2378
p1	241.8 ± 0.5632
p2	154.6 ± 84.85
p3	120.1 ± 1.414
p4	0.4222 ± 0.001984

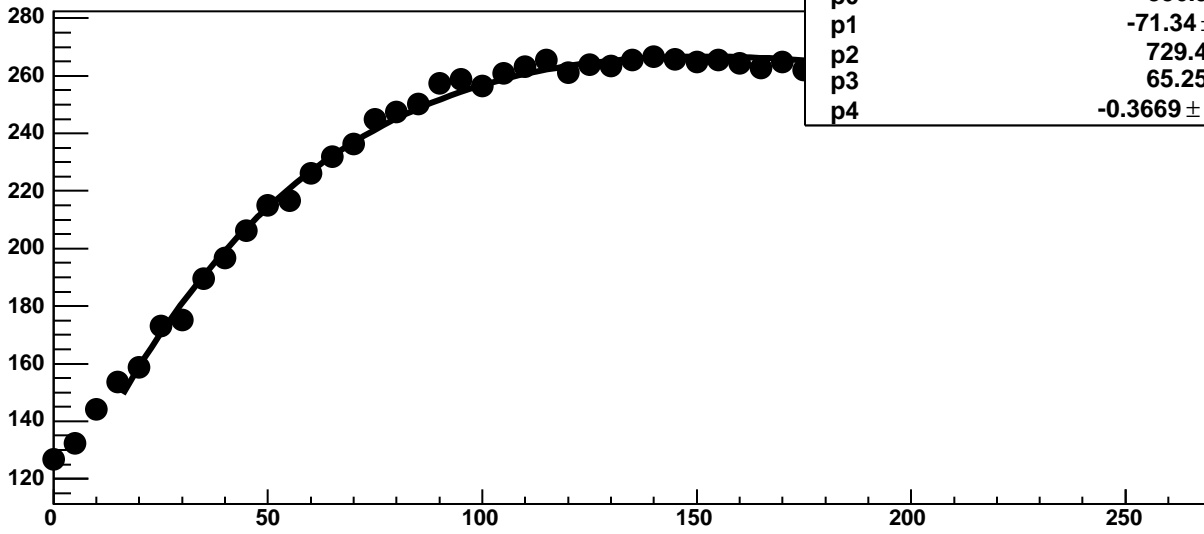
Chip 10, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

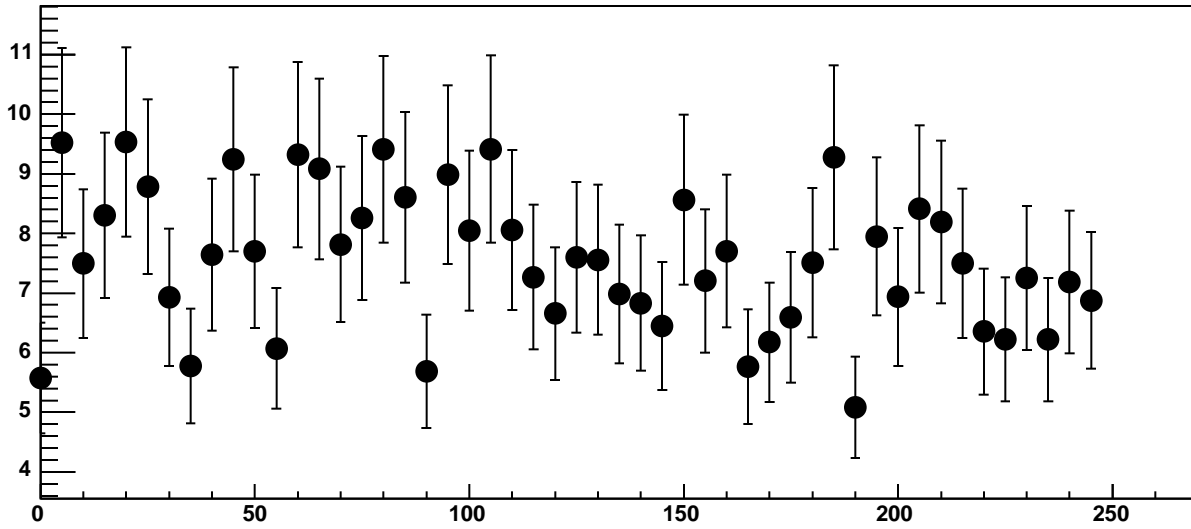


Chip 10, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold

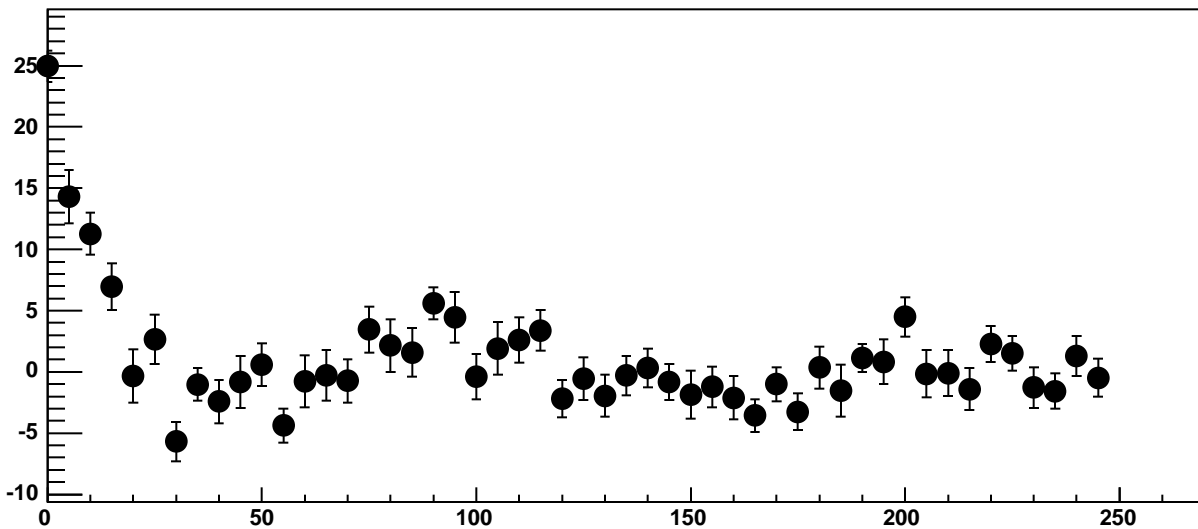


χ^2 / ndf	111.3 / 41
p0	-356.9 ± 29.87
p1	-71.34 ± 0.6403
p2	729.4 ± 20.62
p3	65.25 ± 2.898
p4	-0.3669 ± 0.02978

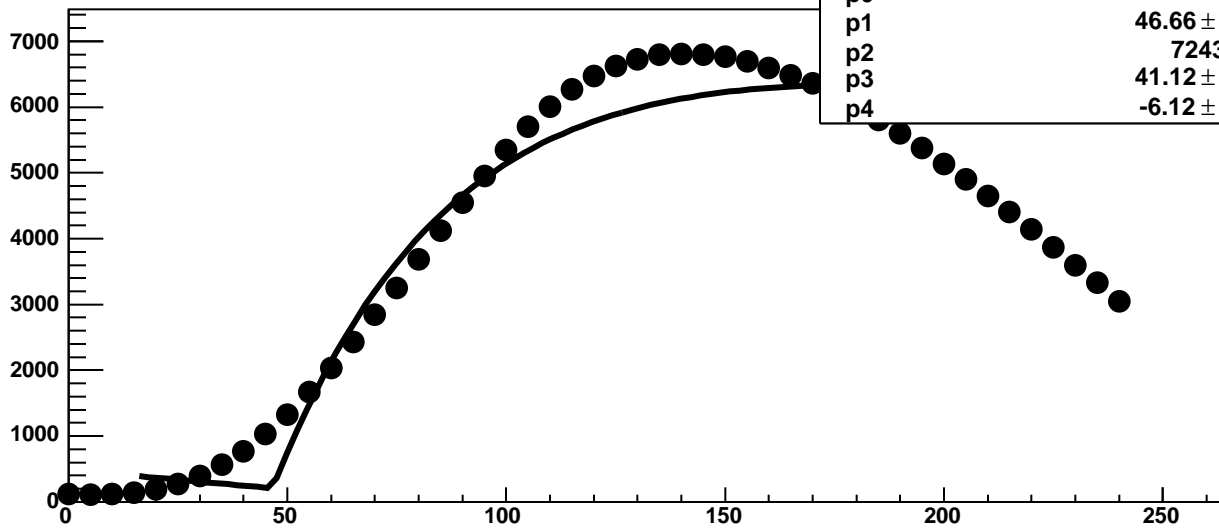
Chip 10, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold

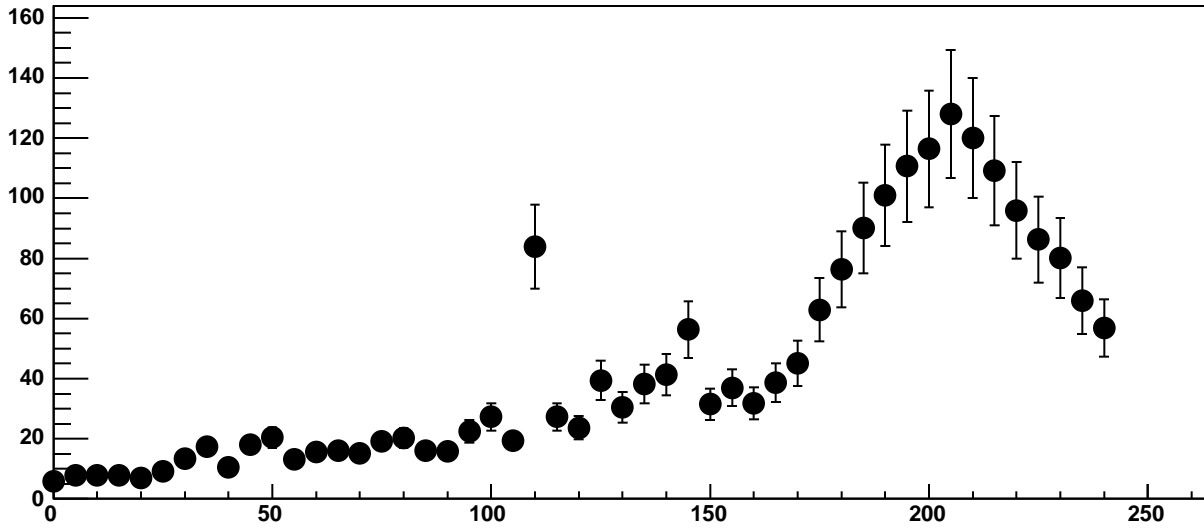


Chip 10, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold

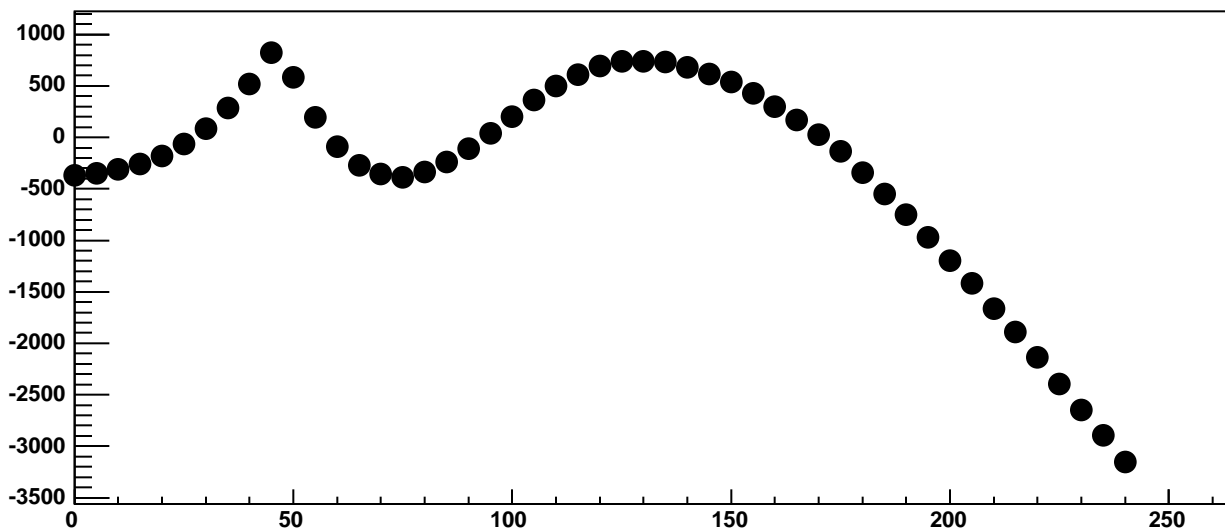


χ^2 / ndf	4.136e+05 / 41
p0	204.6 ± 1.595
p1	46.66 ± 0.02017
p2	7243 ± 11.06
p3	41.12 ± 0.06862
p4	-6.12 ± 0.06435

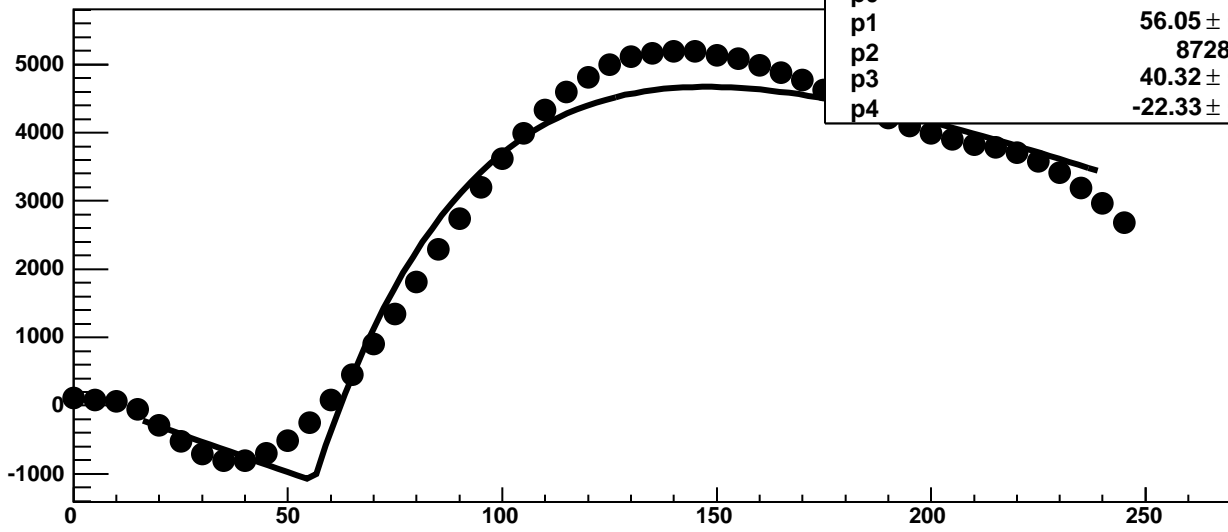
Chip 10, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



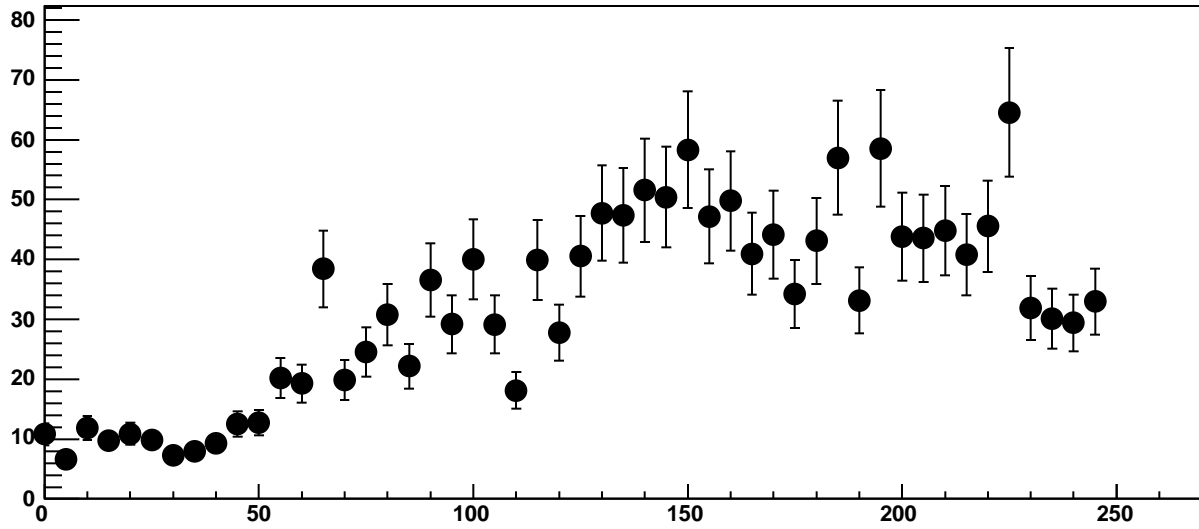
Chip 10, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold



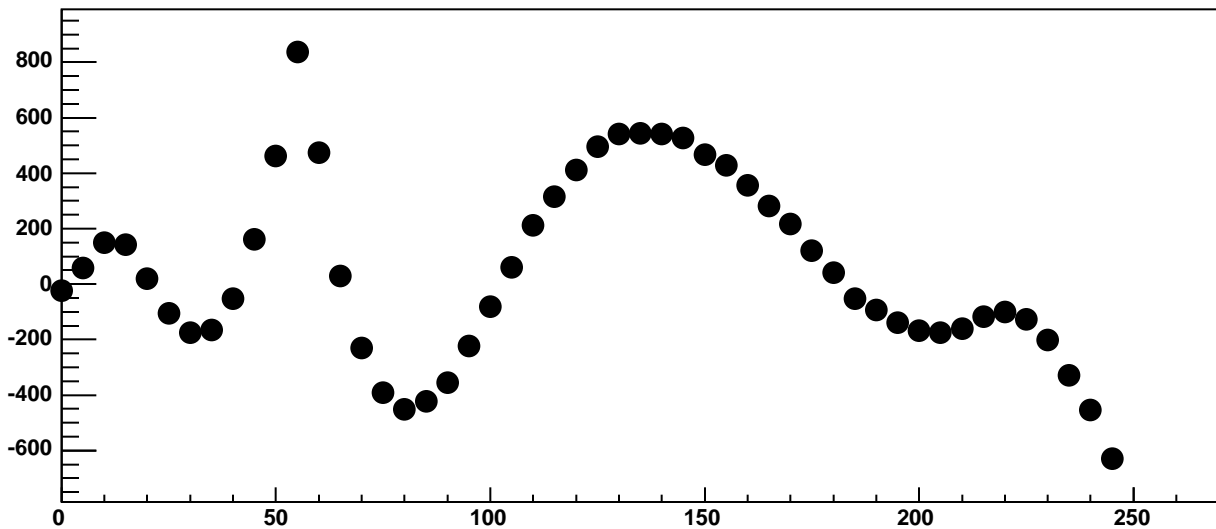
Chip 10, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold



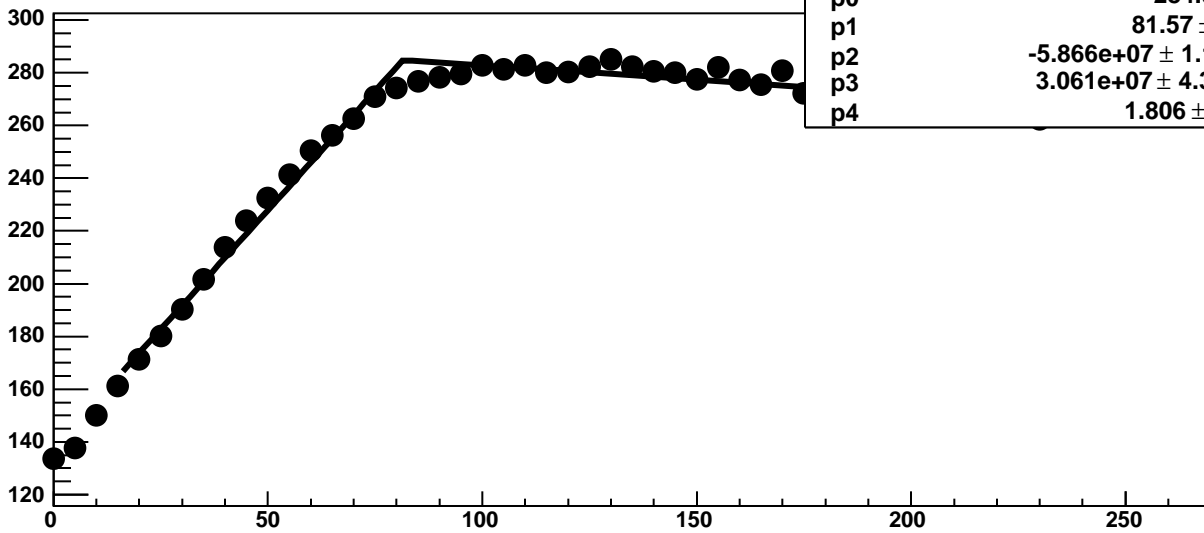
Chip 10, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

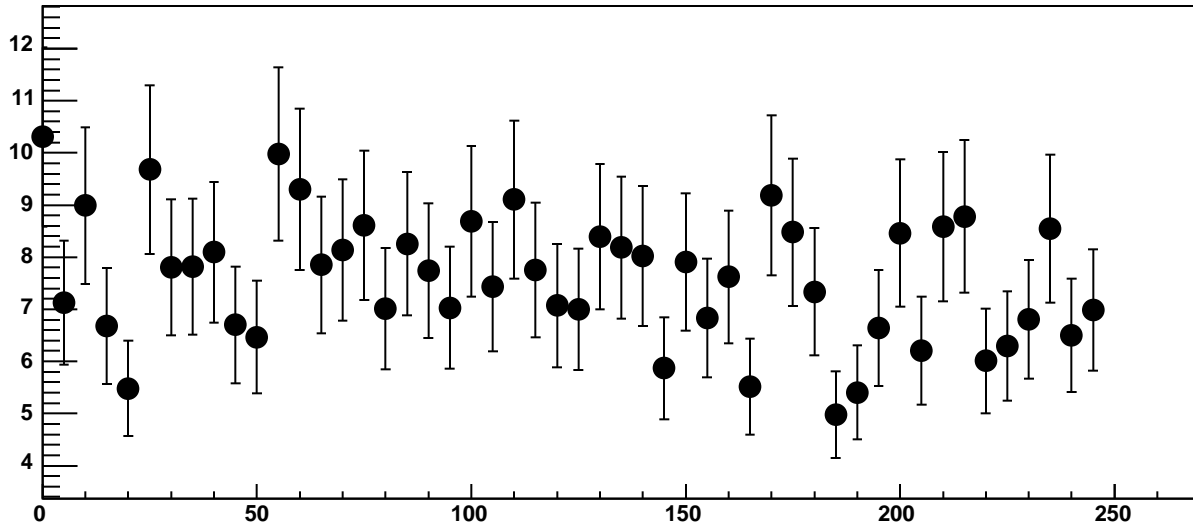


Chip 10, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold

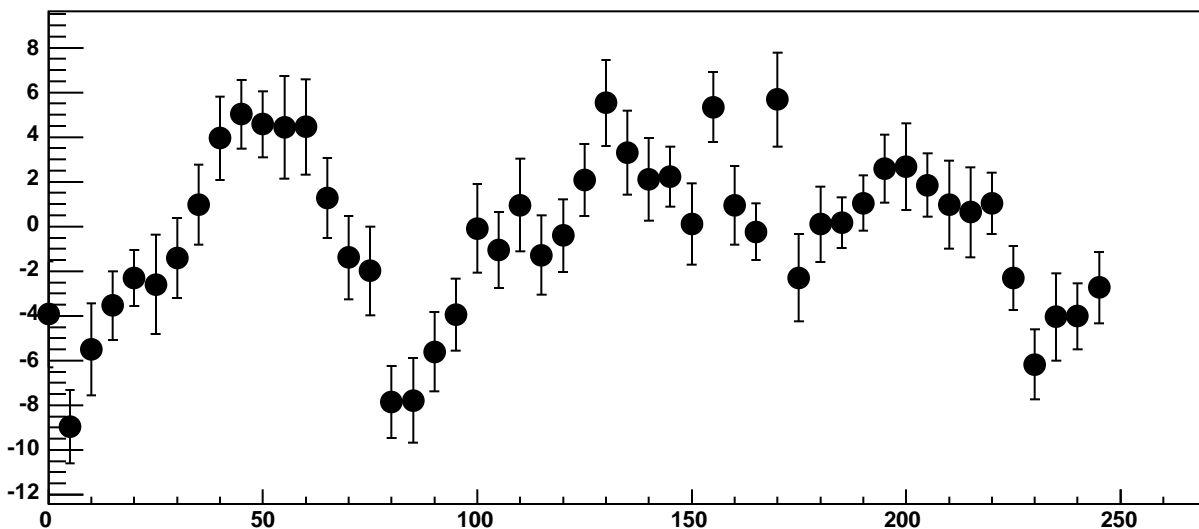


χ^2 / ndf	179.5 / 41
p0	284.9 ± 0.59
p1	81.57 ± 0.5864
p2	$-5.866\text{e}+07 \pm 1.106\text{e}+06$
p3	$3.061\text{e}+07 \pm 4.322\text{e}+05$
p4	1.806 ± 0.02211

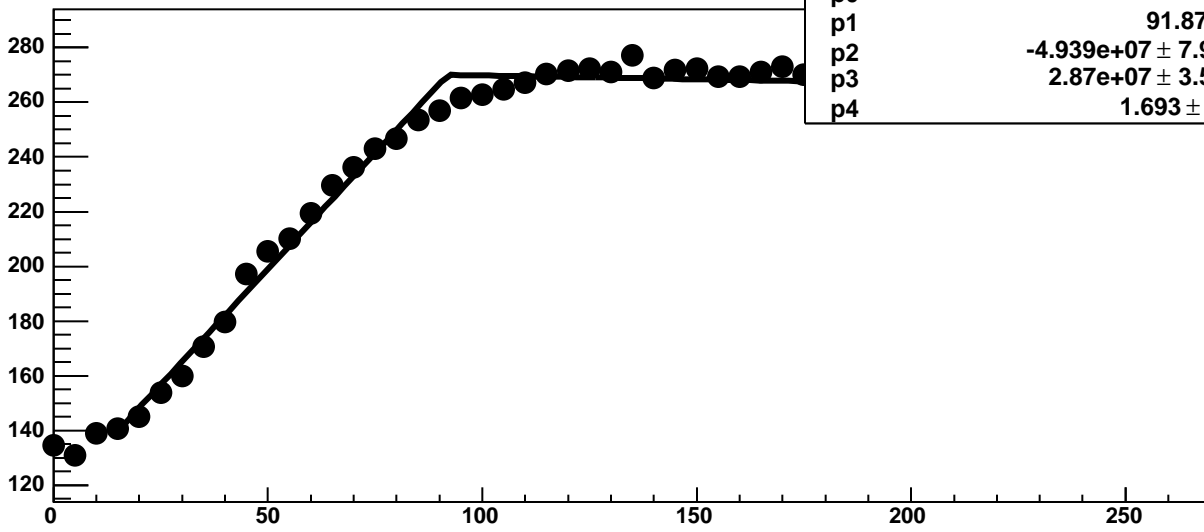
Chip 10, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold

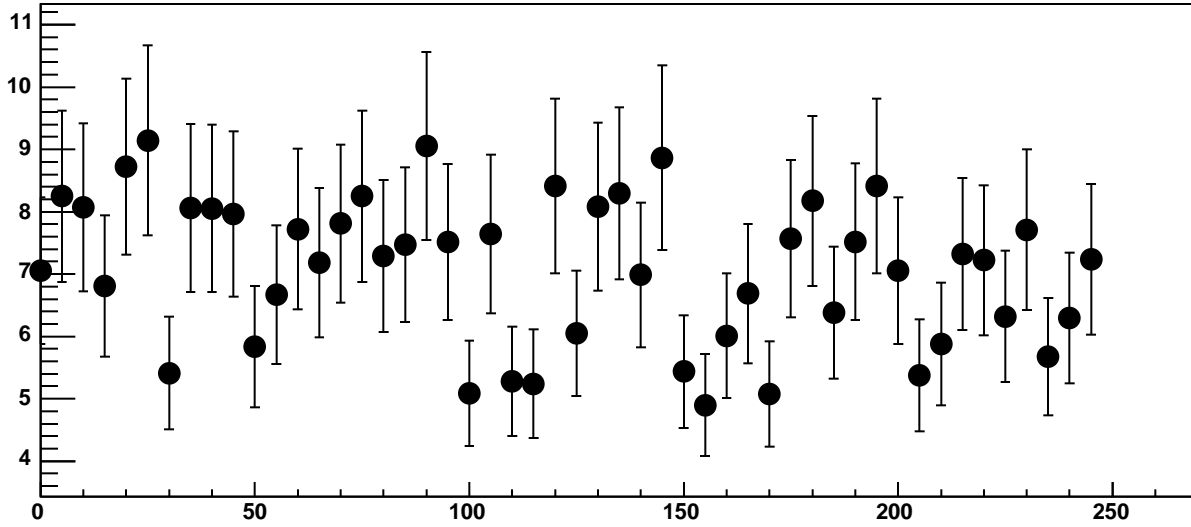


Chip 10, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

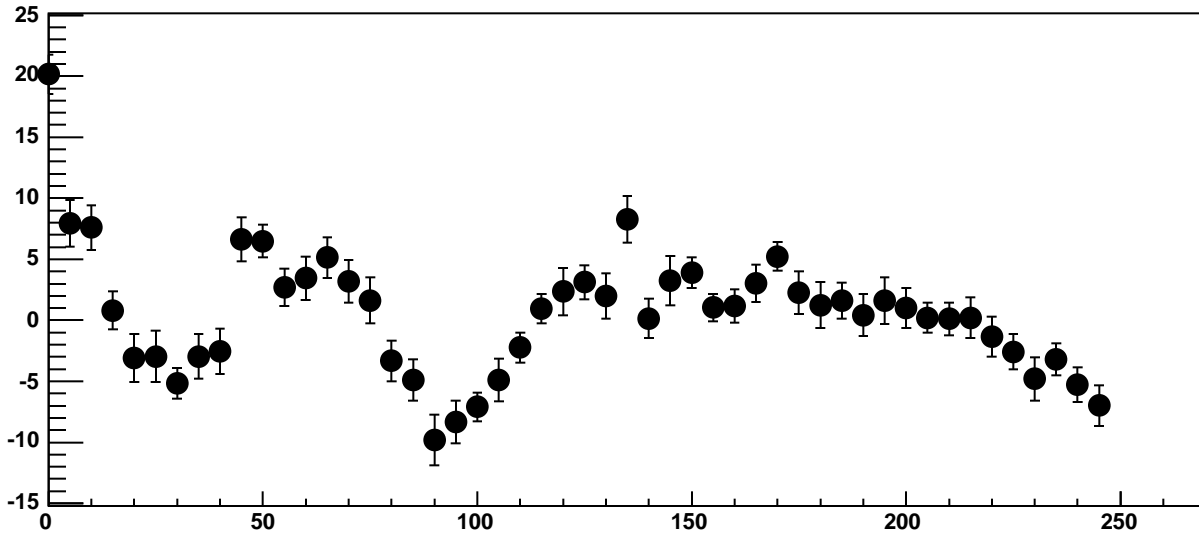


χ^2 / ndf	290.1 / 41
p0	270 ± 0.5231
p1	91.87 ± 0.597
p2	$-4.939\text{e}+07 \pm 7.972\text{e}+05$
p3	$2.87\text{e}+07 \pm 3.531\text{e}+05$
p4	1.693 ± 0.01896

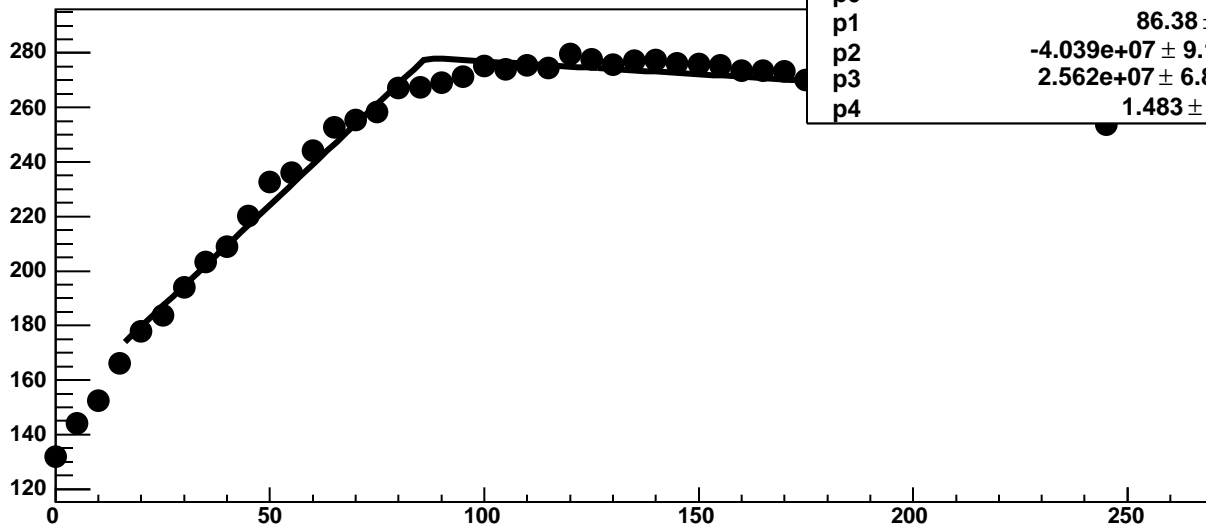
Chip 10, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

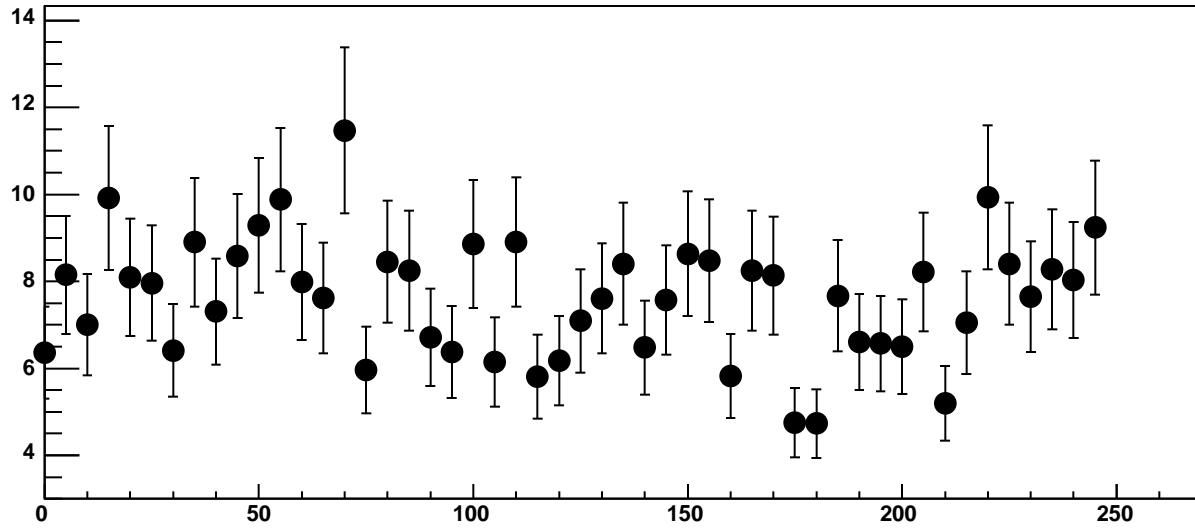


Chip 10, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

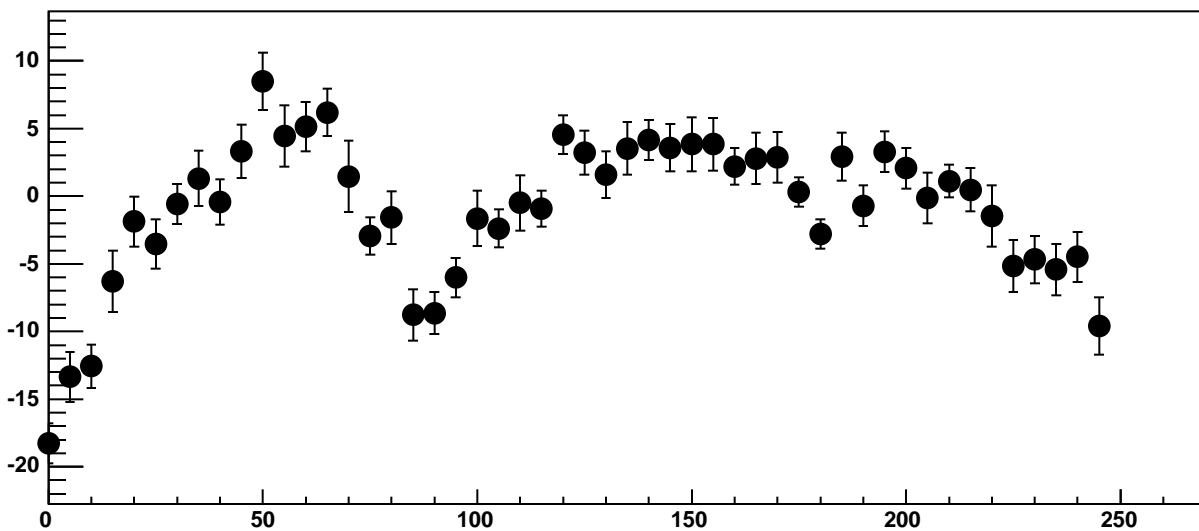


χ^2 / ndf	227.3 / 41
p0	278.2 ± 0.5386
p1	86.38 ± 0.6253
p2	$-4.039\text{e}+07 \pm 9.115\text{e}+05$
p3	$2.562\text{e}+07 \pm 6.848\text{e}+05$
p4	1.483 ± 0.01857

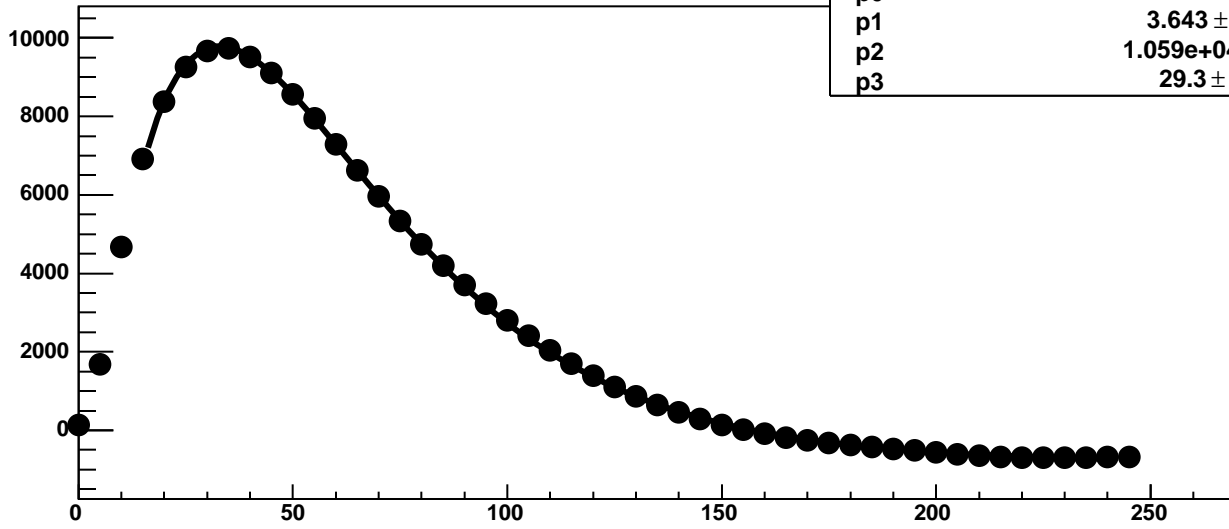
Chip 10, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

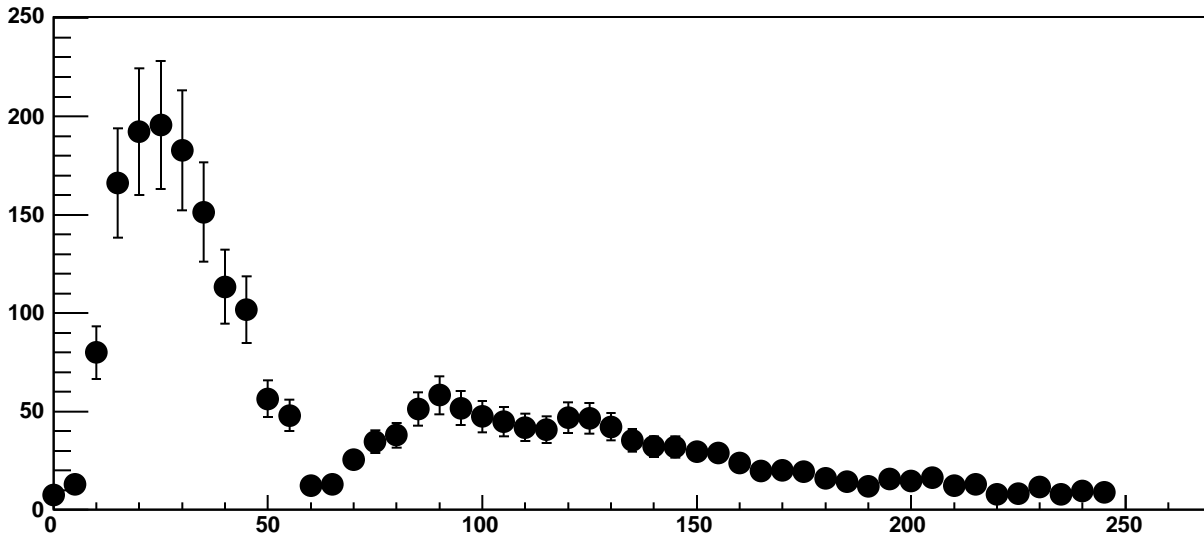


Chip 10, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold

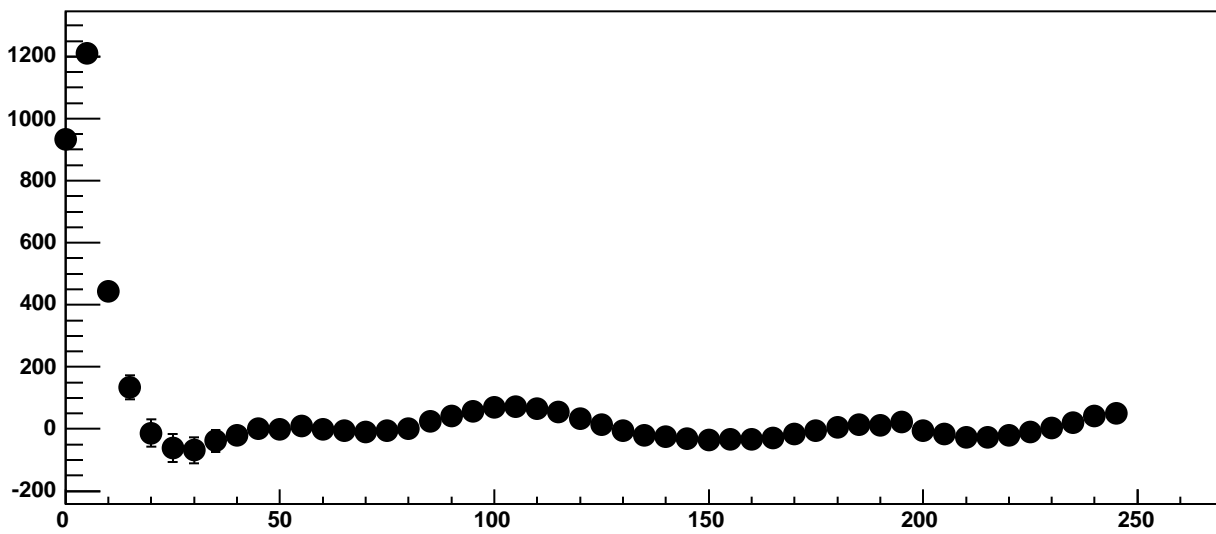


χ^2 / ndf	1337 / 42
p0	-800.8 ± 1.003
p1	3.643 ± 0.06421
p2	$1.059\text{e}+04 \pm 8.35$
p3	29.3 ± 0.02289

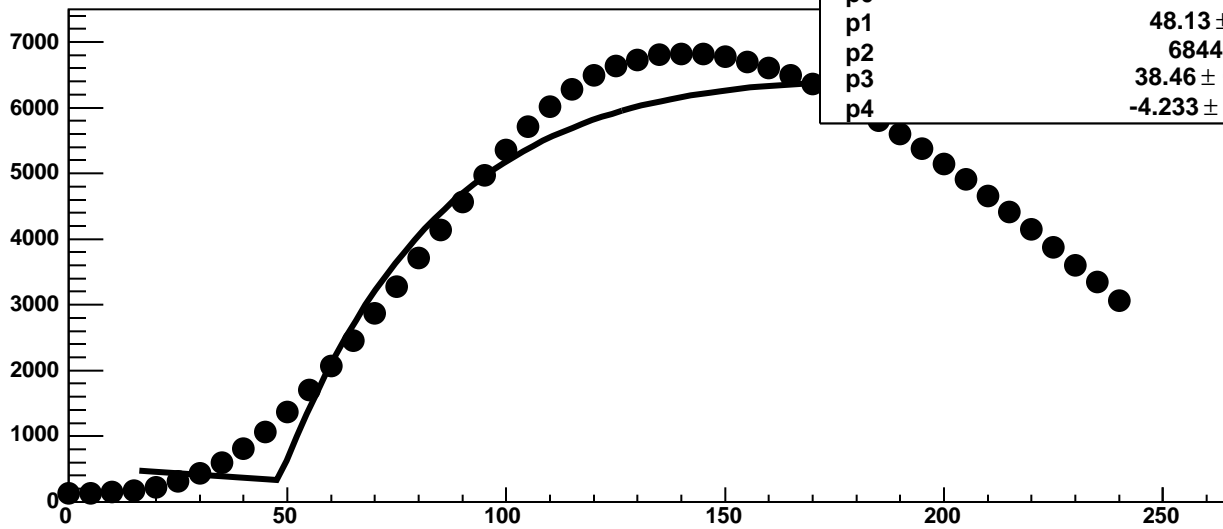
Chip 10, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold

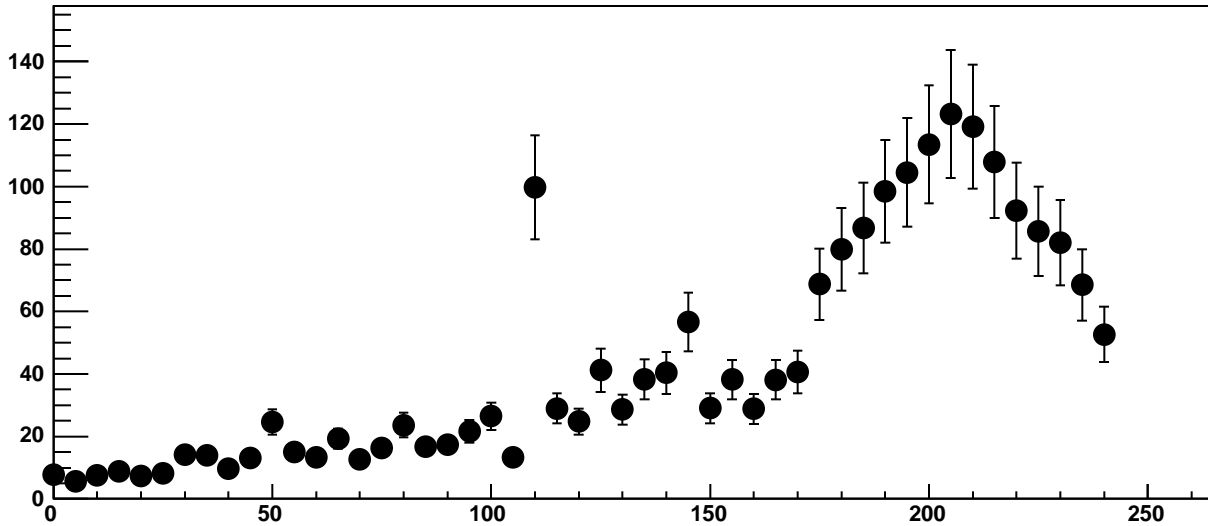


Chip 10, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold

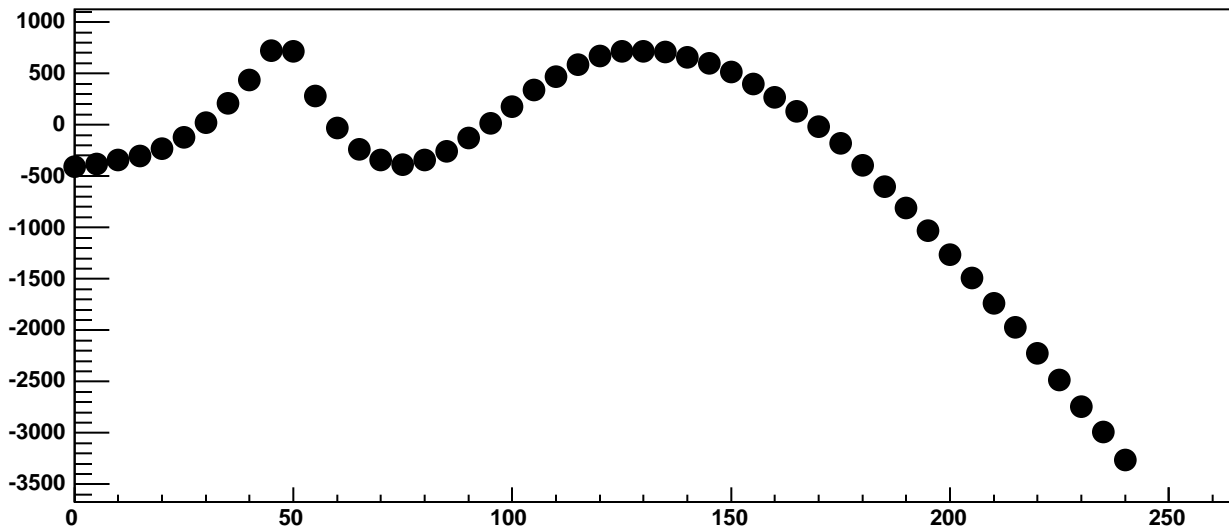


χ^2 / ndf	4.585e+05 / 41
p0	334.6 ± 1.49
p1	48.13 ± 0.0192
p2	6844 ± 9.766
p3	38.46 ± 0.06228
p4	-4.233 ± 0.05881

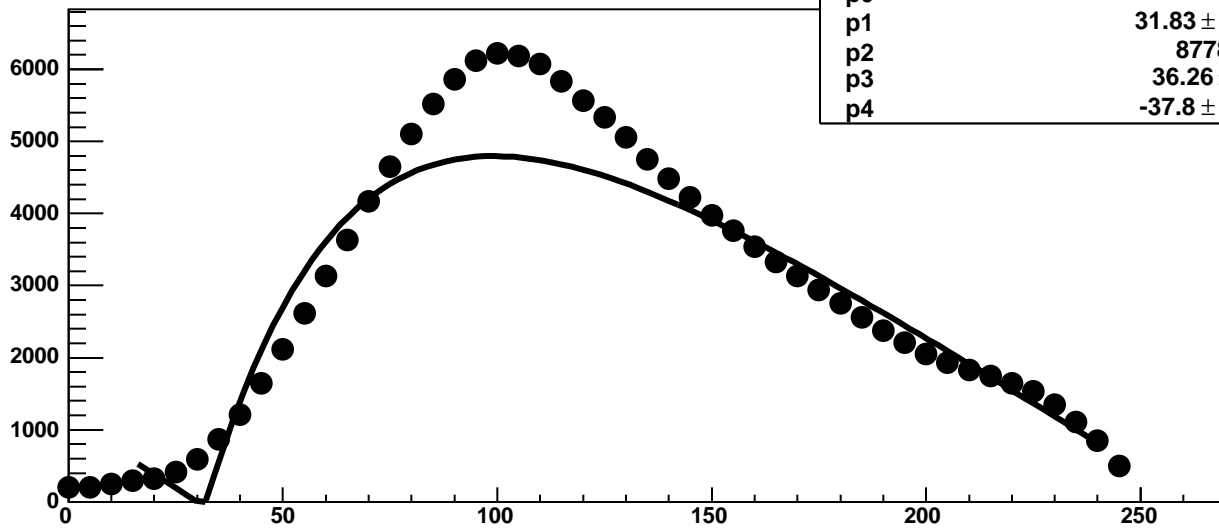
Chip 10, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold

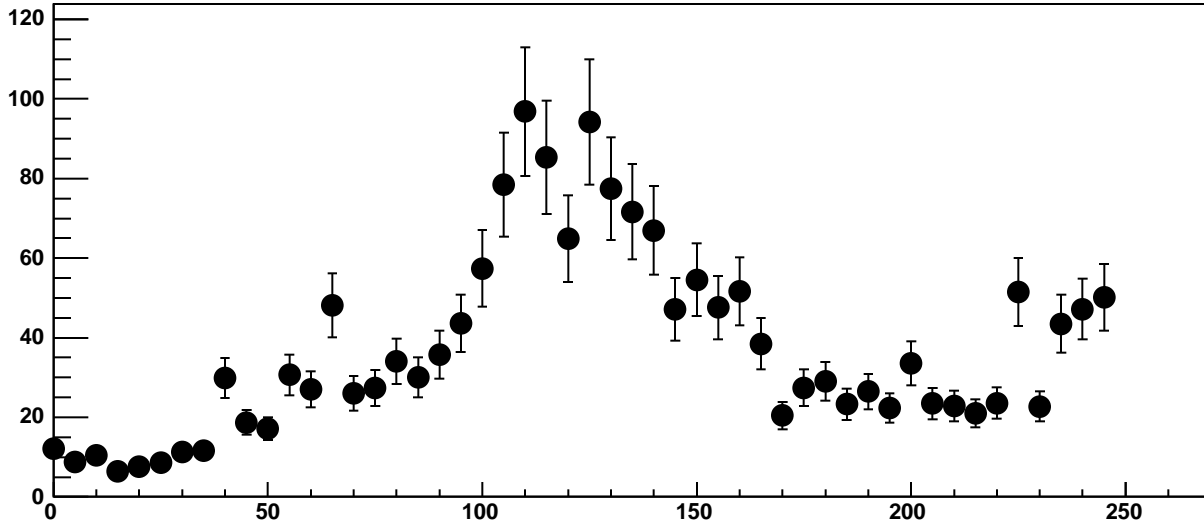


Chip 10, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold

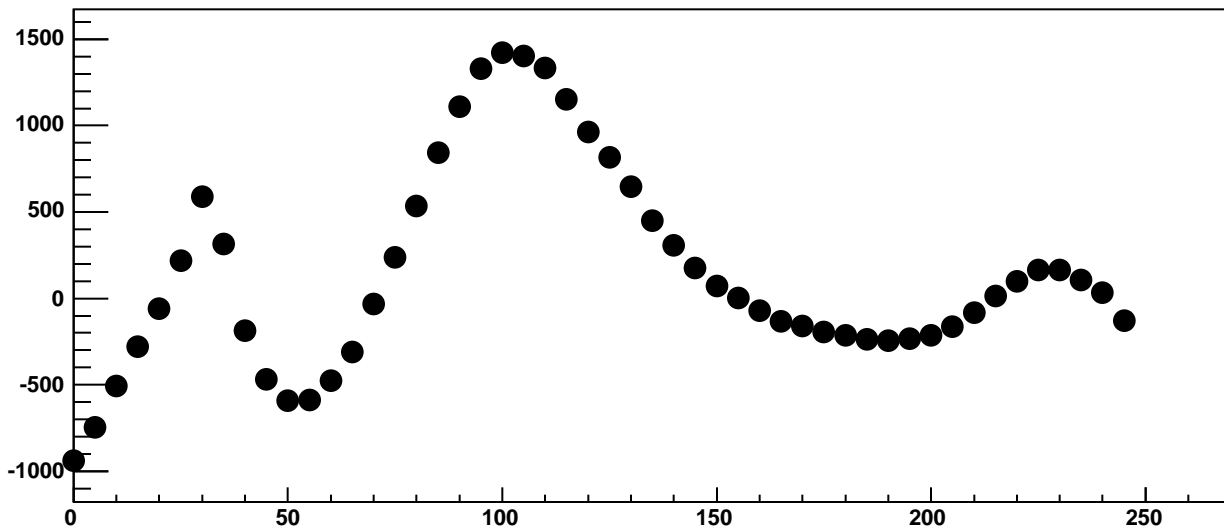


χ^2 / ndf	2.668e+05 / 41
p0	-65.65 ± 1.024
p1	31.83 ± 0.01338
p2	8778 ± 10.8
p3	36.26 ± 0.0581
p4	-37.8 ± 0.05695

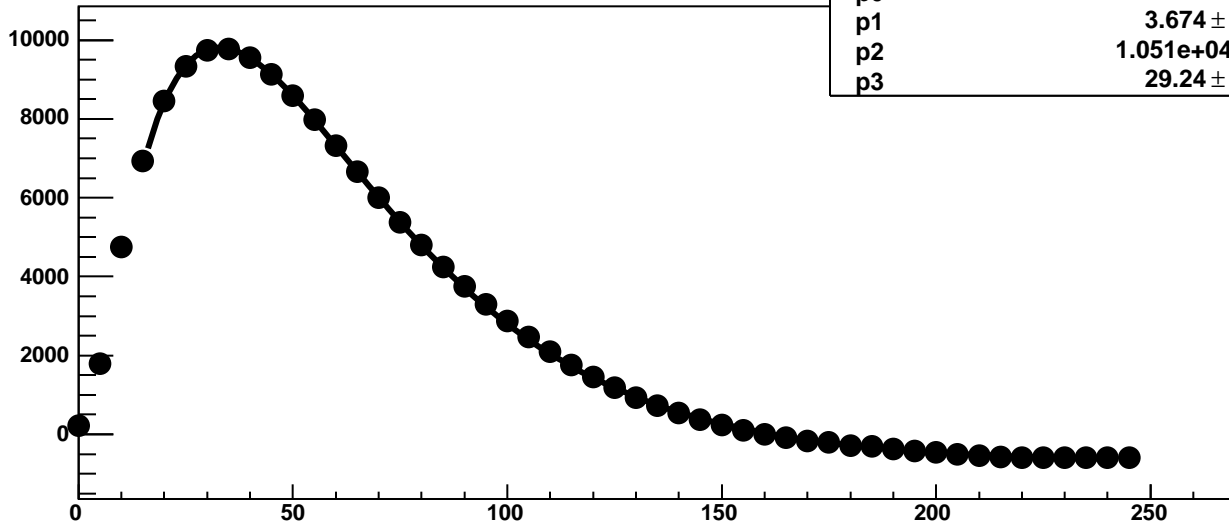
Chip 10, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

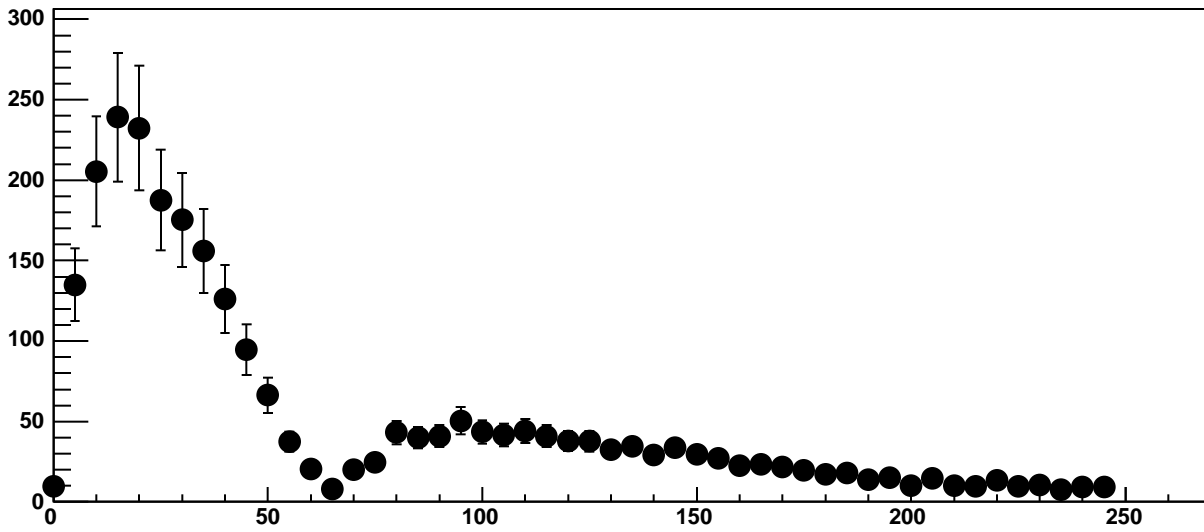


Chip 10, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold

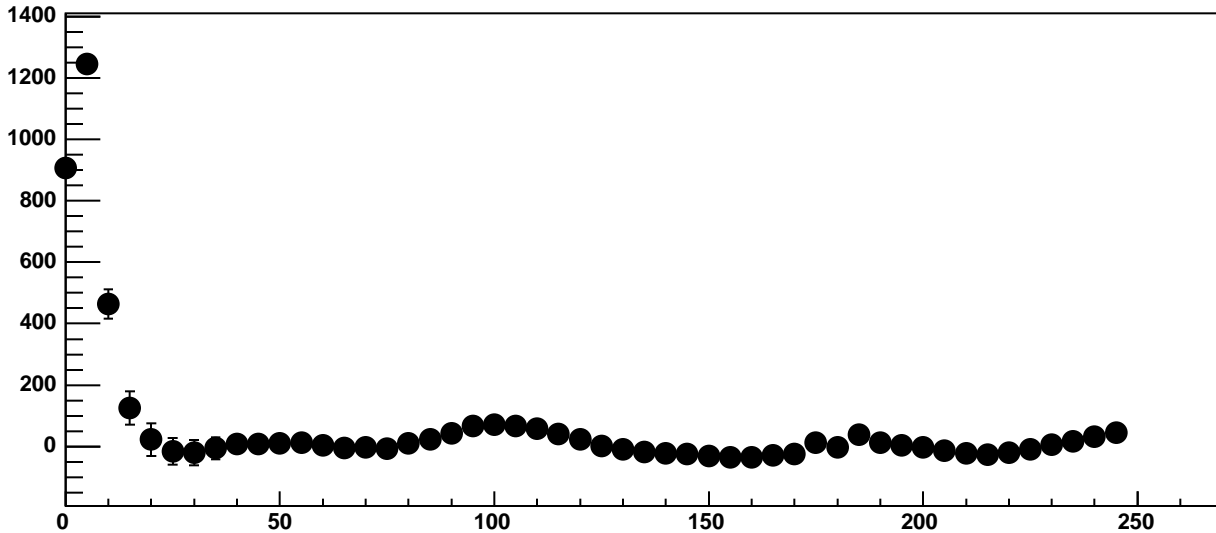


χ^2 / ndf	1144 / 42
p0	-694.2 ± 1.004
p1	3.674 ± 0.07769
p2	1.051e+04 ± 9.933
p3	29.24 ± 0.02386

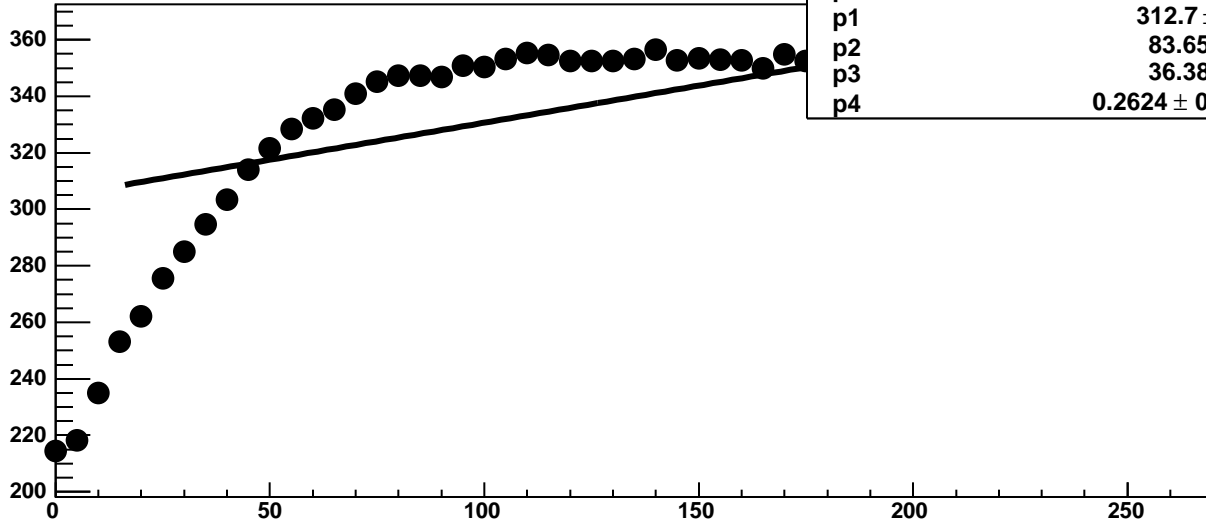
Chip 10, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold

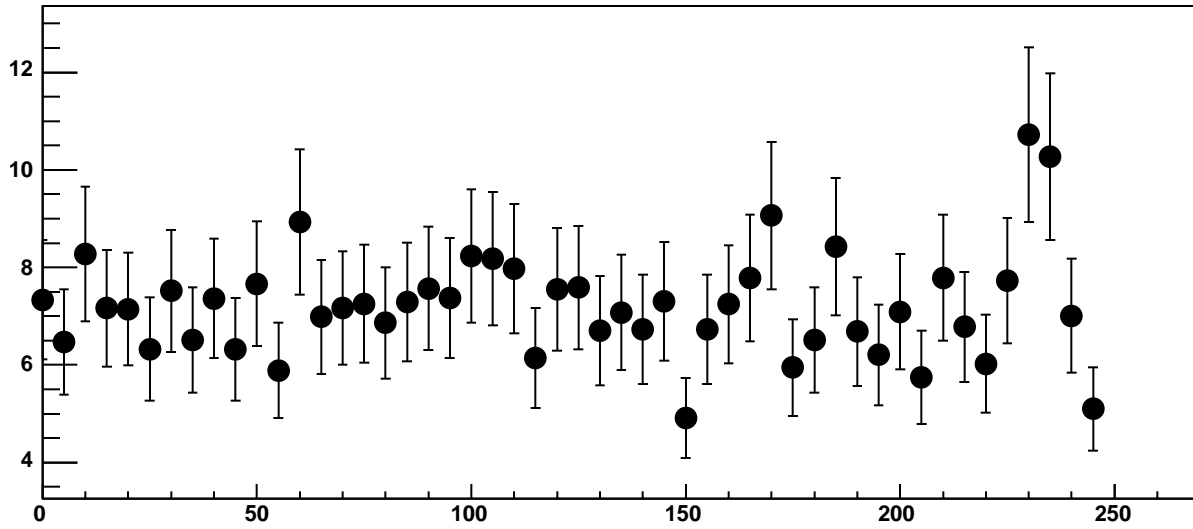


Chip 10, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

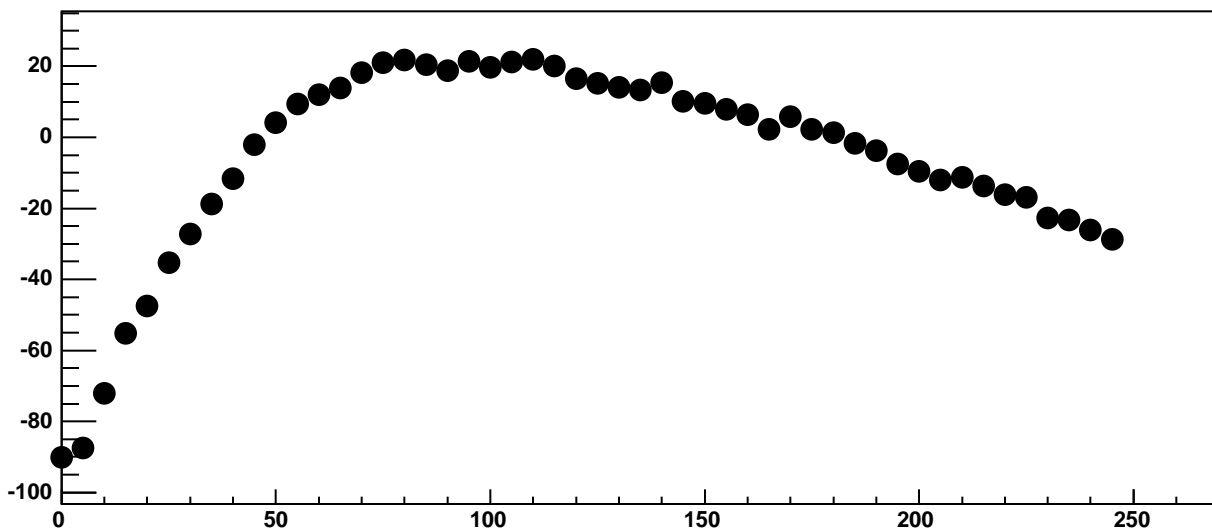


χ^2 / ndf	6244 / 41
p0	386.5 ± 0.2378
p1	312.7 ± 0.9063
p2	83.65 ± 84.85
p3	36.38 ± 1.414
p4	0.2624 ± 0.001206

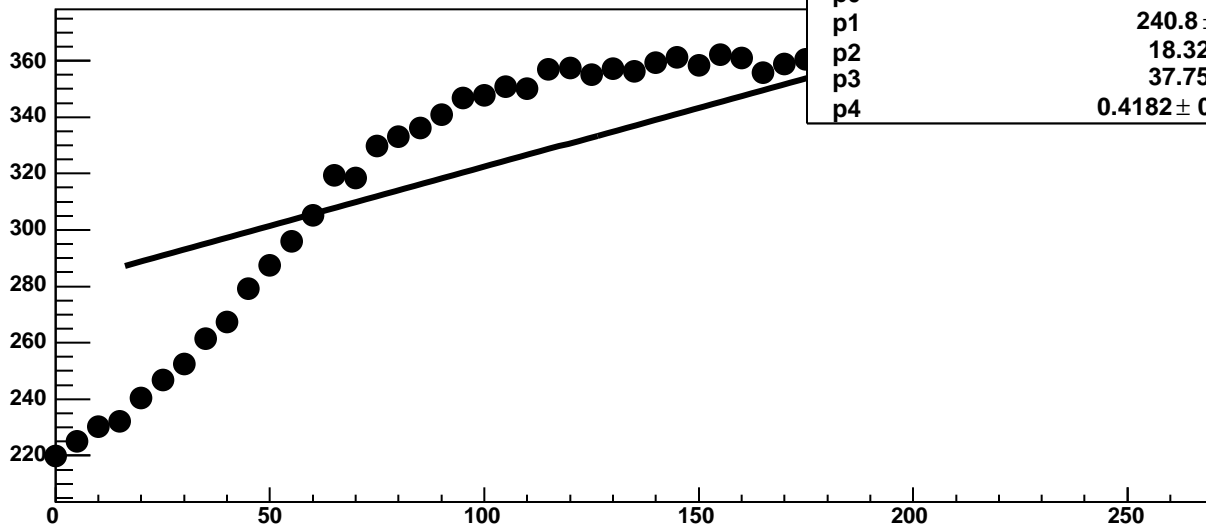
Chip 10, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

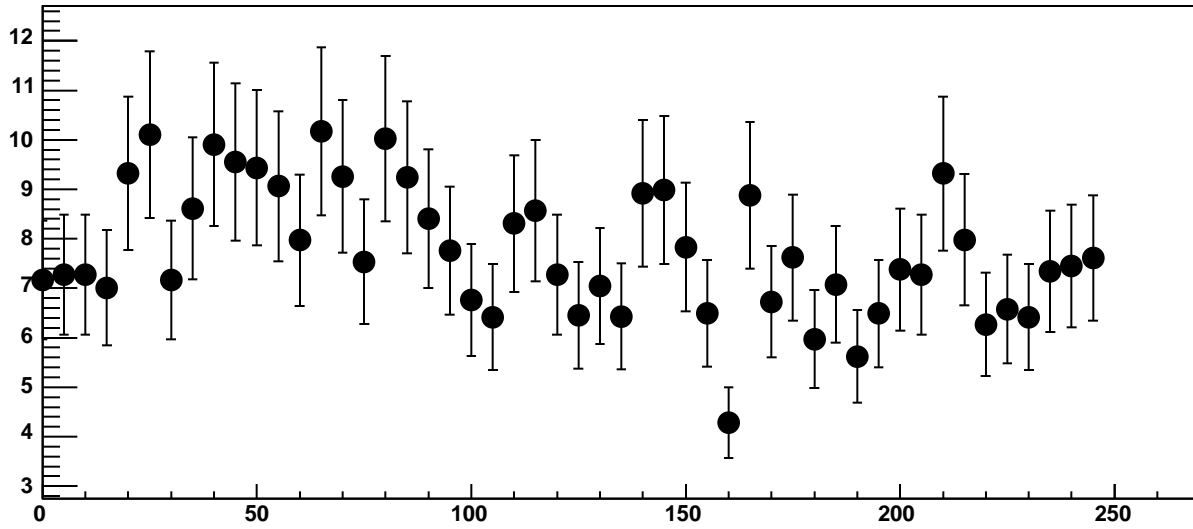


Chip 10, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

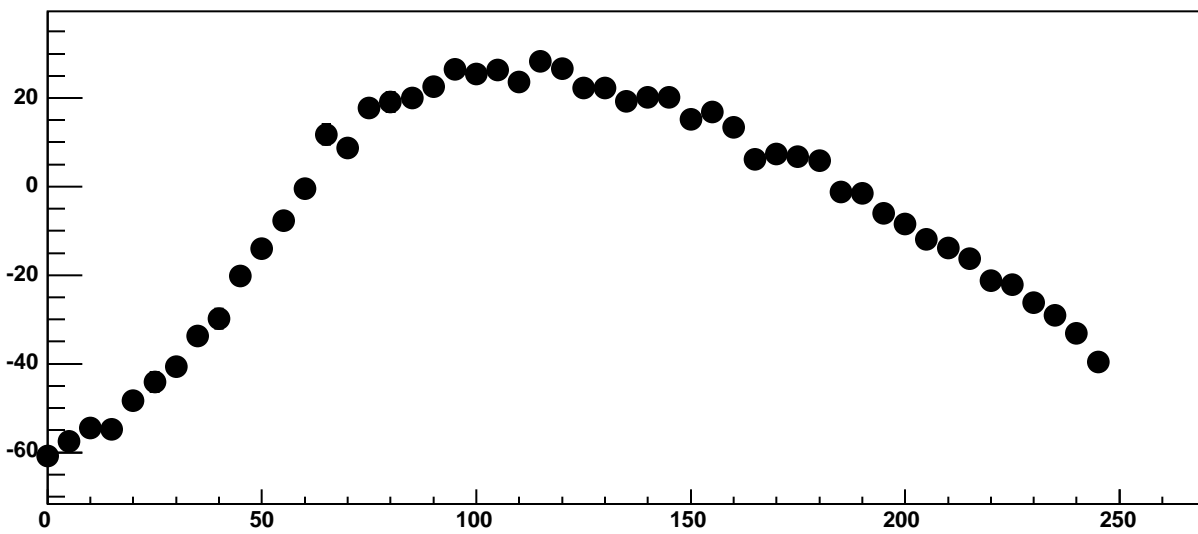


χ^2 / ndf	7981 / 41
p0	381.3 ± 0.2508
p1	240.8 ± 0.5998
p2	18.32 ± 84.85
p3	37.75 ± 1.414
p4	0.4182 ± 0.002092

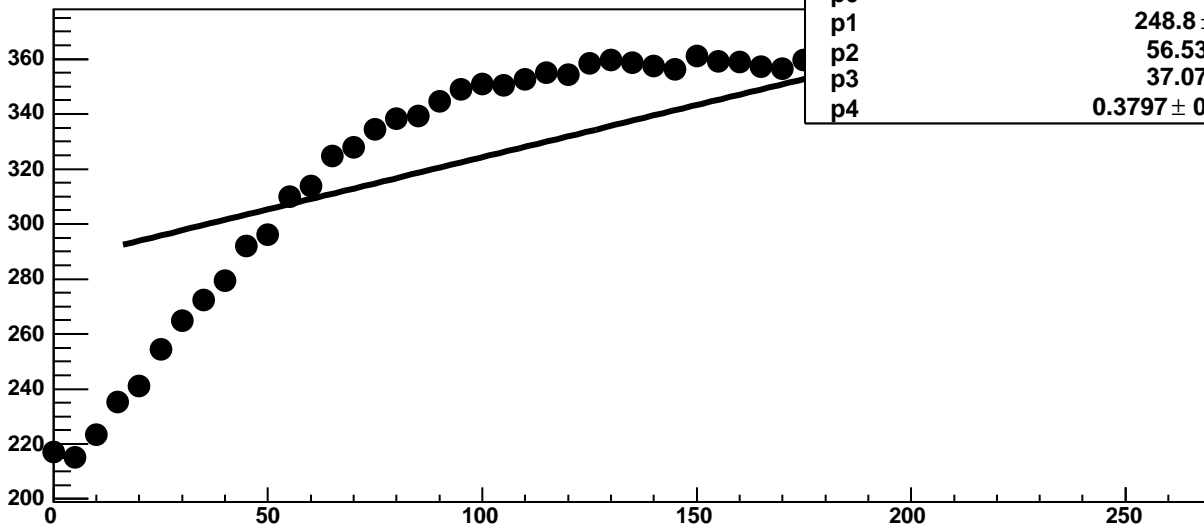
Chip 10, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

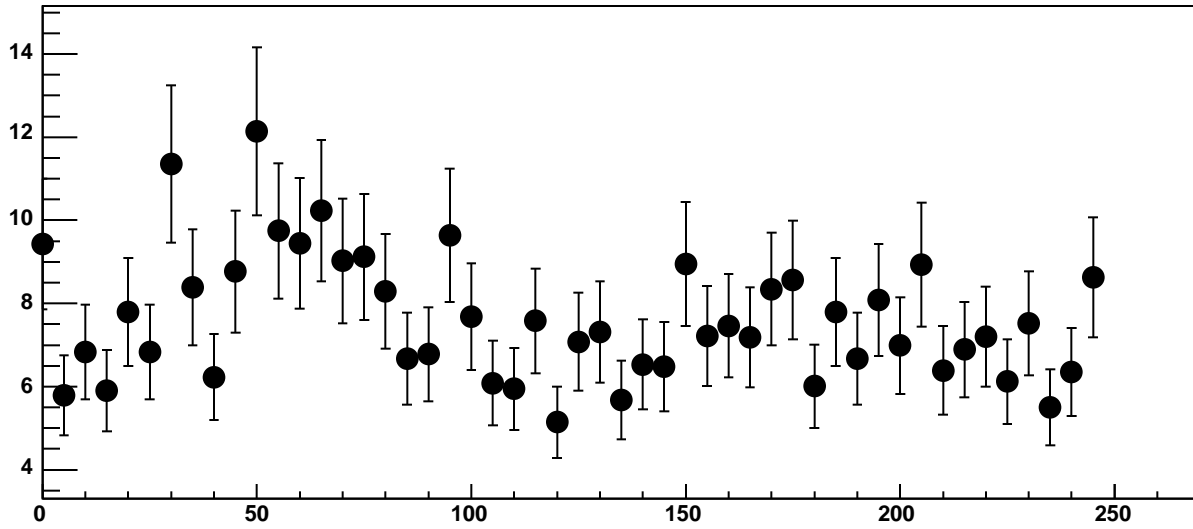


Chip 10, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold

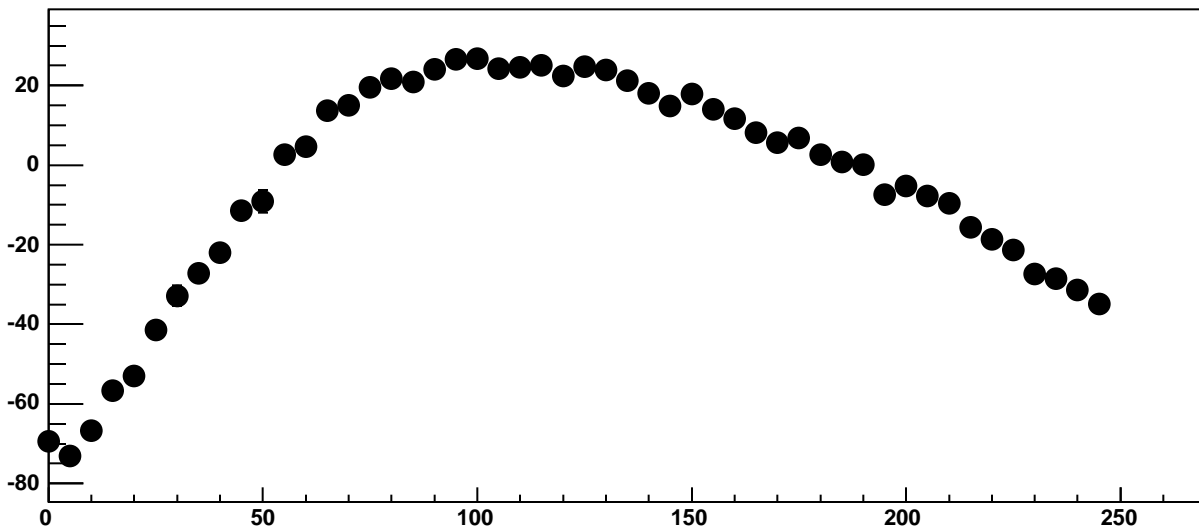


χ^2 / ndf	9225 / 41
p0	380.8 ± 0.244
p1	248.8 ± 0.6427
p2	56.53 ± 84.85
p3	37.07 ± 1.414
p4	0.3797 ± 0.001853

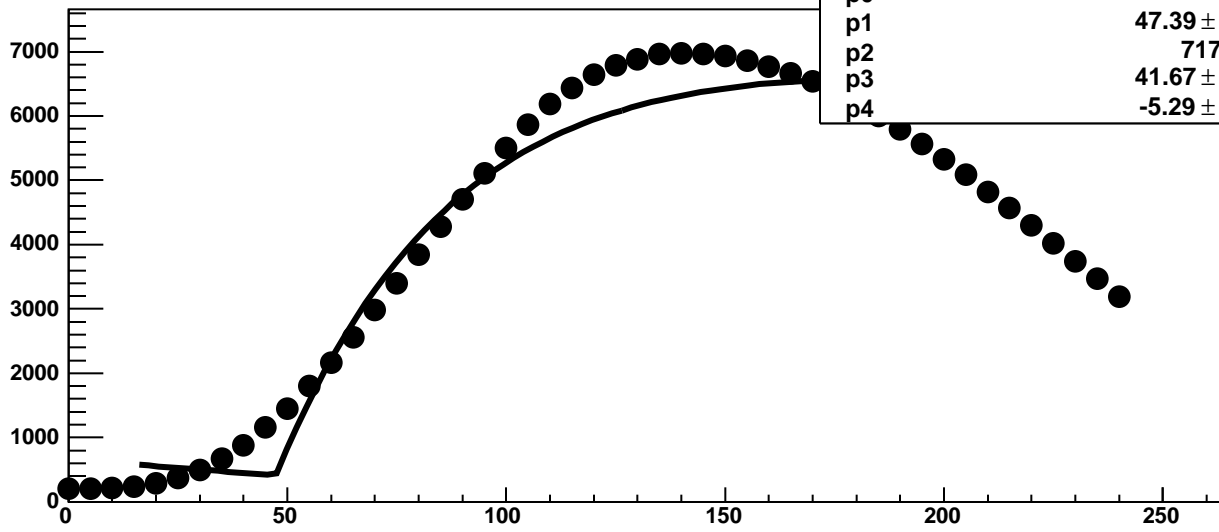
Chip 10, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold

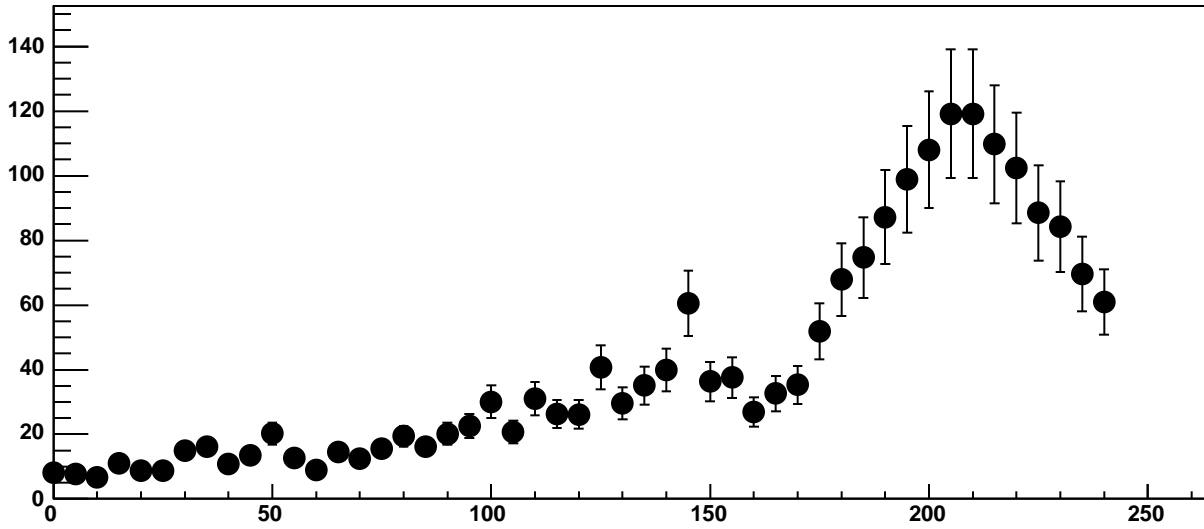


Chip 10, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

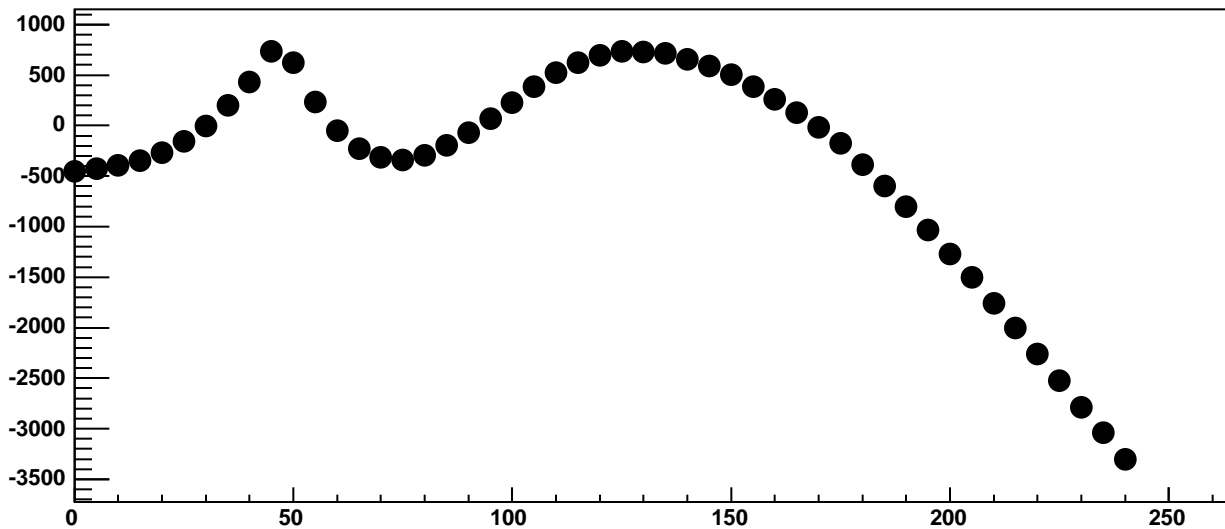


χ^2 / ndf	4.272e+05 / 41
p0	410.3 ± 1.572
p1	47.39 ± 0.01842
p2	7174 ± 11.1
p3	41.67 ± 0.06776
p4	-5.29 ± 0.06582

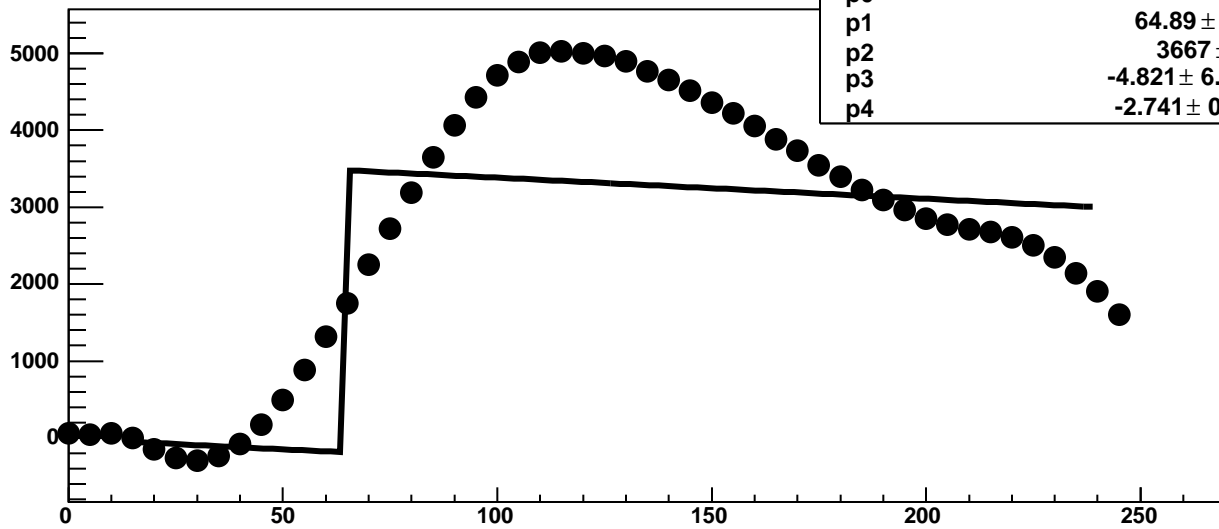
Chip 10, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

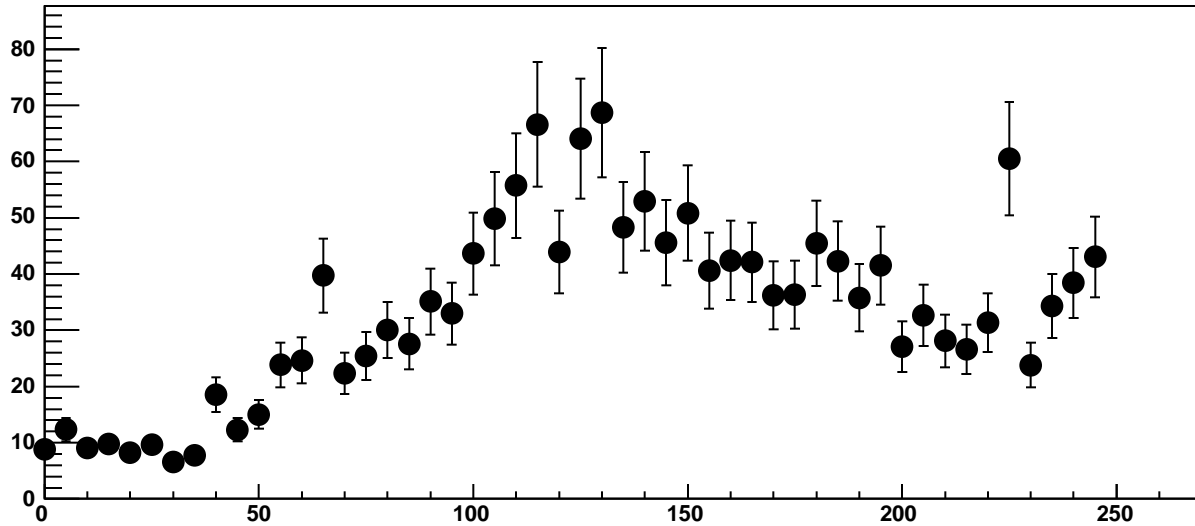


Chip 10, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

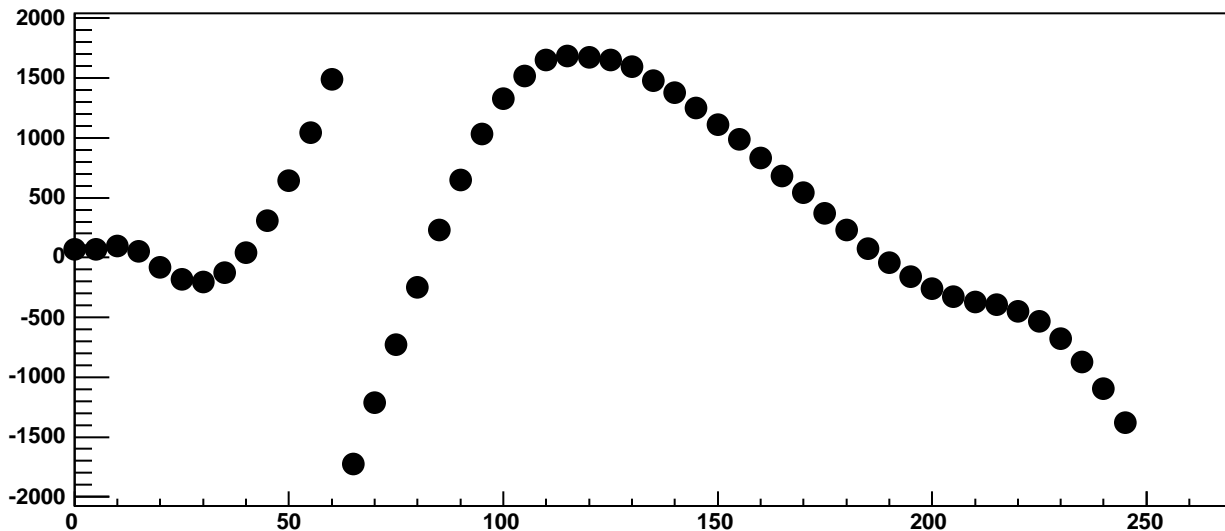


χ^2 / ndf	5.792e+05 / 41
p0	-188 ± 0.1528
p1	64.89 ± 0.05577
p2	3667 ± 0.7918
p3	-4.821 ± 6.962e-07
p4	-2.741 ± 0.003797

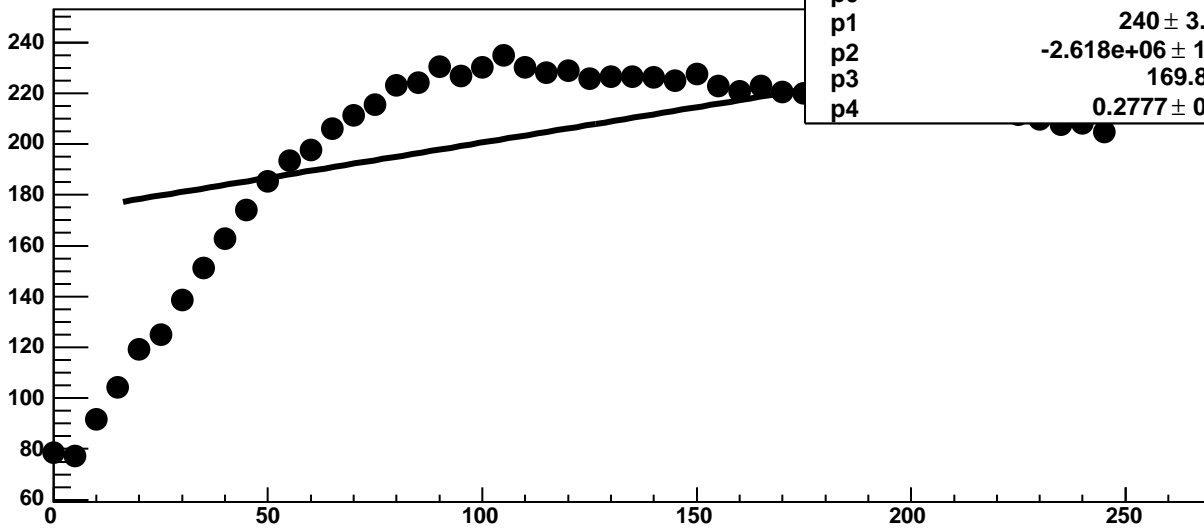
Chip 10, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold

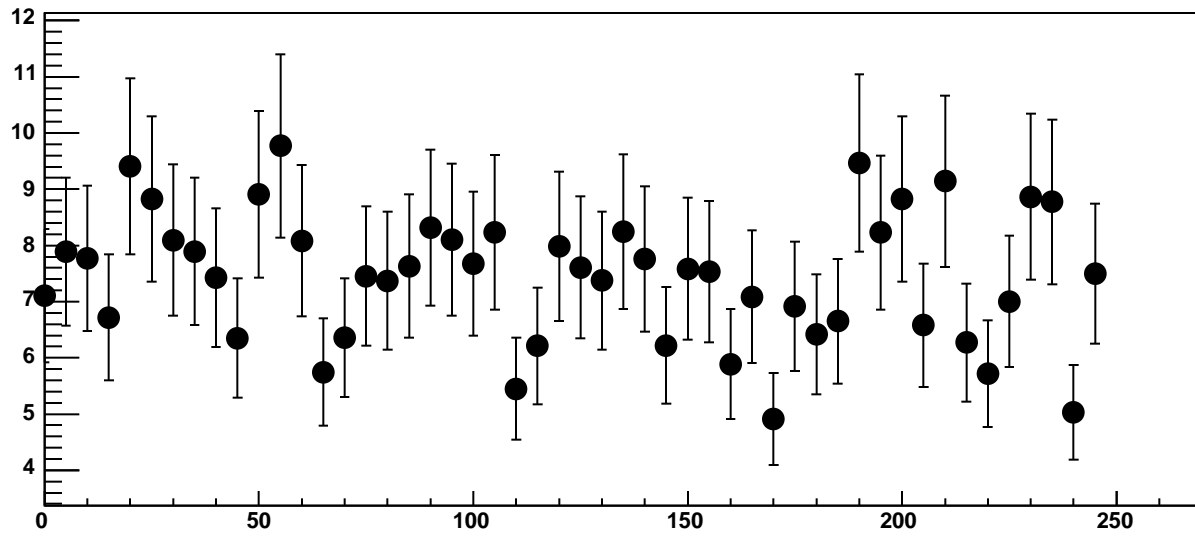


Chip 10, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold

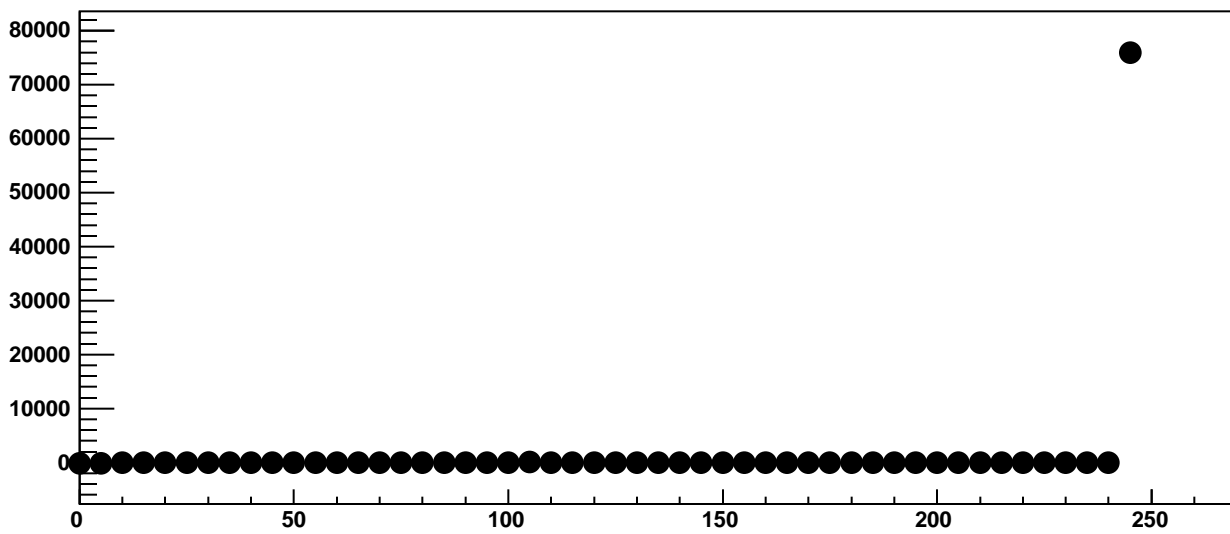


χ^2 / ndf	9550 / 41
p0	239.4 ± 0.5031
p1	$240 \pm 3.564e-05$
p2	$-2.618e+06 \pm 1.13e+05$
p3	169.8 ± 2.609
p4	0.2777 ± 0.003903

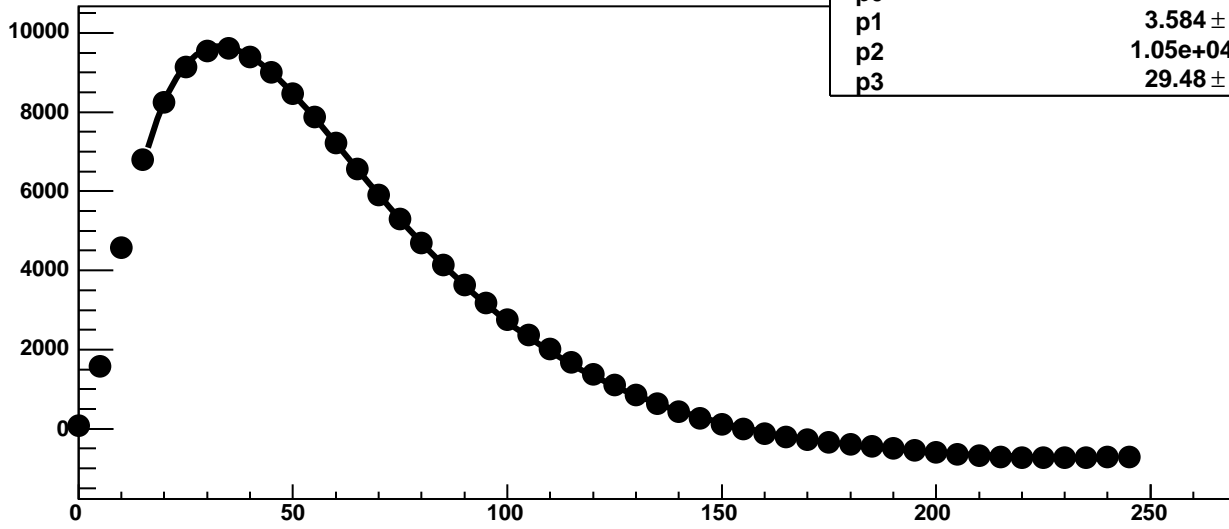
Chip 10, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold

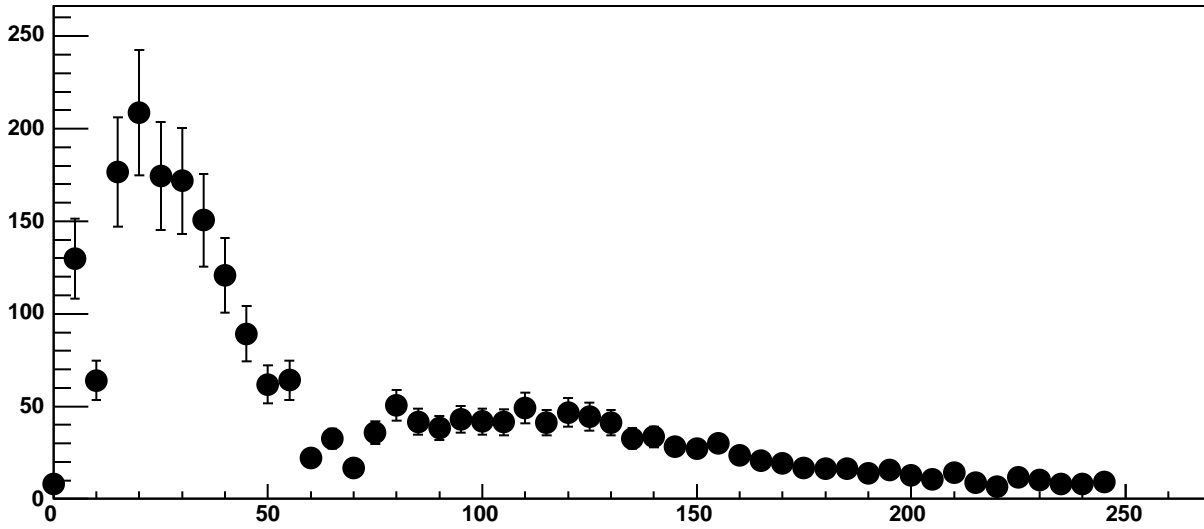


Chip 10, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold

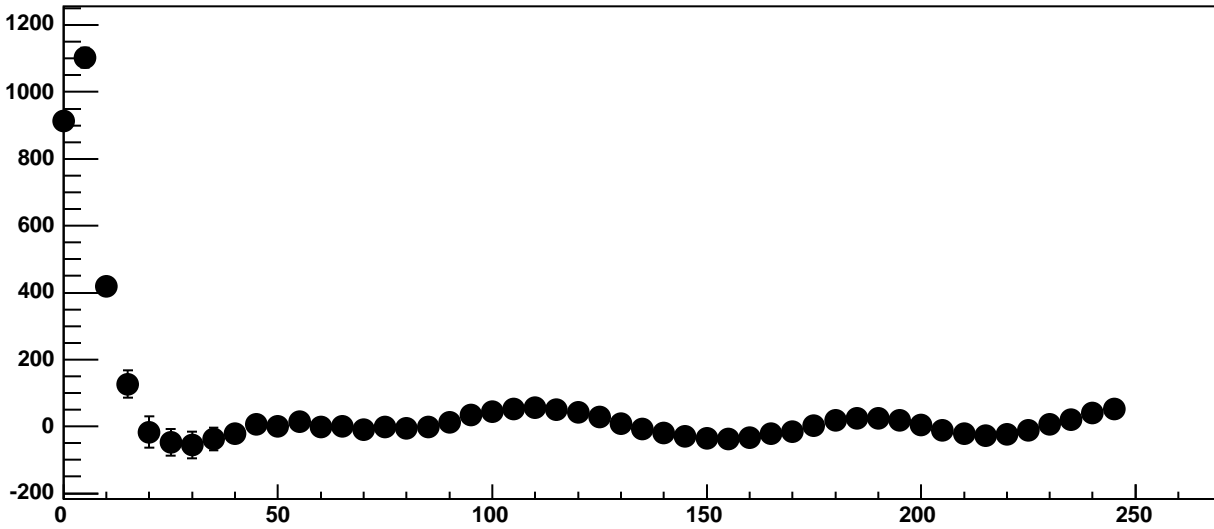


χ^2 / ndf	1622 / 42
p0	-835.4 \pm 0.957
p1	3.584 \pm 0.06794
p2	1.05e+04 \pm 8.826
p3	29.48 \pm 0.02376

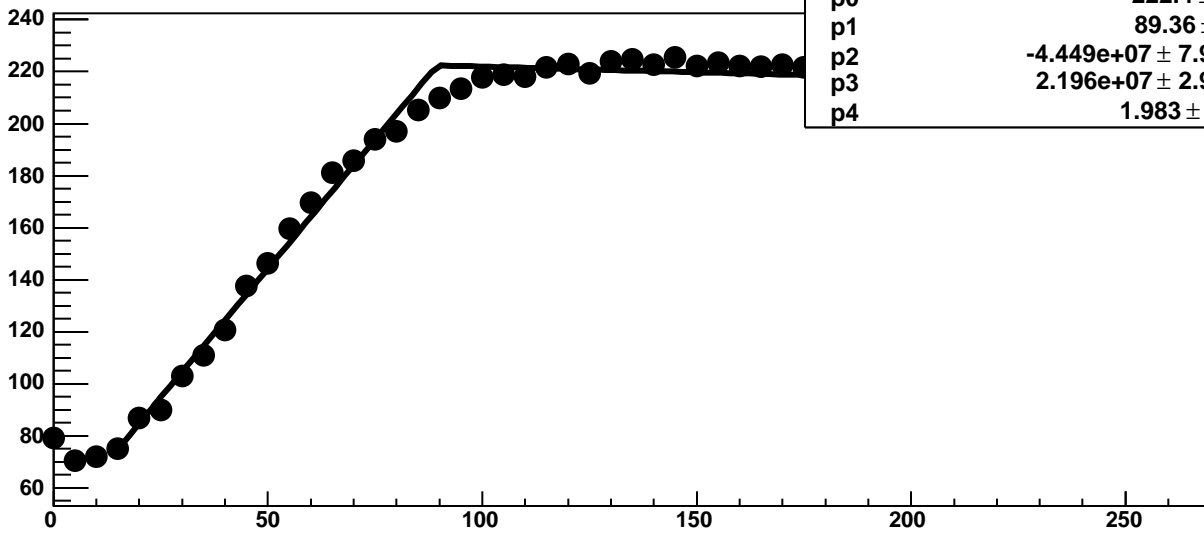
Chip 10, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold

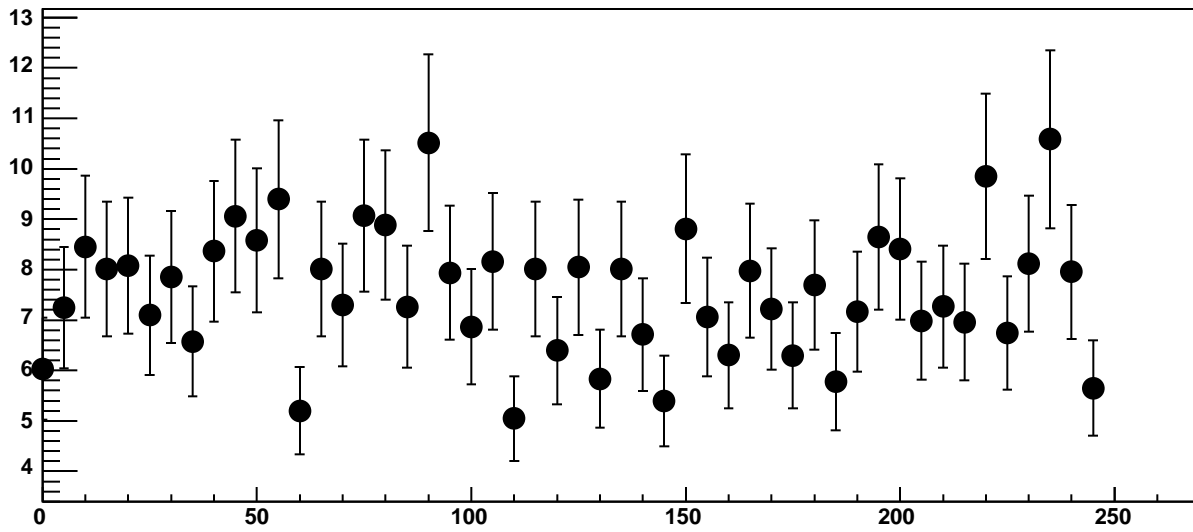


Chip 10, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

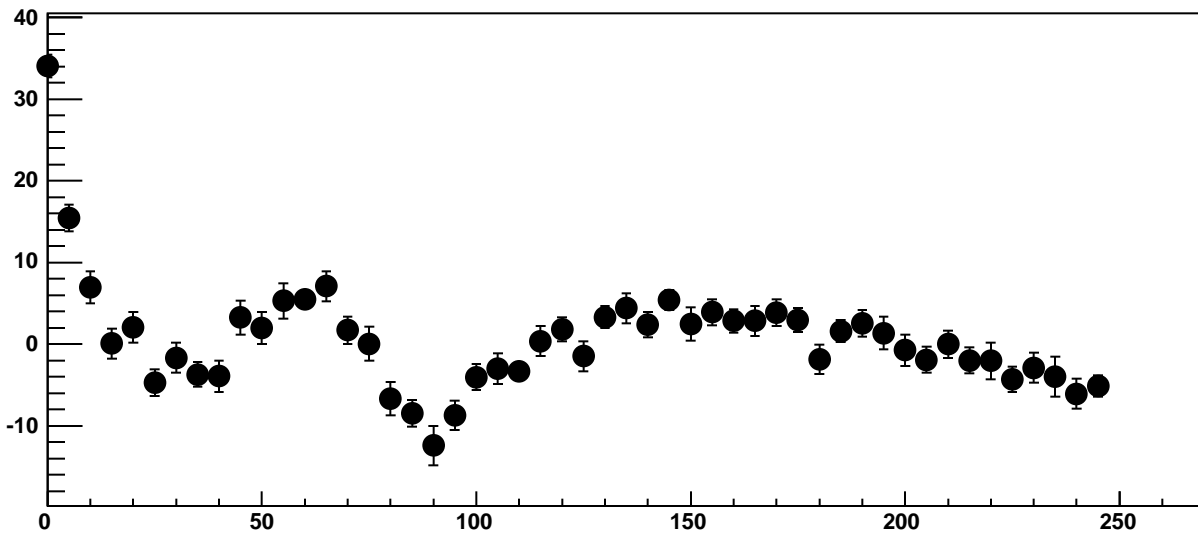


χ^2 / ndf	263.4 / 41
p0	222.4 ± 0.5746
p1	89.36 ± 0.5639
p2	$-4.449\text{e}+07 \pm 7.901\text{e}+05$
p3	$2.196\text{e}+07 \pm 2.973\text{e}+05$
p4	1.983 ± 0.02192

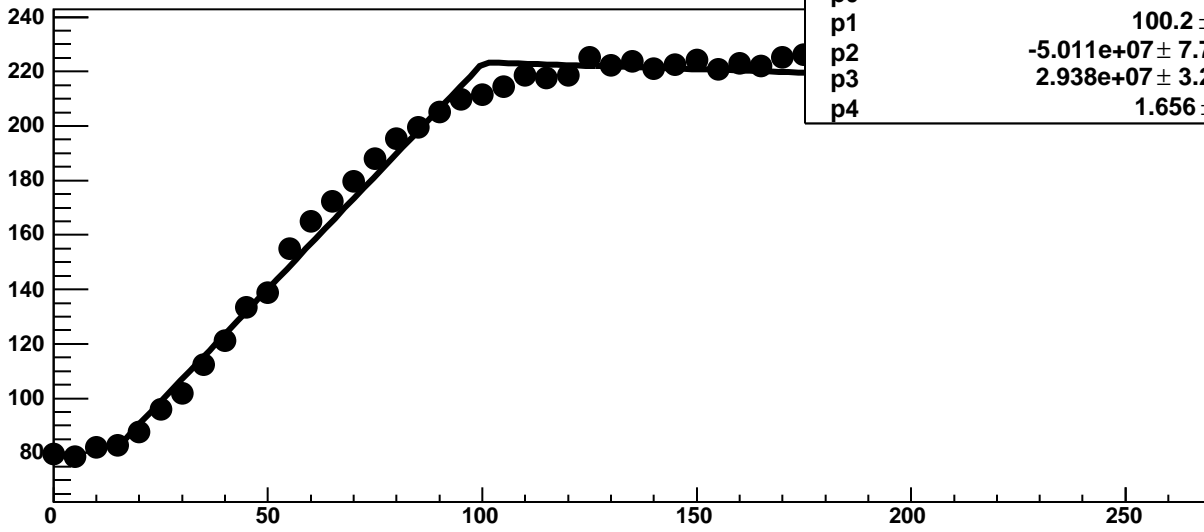
Chip 10, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

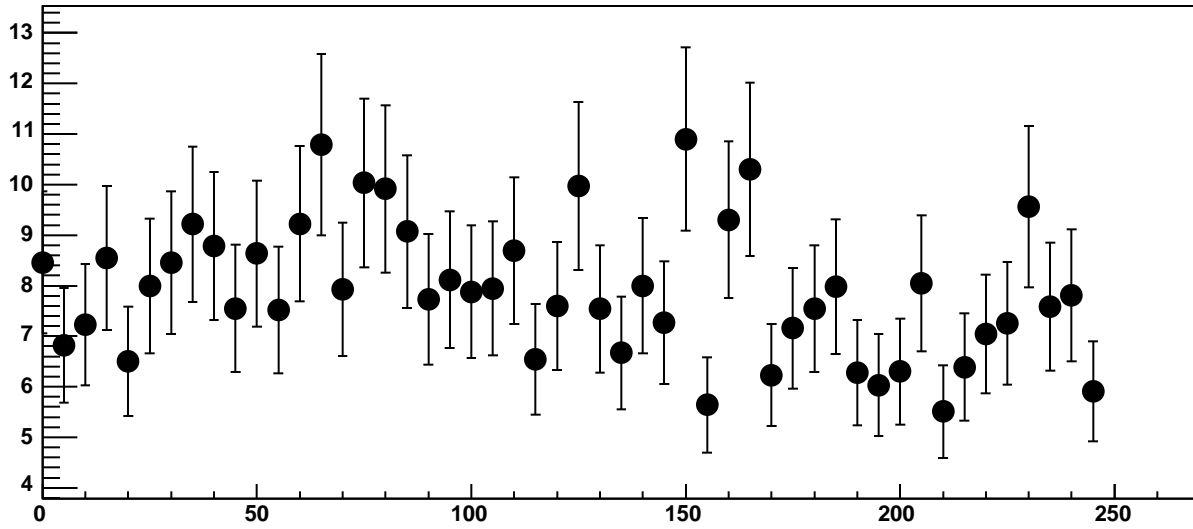


Chip 10, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

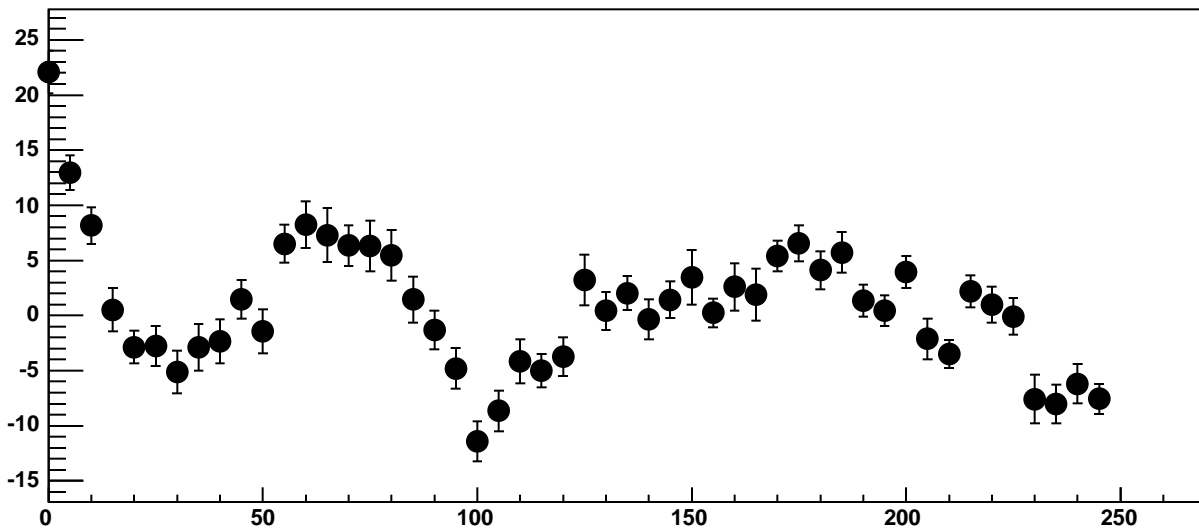


χ^2 / ndf	291.1 / 41
p0	223.3 ± 0.6636
p1	100.2 ± 0.6486
p2	$-5.011\text{e}+07 \pm 7.792\text{e}+05$
p3	$2.938\text{e}+07 \pm 3.204\text{e}+05$
p4	1.656 ± 0.0168

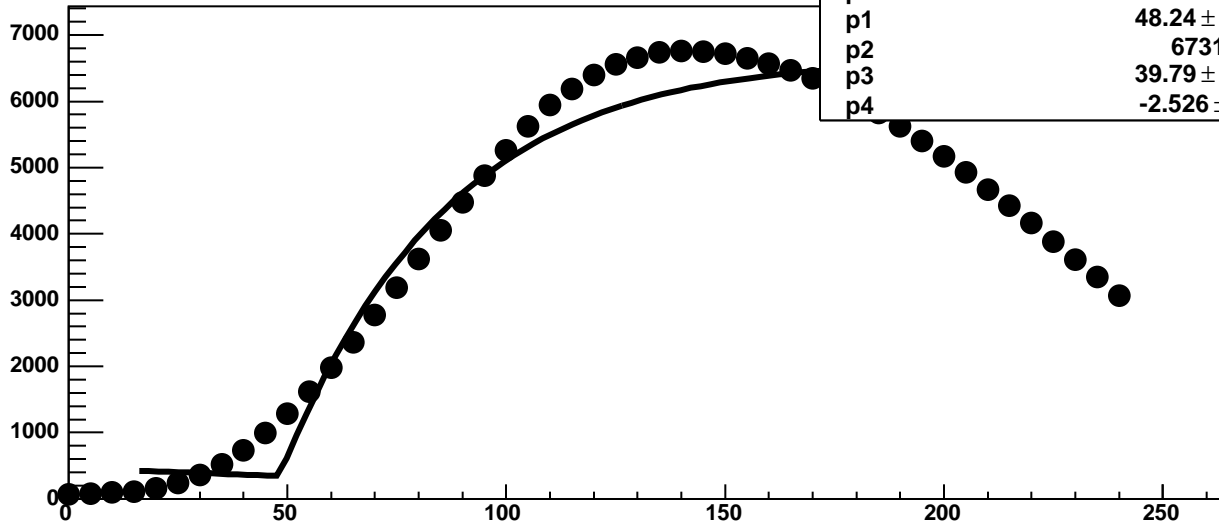
Chip 10, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

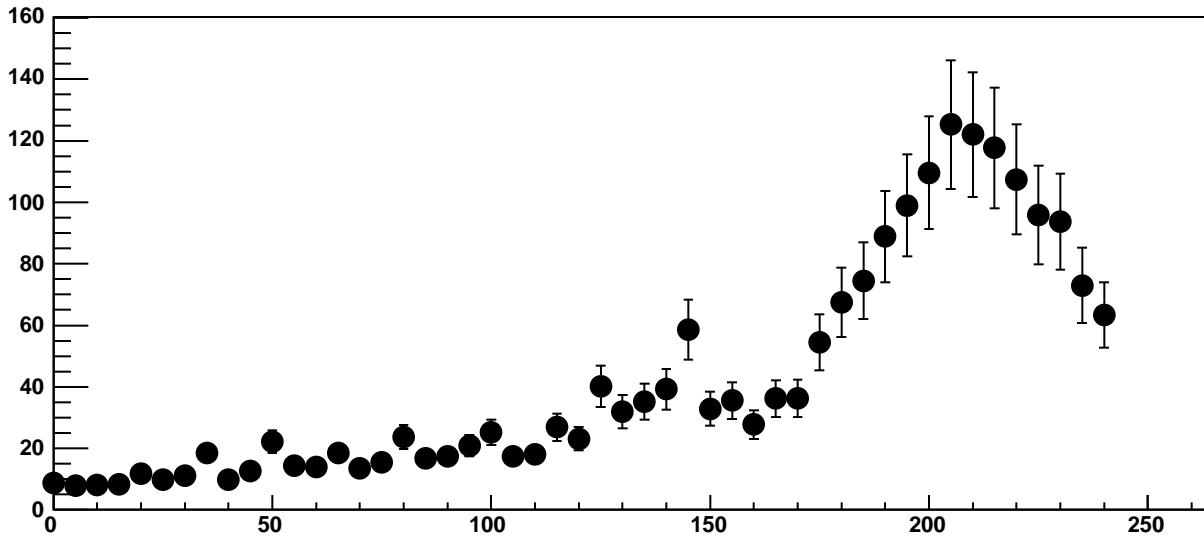


Chip 10, Channel 6, Enable 5, DAC=4000, ADC Mean vs Hold

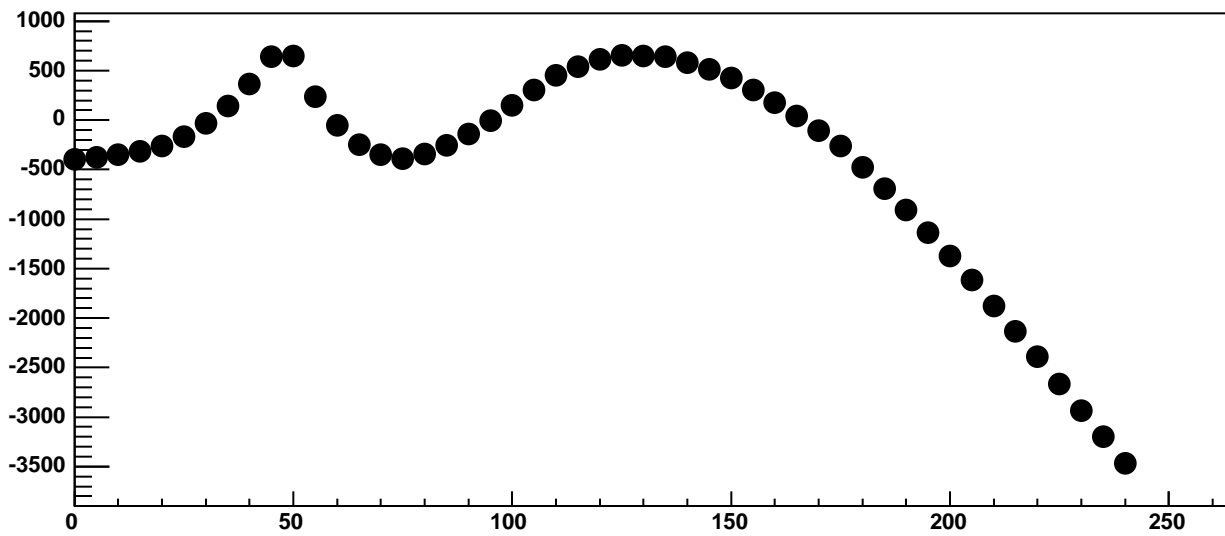


χ^2 / ndf	4.079e+05 / 41
p0	344 ± 1.56
p1	48.24 ± 0.01985
p2	6731 ± 10.28
p3	39.79 ± 0.06646
p4	-2.526 ± 0.0626

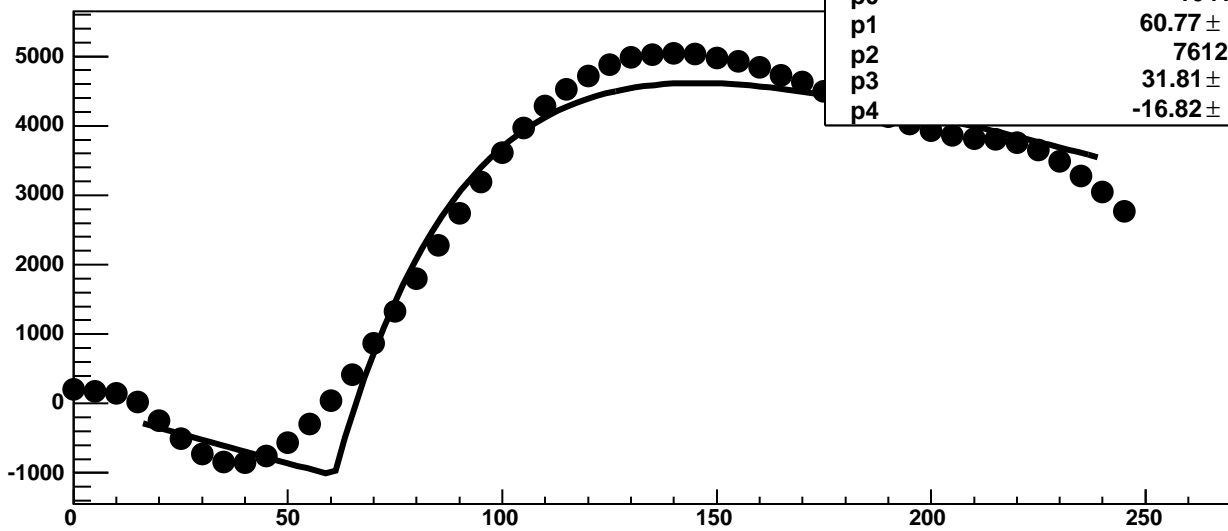
Chip 10, Channel 6, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 6, Enable 5, DAC=4000, ADC Residuals vs Hold

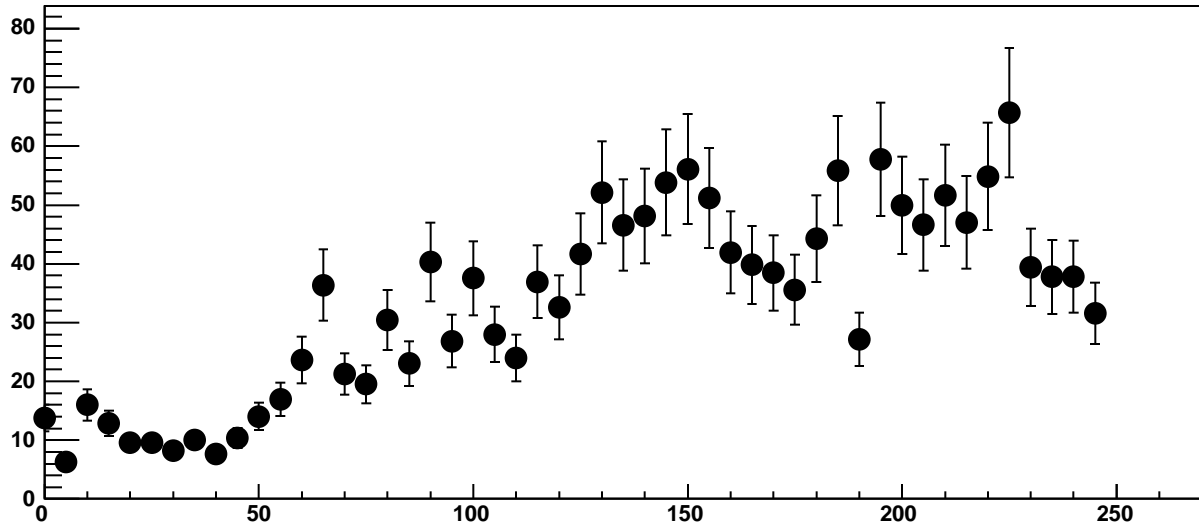


Chip 10, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

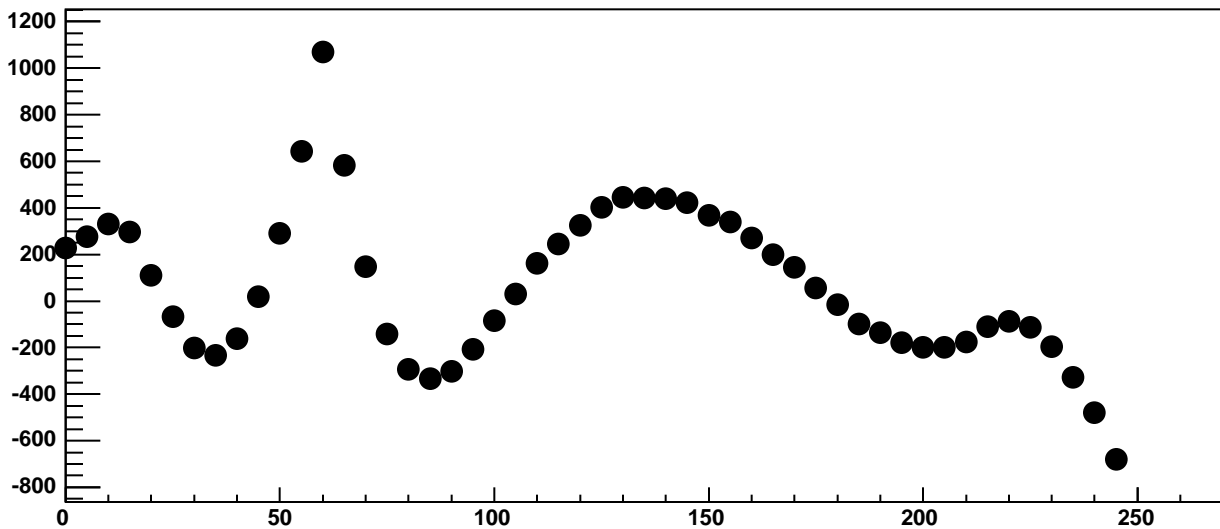


χ^2 / ndf	1.546e+05 / 41
p0	-1041 ± 1.519
p1	60.77 ± 0.02743
p2	7612 ± 9.747
p3	31.81 ± 0.06605
p4	-16.82 ± 0.05377

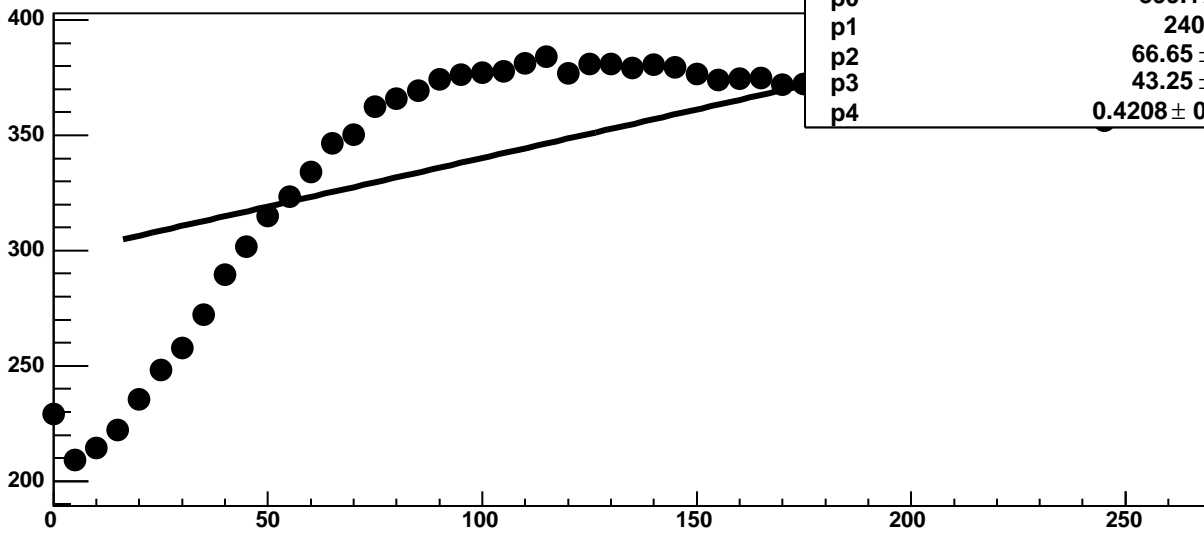
Chip 10, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

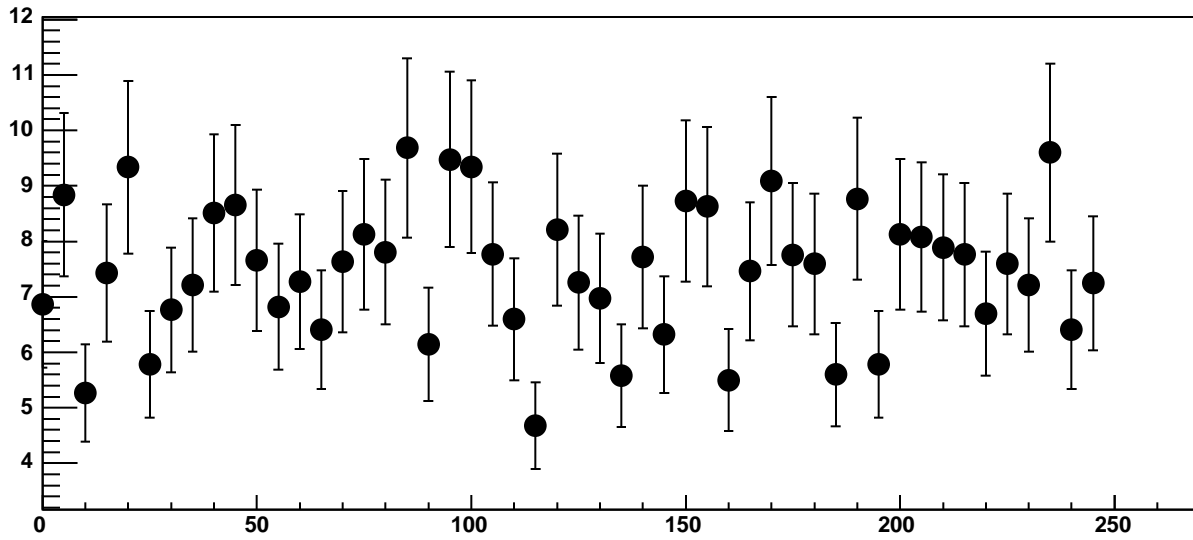


Chip 10, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold

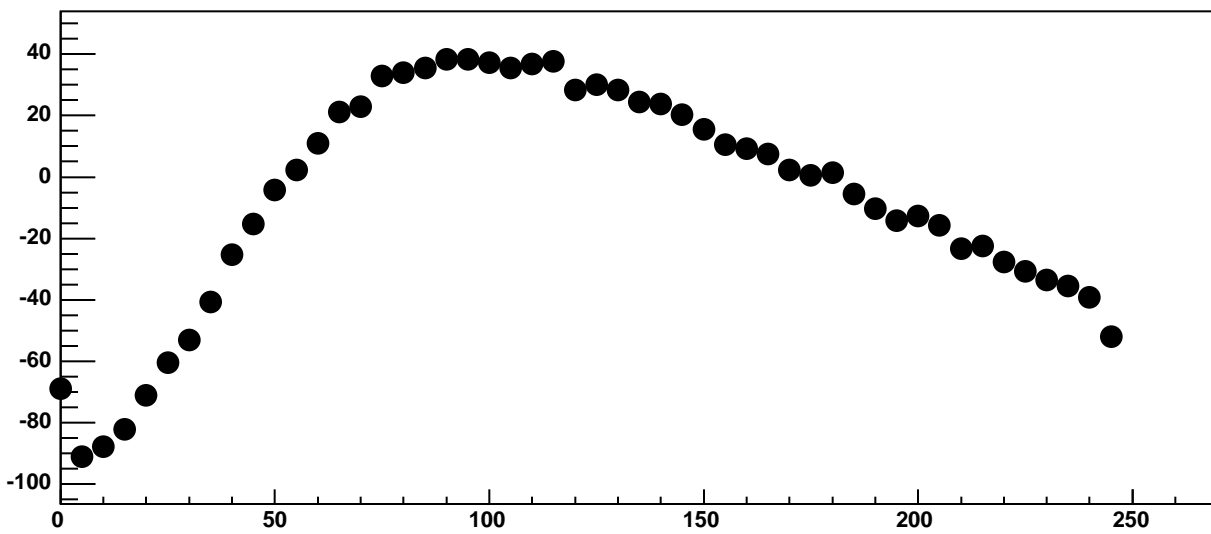


χ^2 / ndf	1.682e+04 / 41
p0	399.1 ± 0.5011
p1	240.1 ± 0.3
p2	66.65 ± 0.8124
p3	43.25 ± 0.1416
p4	0.4208 ± 0.003727

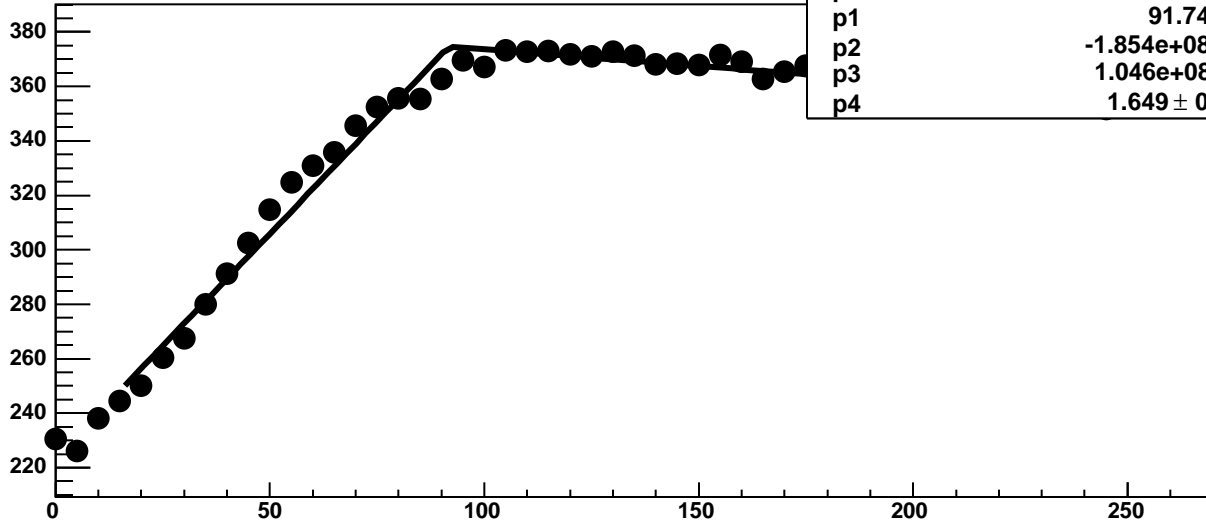
Chip 10, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

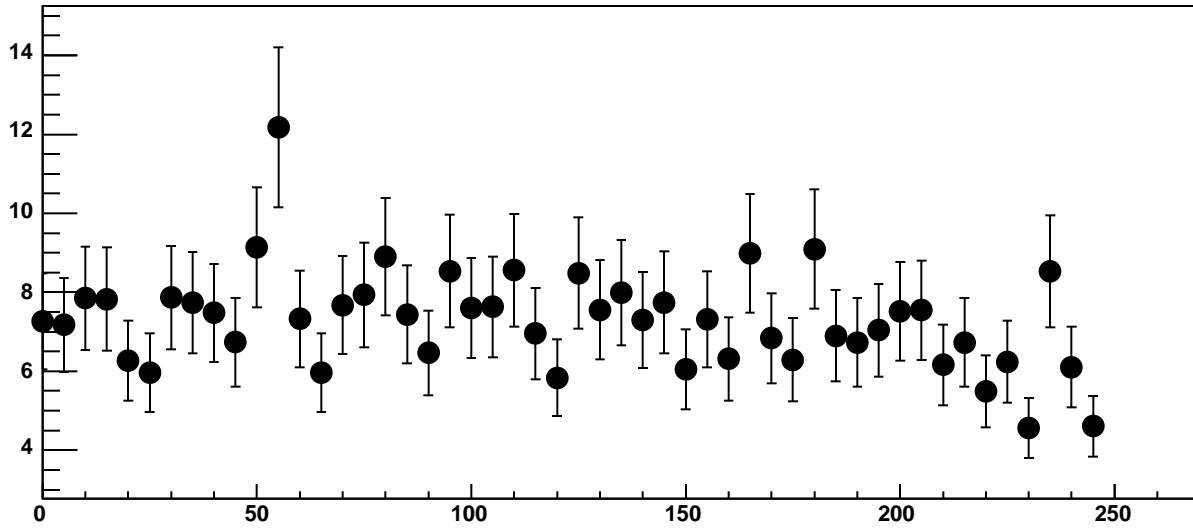


Chip 10, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold

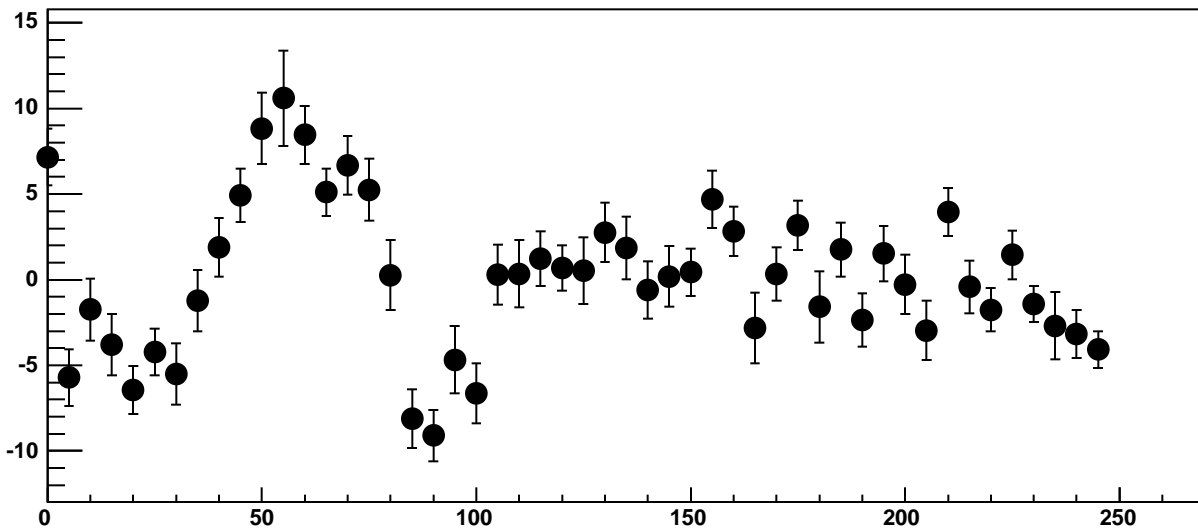


χ^2 / ndf	282.4 / 41
p0	374.7 ± 0.5284
p1	91.74 ± 0.512
p2	$-1.854\text{e}+08 \pm 1.414$
p3	$1.046\text{e}+08 \pm 1.414$
p4	1.649 ± 0.006185

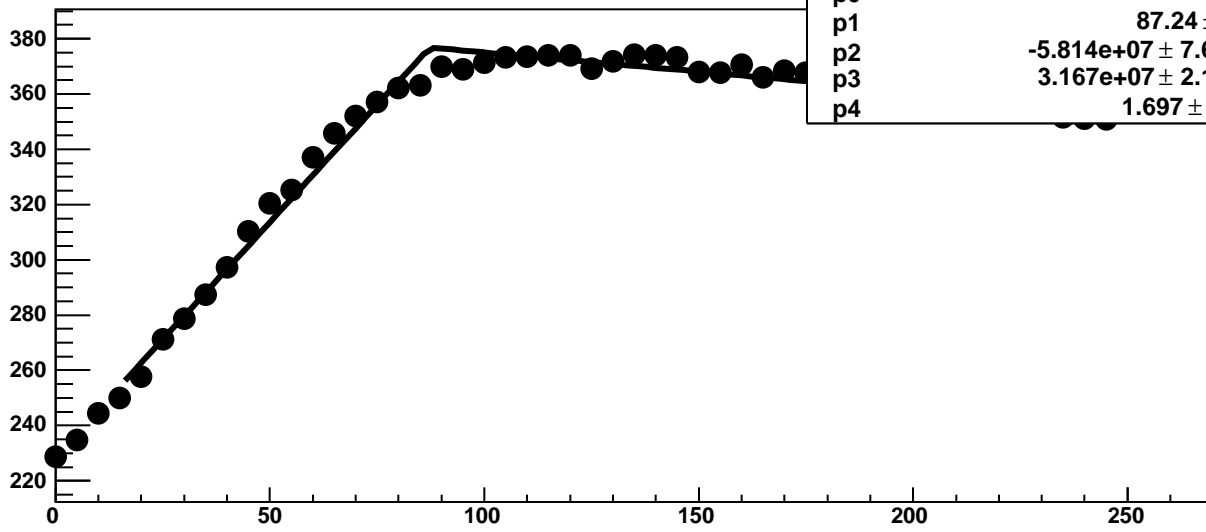
Chip 10, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold

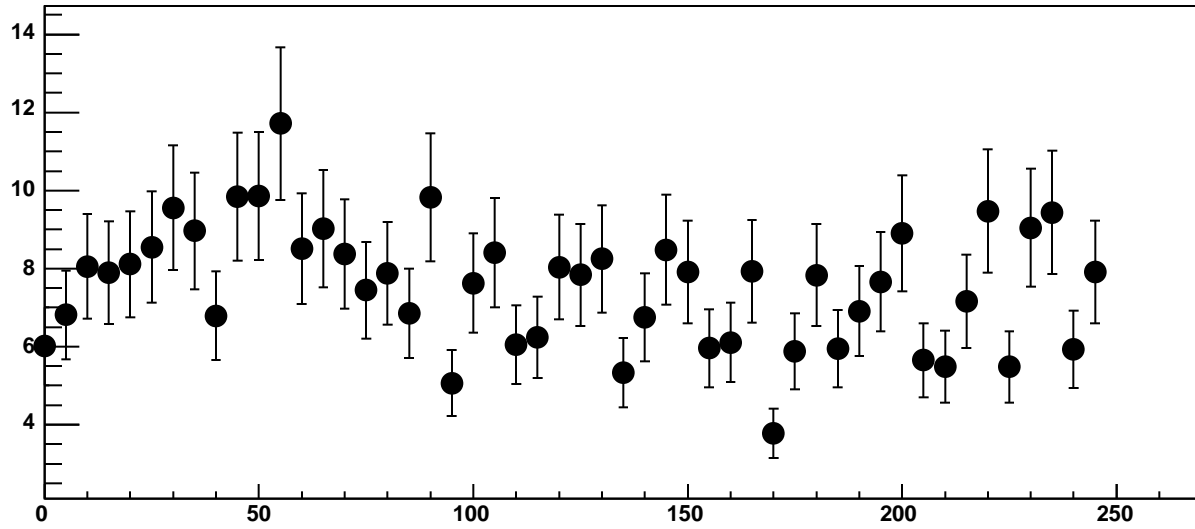


Chip 10, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

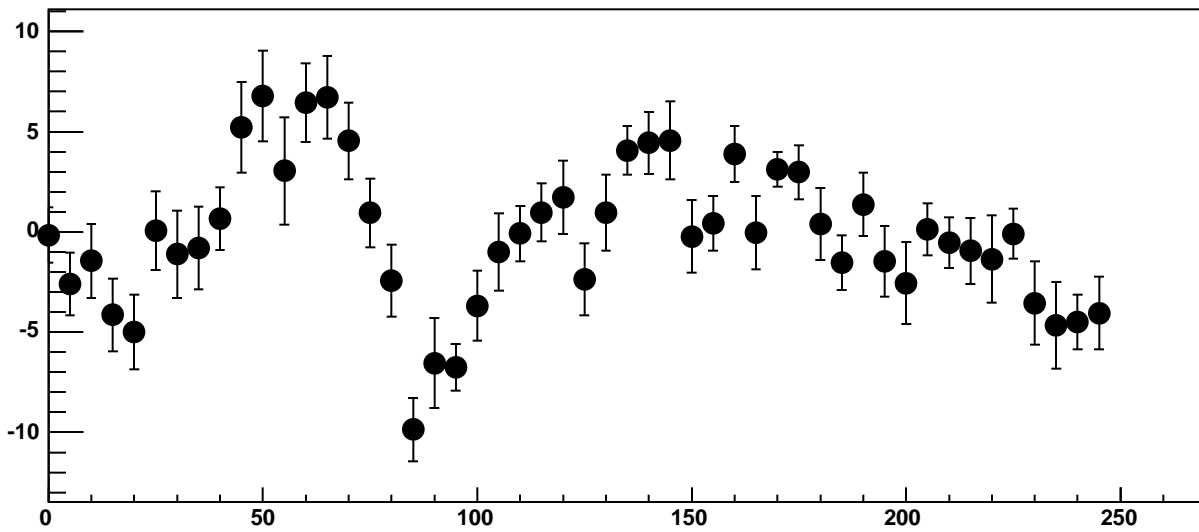


χ^2 / ndf	221.4 / 41
p0	376.8 ± 0.5237
p1	87.24 ± 0.5813
p2	$-5.814\text{e}+07 \pm 7.616\text{e}+05$
p3	$3.167\text{e}+07 \pm 2.104\text{e}+05$
p4	1.697 ± 0.02195

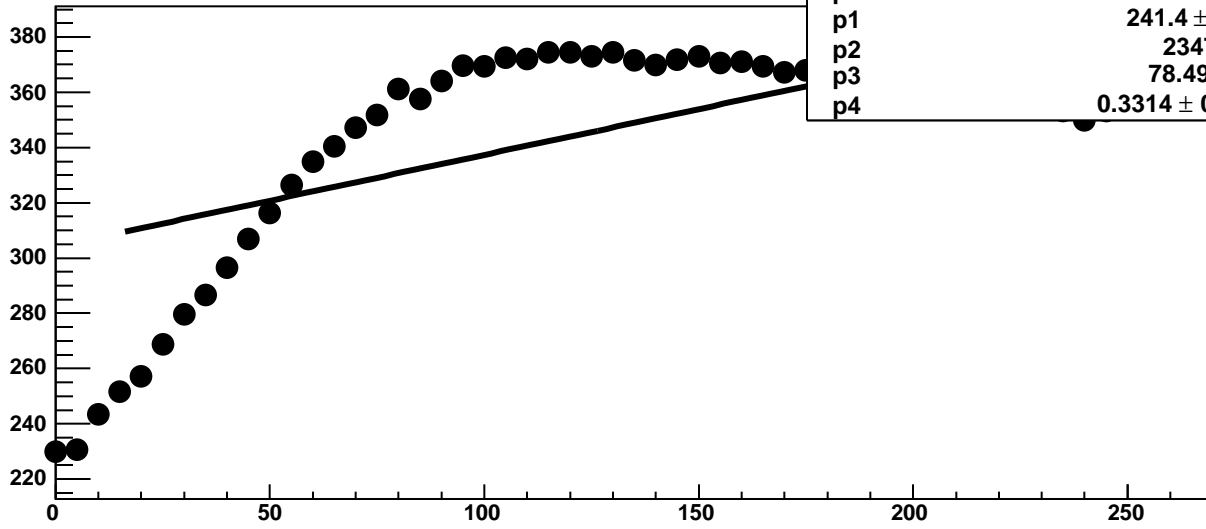
Chip 10, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

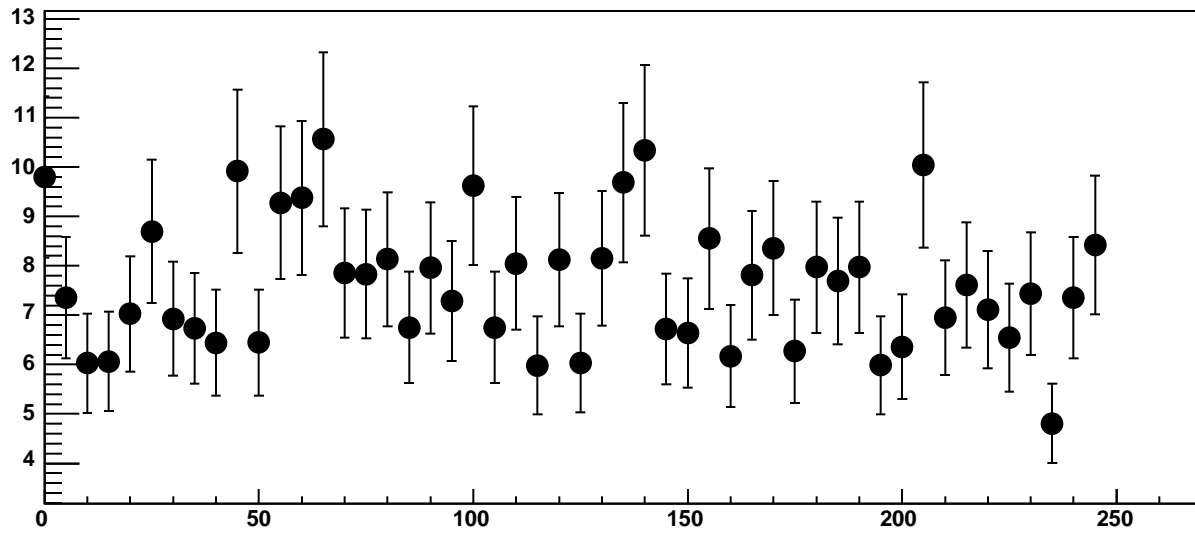


Chip 10, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

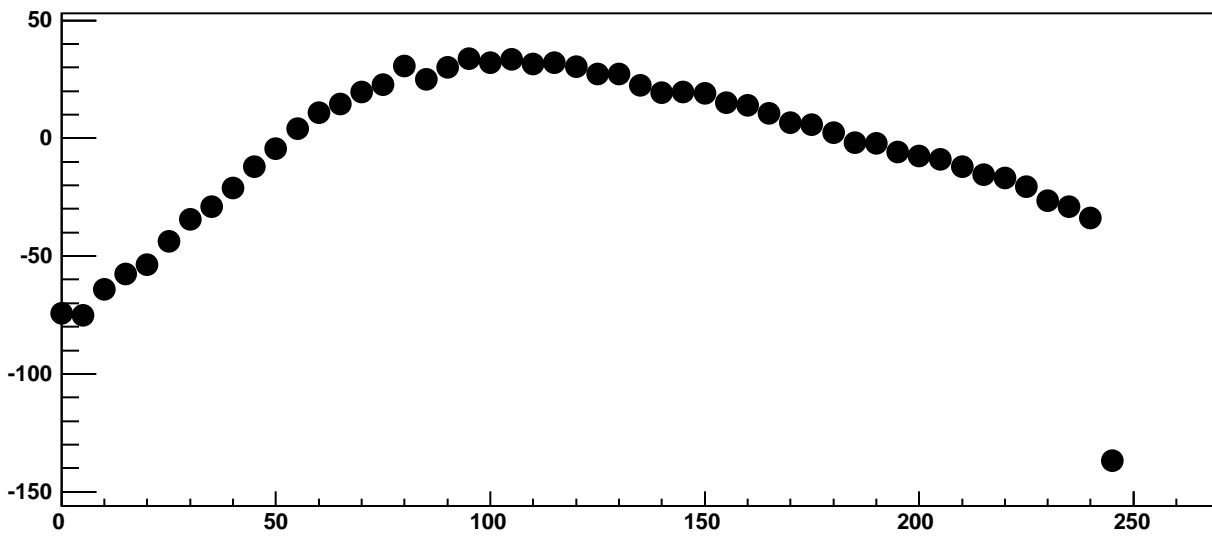


χ^2 / ndf	1.083e+04 / 41
p0	384.2 ± 0.4637
p1	241.4 ± 0.07111
p2	2347 ± 20.7
p3	78.49 ± 1.357
p4	0.3314 ± 0.003581

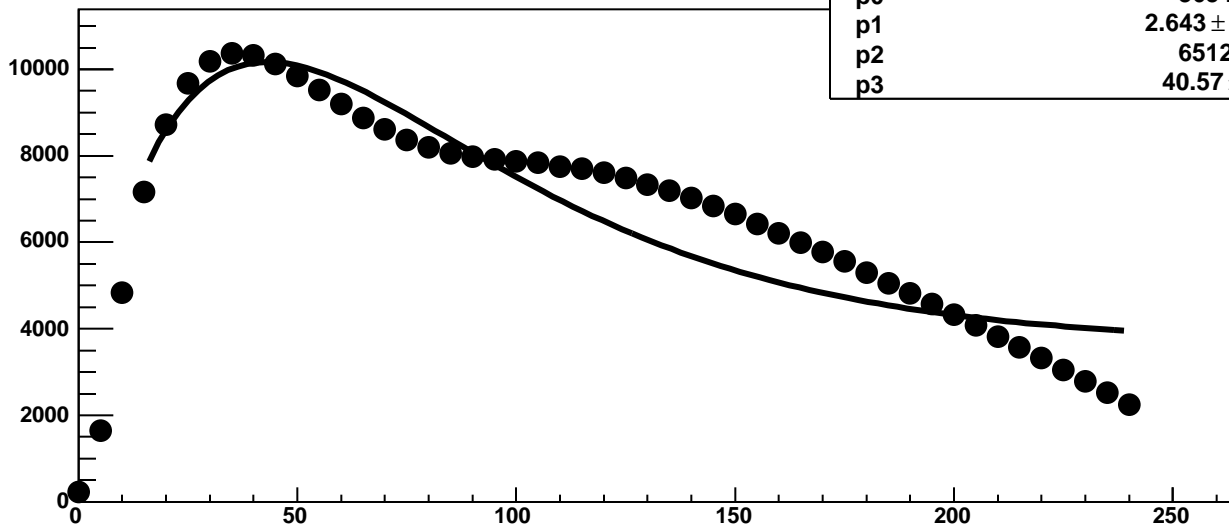
Chip 10, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

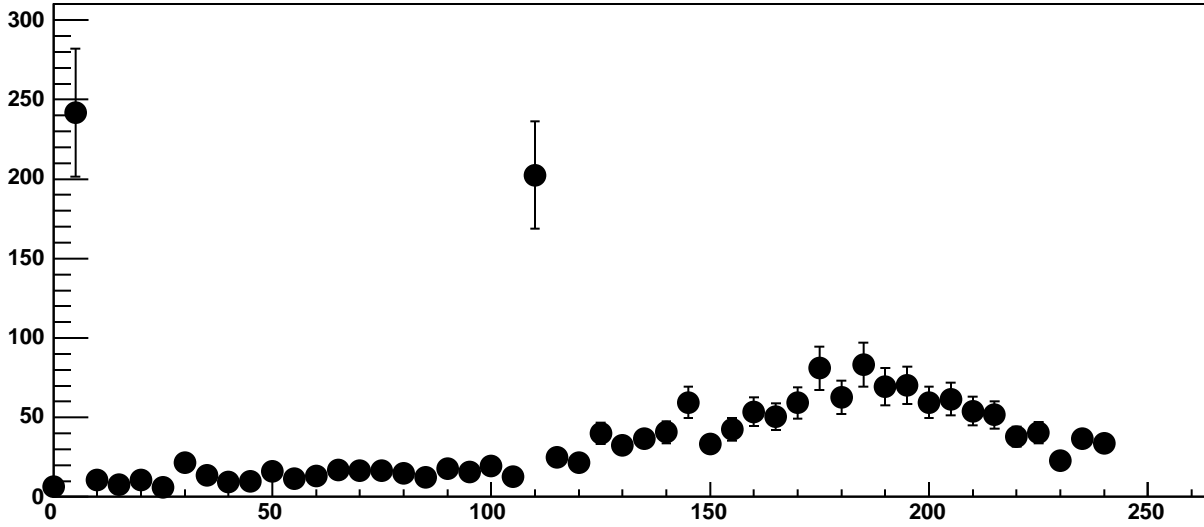


Chip 10, Channel 7, Enable 5!, DAC=4000, ADC Mean vs Hold

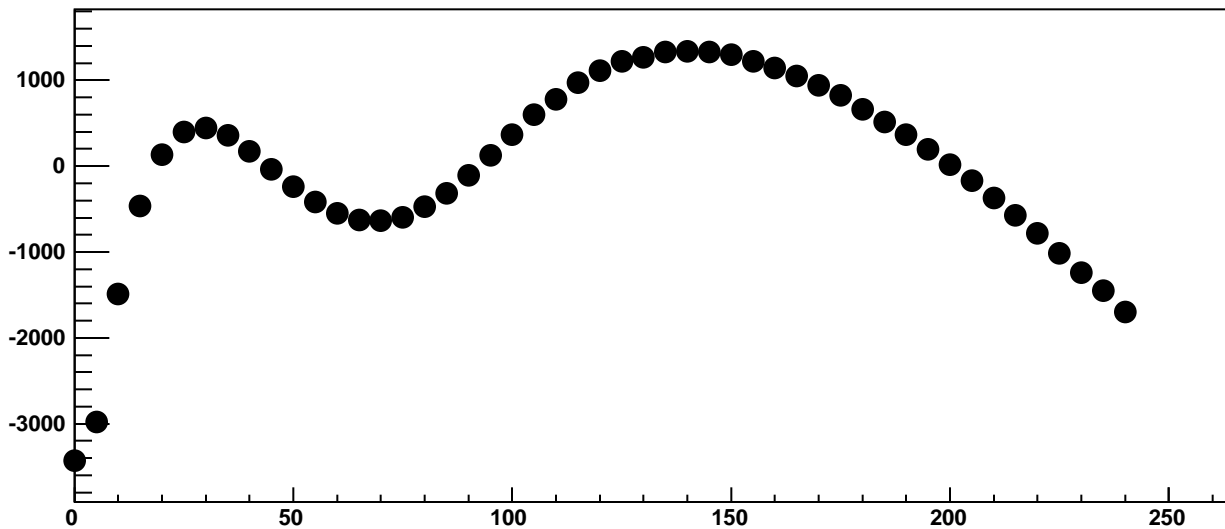


χ^2 / ndf	8.072e+05 / 42
p0	3654 \pm 4.104
p1	2.643 \pm 0.02143
p2	6512 \pm 3.914
p3	40.57 \pm 0.0291

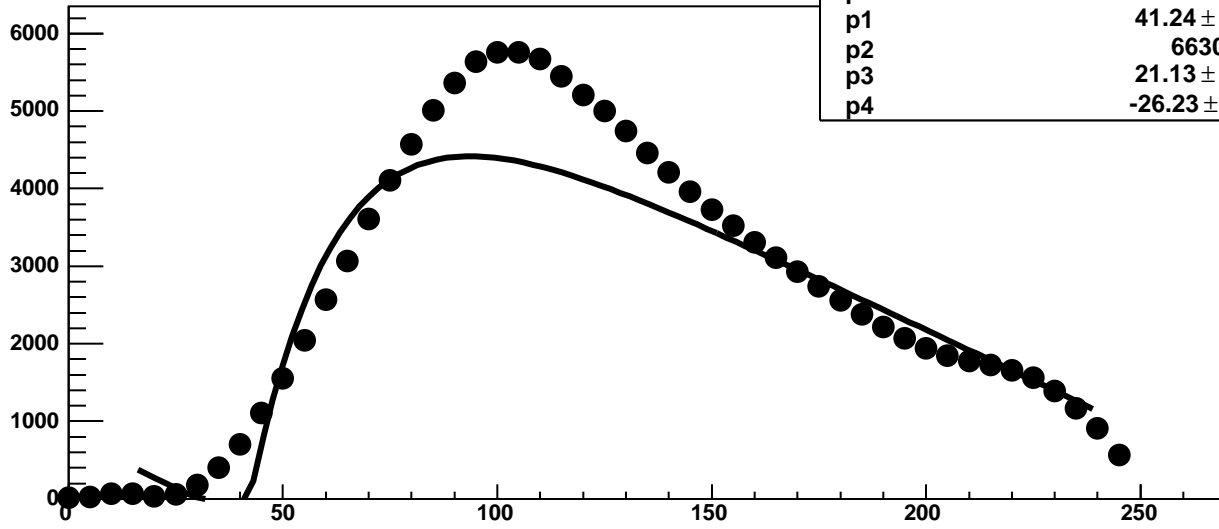
Chip 10, Channel 7, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 7, Enable 5!, DAC=4000, ADC Residuals vs Hold

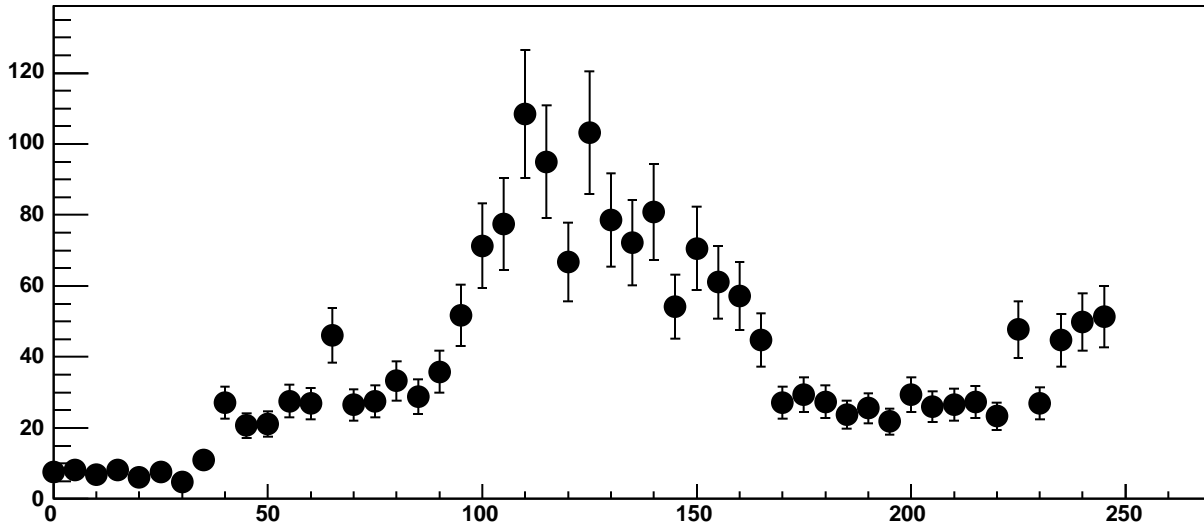


Chip 10, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

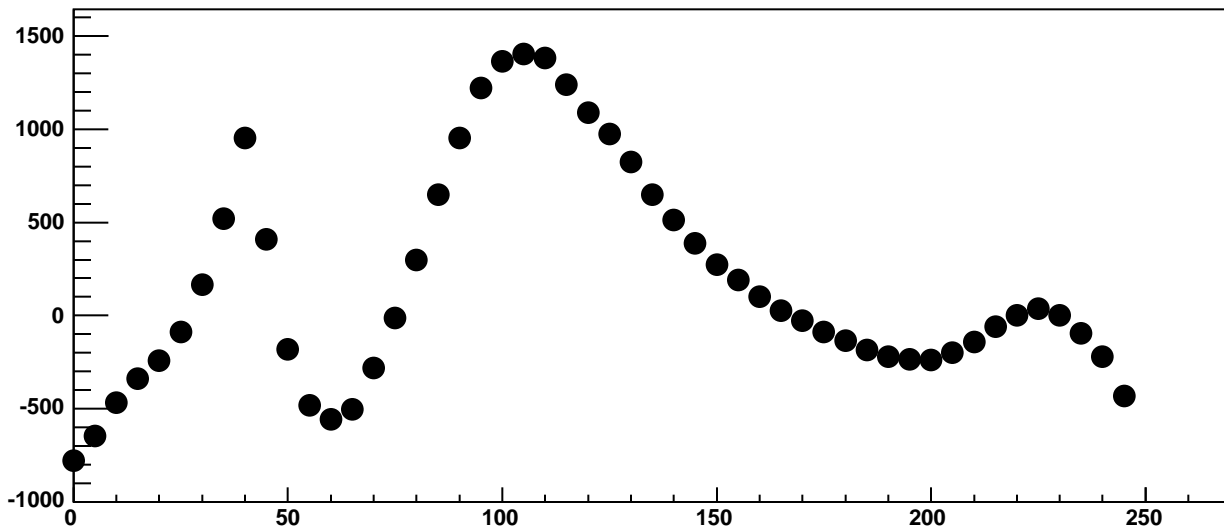


χ^2 / ndf	2.542e+05 / 41
p0	-283.9 ± 0.9693
p1	41.24 ± 0.01844
p2	6630 ± 7.301
p3	21.13 ± 0.04356
p4	-26.23 ± 0.04281

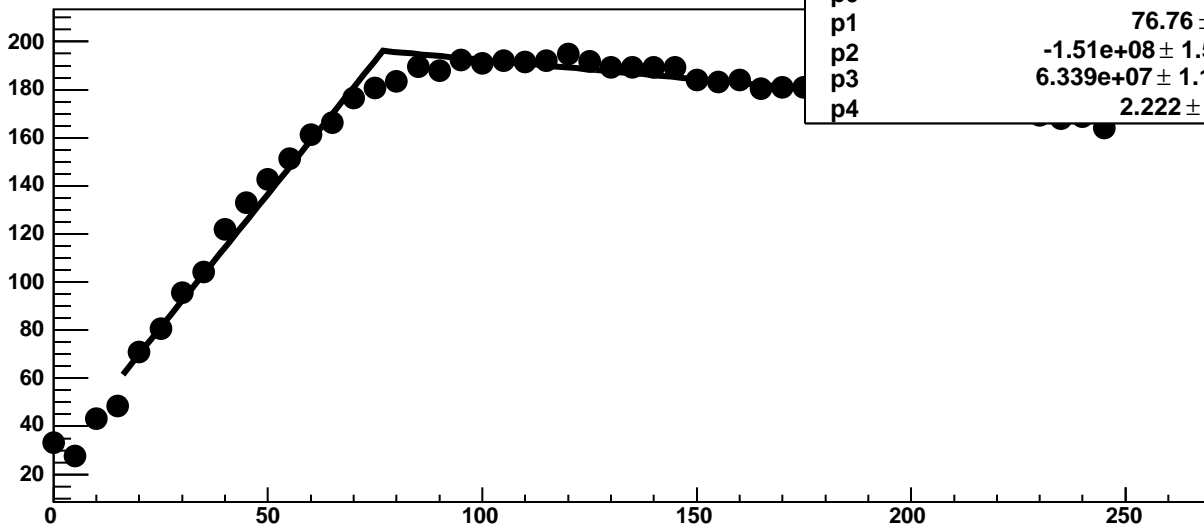
Chip 10, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

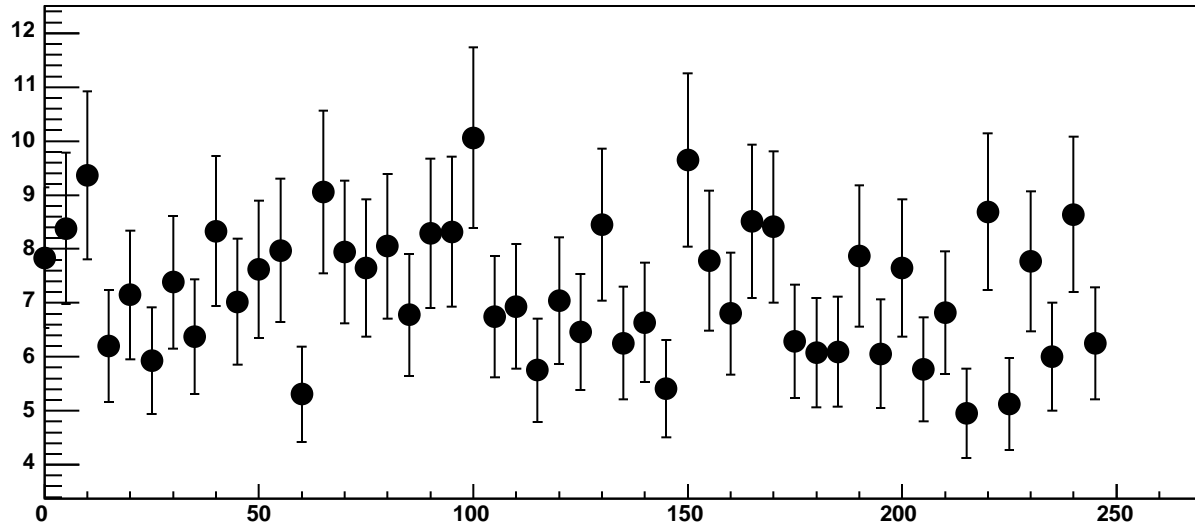


Chip 10, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

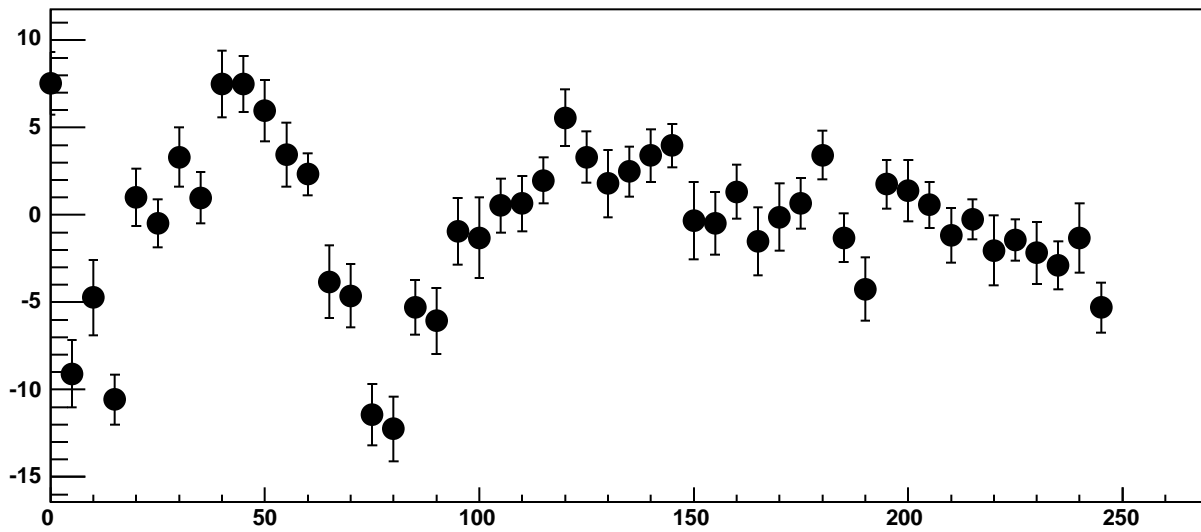


χ^2 / ndf	299.1 / 41
p0	196.1 ± 0.5452
p1	76.76 ± 0.4526
p2	$-1.51\text{e}+08 \pm 1.567\text{e}+06$
p3	$6.339\text{e}+07 \pm 1.155\text{e}+05$
p4	2.222 ± 0.02369

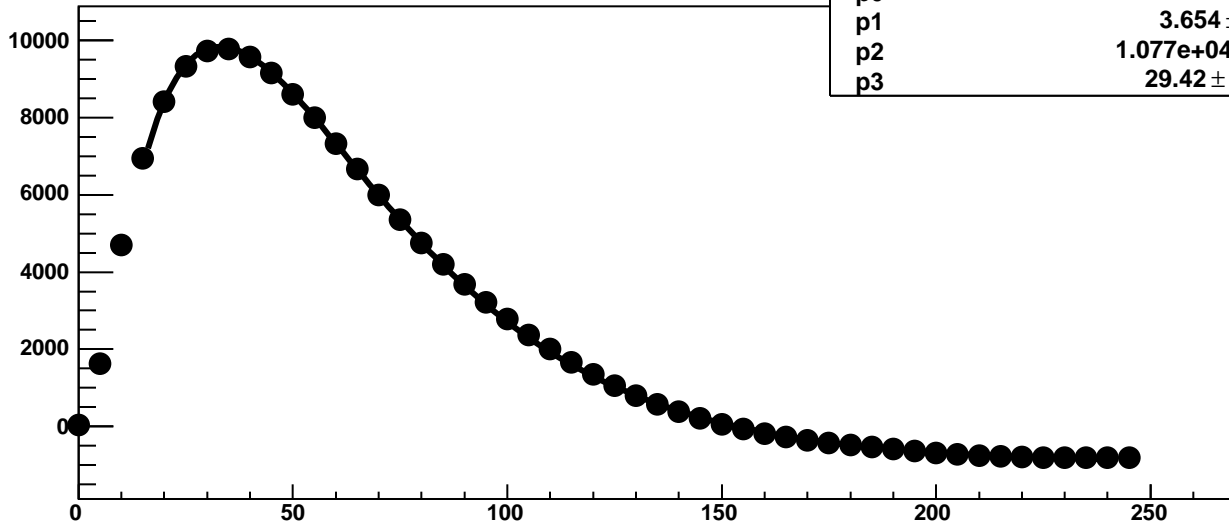
Chip 10, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

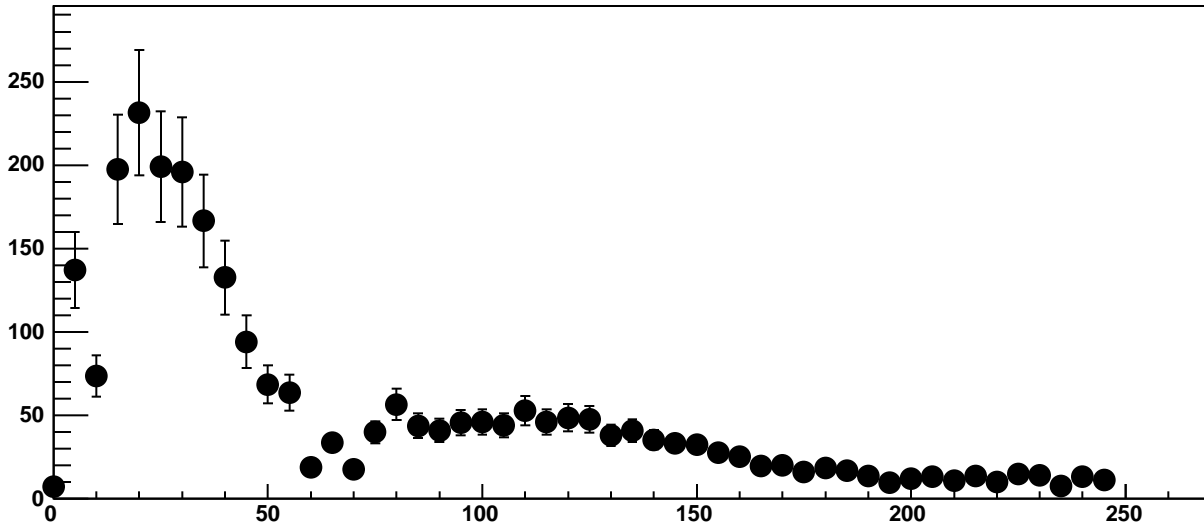


Chip 10, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold

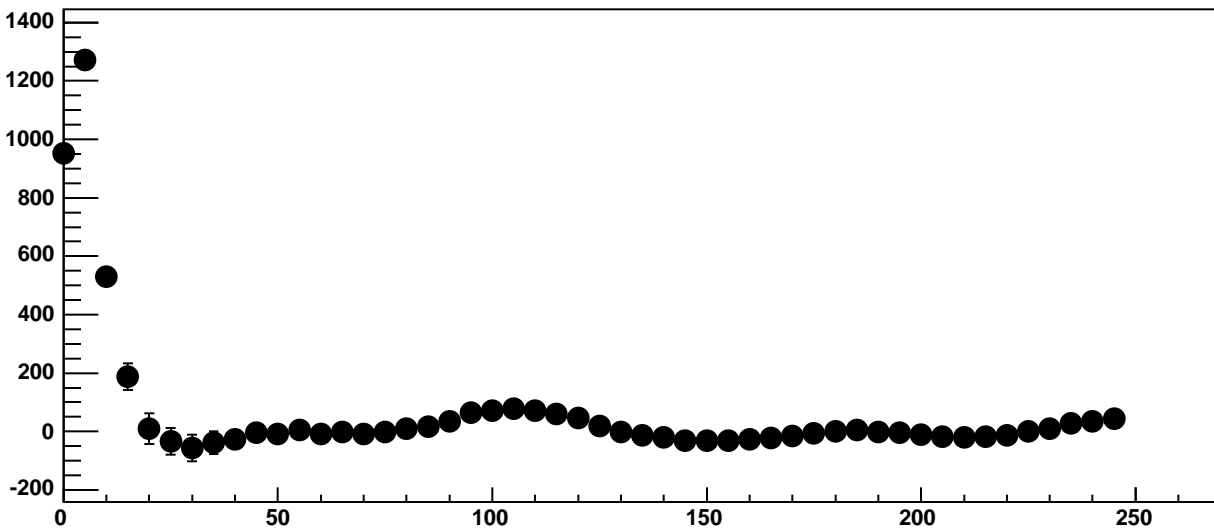


χ^2 / ndf	985 / 42
p0	-920 ± 1.129
p1	3.654 ± 0.0729
p2	$1.077\text{e}+04 \pm 9.618$
p3	29.42 ± 0.02495

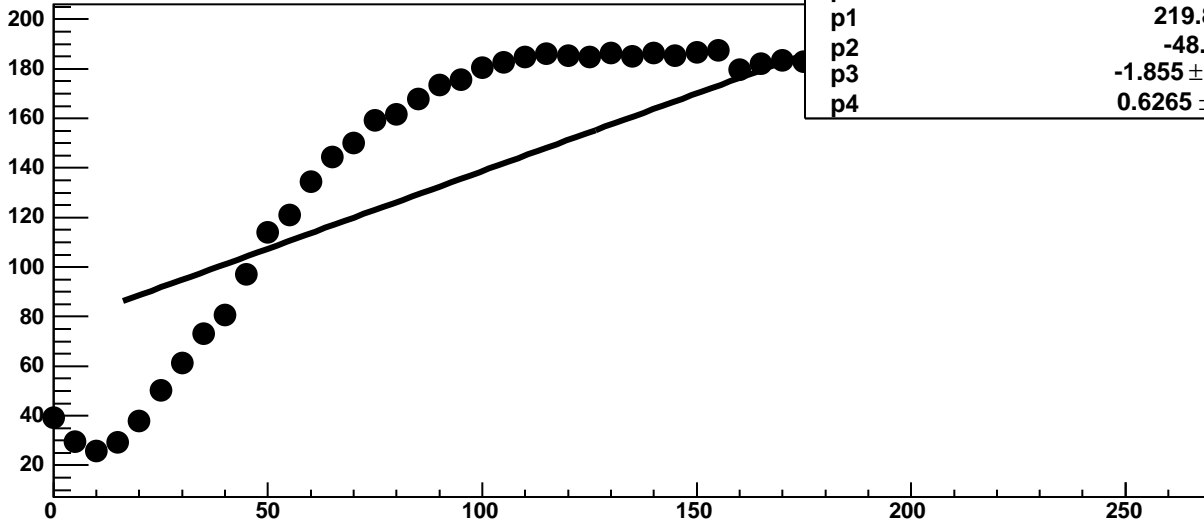
Chip 10, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold

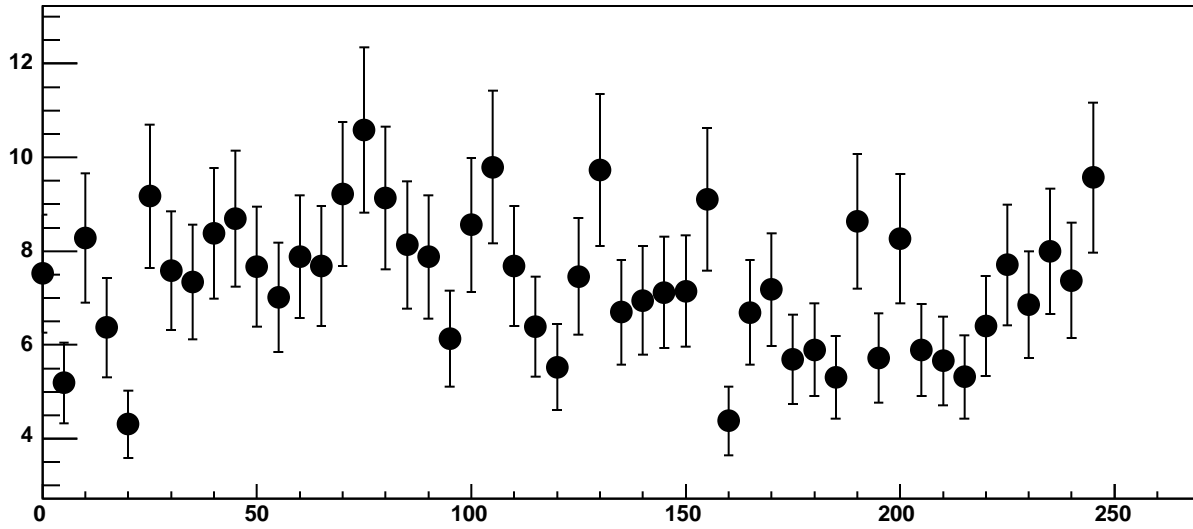


Chip 10, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

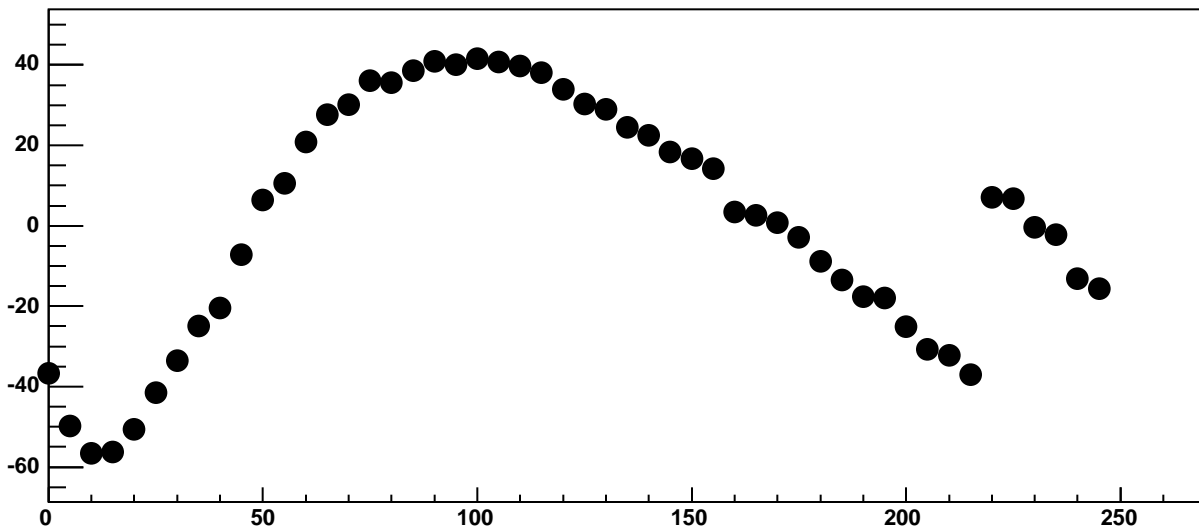


χ^2 / ndf	1.478e+04 / 41
p0	213.8 ± 1.184
p1	219.8 ± 1.33
p2	-48.1 ± 1.34
p3	-1.855 ± 0.02821
p4	0.6265 ± 0.9996

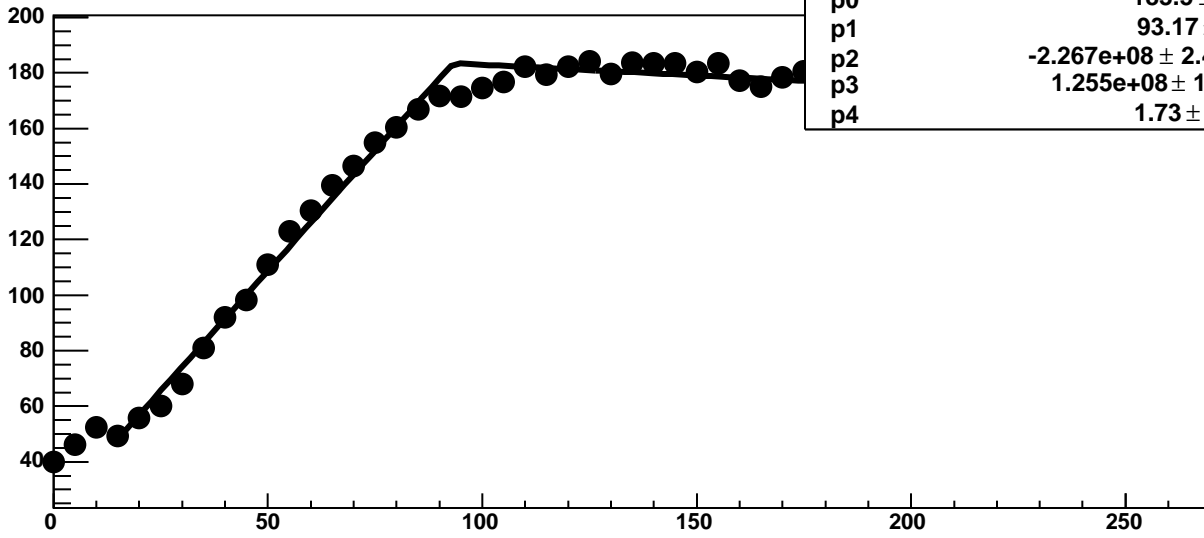
Chip 10, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

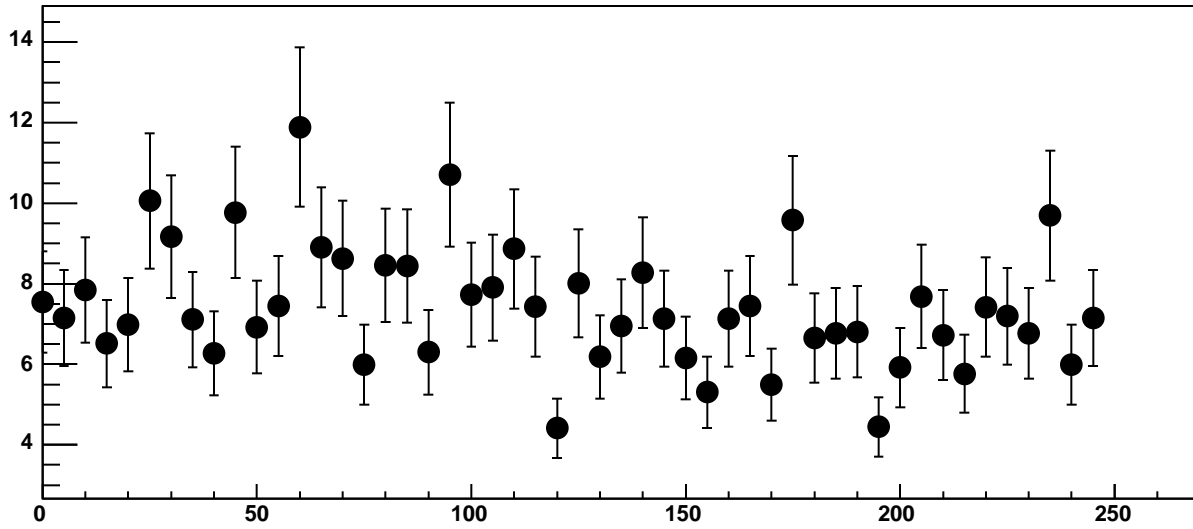


Chip 10, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

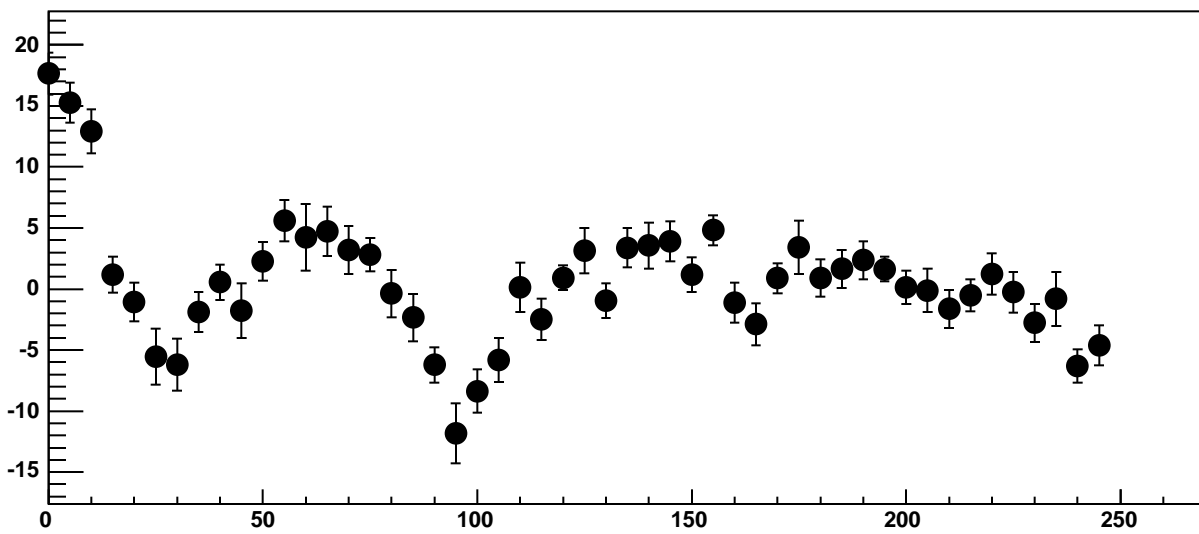


χ^2 / ndf	195.6 / 41
p0	183.5 ± 0.5675
p1	93.17 ± 0.5671
p2	$-2.267\text{e}+08 \pm 2.429\text{e}+06$
p3	$1.255\text{e}+08 \pm 1.52\text{e}+05$
p4	1.73 ± 0.01796

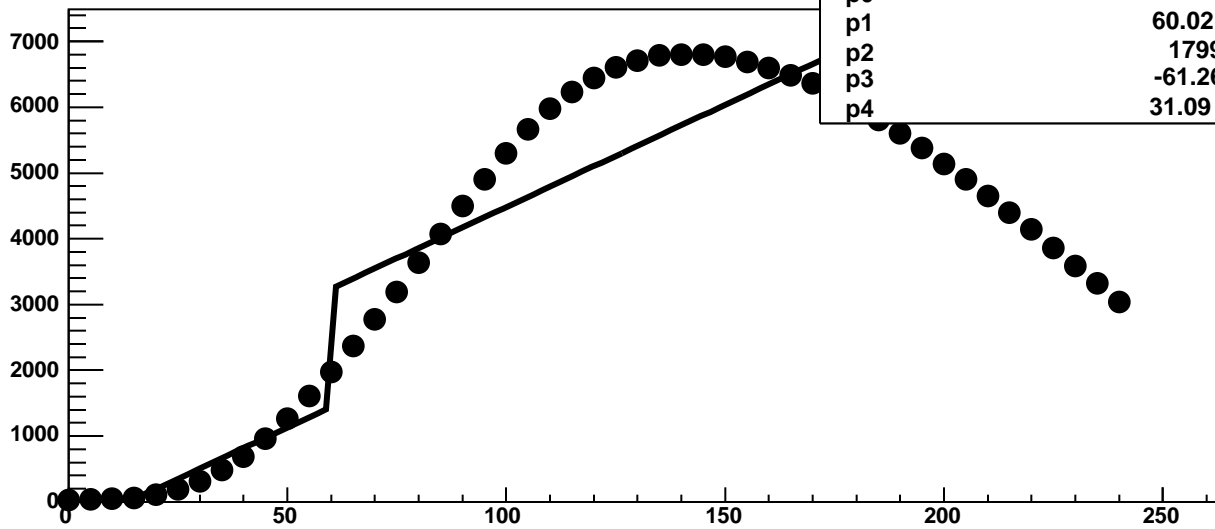
Chip 10, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

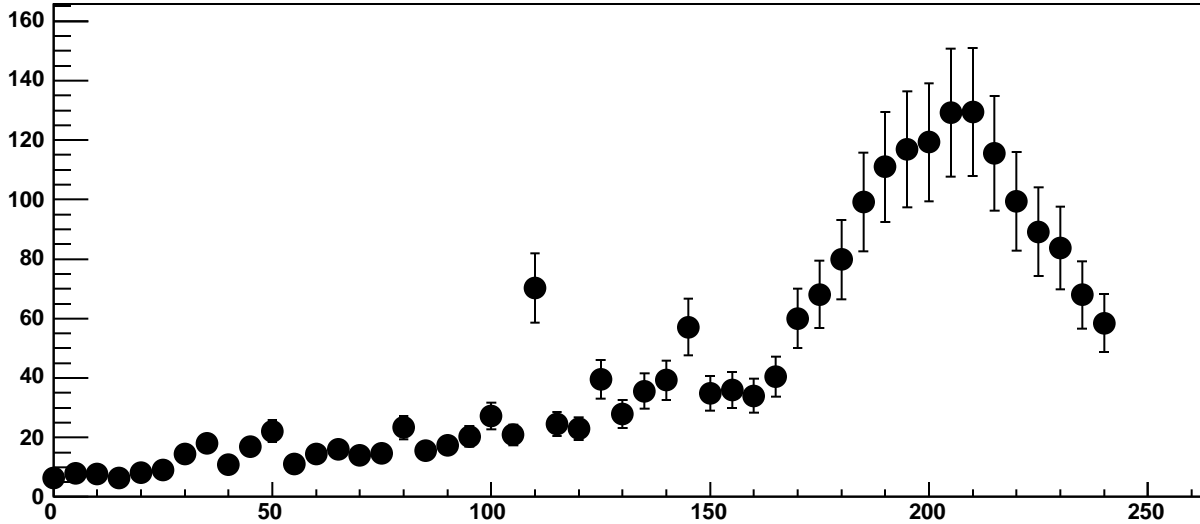


Chip 10, Channel 8, Enable 5, DAC=4000, ADC Mean vs Hold

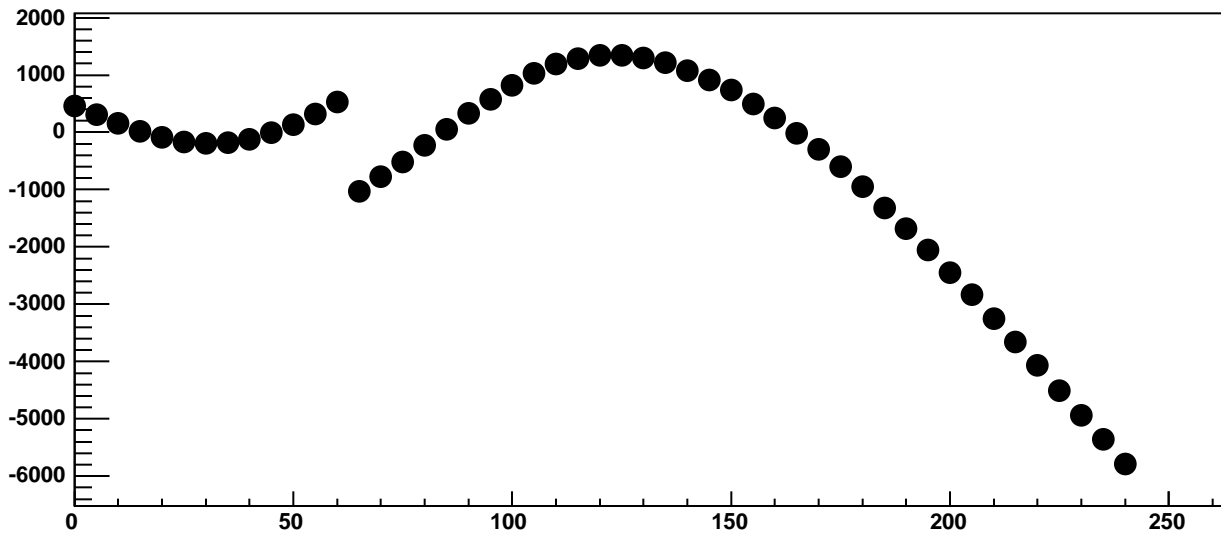


χ^2 / ndf	1.061e+06 / 41
p0	1440 ± 3.257
p1	60.02 ± 0.1411
p2	1799 ± 8.093
p3	-61.26 ± 5.778
p4	31.09 ± 0.1423

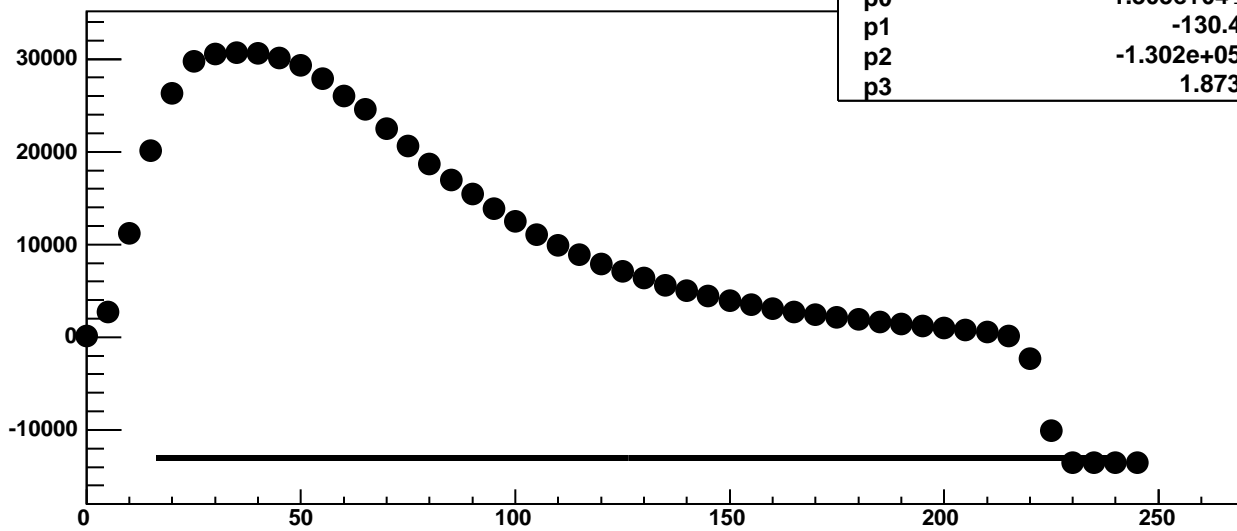
Chip 10, Channel 8, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 8, Enable 5, DAC=4000, ADC Residuals vs Hold

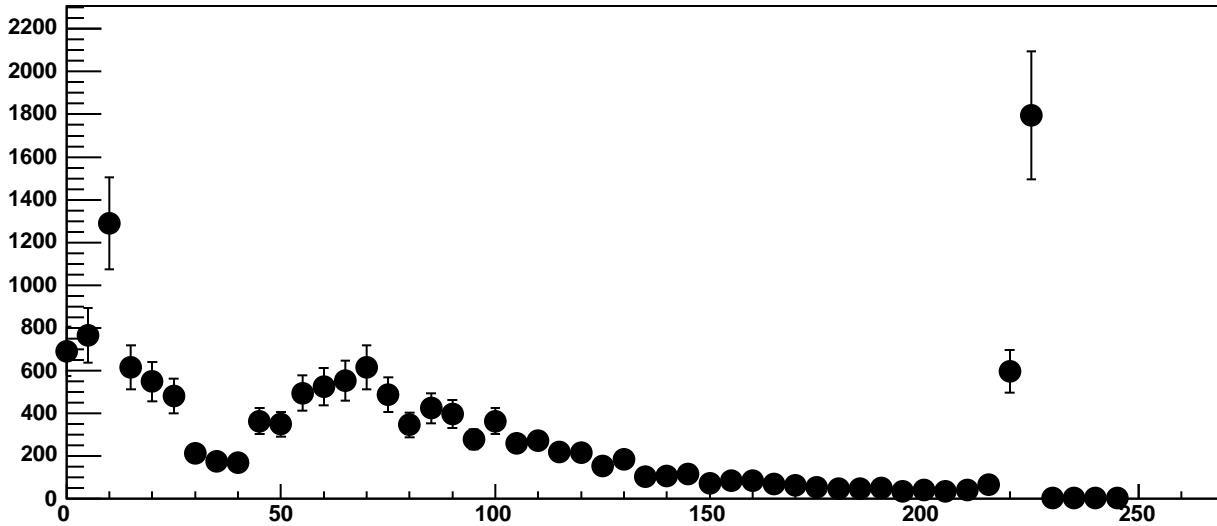


Chip 10, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold

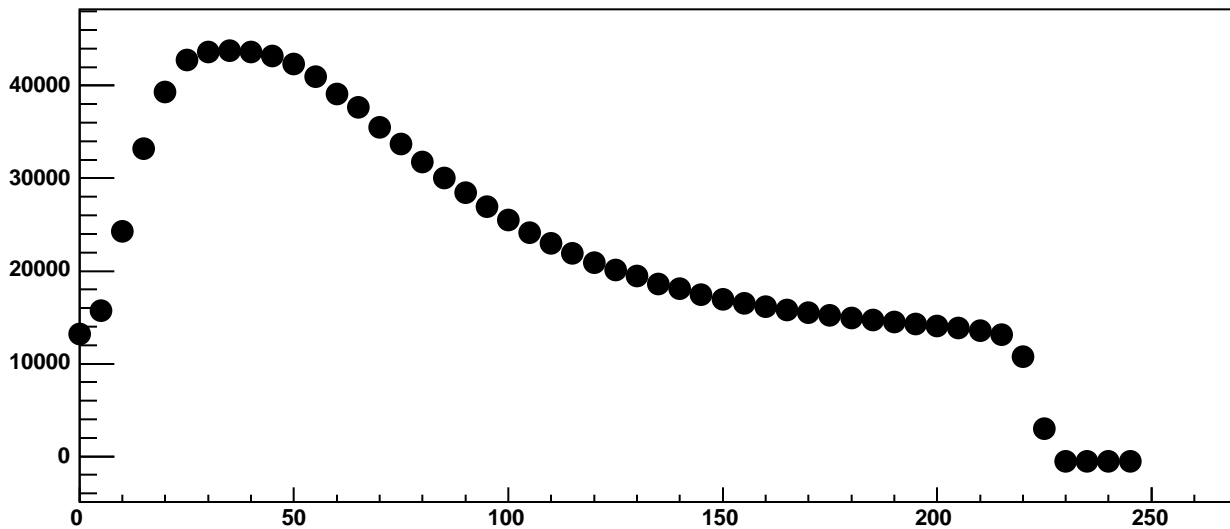


χ^2 / ndf	3.109e+07 / 42
p0	-1.305e+04 \pm 0.5159
p1	-130.4 \pm 1.909
p2	-1.302e+05 \pm 1.414
p3	1.873 \pm 1.414

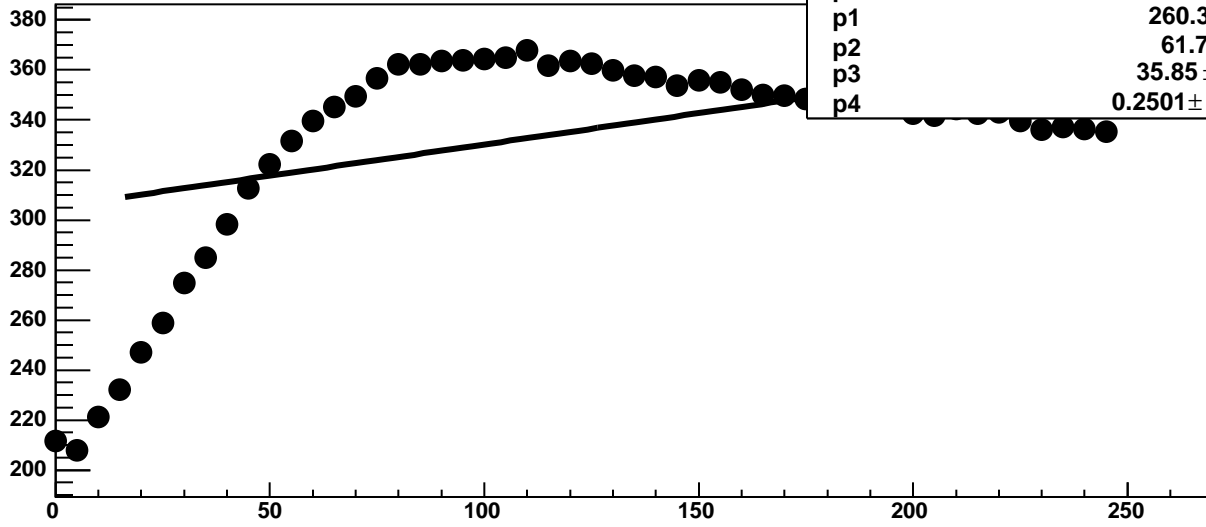
Chip 10, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold

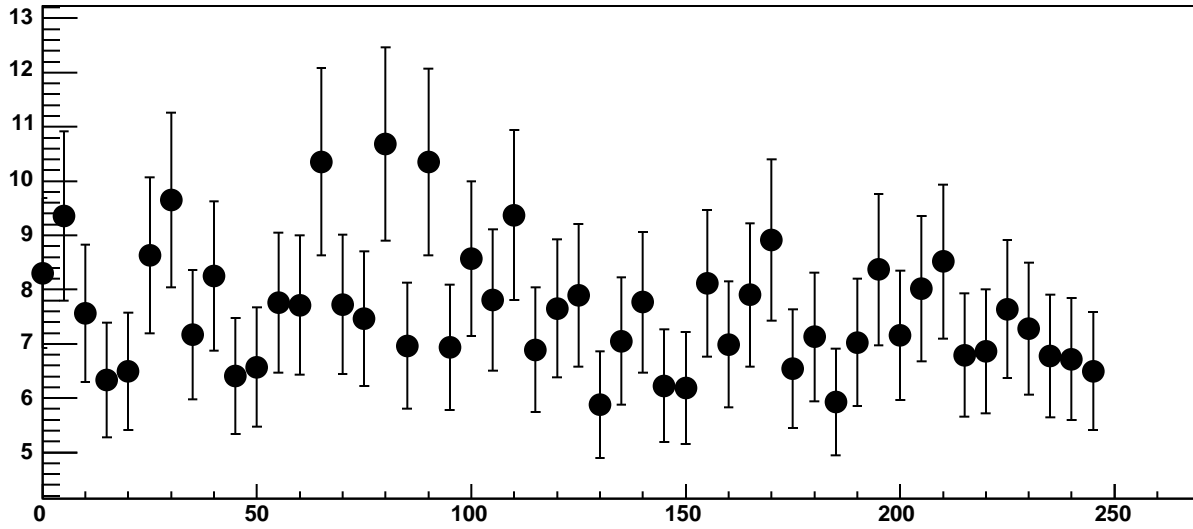


Chip 10, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

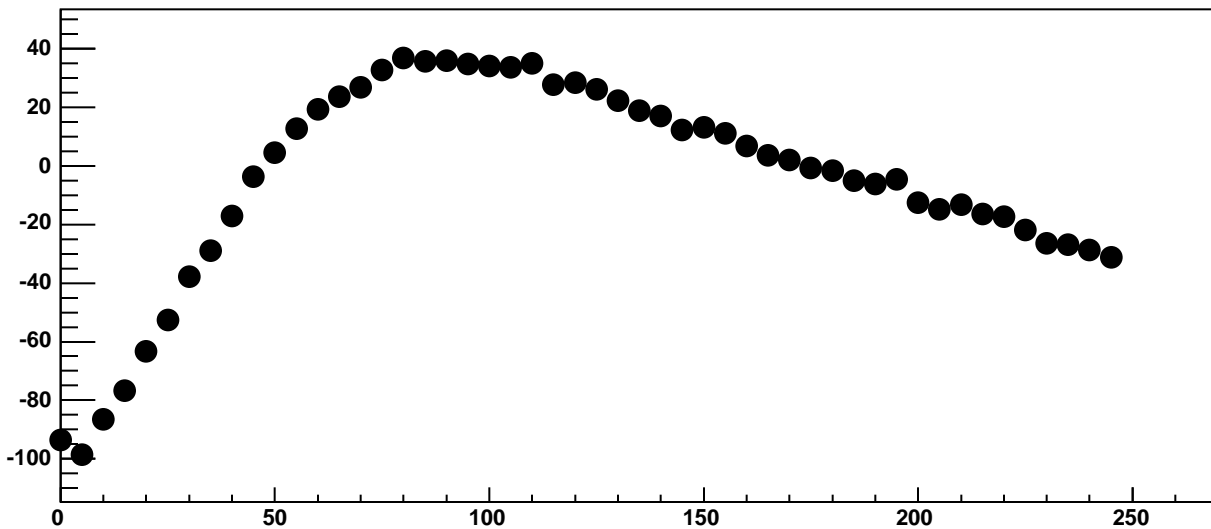


χ^2 / ndf	1.219e+04 / 41
p0	370.3 ± 0.4945
p1	260.3 ± 0.517
p2	61.74 ± 2.4
p3	35.85 ± 0.2188
p4	0.2501 ± 0.00372

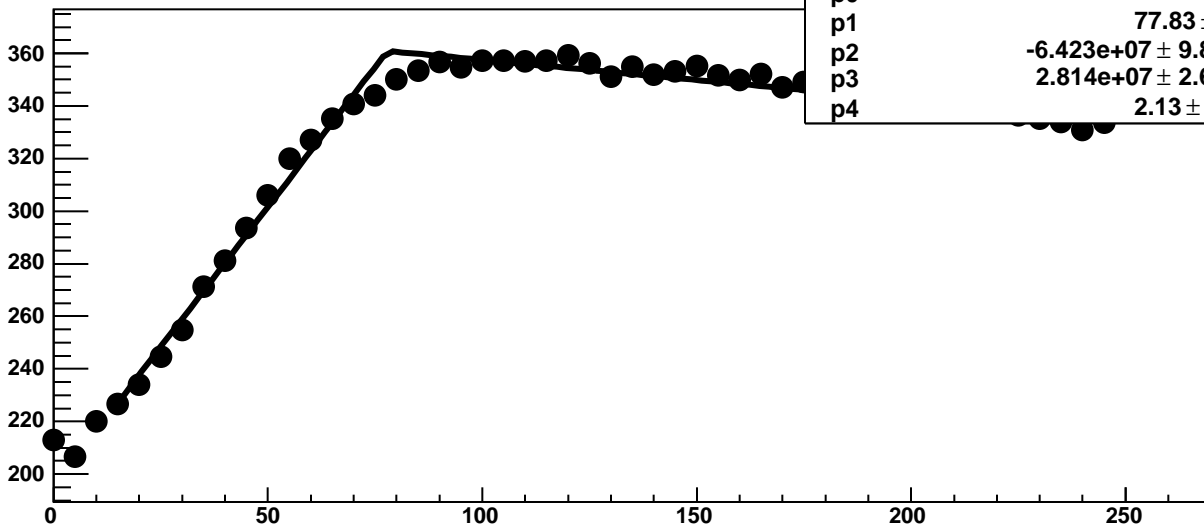
Chip 10, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

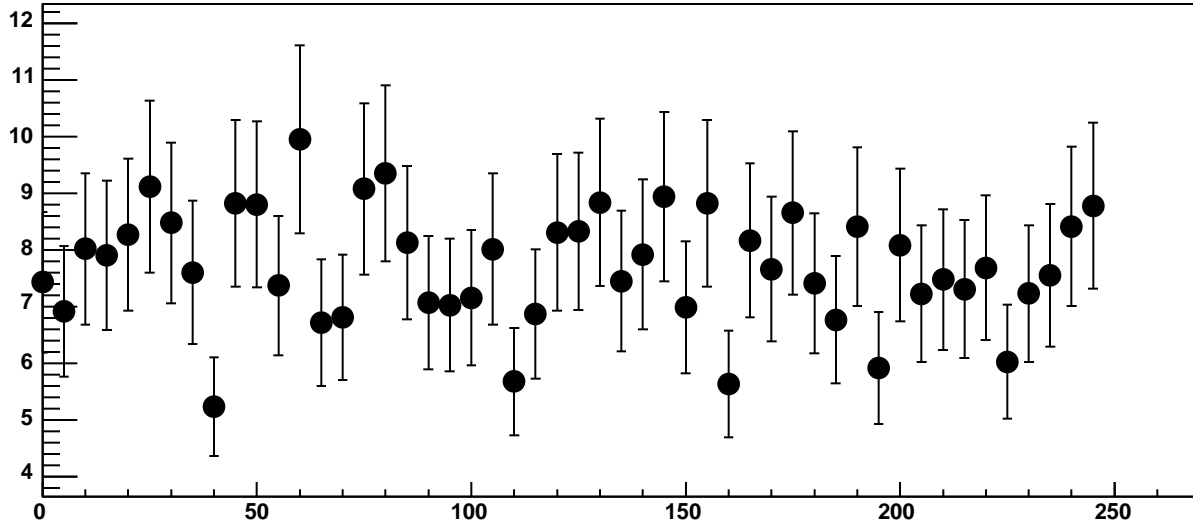


Chip 10, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

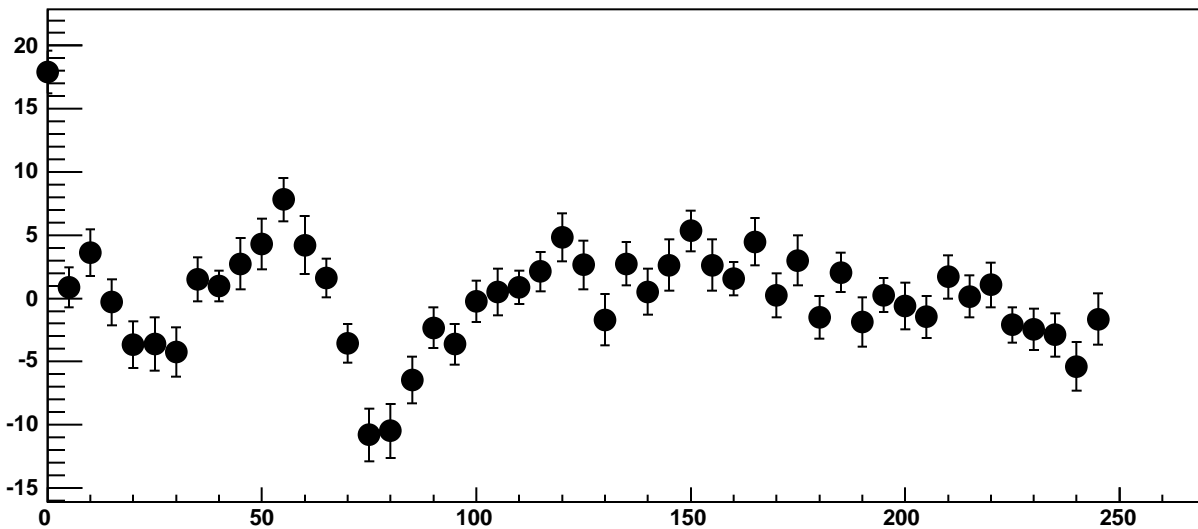


χ^2 / ndf	179.3 / 41
p0	360.9 ± 0.5626
p1	77.83 ± 0.5079
p2	$-6.423\text{e}+07 \pm 9.882\text{e}+05$
p3	$2.814\text{e}+07 \pm 2.677\text{e}+05$
p4	2.13 ± 0.02683

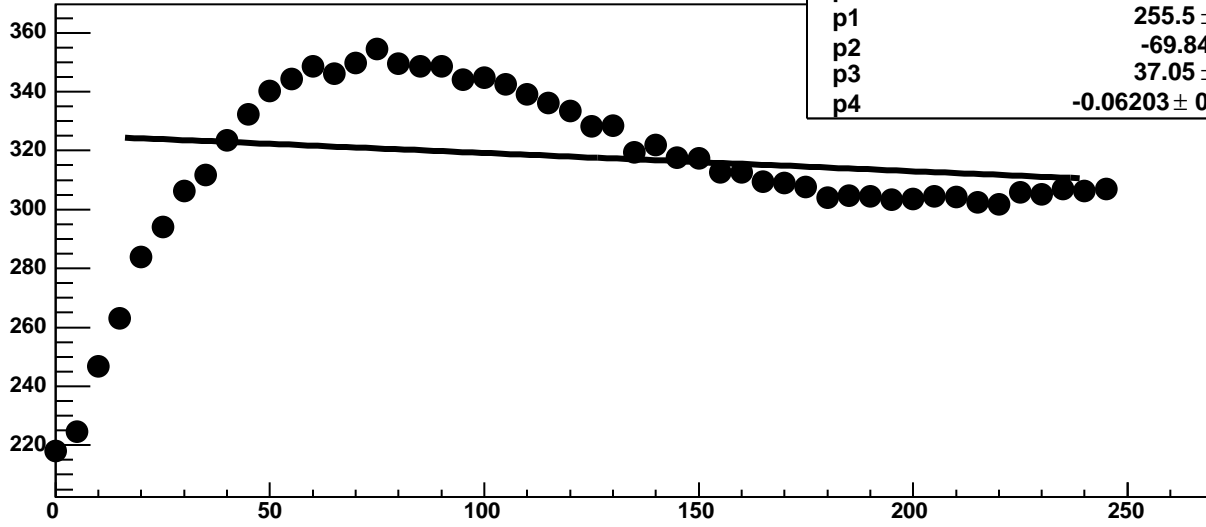
Chip 10, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



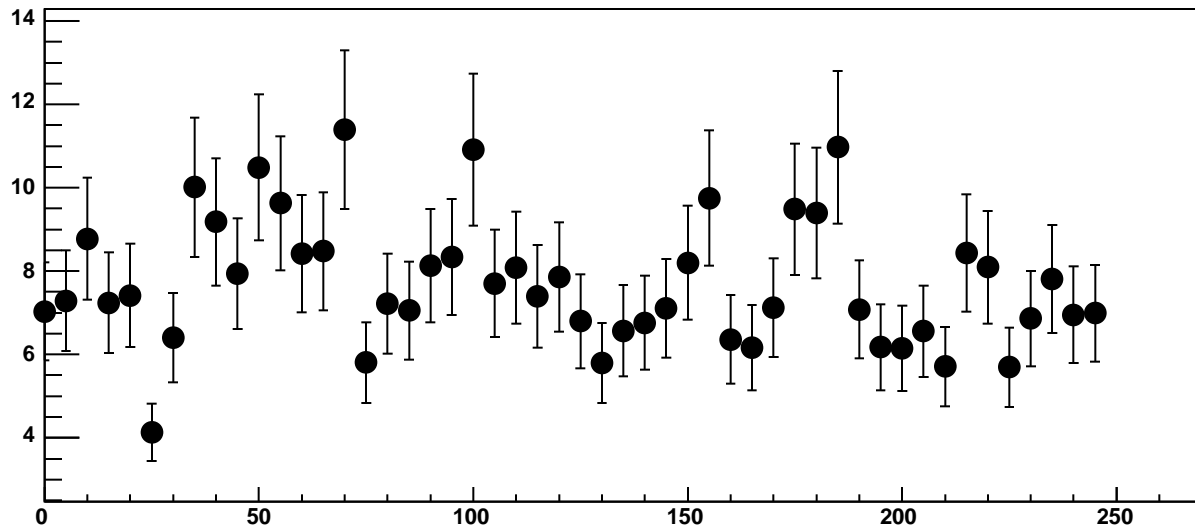
Chip 10, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold



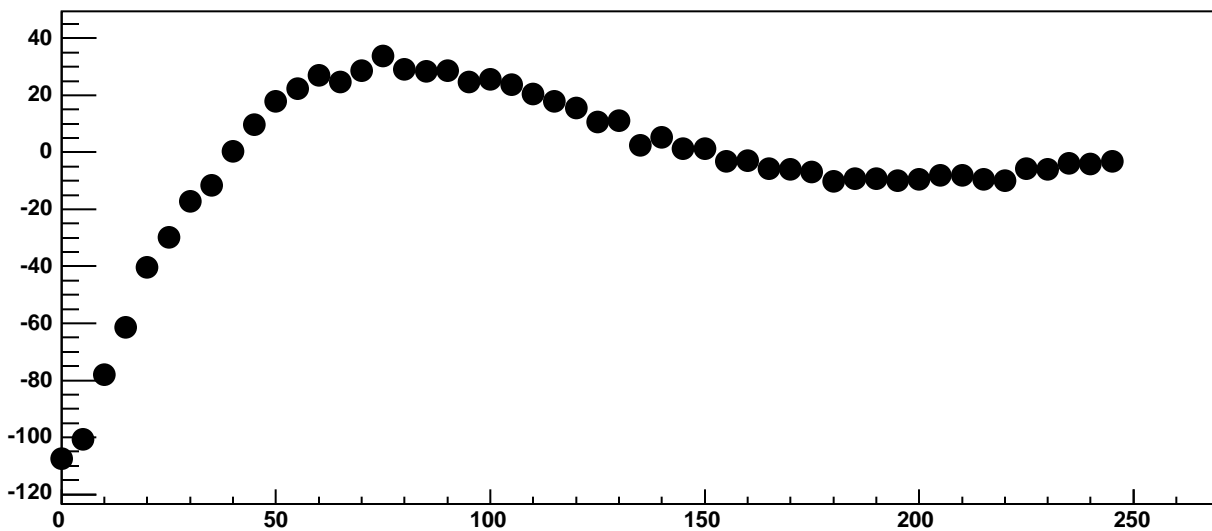
Chip 10, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold



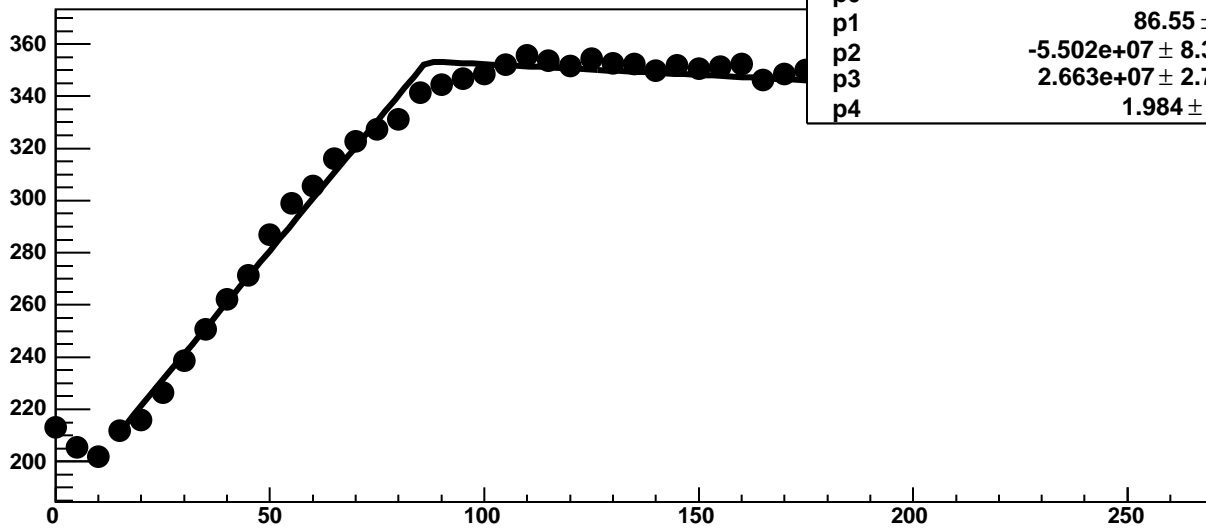
Chip 10, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold

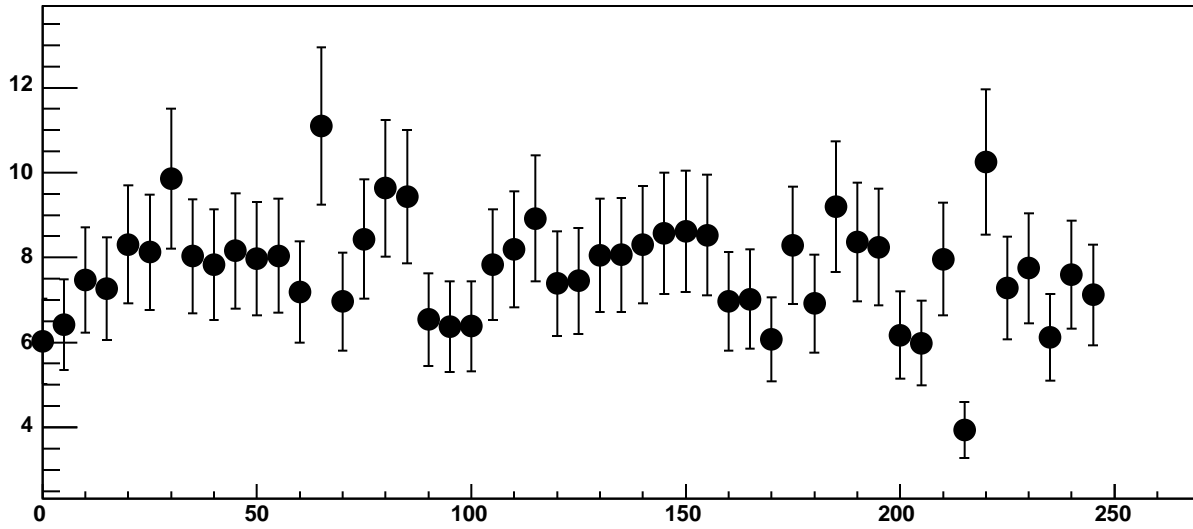


Chip 10, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

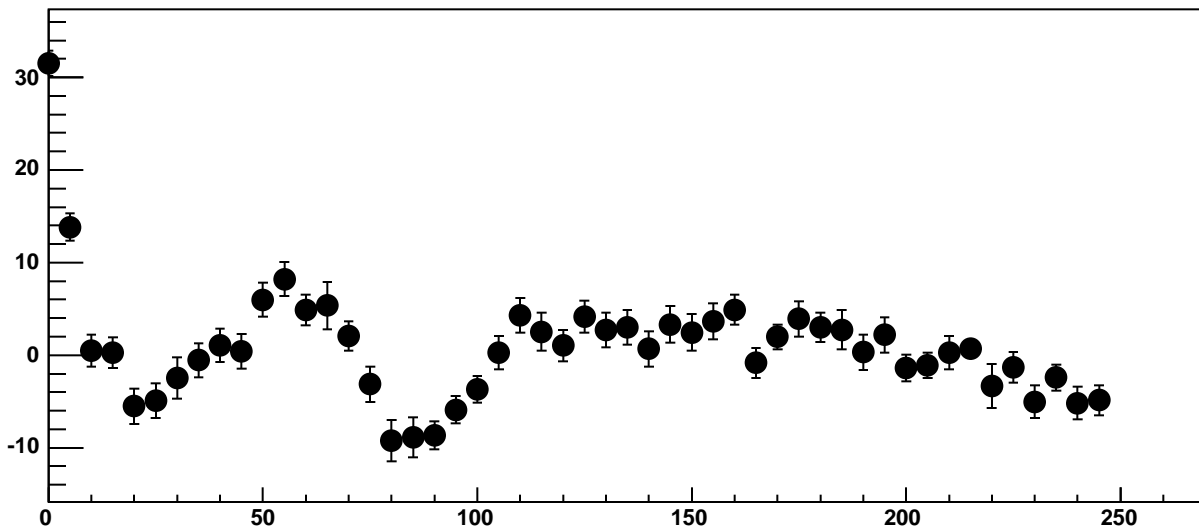


χ^2 / ndf	228.4 / 41
p0	353.4 ± 0.5723
p1	86.55 ± 0.5645
p2	$-5.502\text{e}+07 \pm 8.309\text{e}+05$
p3	$2.663\text{e}+07 \pm 2.763\text{e}+05$
p4	1.984 ± 0.02282

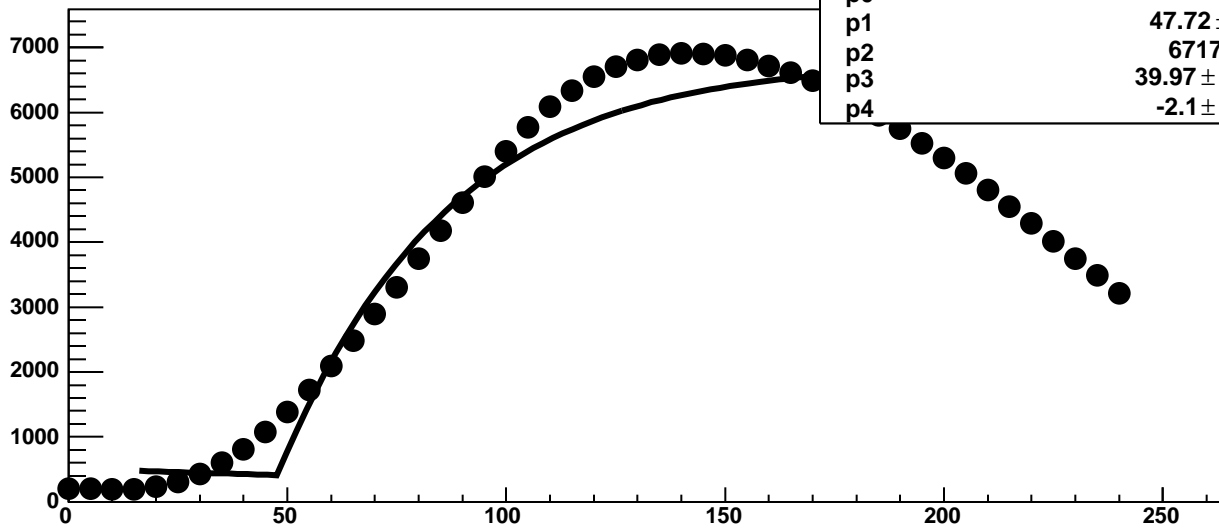
Chip 10, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

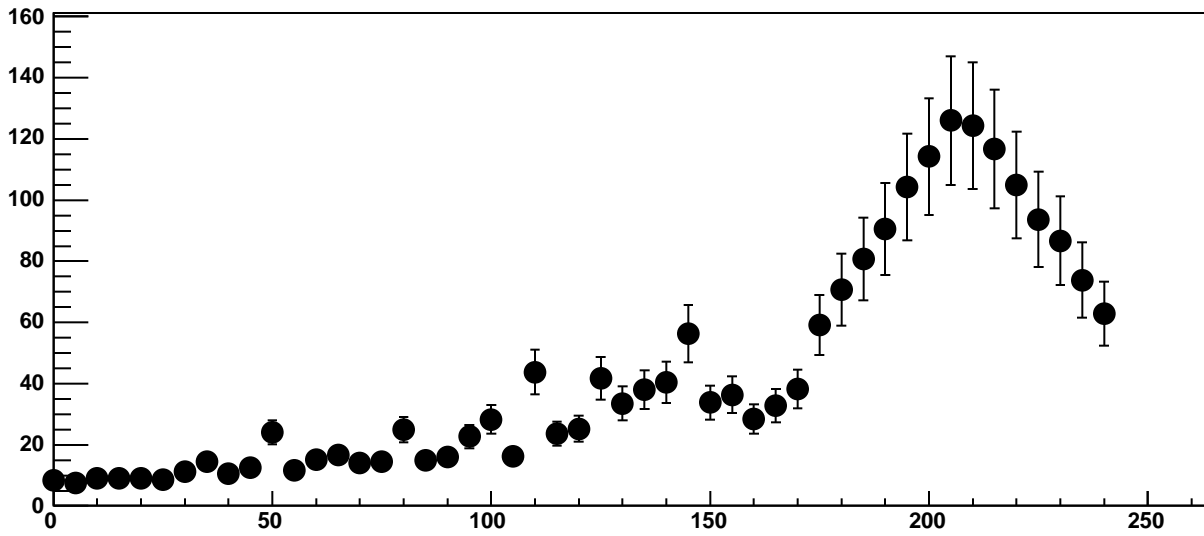


Chip 10, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold

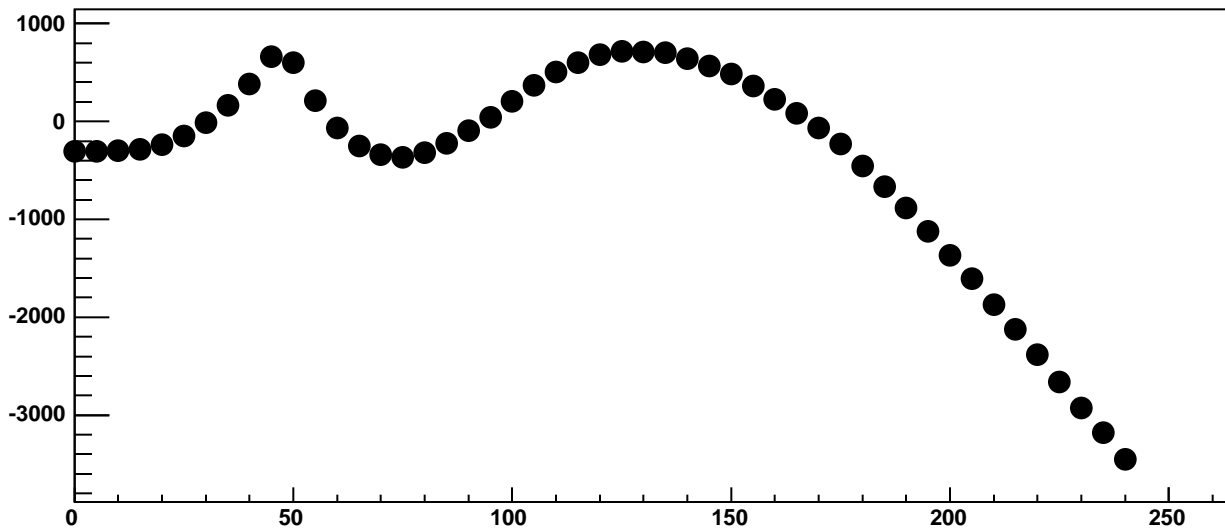


χ^2 / ndf	4.081e+05 / 41
p0	410.7 ± 1.541
p1	47.72 ± 0.0198
p2	6717 ± 10.47
p3	39.97 ± 0.06805
p4	-2.1 ± 0.06326

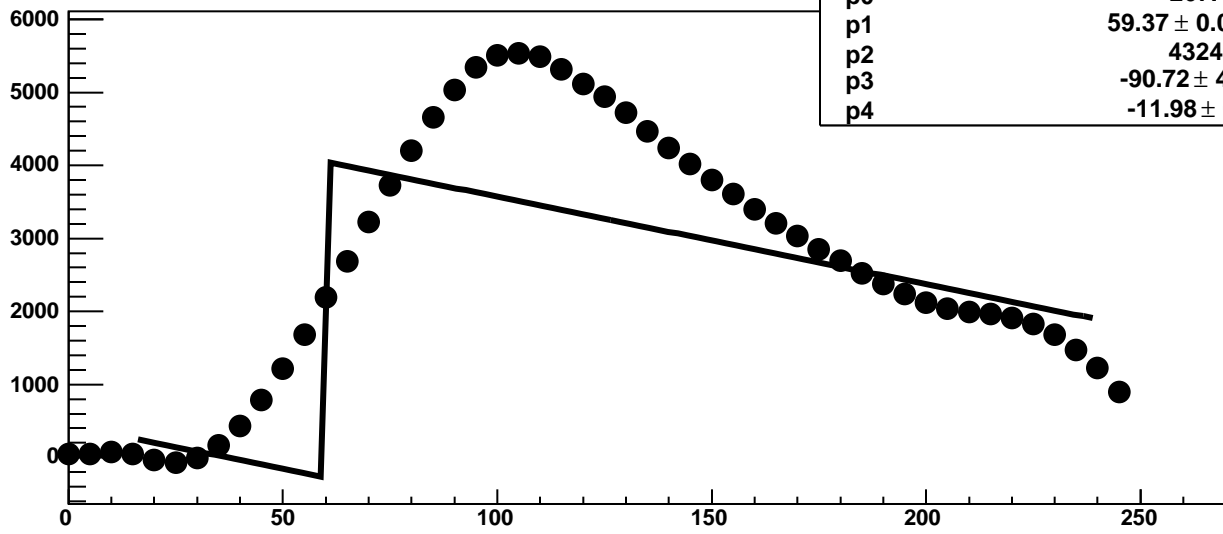
Chip 10, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold

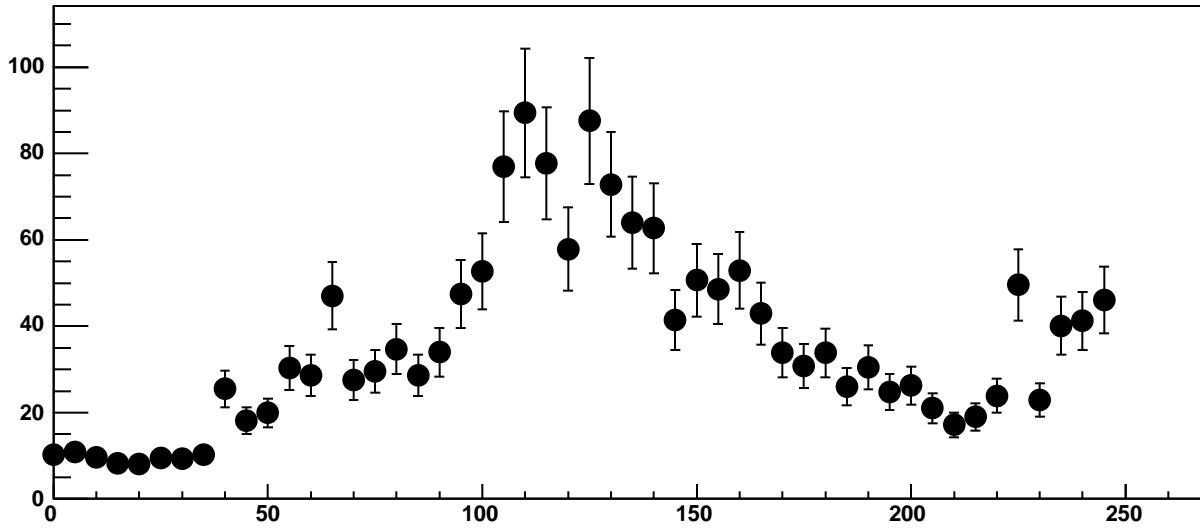


Chip 10, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold

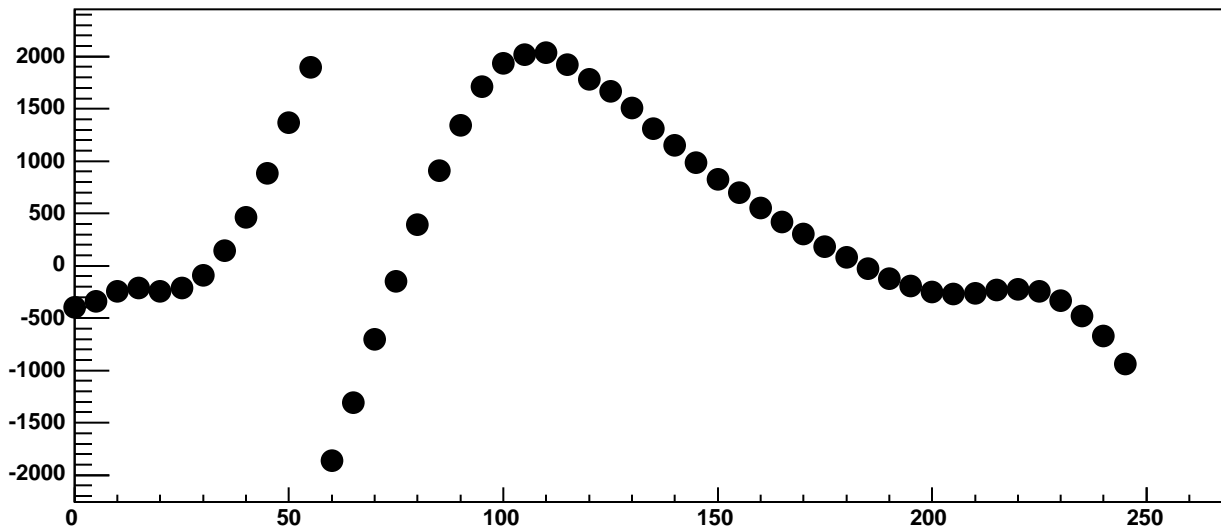


χ^2 / ndf	6.046e+05 / 41
p0	-267.4 ± 1.12
p1	59.37 ± 0.0008237
p2	4324 ± 3.406
p3	-90.72 ± 4.47e-07
p4	-11.98 ± 0.02128

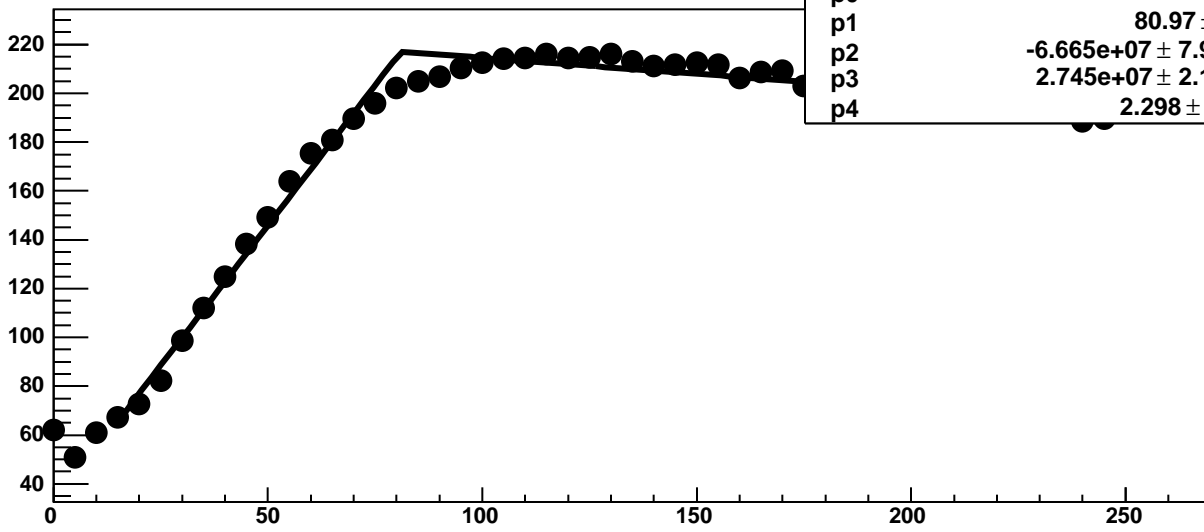
Chip 10, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold

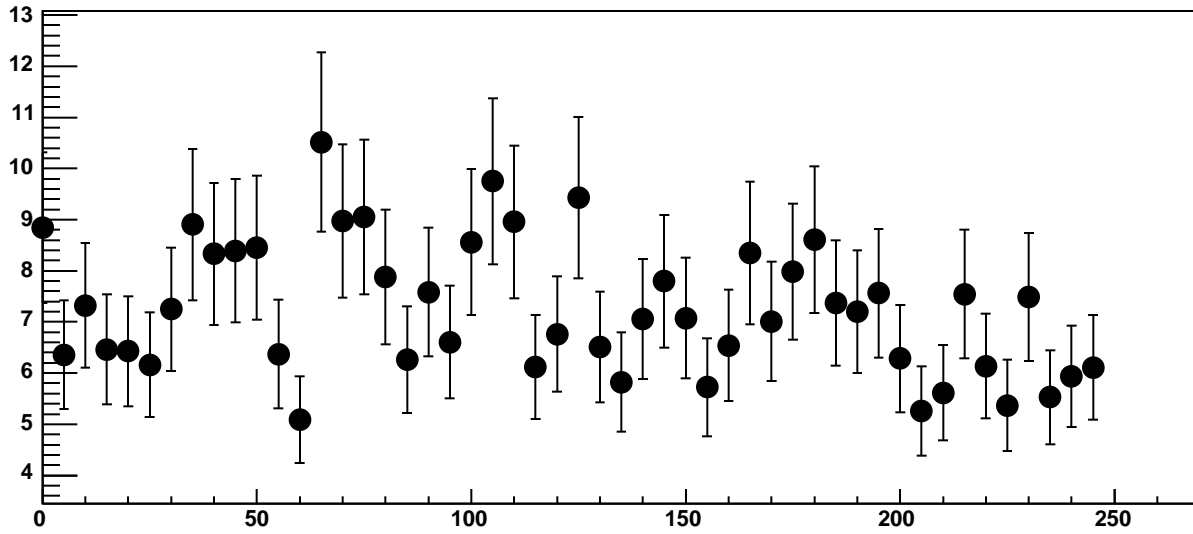


Chip 10, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

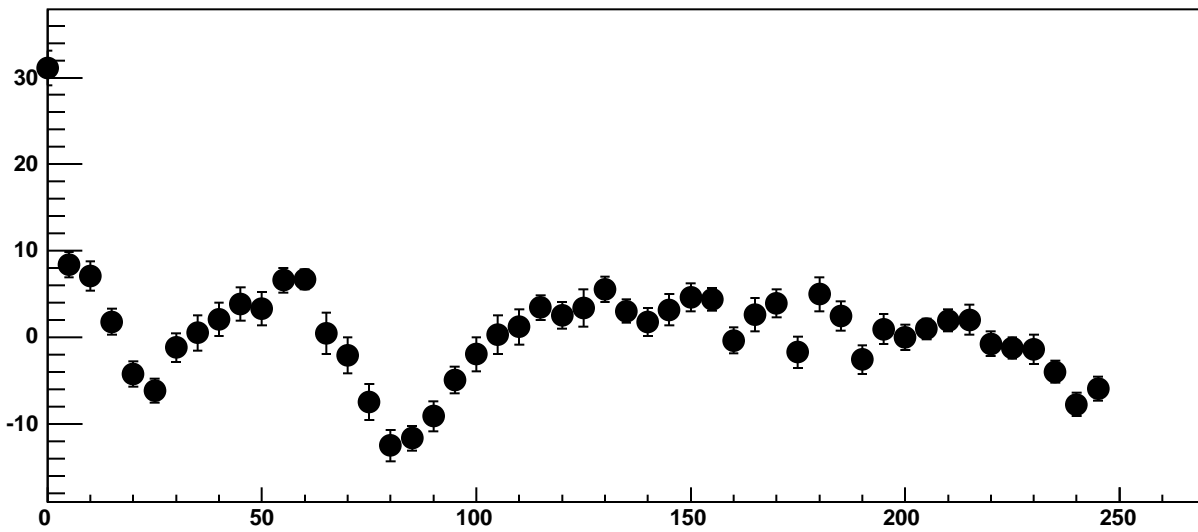


χ^2 / ndf	379.4 / 41
p0	217 ± 0.5557
p1	80.97 ± 0.4607
p2	$-6.665e+07 \pm 7.988e+05$
p3	$2.745e+07 \pm 2.175e+05$
p4	2.298 ± 0.02215

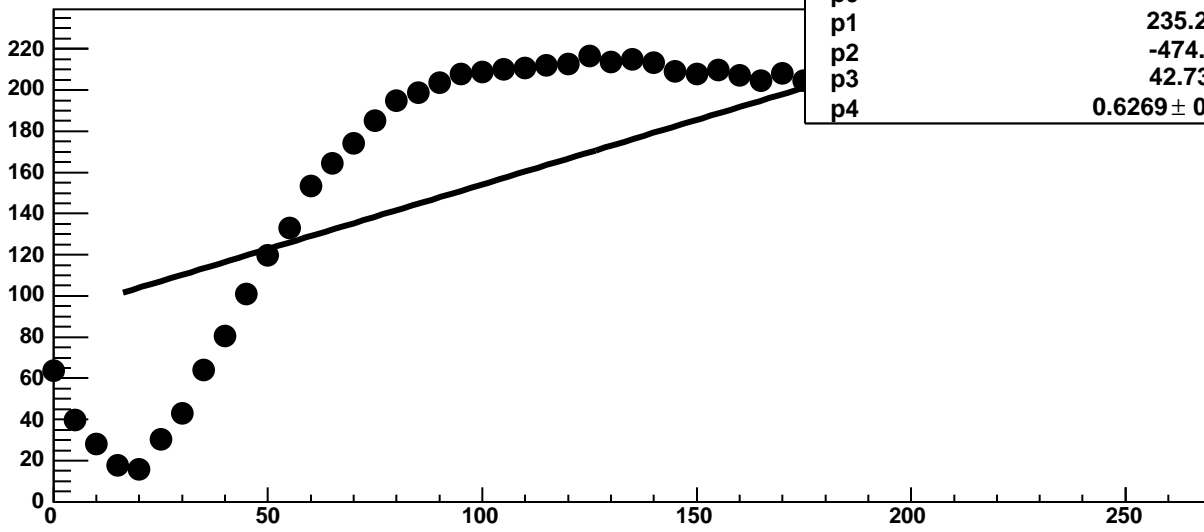
Chip 10, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

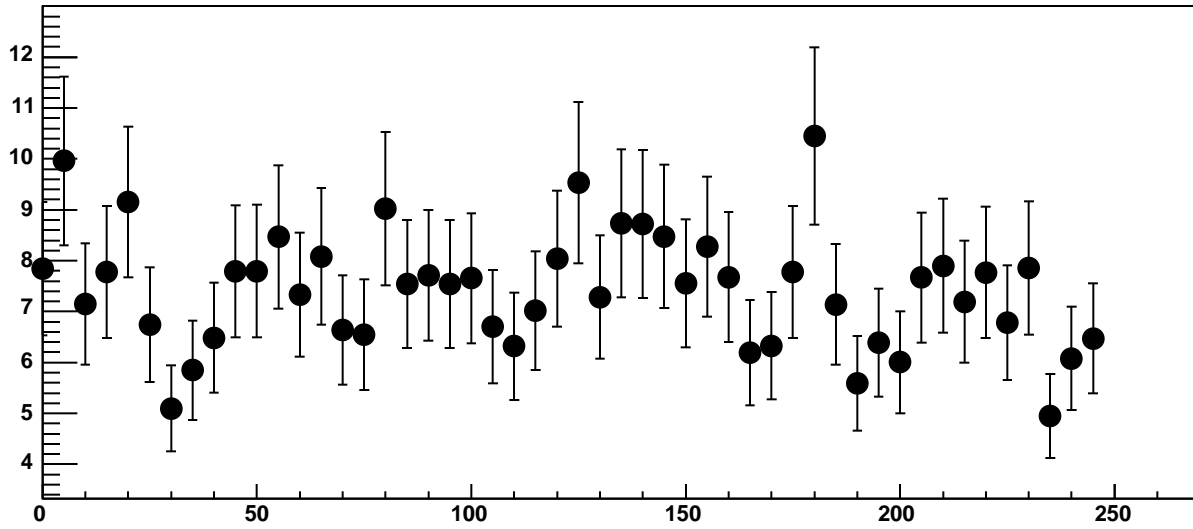


Chip 10, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

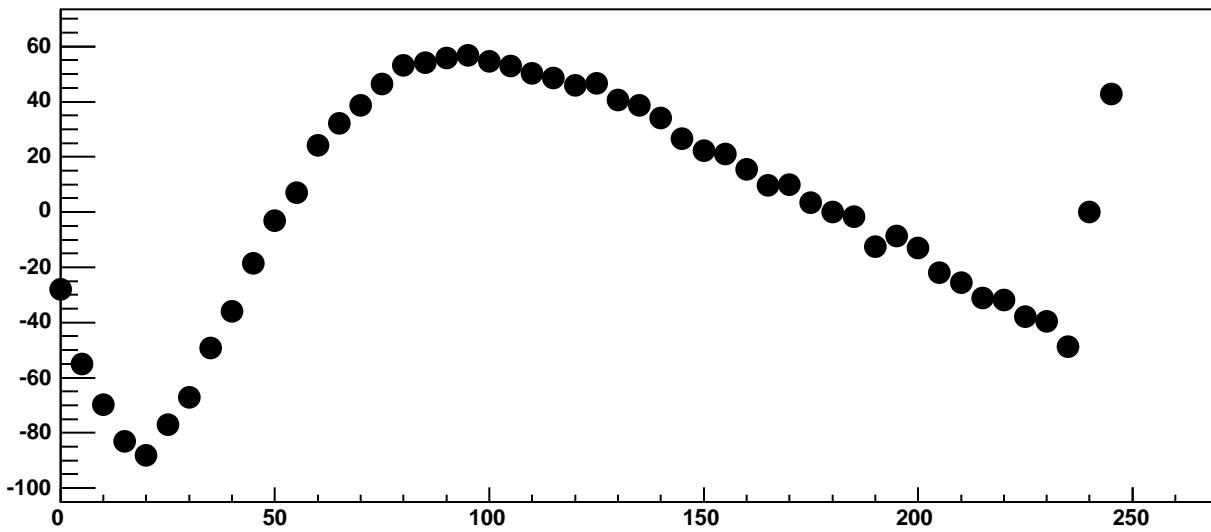


χ^2 / ndf	2.913e+04 / 41
p0	238.9 ± 1.151
p1	235.2 ± 1.693
p2	-474.9 ± 151
p3	42.73 ± 11.88
p4	0.6269 ± 0.003593

Chip 10, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold

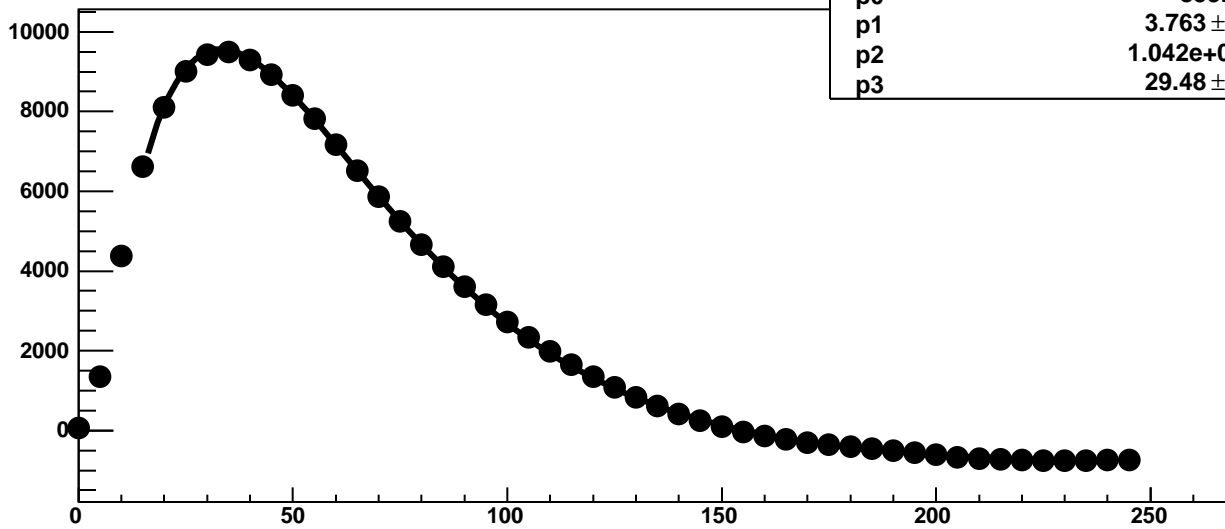


Chip 10, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

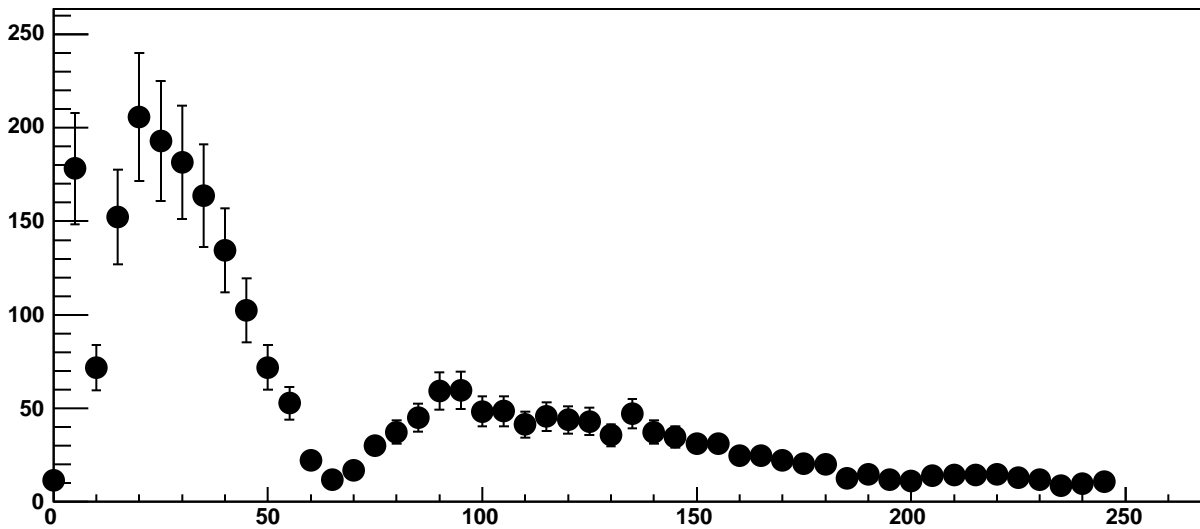


Chip 10, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold

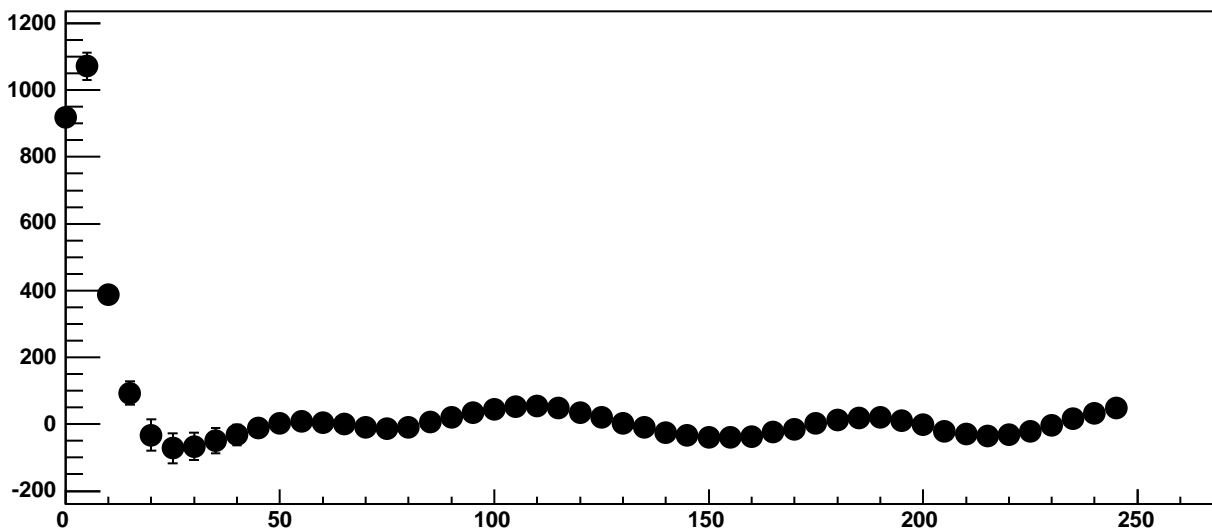
χ^2 / ndf	1076 / 42
p0	-853.3 ± 1.14
p1	3.763 ± 0.06346
p2	1.042e+04 ± 8.4
p3	29.48 ± 0.02482



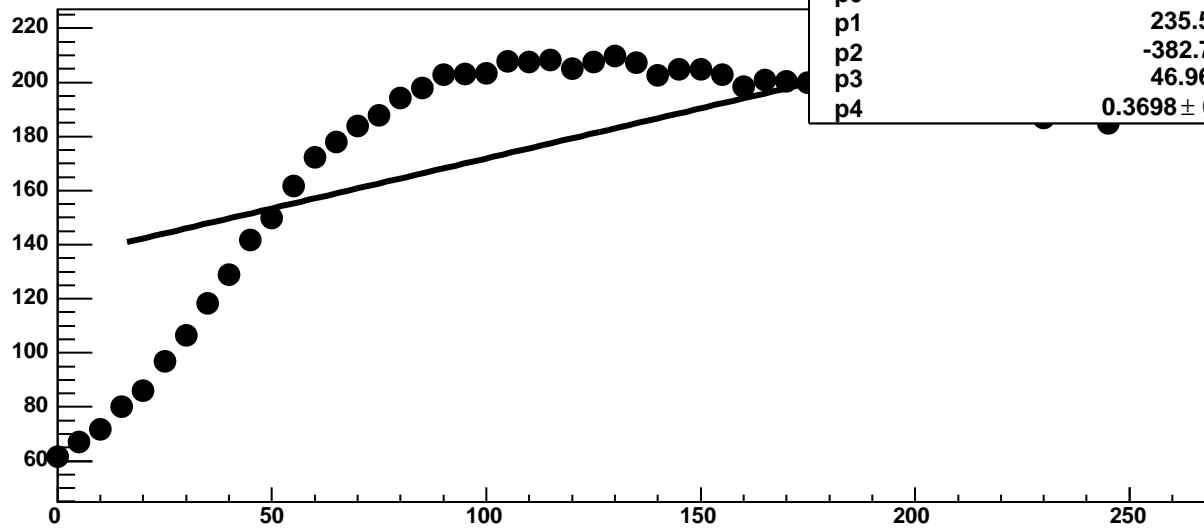
Chip 10, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold

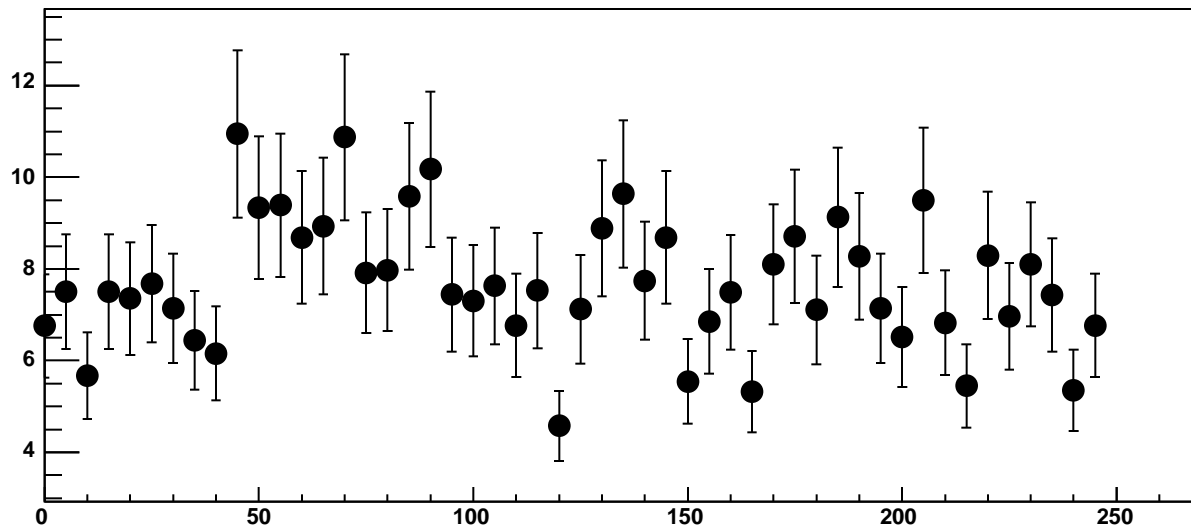


Chip 10, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

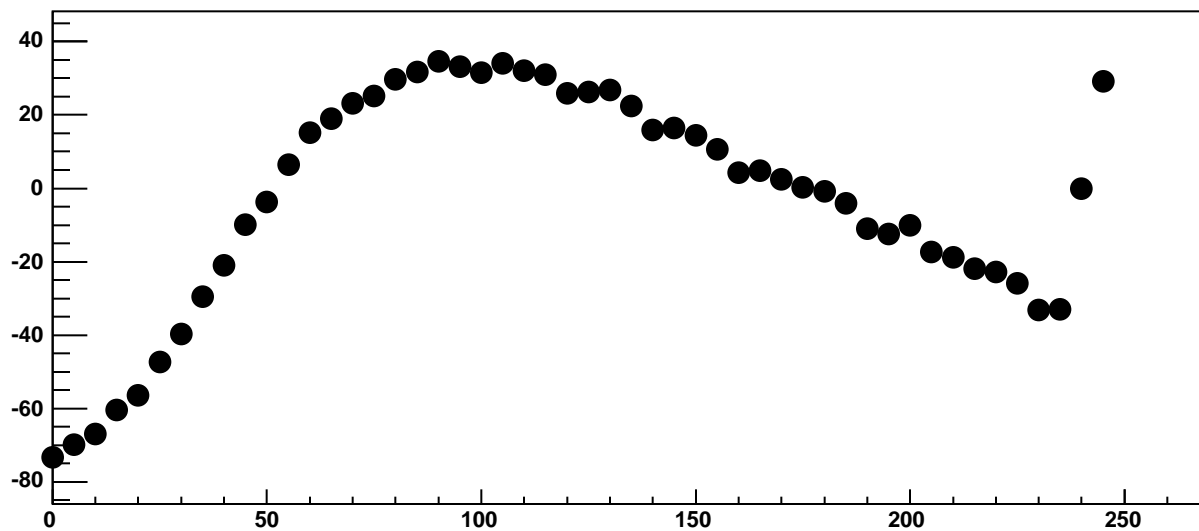


χ^2 / ndf	1.05e+04 / 41
p0	222 ± 0.9542
p1	235.5 ± 2.233
p2	-382.7 ± 177.4
p3	46.96 ± 18.86
p4	0.3698 ± 0.003941

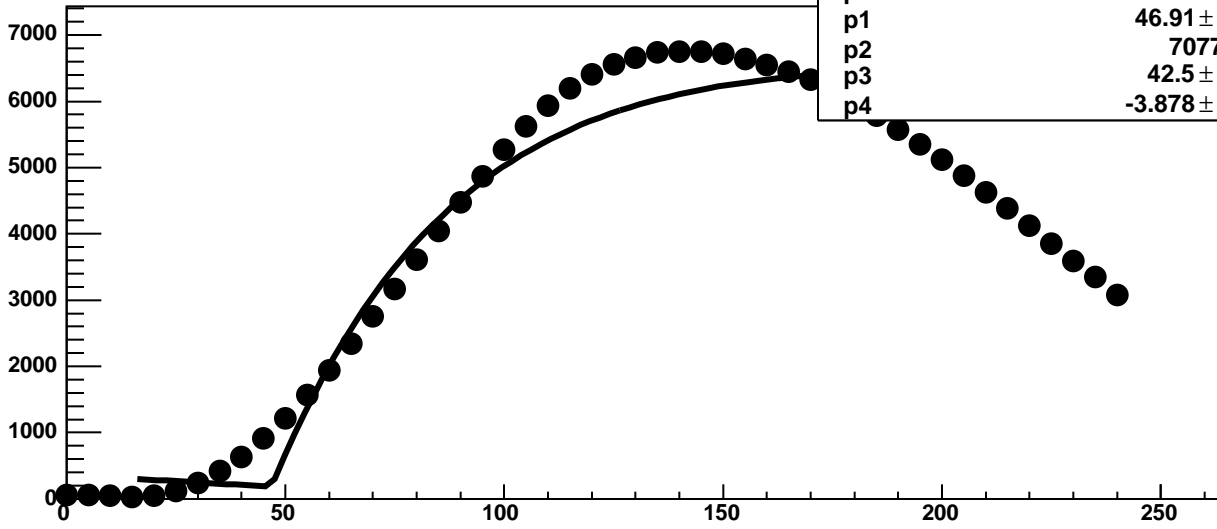
Chip 10, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

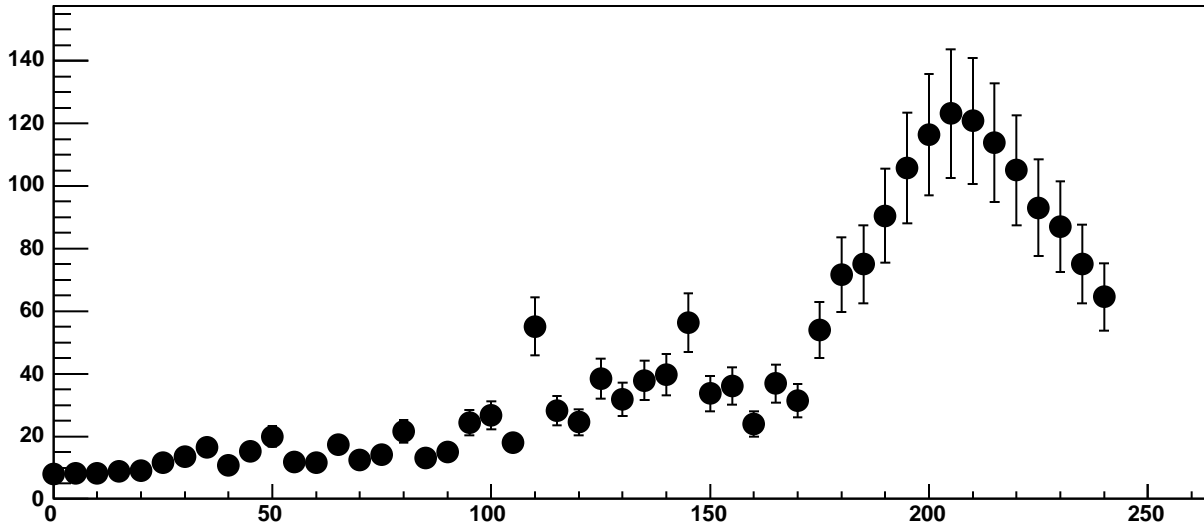


Chip 10, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold

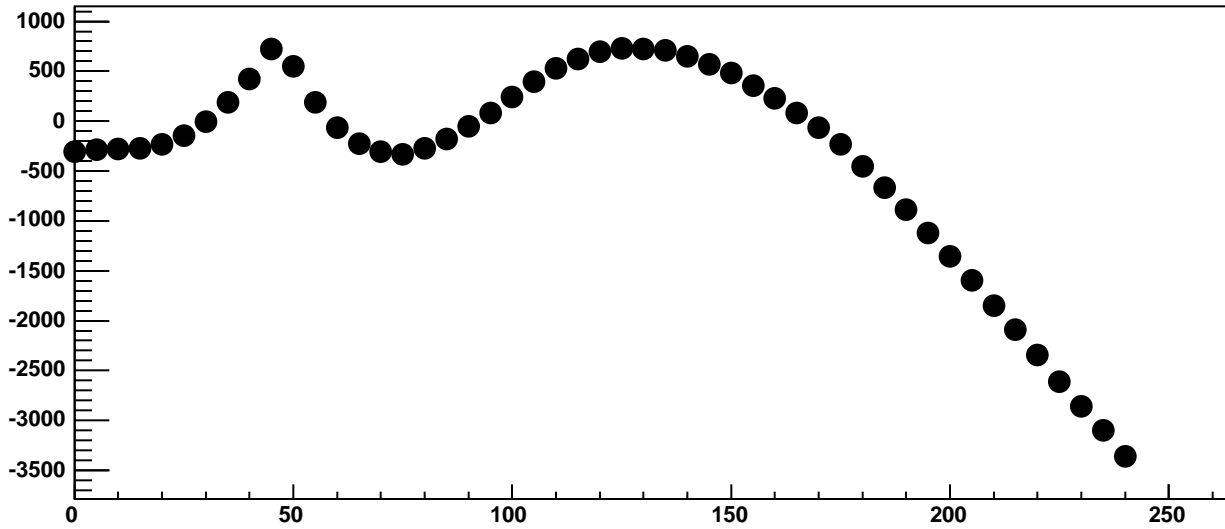


χ^2 / ndf	3.878e+05 / 41
p0	182.3 ± 1.632
p1	46.91 ± 0.01982
p2	7077 ± 11.39
p3	42.5 ± 0.07204
p4	-3.878 ± 0.06628

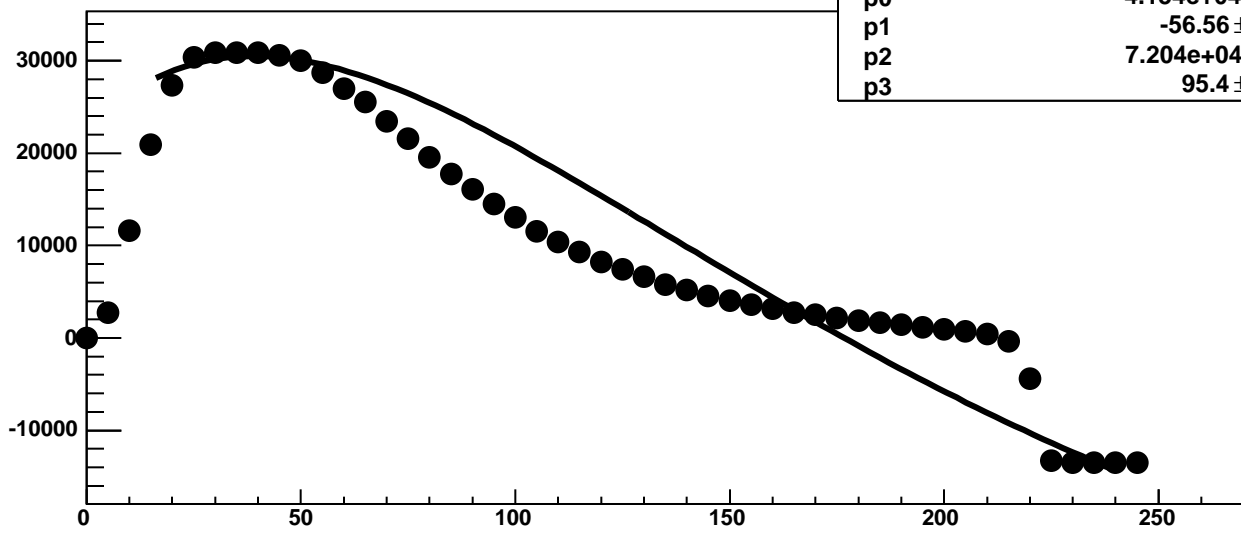
Chip 10, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

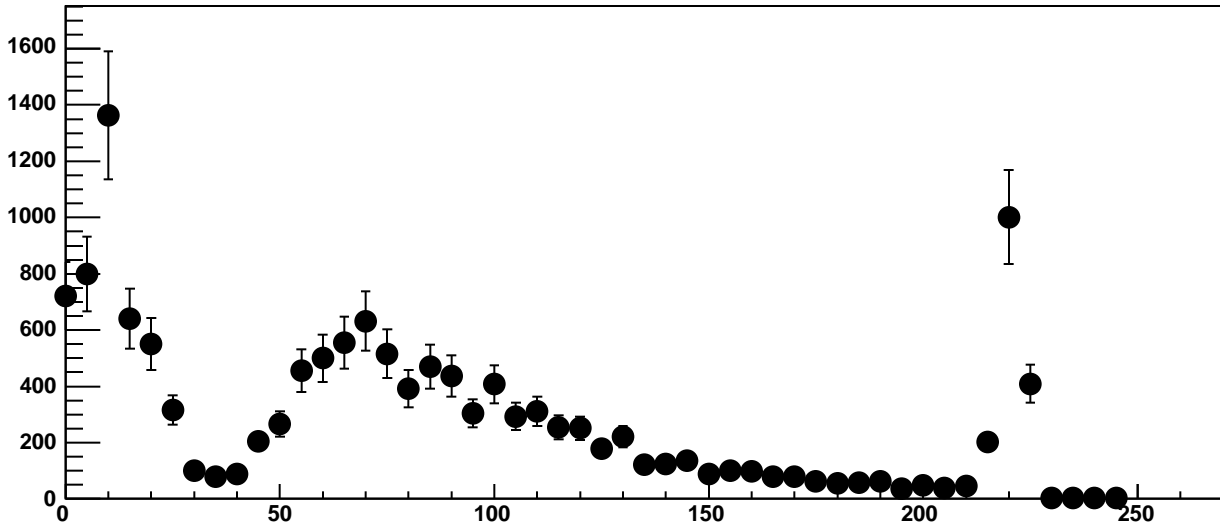


Chip 10, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold

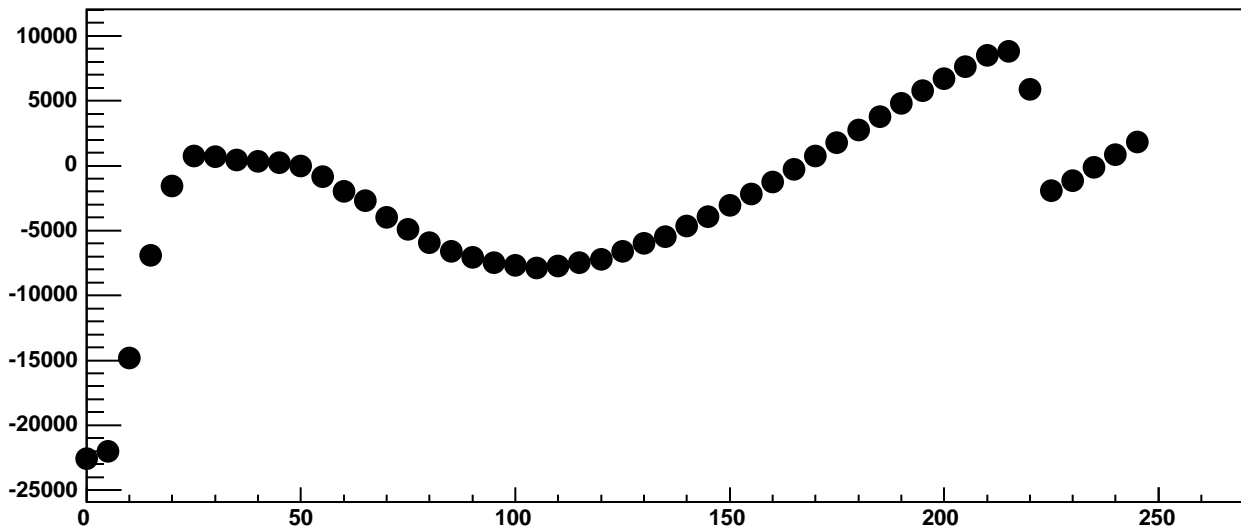


χ^2 / ndf	4.602e+06 / 42
p0	-4.154e+04 ± 51.46
p1	-56.56 ± 0.1953
p2	7.204e+04 ± 50.41
p3	95.4 ± 0.1145

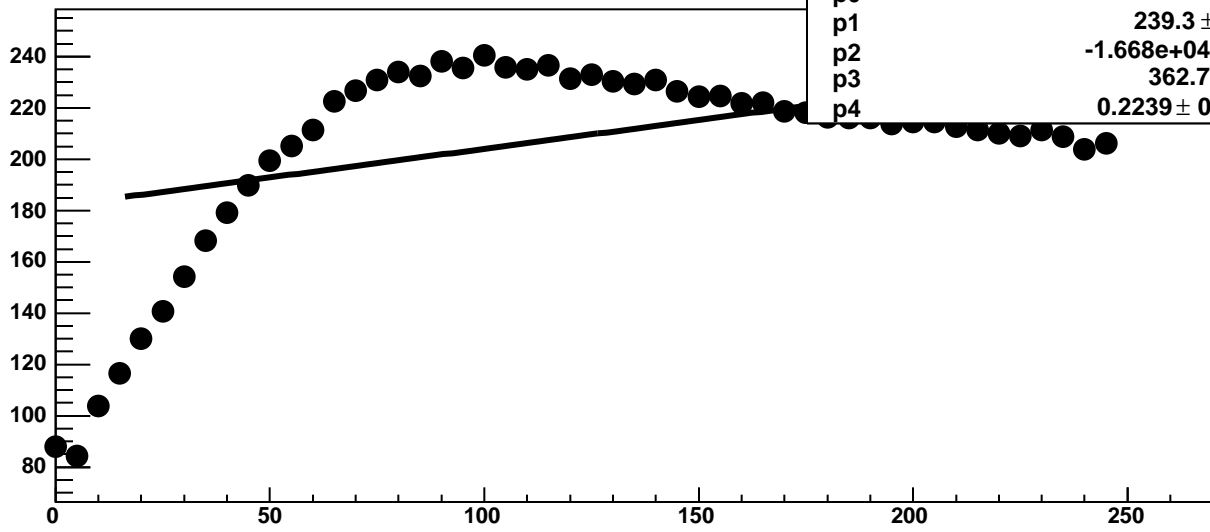
Chip 10, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold

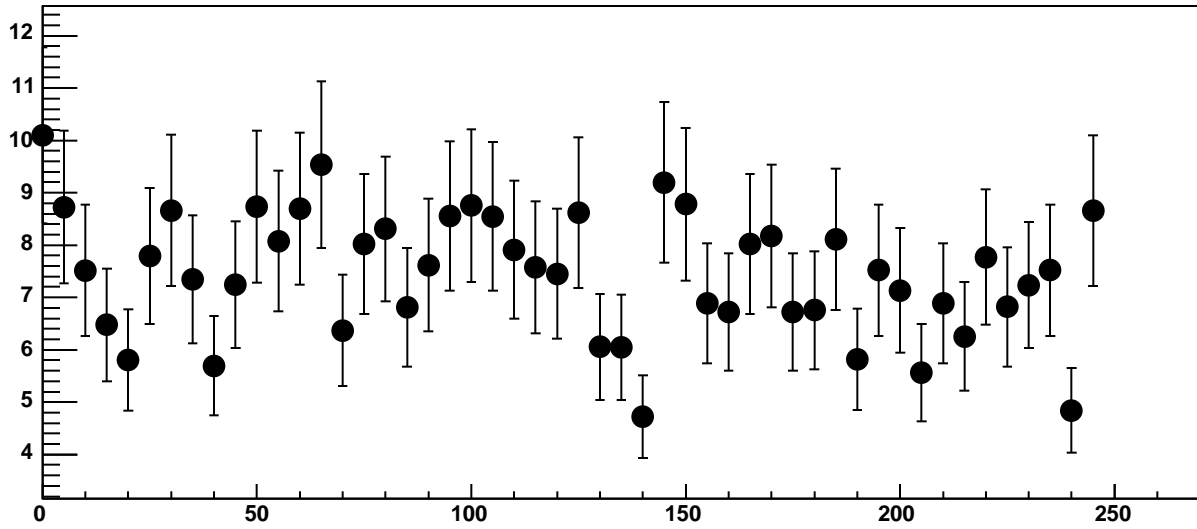


Chip 10, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

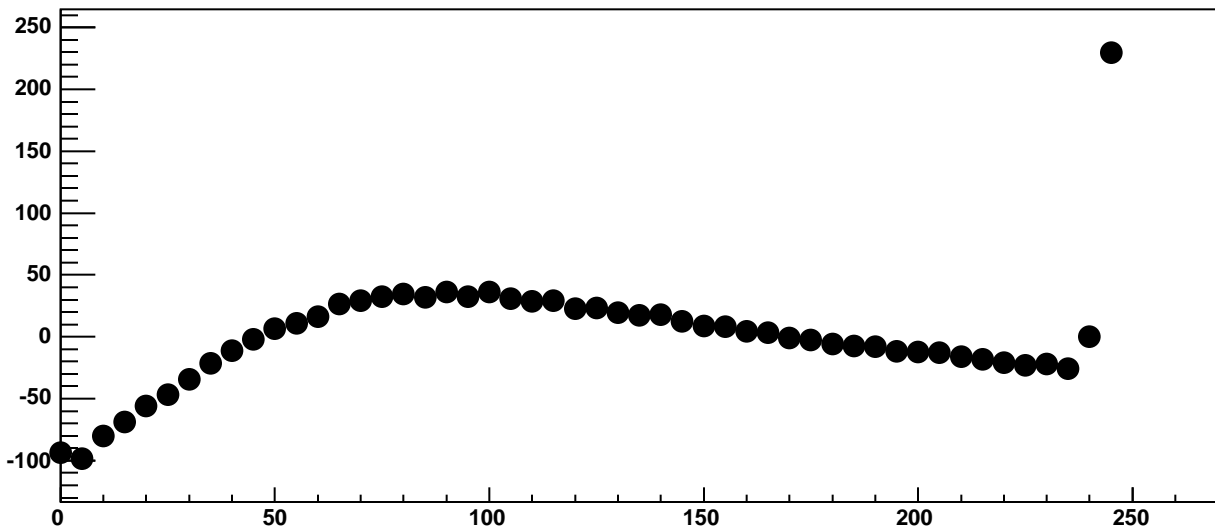


χ^2 / ndf	1.096e+04 / 41
p0	235.3 ± 0.4829
p1	239.3 ± 0.3136
p2	-1.668e+04 ± 7582
p3	362.7 ± 134.8
p4	0.2239 ± 0.003702

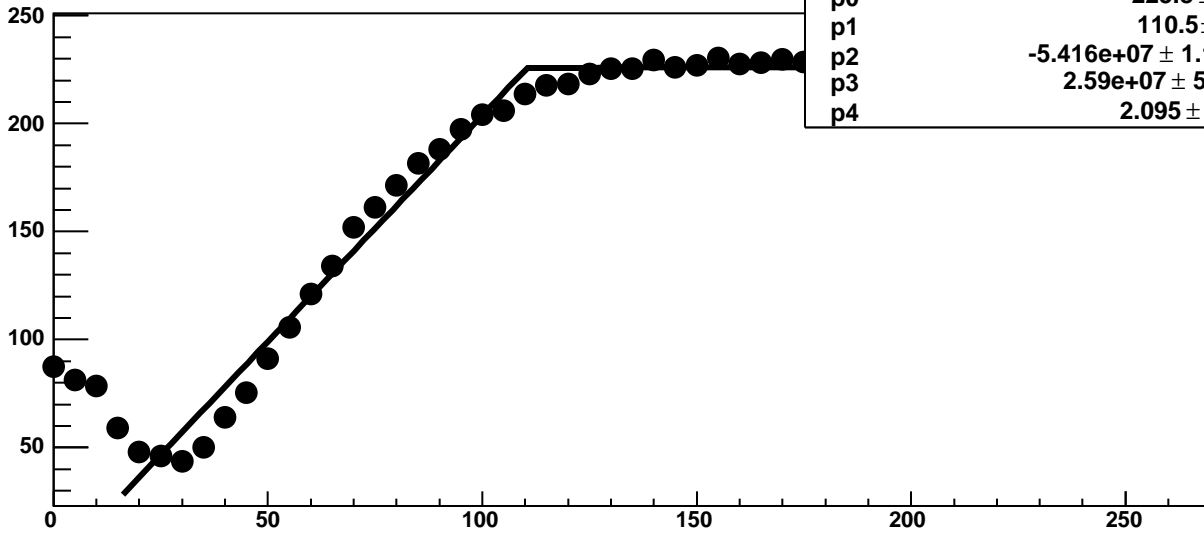
Chip 10, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold

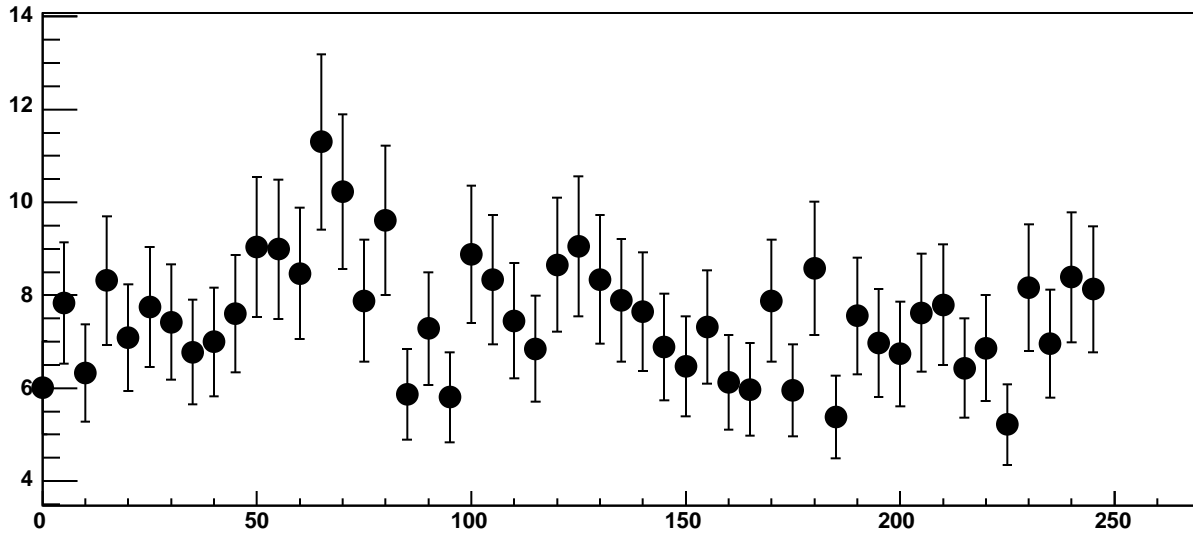


Chip 10, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

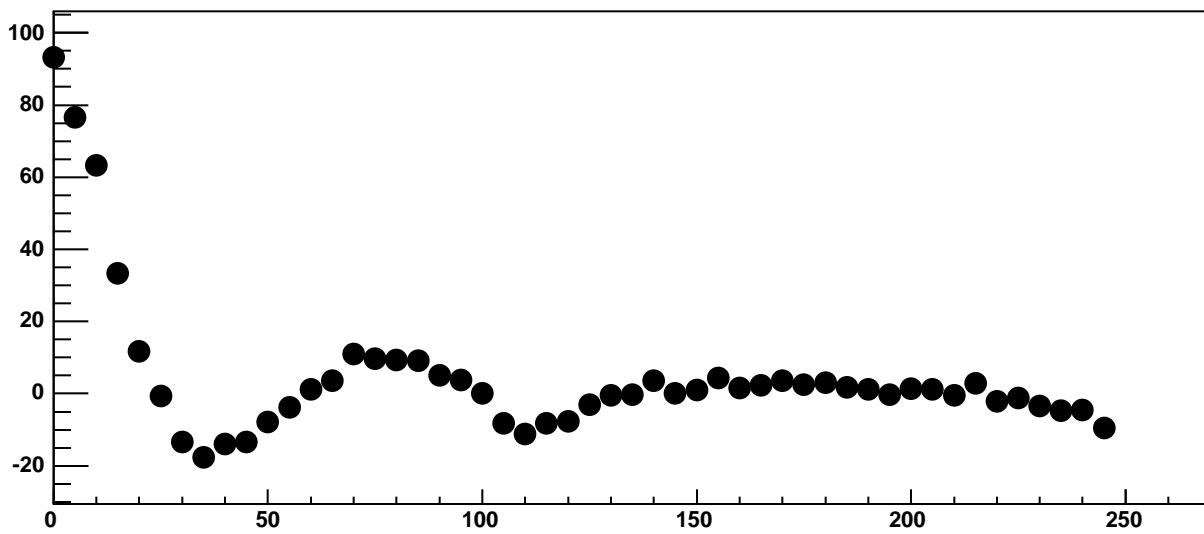


χ^2 / ndf	1000 / 41
p0	225.8 ± 0.6884
p1	110.5 ± 0.4847
p2	$-5.416\text{e}+07 \pm 1.151\text{e}+06$
p3	$2.59\text{e}+07 \pm 5.14\text{e}+05$
p4	2.095 ± 0.01326

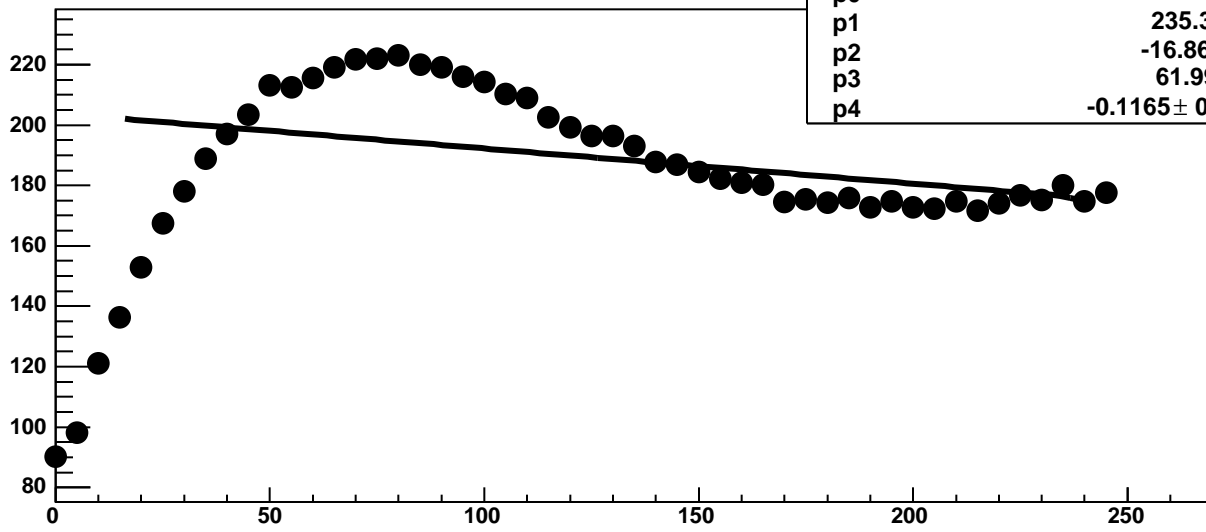
Chip 10, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

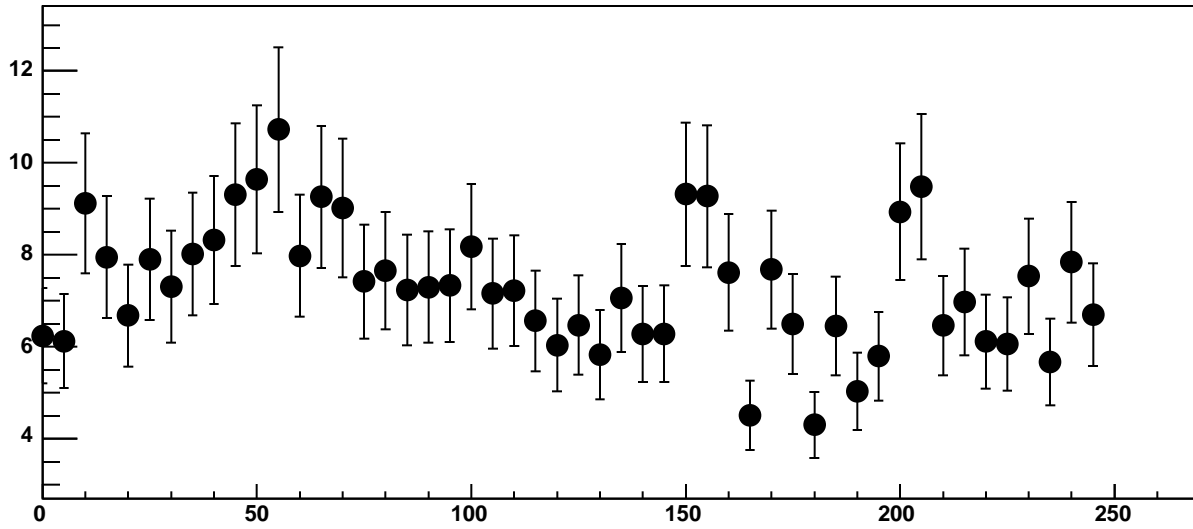


Chip 10, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold

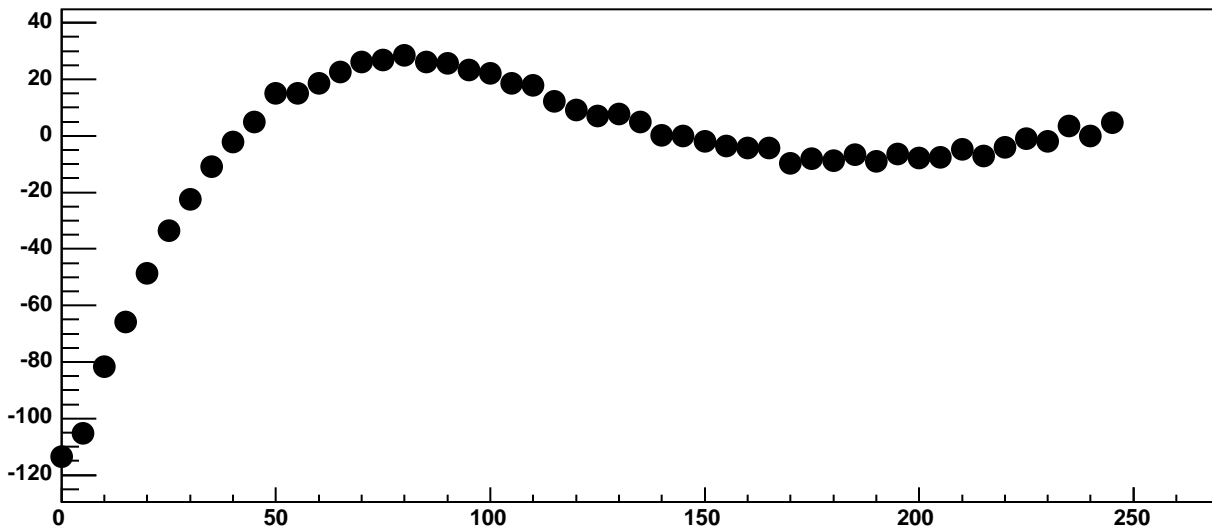


χ^2 / ndf	5408 / 41
p0	176.5 ± 2.613
p1	235.3 ± 22.01
p2	-16.86 ± 67.24
p3	61.99 ± 203
p4	-0.1165 ± 0.003774

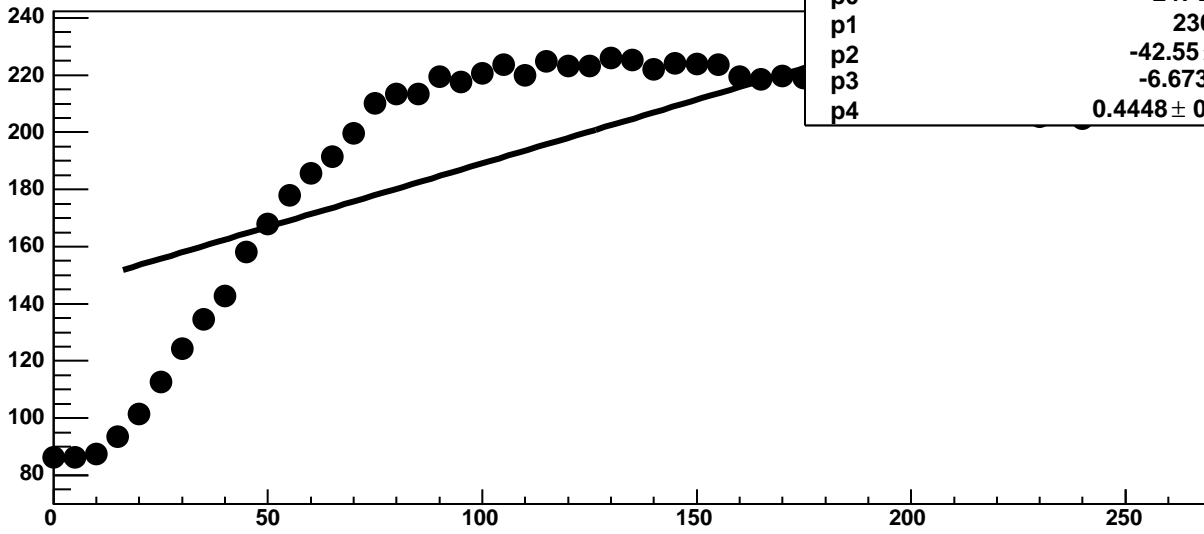
Chip 10, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold

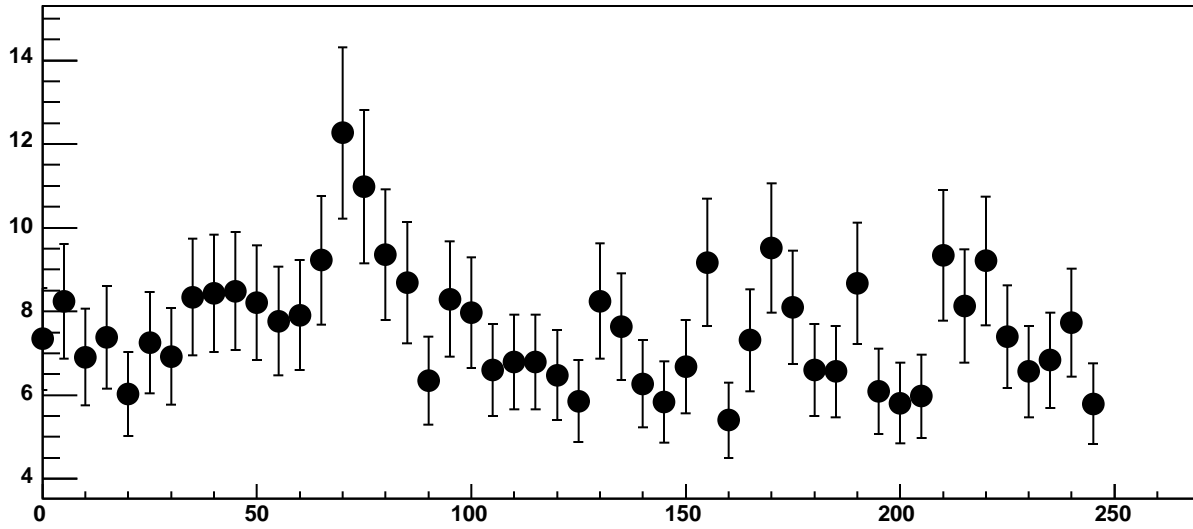


Chip 10, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

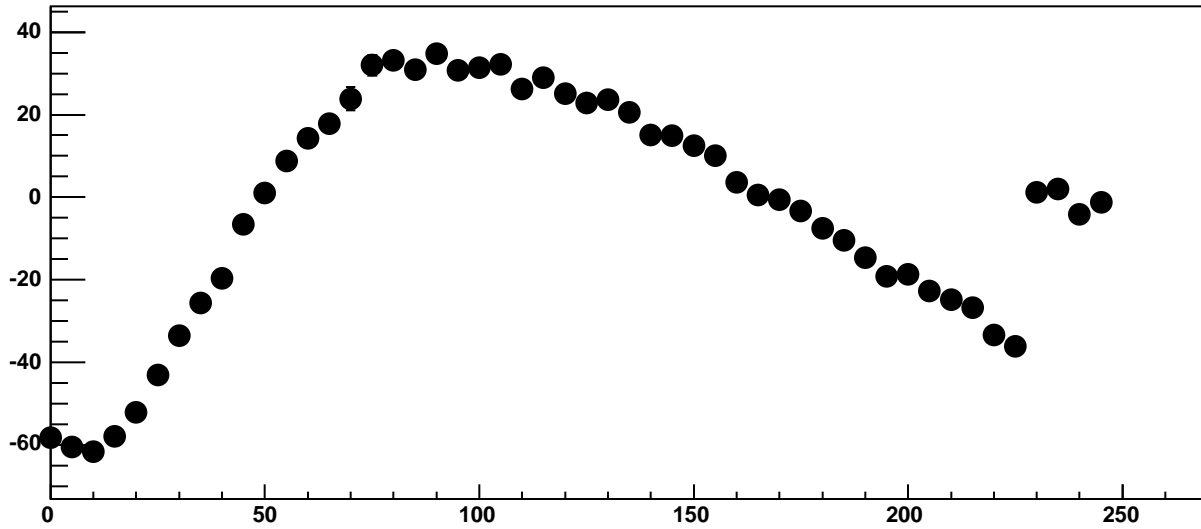


χ^2 / ndf	1.013e+04 / 41
p0	247 ± 0.5216
p1	230 ± 5.011
p2	-42.55 ± 0.8641
p3	-6.673 ± 4.857
p4	0.4448 ± 0.002529

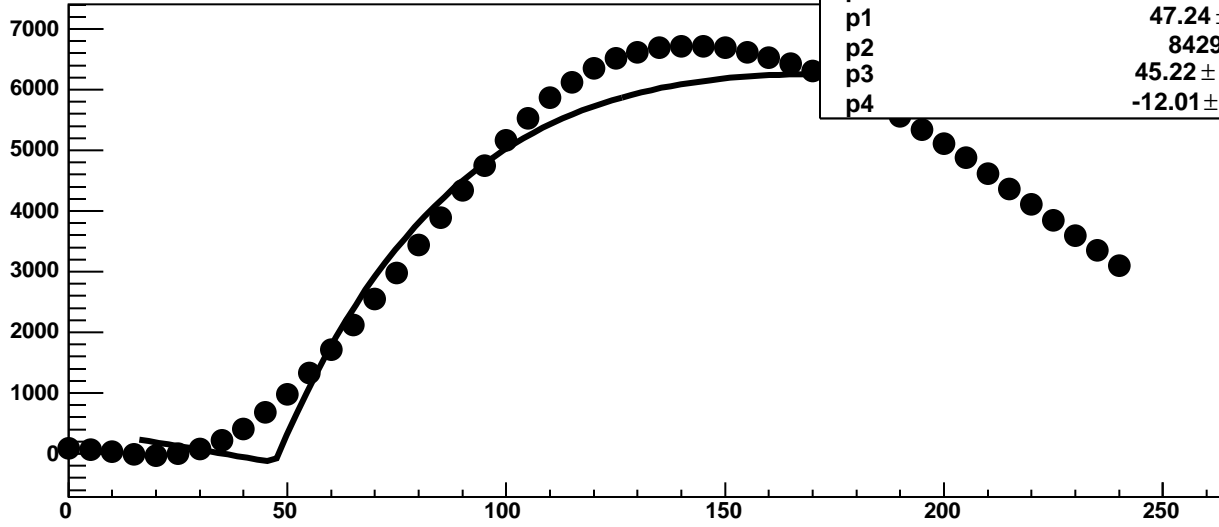
Chip 10, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

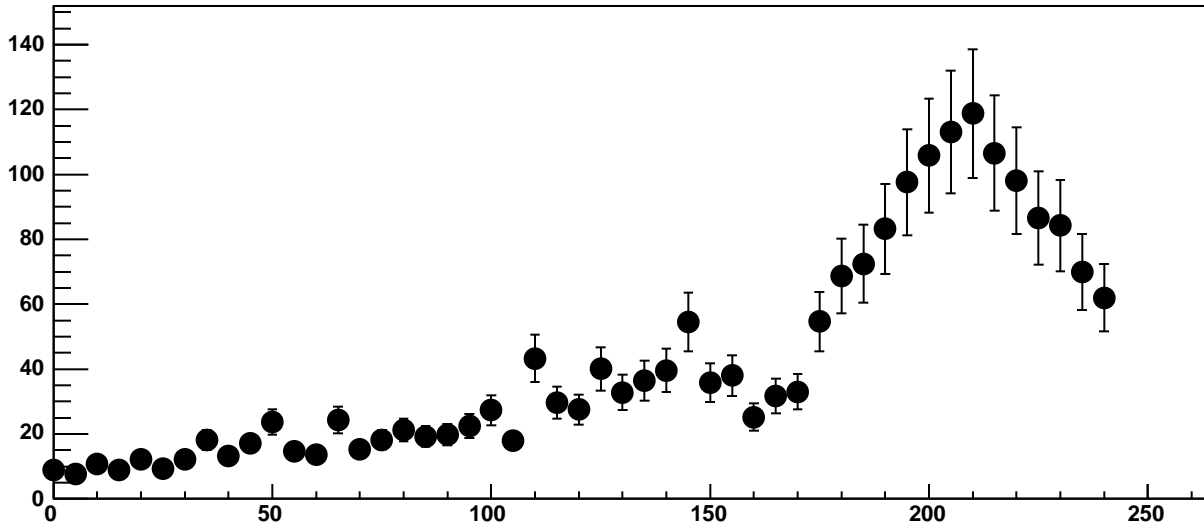


Chip 10, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold

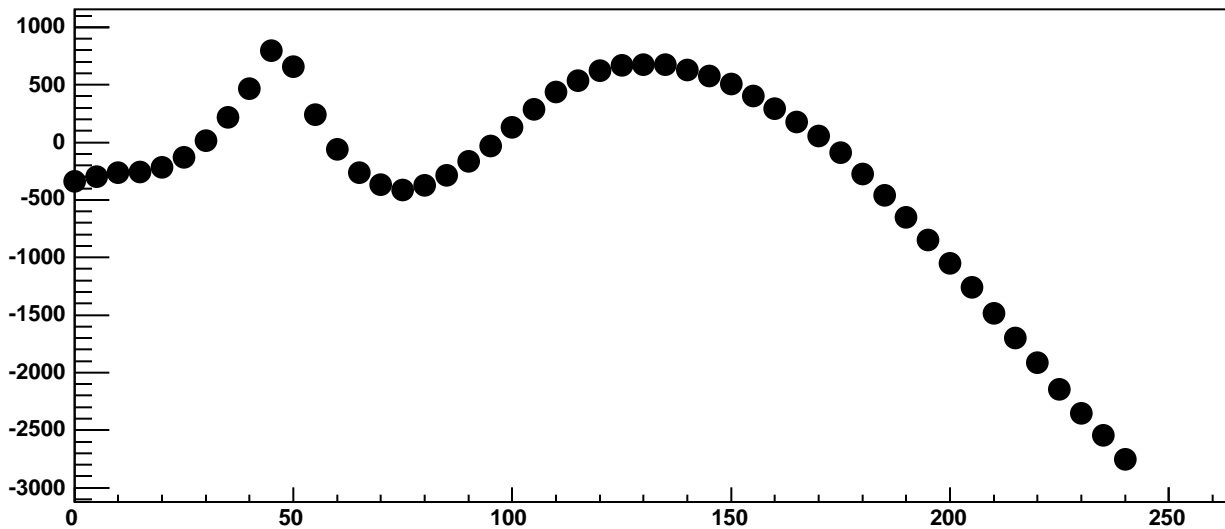


χ^2 / ndf	3.179e+05 / 41
p0	-142.2 ± 1.768
p1	47.24 ± 0.0202
p2	8429 ± 13.53
p3	45.22 ± 0.07597
p4	-12.01 ± 0.07471

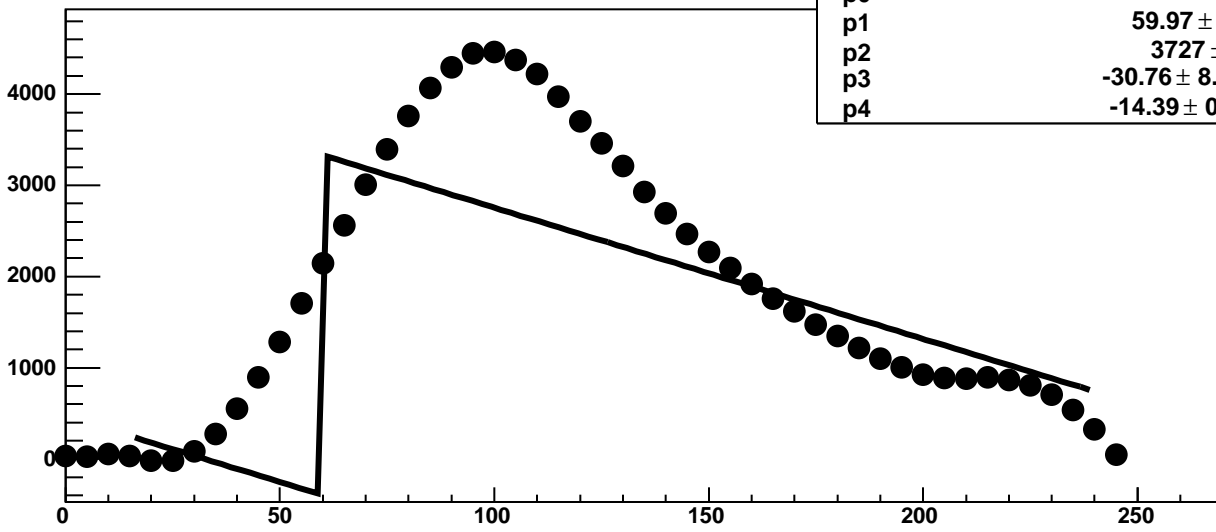
Chip 10, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

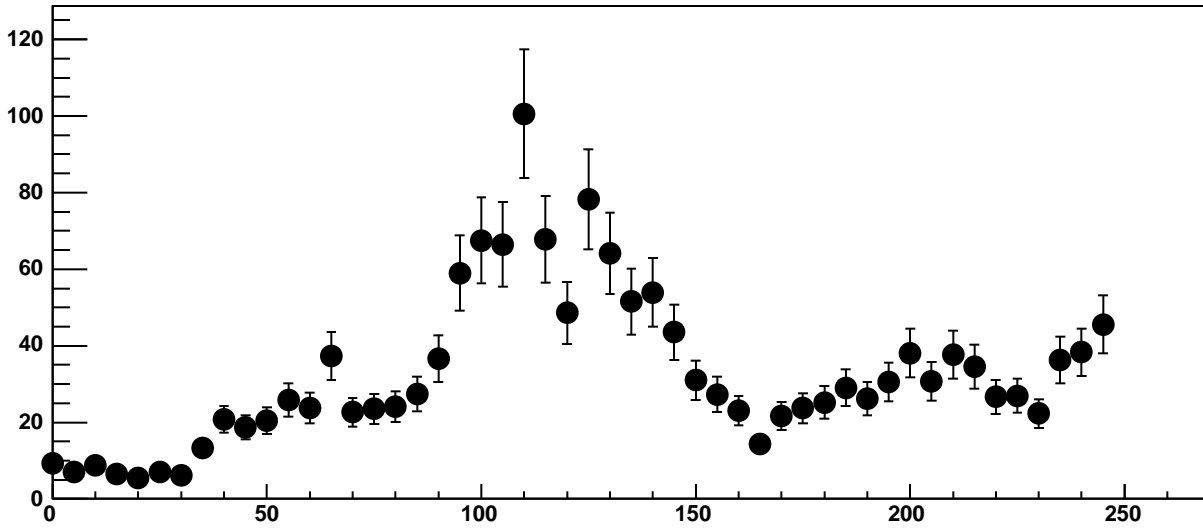


Chip 10, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

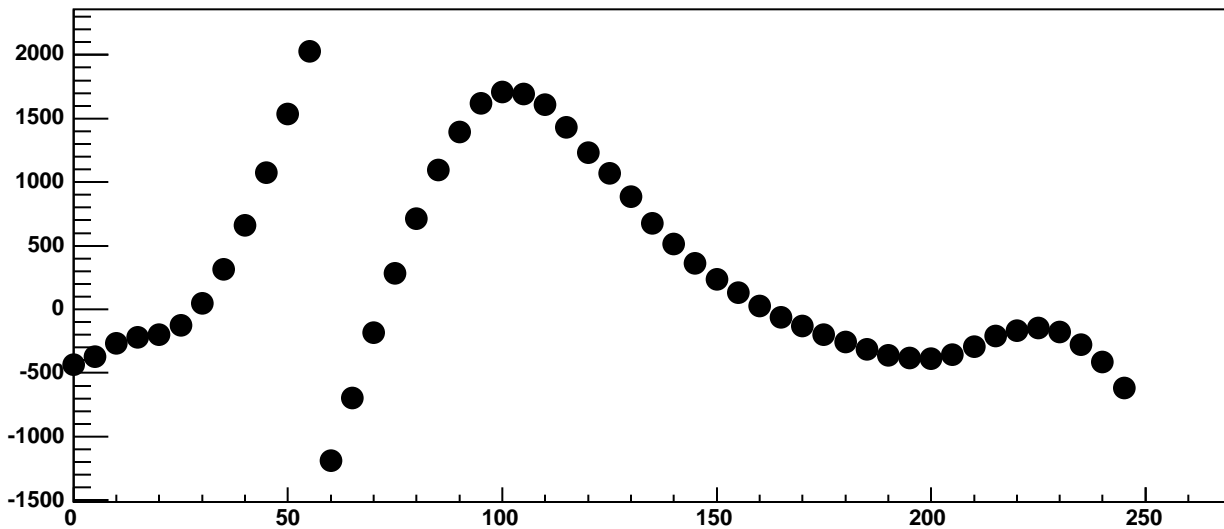


χ^2 / ndf	6.076e+05 / 41
p0	-396.1 ± 0.2253
p1	59.97 ± 0.01566
p2	3727 ± 0.9224
p3	-30.76 ± 8.159e-07
p4	-14.39 ± 0.003498

Chip 10, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold

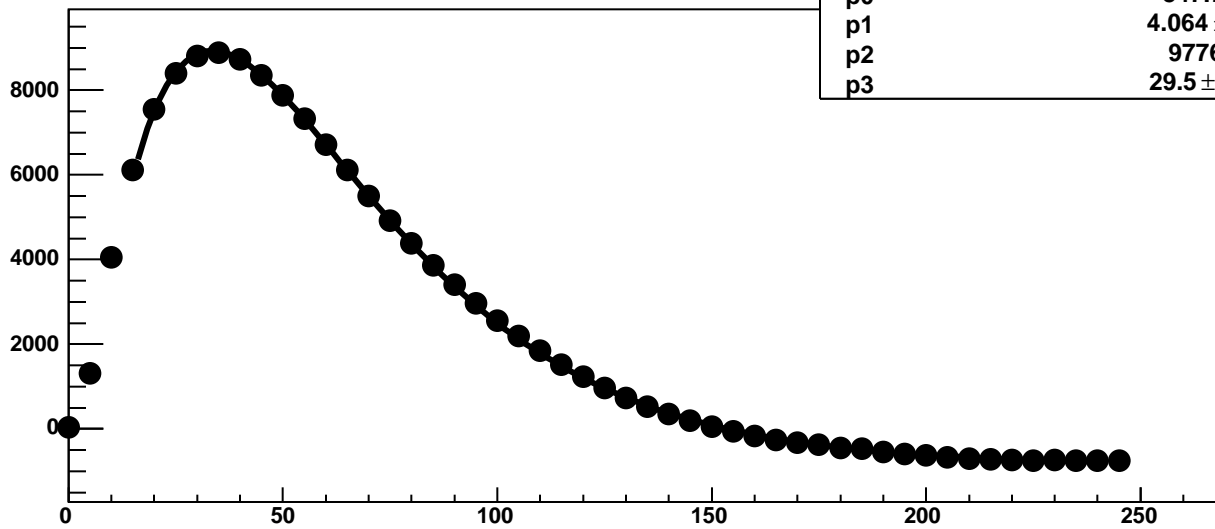


Chip 10, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

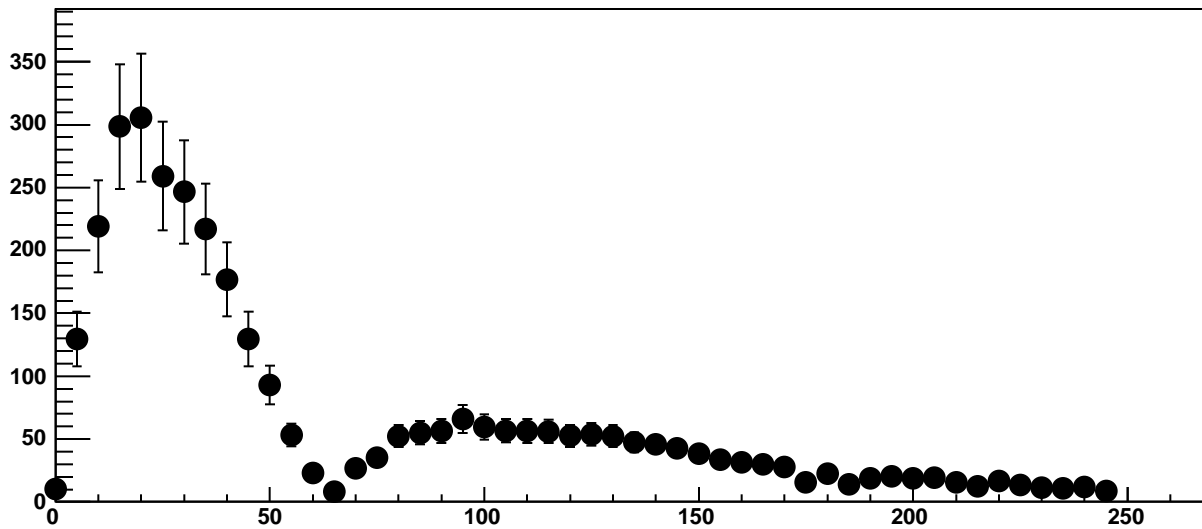


Chip 10, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold

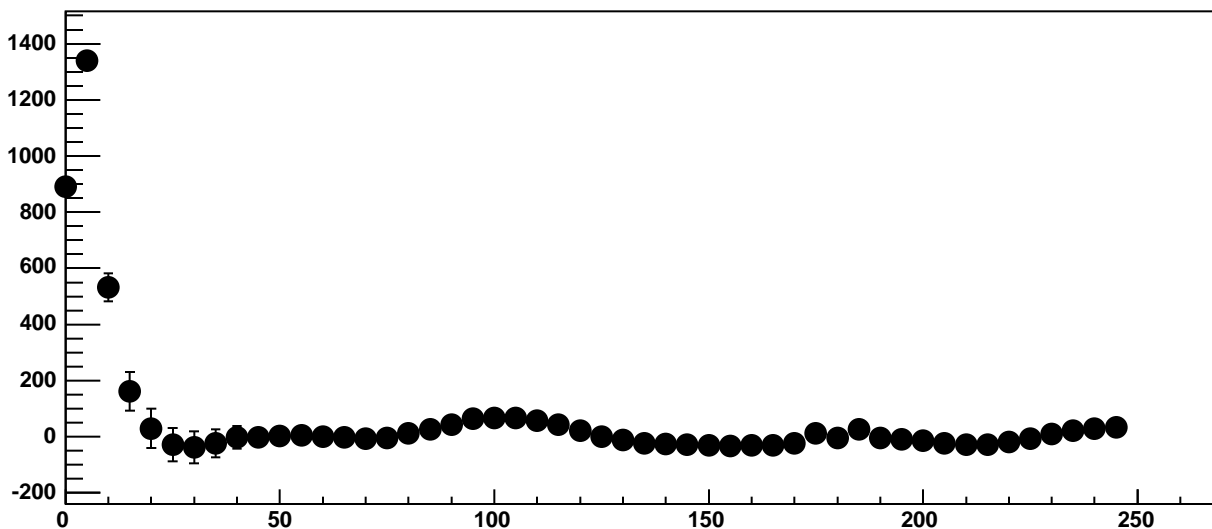
χ^2 / ndf	728.6 / 42
p0	-847.7 ± 1.355
p1	4.064 ± 0.1054
p2	9776 ± 12.52
p3	29.5 ± 0.03307



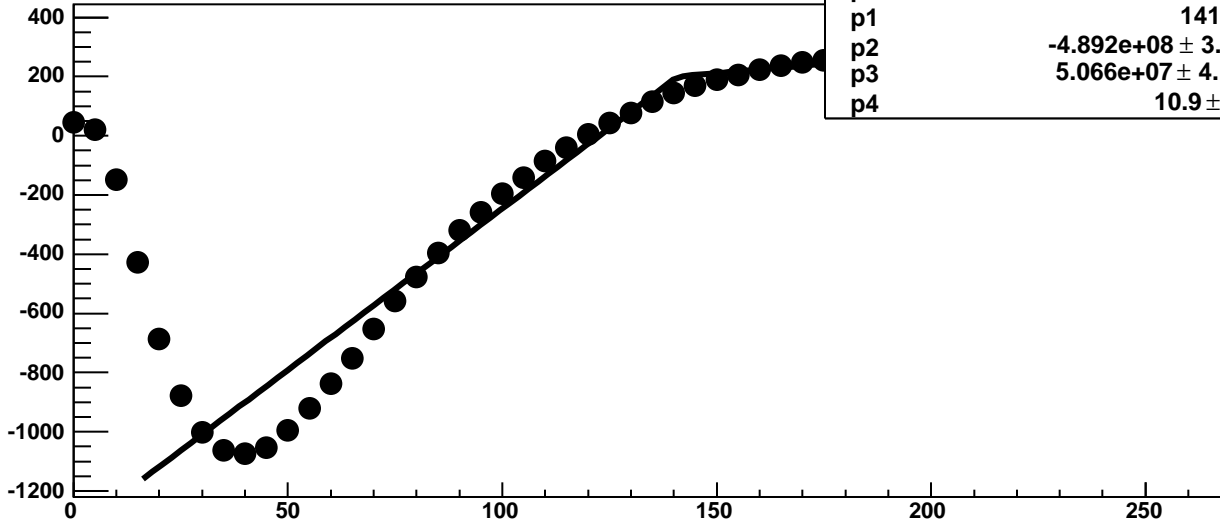
Chip 10, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold

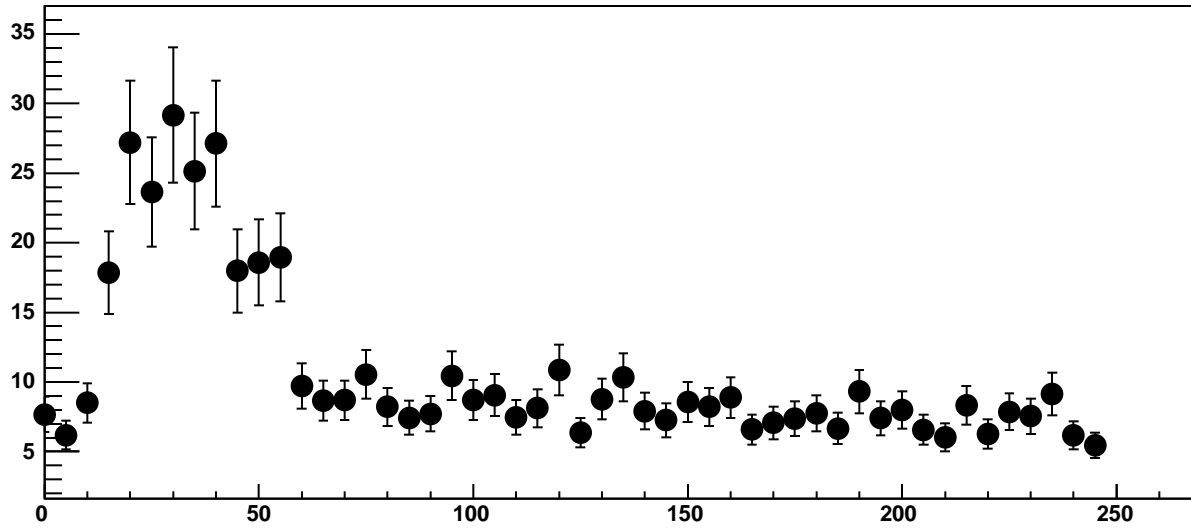


Chip 10, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

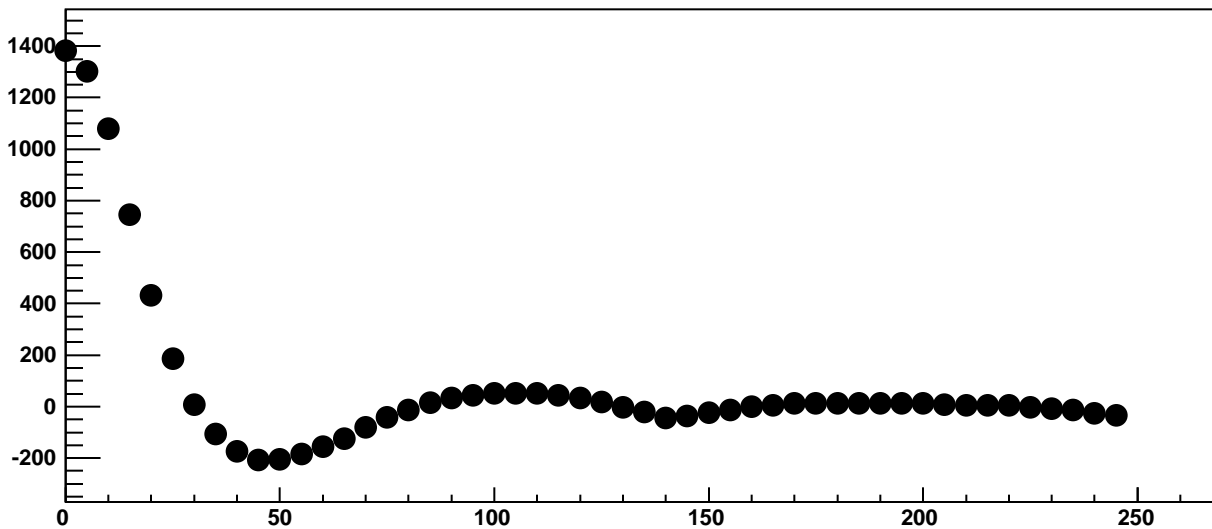


χ^2 / ndf	6.386e+04 / 41
p0	200.3 ± 0.885
p1	141 ± 0.1161
p2	-4.892e+08 ± 3.994e+06
p3	5.066e+07 ± 4.149e+05
p4	10.9 ± 0.01526

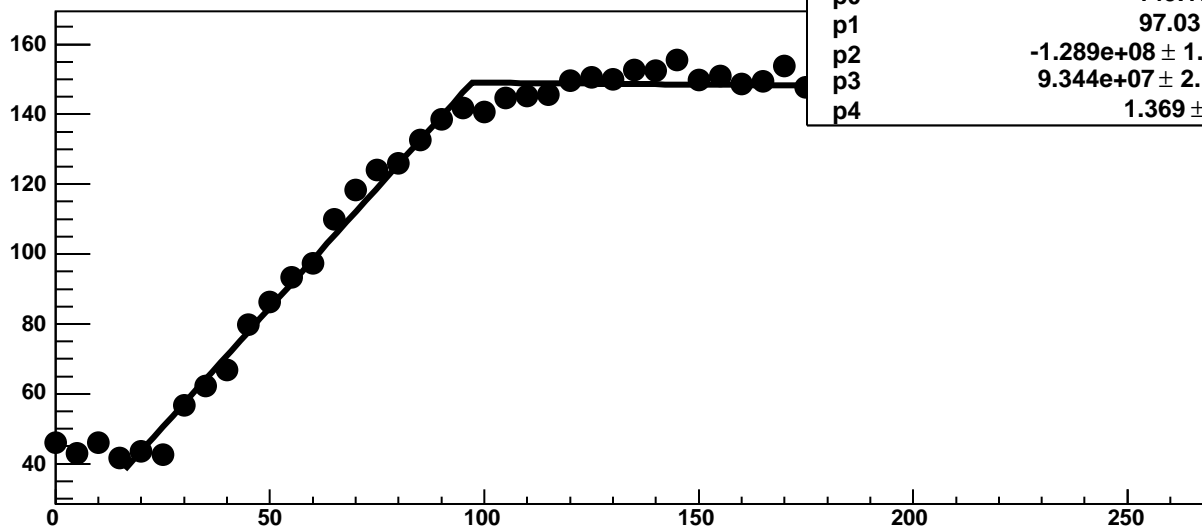
Chip 10, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold



Chip 10, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold



χ^2 / ndf

181.4 / 41

p0

149.1 ± 0.6294

p1

97.03 ± 0.7331

p2

$-1.289\text{e}+08 \pm 1.618\text{e}+06$

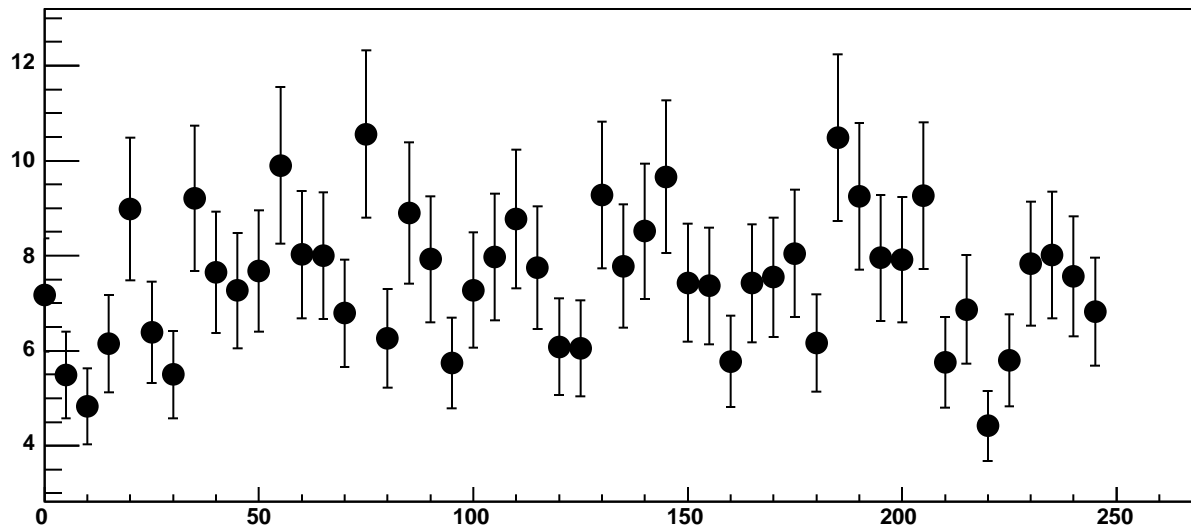
p3

$9.344\text{e}+07 \pm 2.108\text{e}+05$

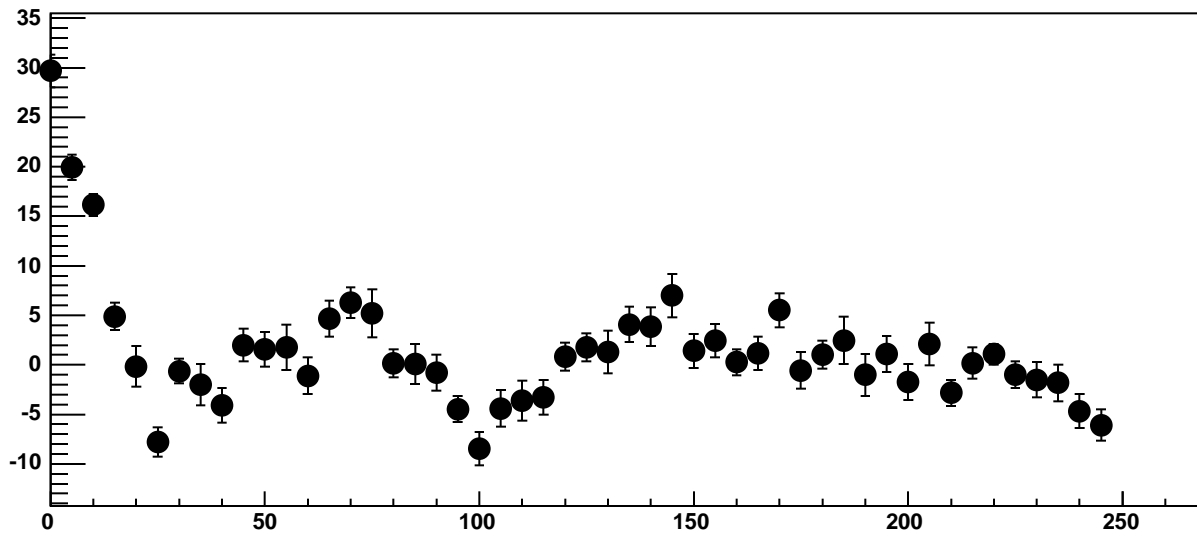
p4

1.369 ± 0.01561

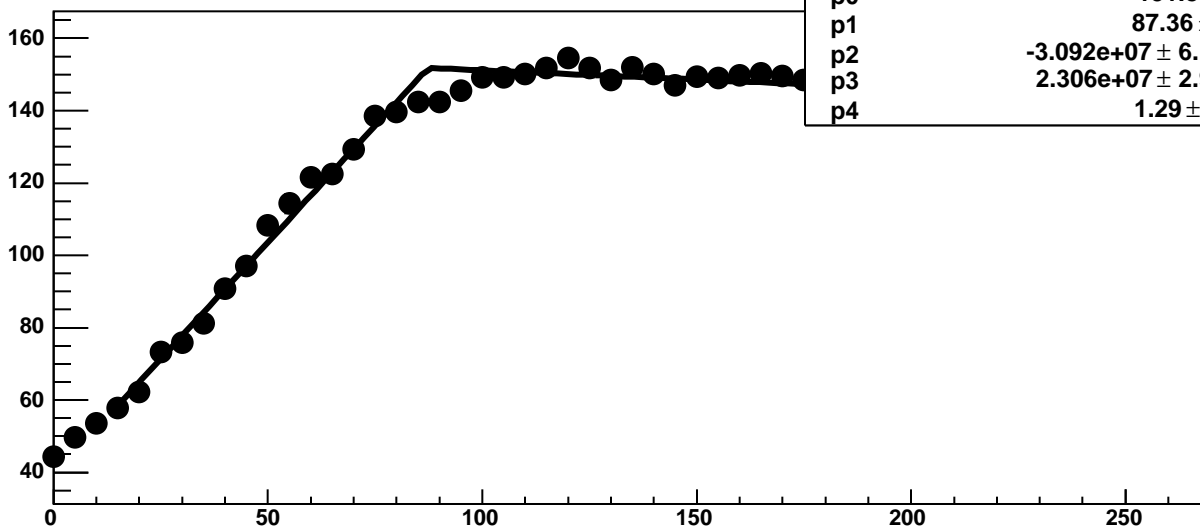
Chip 10, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold



Chip 10, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold



χ^2 / ndf

150 / 41

p0 151.8 ± 0.5611

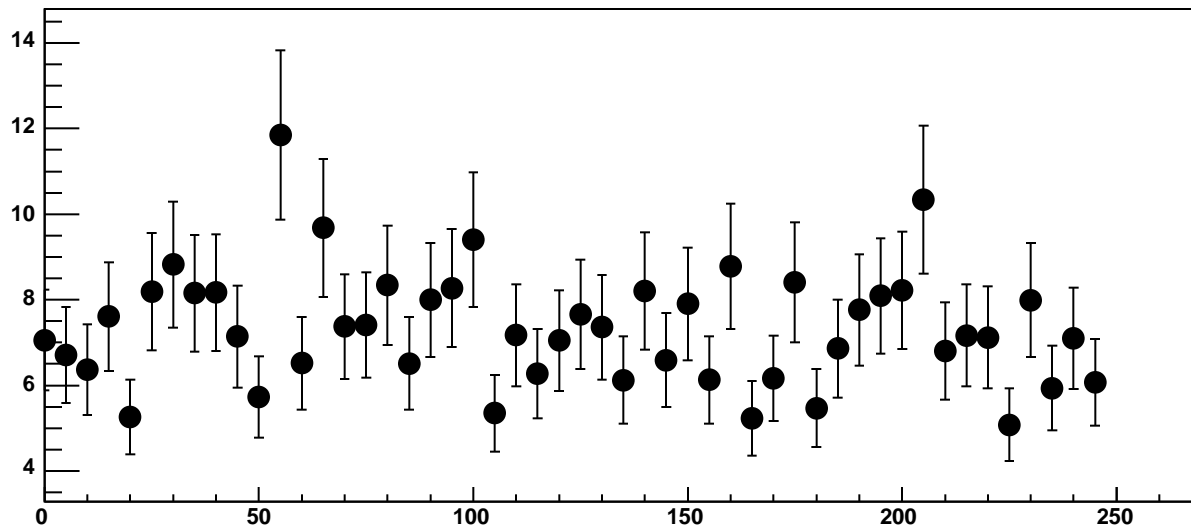
p1 87.36 ± 0.7764

p2 $-3.092\text{e}+07 \pm 6.106\text{e}+05$

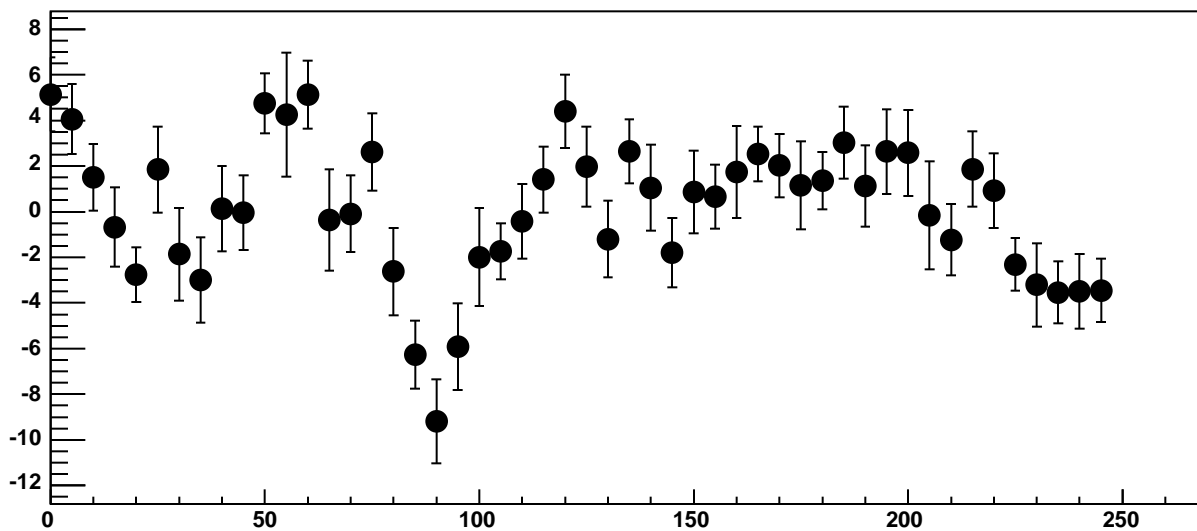
p3 $2.306\text{e}+07 \pm 2.941\text{e}+05$

p4 1.29 ± 0.01946

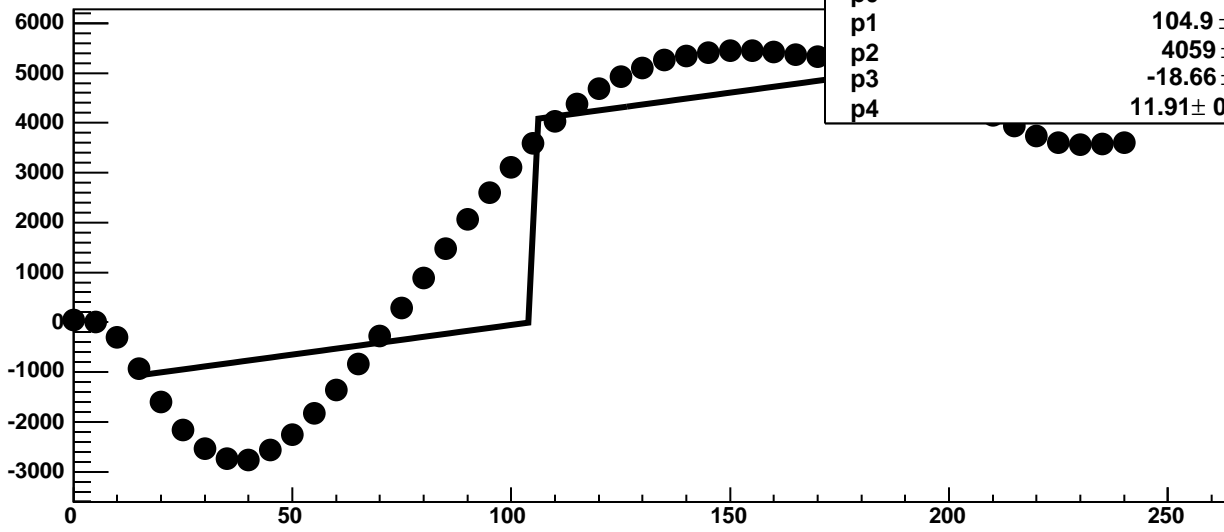
Chip 10, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold

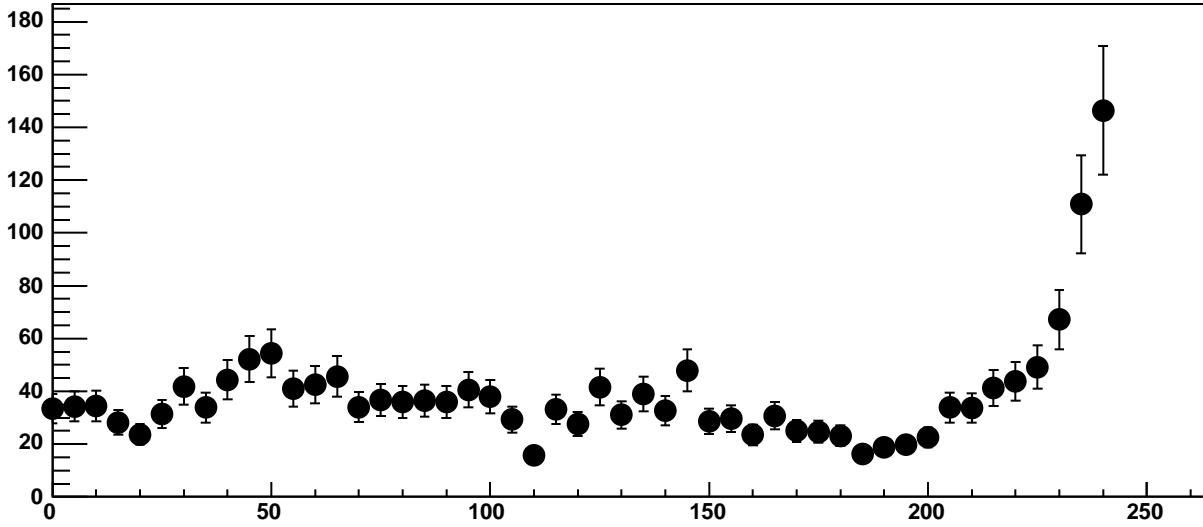


Chip 10, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

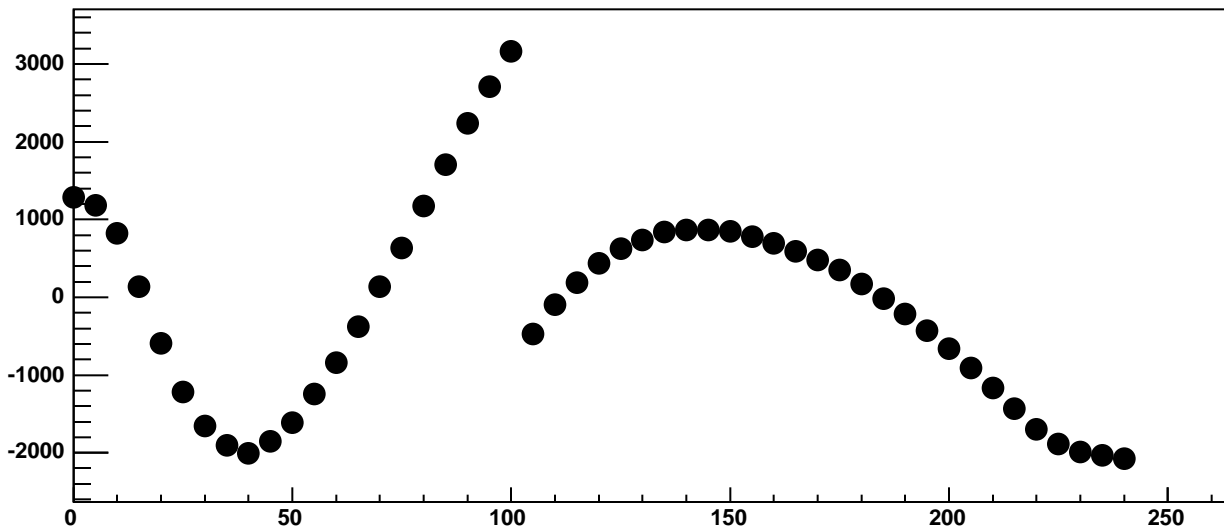


χ^2 / ndf	8.835e+05 / 41
p0	7.151 ± 0.3057
p1	104.9 ± 0.4716
p2	4059 ± 0.3817
p3	-18.66 ± 0.2613
p4	11.91 ± 0.004379

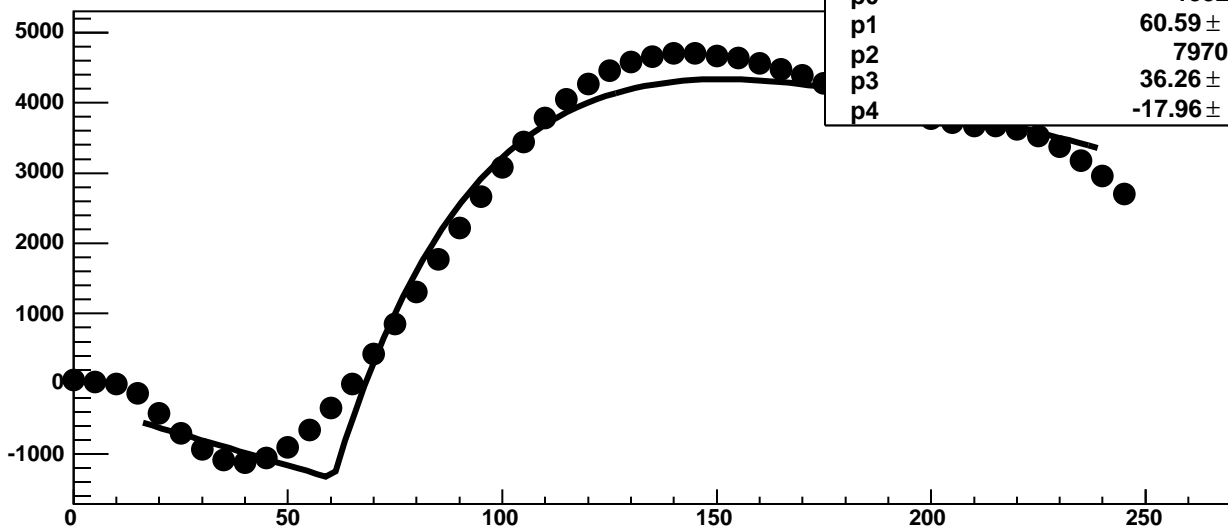
Chip 10, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold

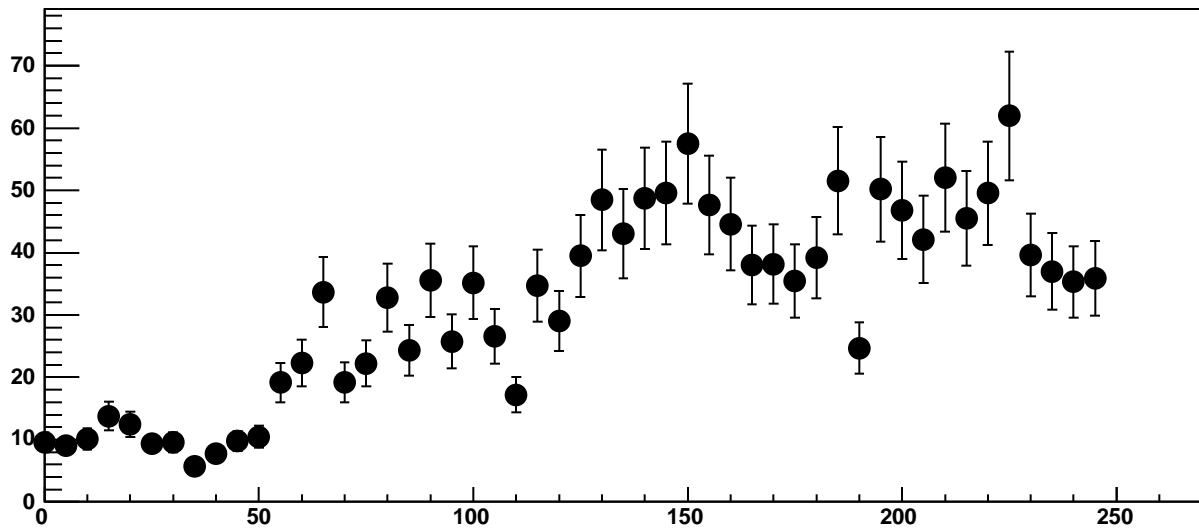


Chip 10, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold

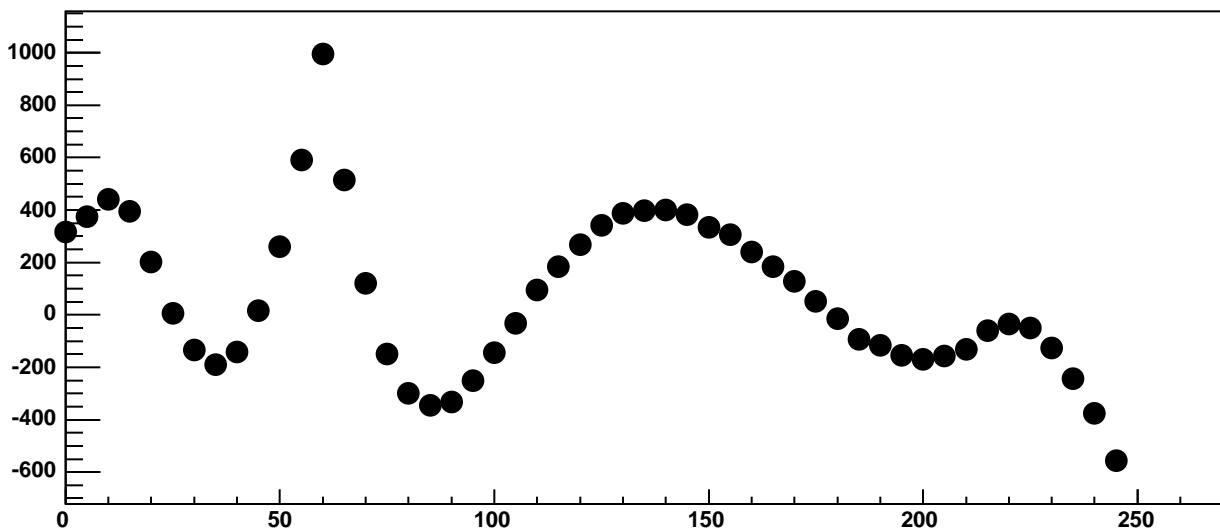


χ^2 / ndf	1.513e+05 / 41
p0	-1352 ± 1.469
p1	60.59 ± 0.02679
p2	7970 ± 10.76
p3	36.26 ± 0.07237
p4	-17.96 ± 0.05696

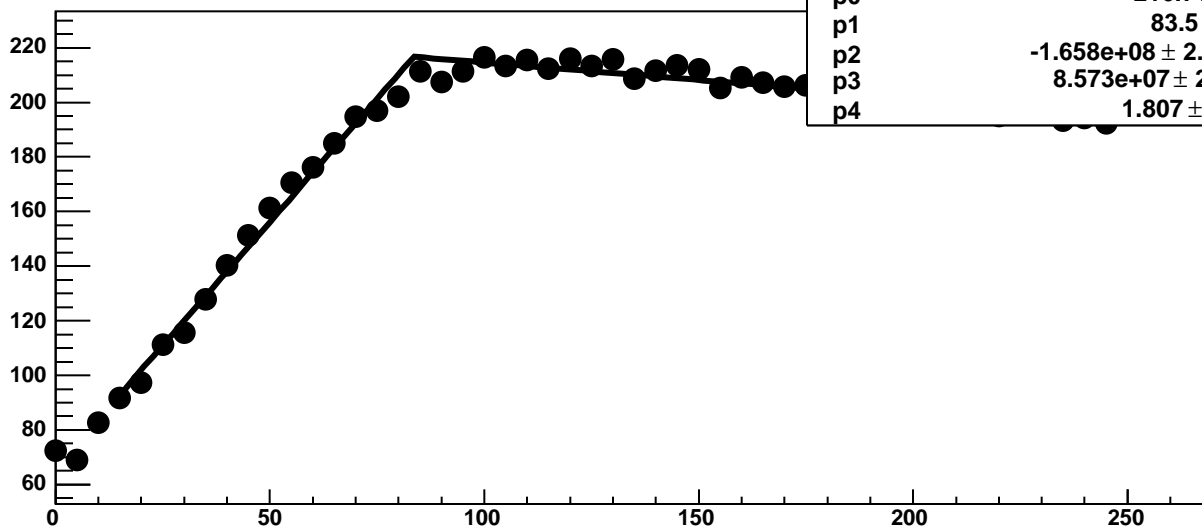
Chip 10, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold

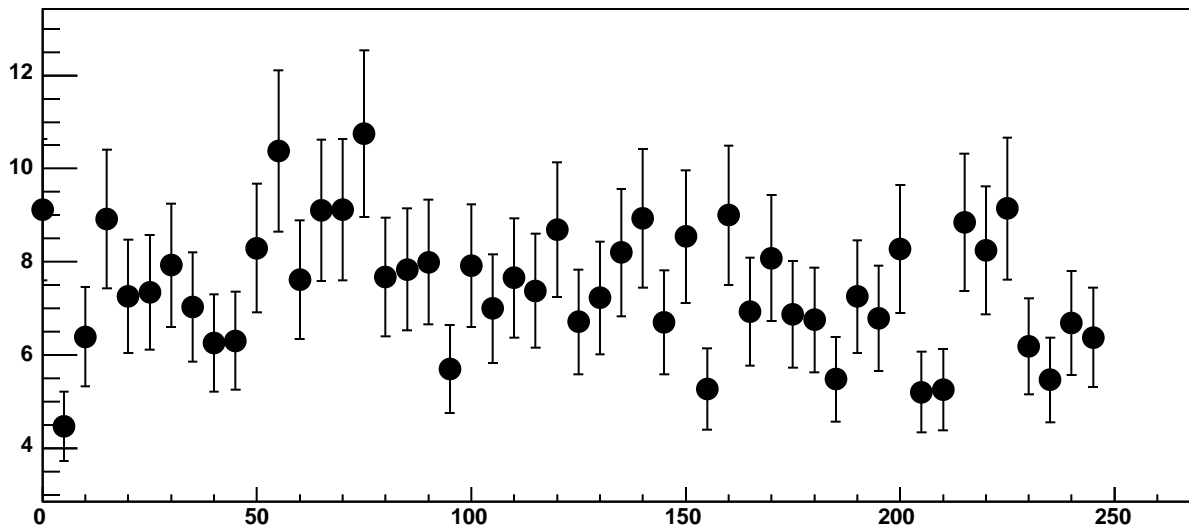


Chip 10, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold

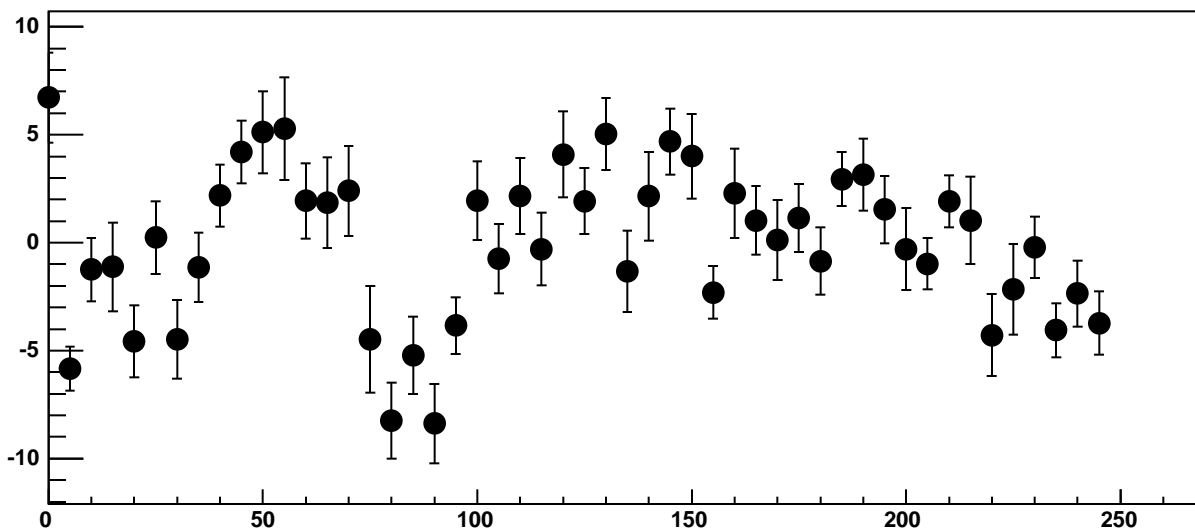


χ^2 / ndf	175.6 / 41
p0	216.7 ± 0.5474
p1	83.5 ± 0.6492
p2	$-1.658\text{e}+08 \pm 2.215\text{e}+06$
p3	$8.573\text{e}+07 \pm 2.26\text{e}+05$
p4	1.807 ± 0.02559

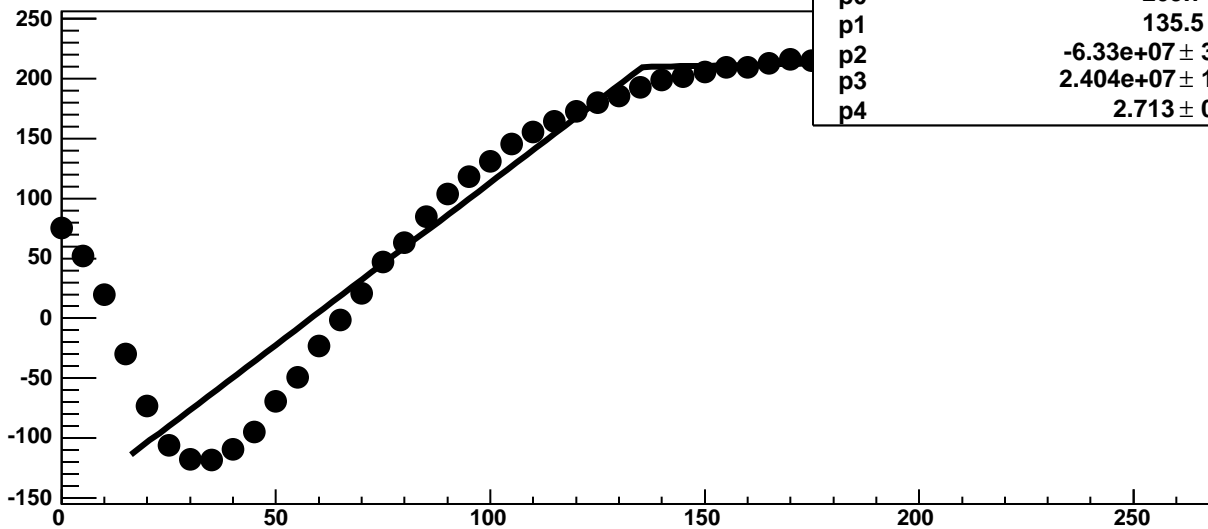
Chip 10, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold

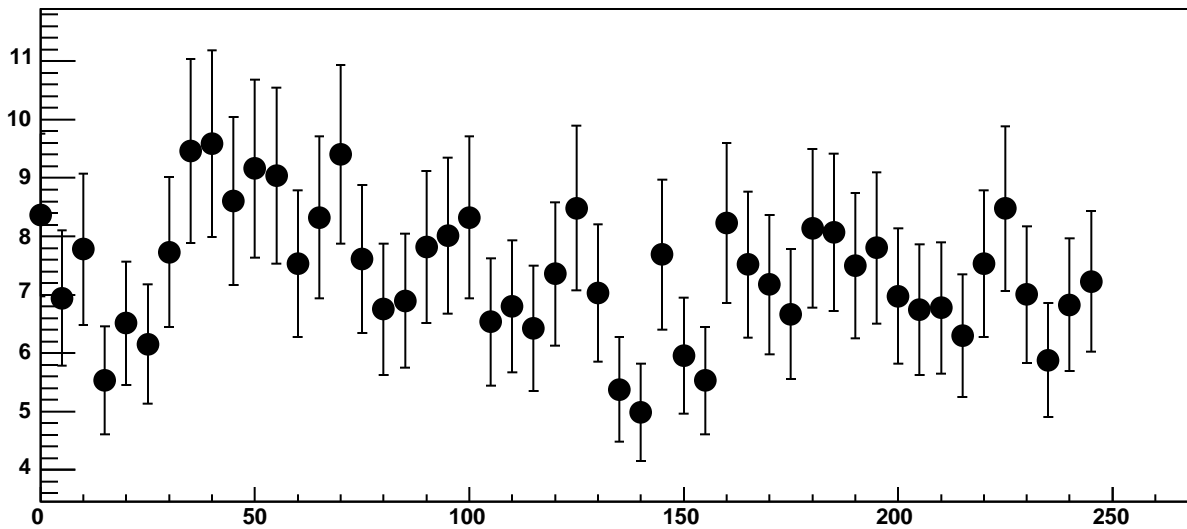


Chip 10, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

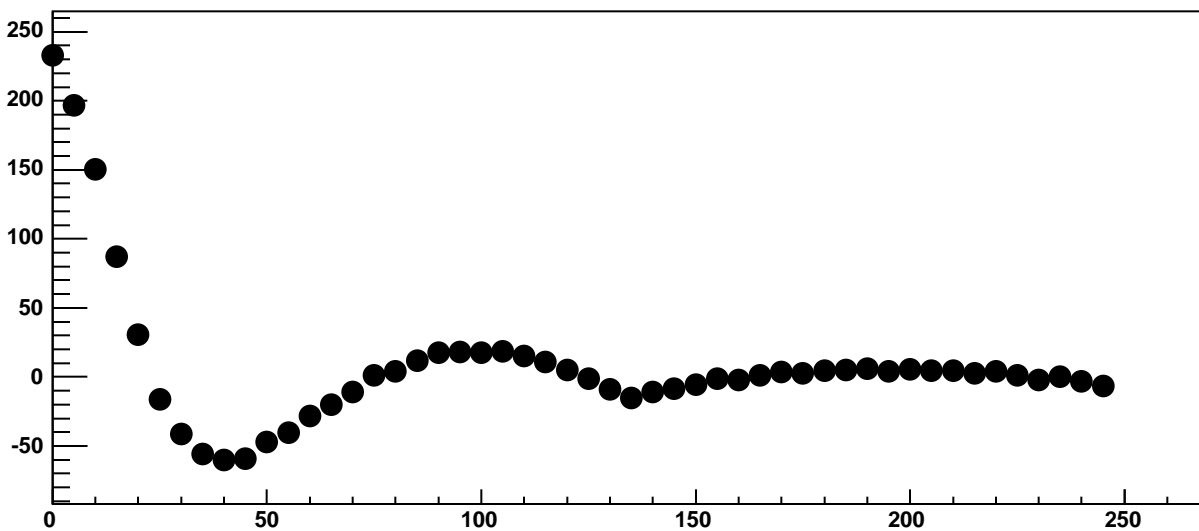


χ^2 / ndf	1.047e+04 / 41
p0	209.7 ± 0.7203
p1	135.5 ± 0.3398
p2	-6.33e+07 ± 3.86e+06
p3	2.404e+07 ± 1.39e+06
p4	2.713 ± 0.009044

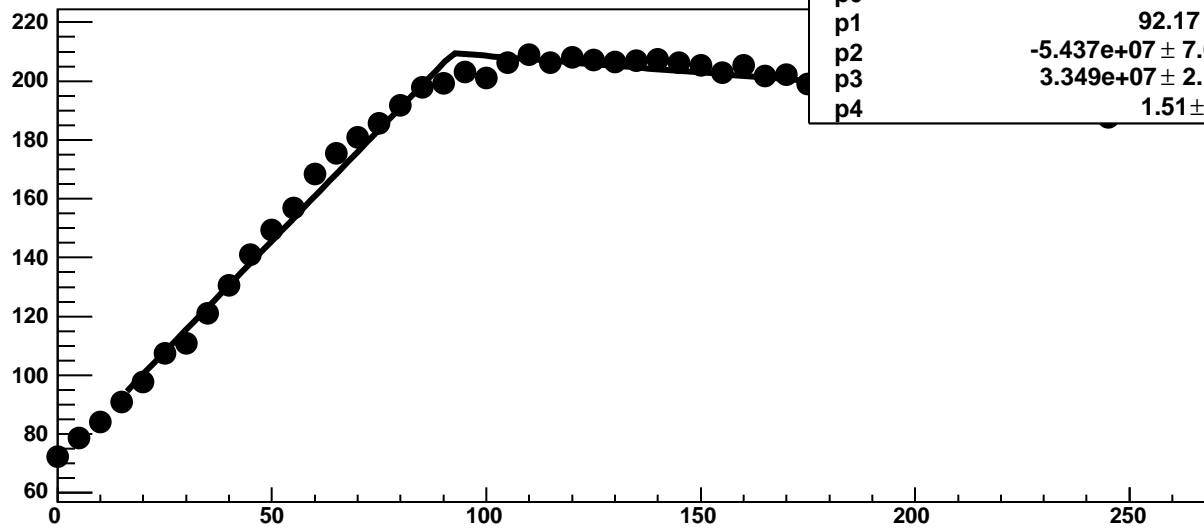
Chip 10, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

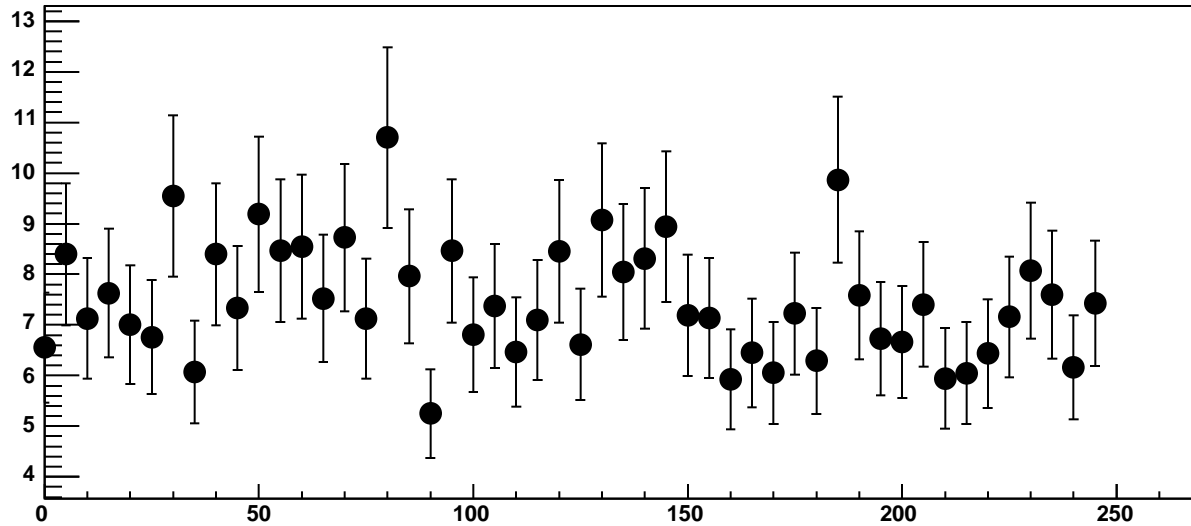


Chip 10, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

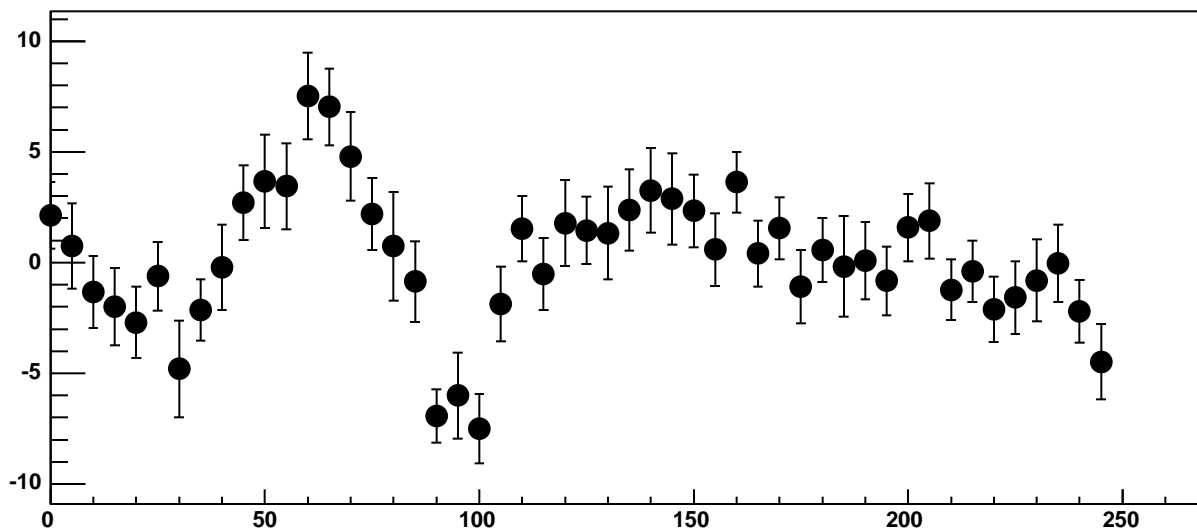


χ^2 / ndf	156.5 / 41
p0	209.5 ± 0.5766
p1	92.17 ± 0.6293
p2	$-5.437\text{e}+07 \pm 7.037\text{e}+05$
p3	$3.349\text{e}+07 \pm 2.182\text{e}+05$
p4	1.51 ± 0.01753

Chip 10, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold

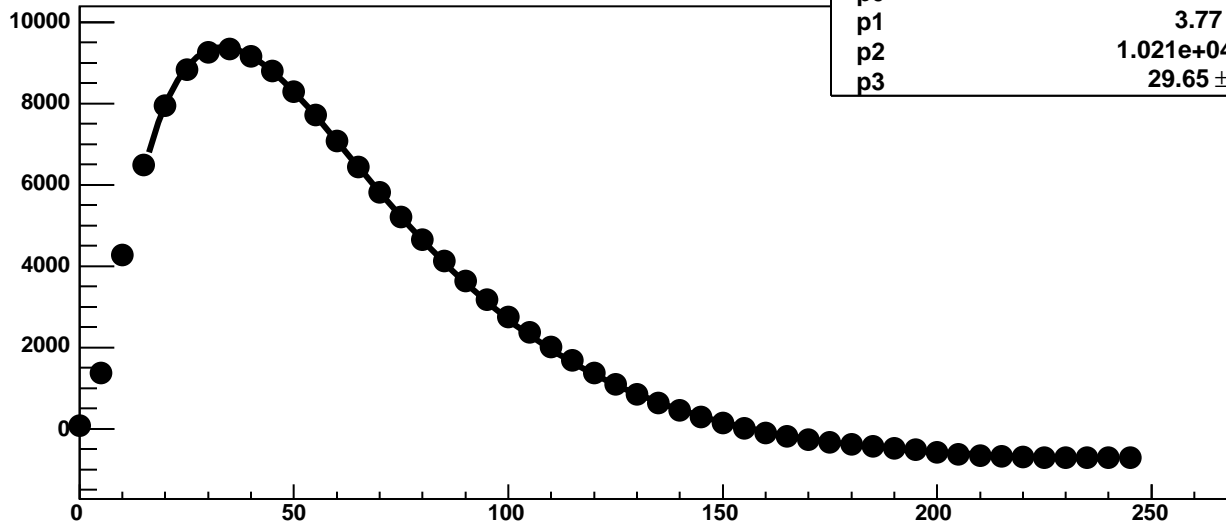


Chip 10, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

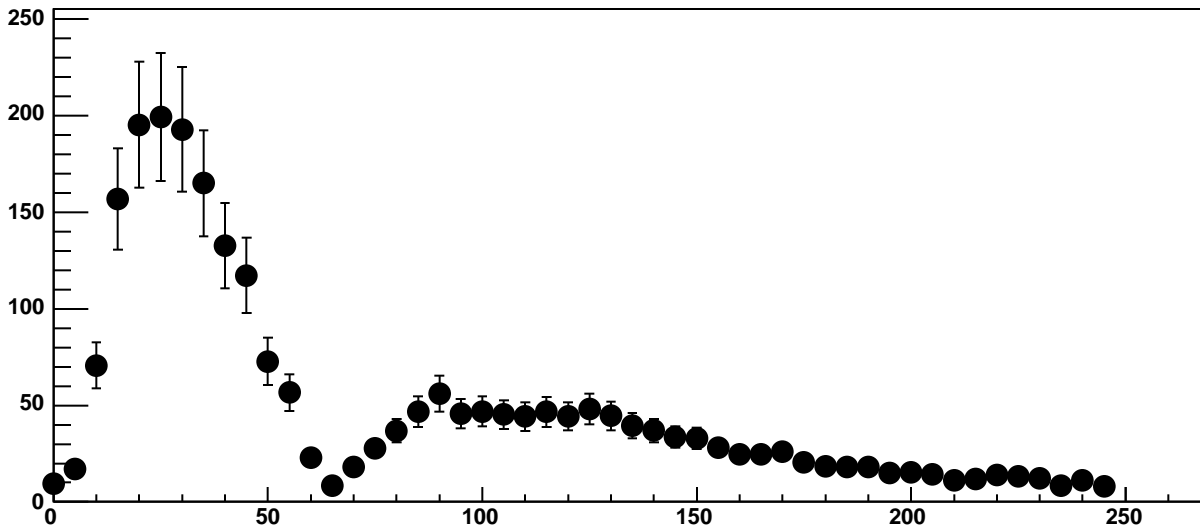


Chip 10, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold

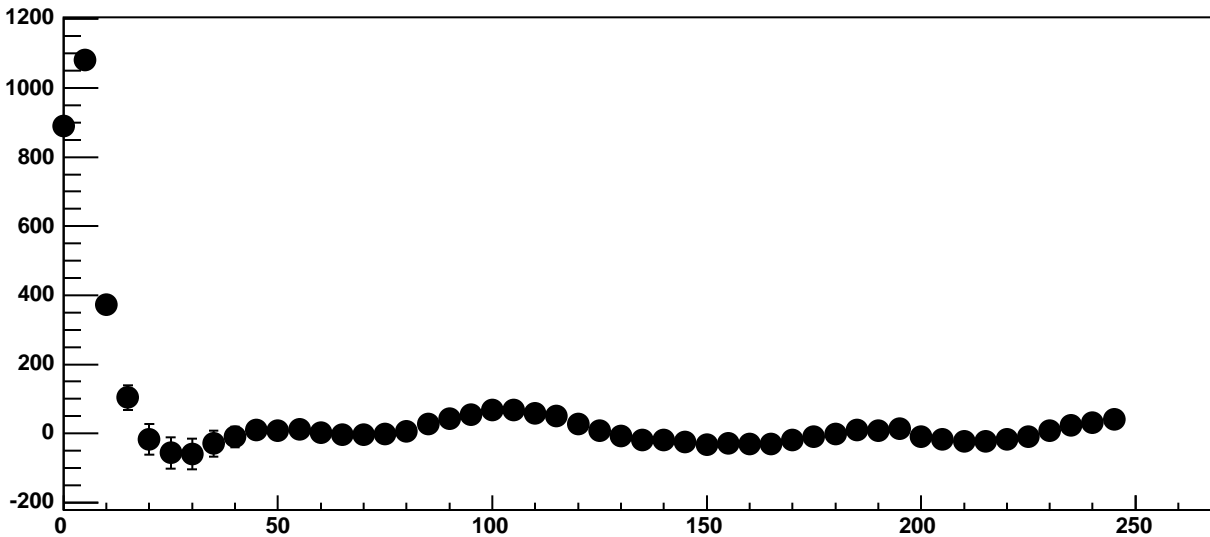
χ^2 / ndf	870.2 / 42
p0	-821.1 ± 1.161
p1	3.77 ± 0.0652
p2	1.021e+04 ± 8.352
p3	29.65 ± 0.02531



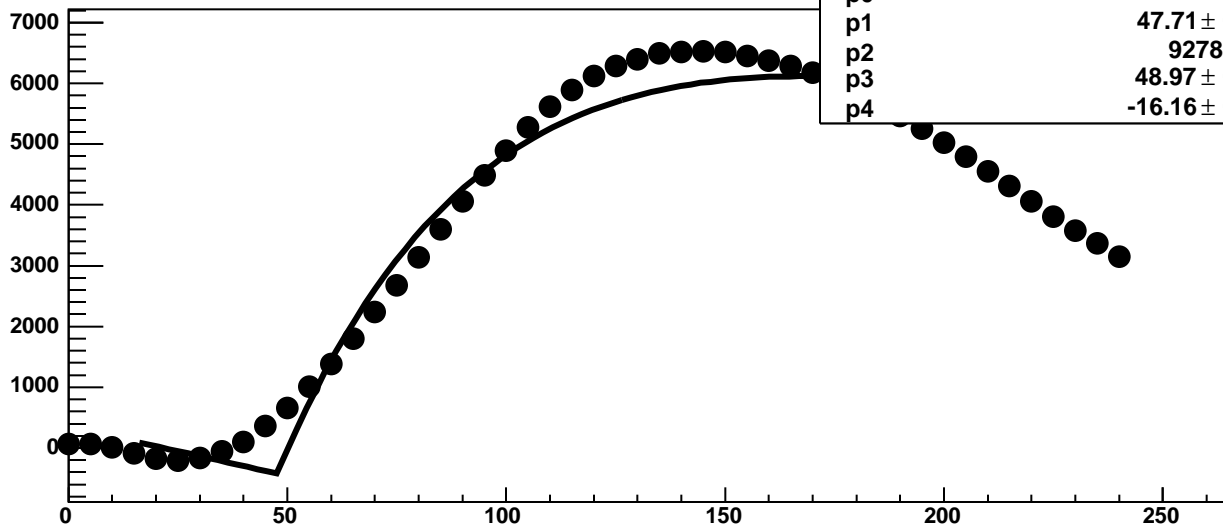
Chip 10, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold

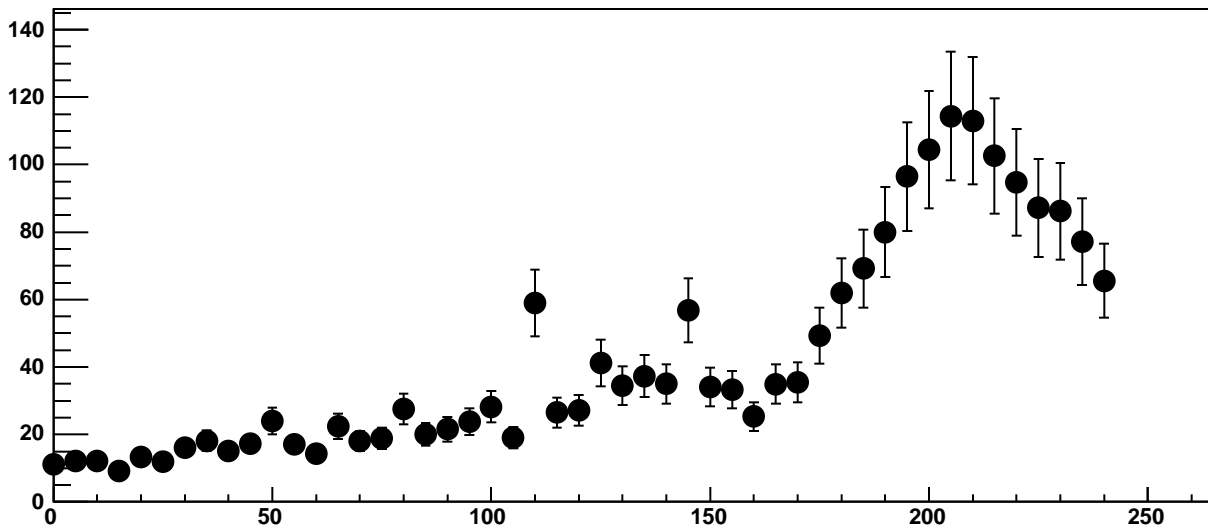


Chip 10, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

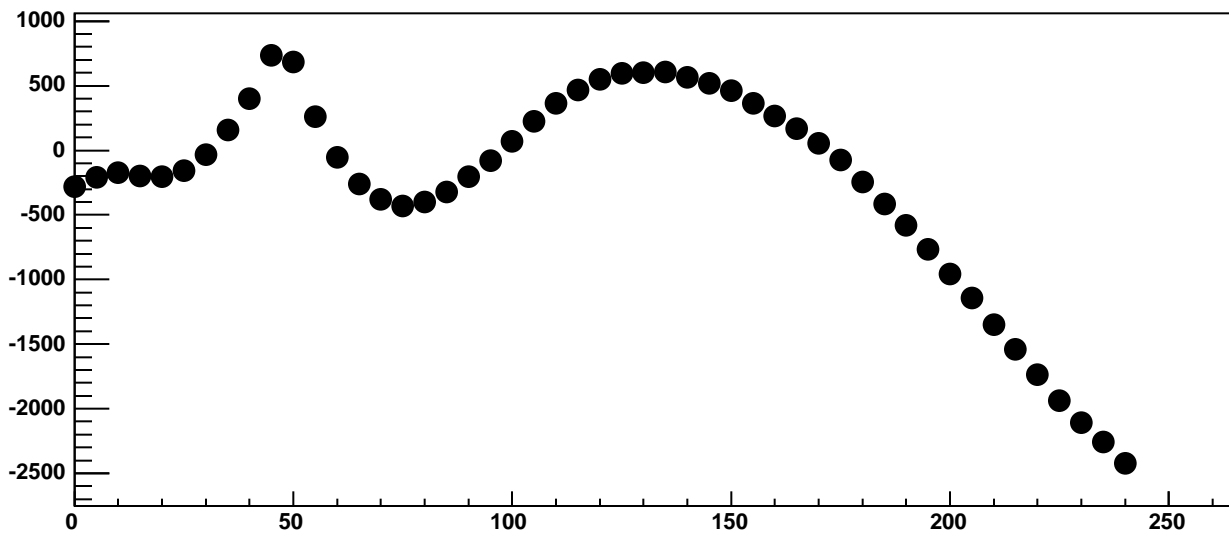


χ^2 / ndf	2.476e+05 / 41
p0	-420.5 ± 1.968
p1	47.71 ± 0.02093
p2	9278 ± 15.69
p3	48.97 ± 0.08415
p4	-16.16 ± 0.08259

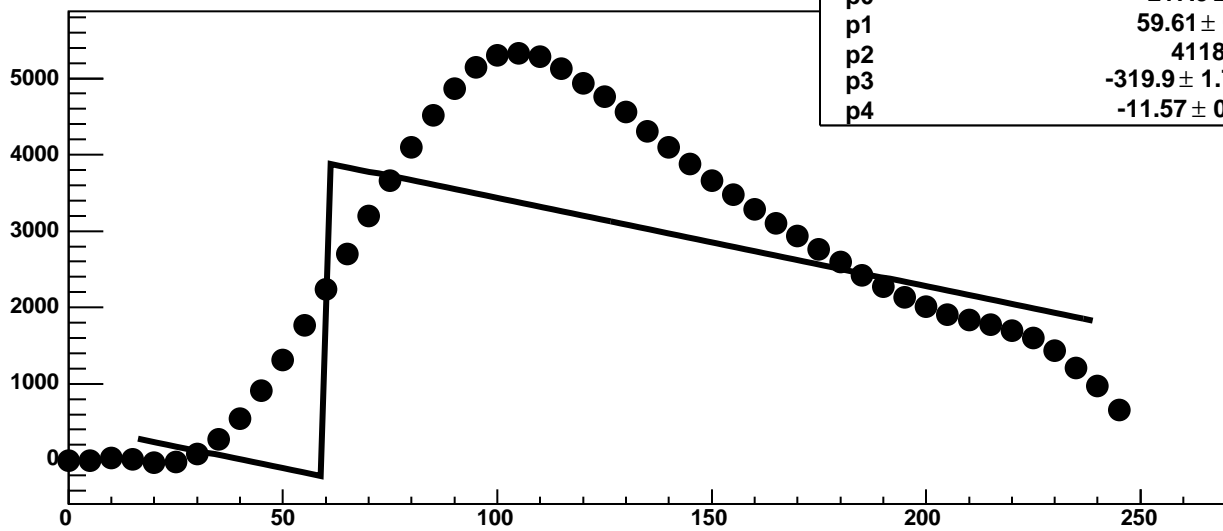
Chip 10, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold

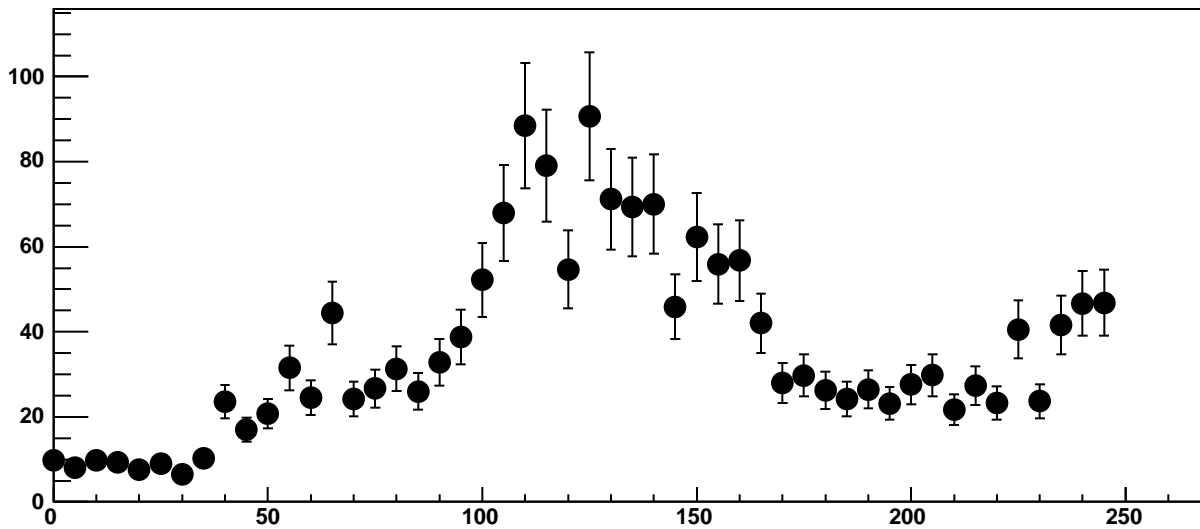


Chip 10, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

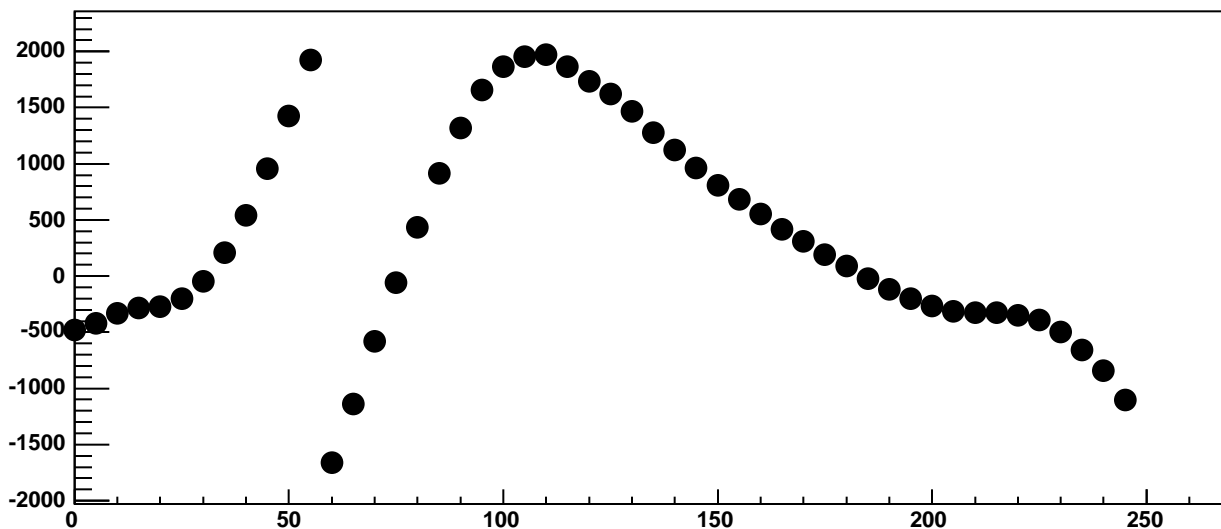


χ^2 / ndf	6.587e+05 / 41
p0	-217.9 ± 0.6054
p1	59.61 ± 0.05233
p2	4118 ± 1.046
p3	-319.9 ± 1.746e-06
p4	-11.57 ± 0.008131

Chip 10, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold



Chip 10, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold

χ^2 / ndf

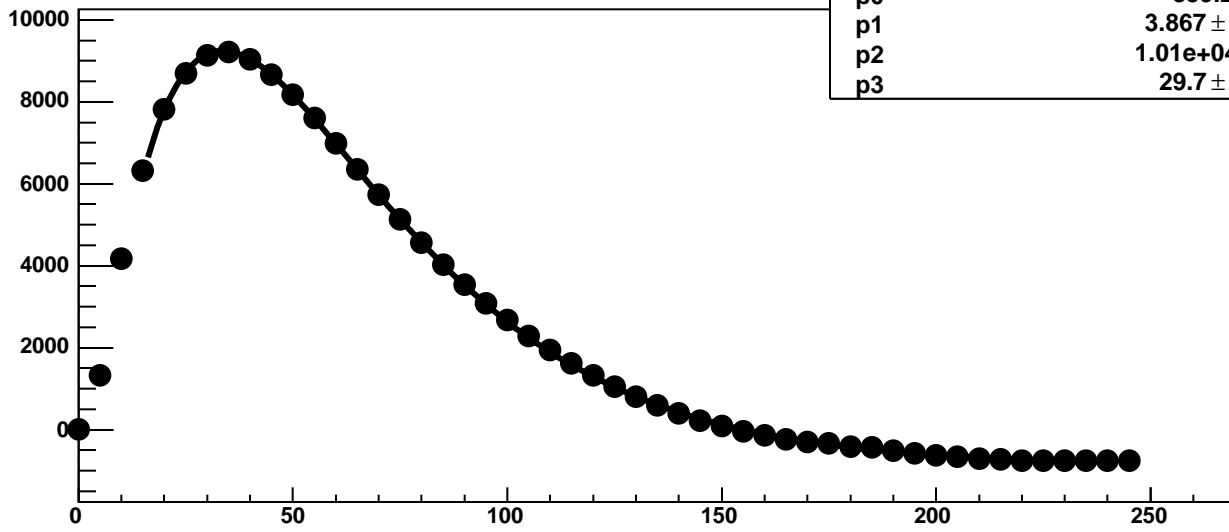
1017 / 42

p0 -859.2 ± 1.179

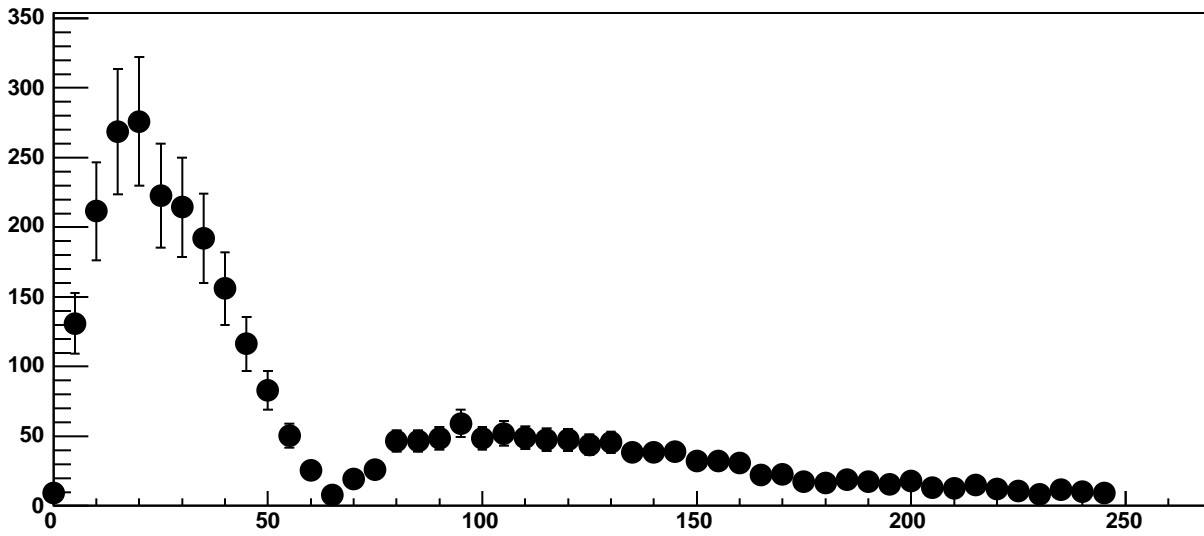
p1 3.867 ± 0.09213

p2 $1.01\text{e}+04 \pm 11.21$

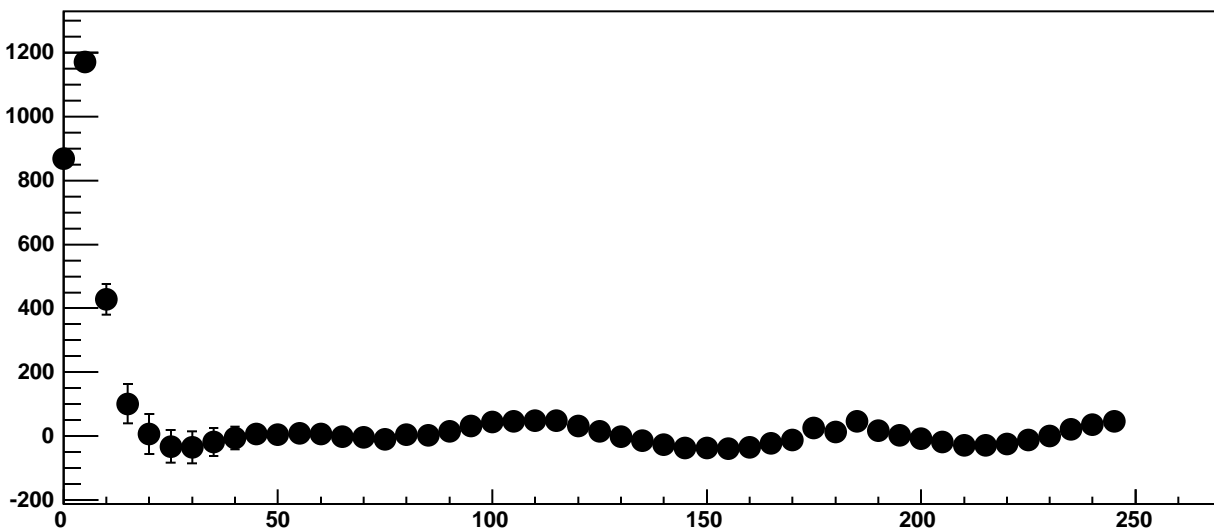
p3 29.7 ± 0.02854



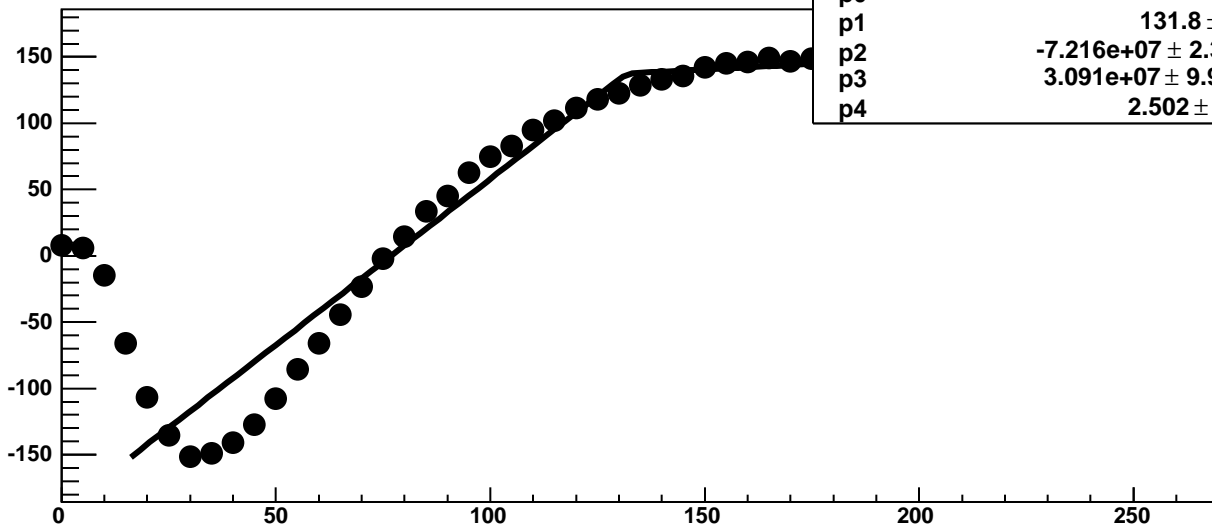
Chip 10, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold

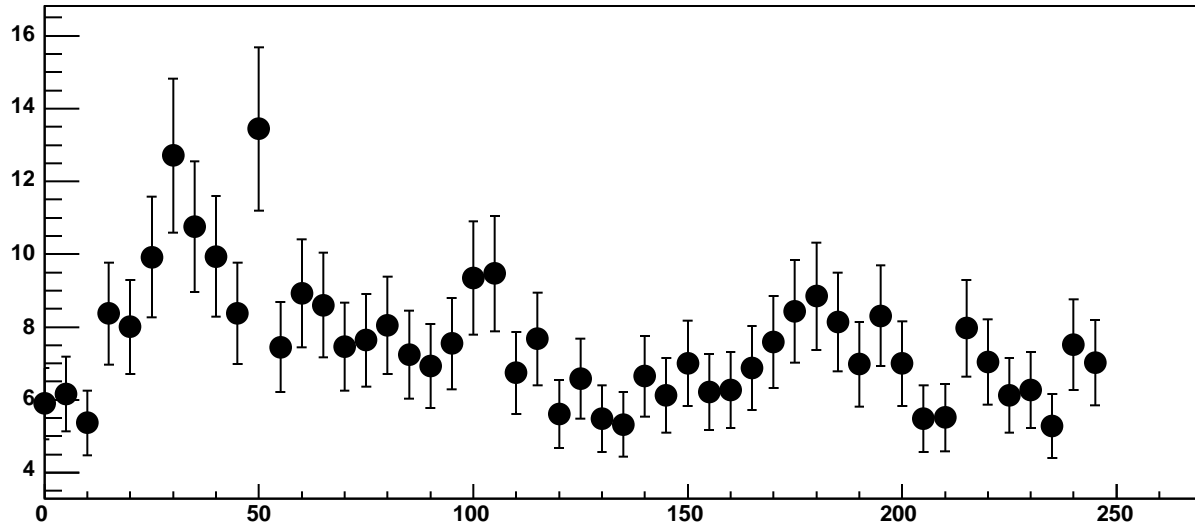


Chip 10, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

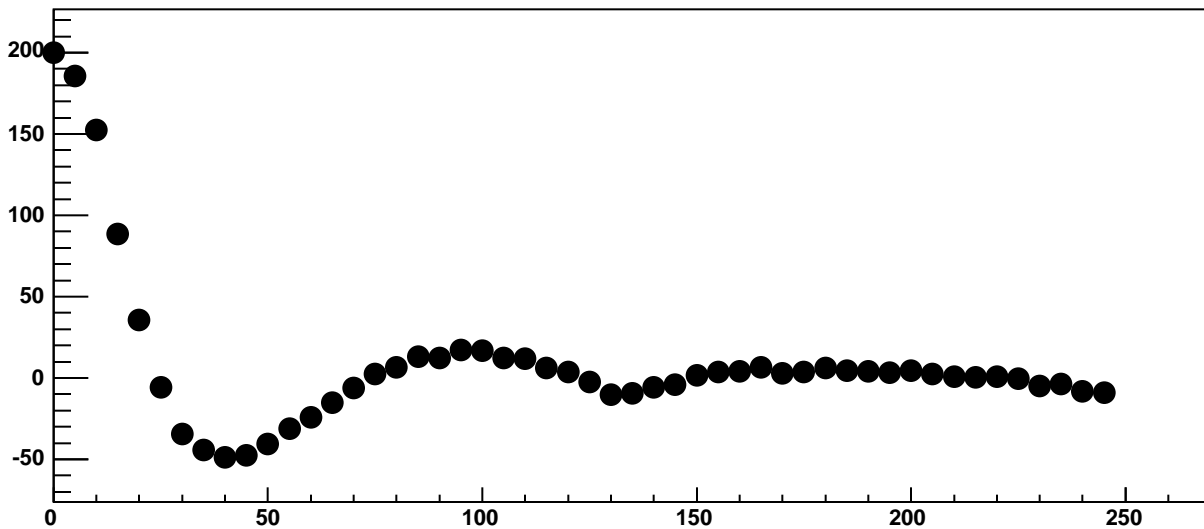


χ^2 / ndf	5459 / 41
p0	137.4 ± 0.6753
p1	131.8 ± 0.3834
p2	$-7.216\text{e}+07 \pm 2.353\text{e}+06$
p3	$3.091\text{e}+07 \pm 9.929\text{e}+05$
p4	2.502 ± 0.01046

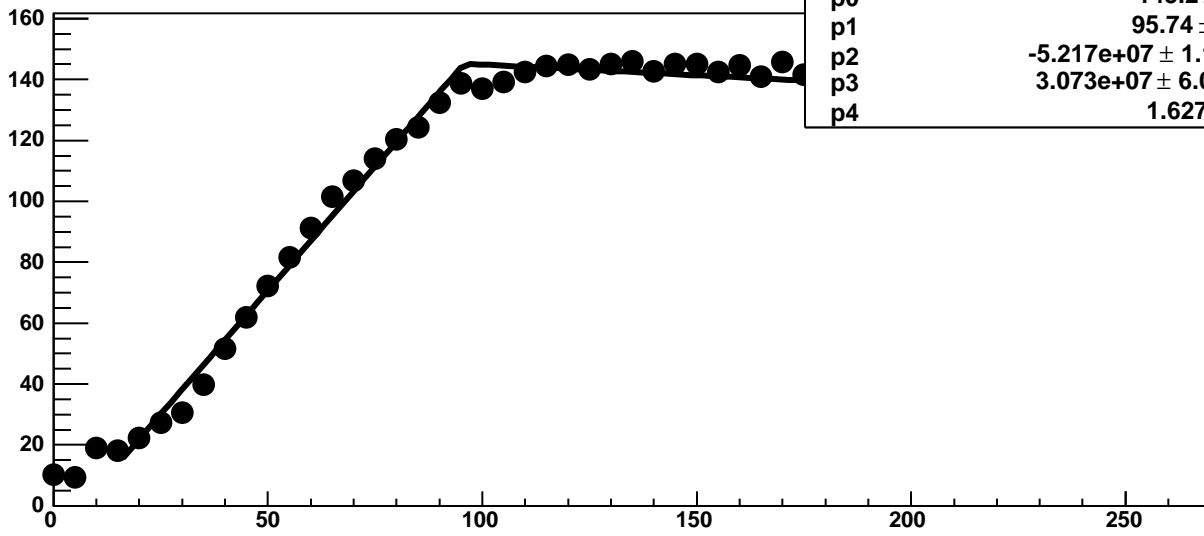
Chip 10, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

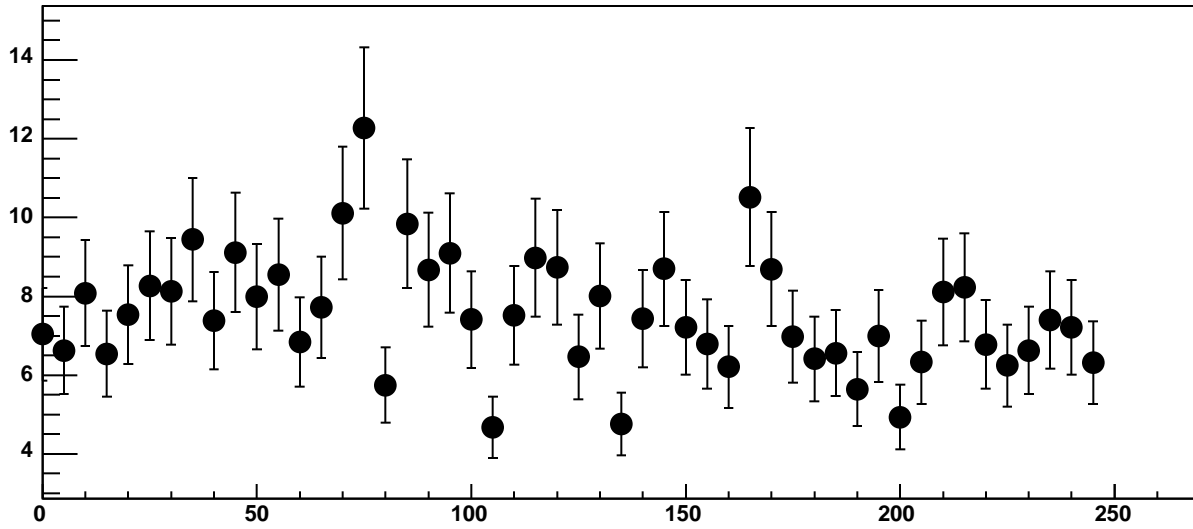


Chip 10, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

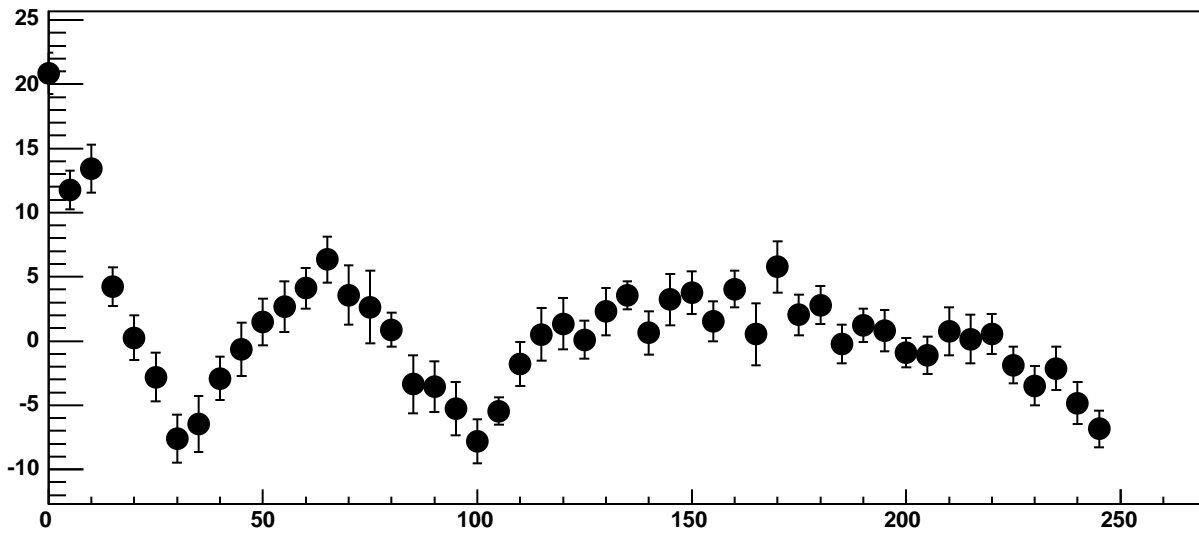


χ^2 / ndf	187.4 / 41
p0	145.2 ± 0.5601
p1	95.74 ± 0.6265
p2	$-5.217\text{e}+07 \pm 1.114\text{e}+06$
p3	$3.073\text{e}+07 \pm 6.071\text{e}+05$
p4	1.627 ± 0.018

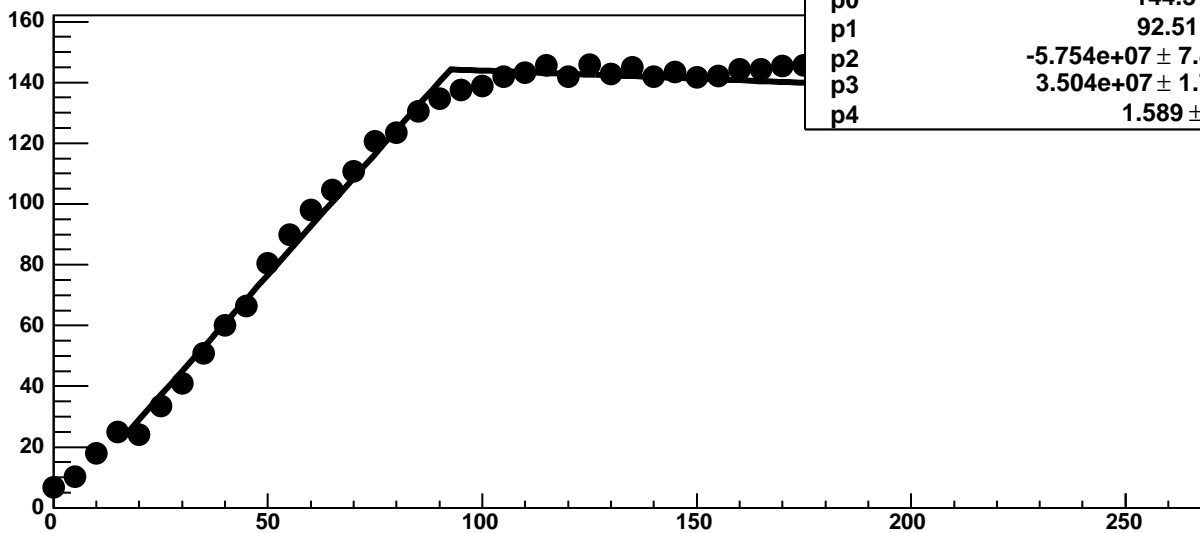
Chip 10, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

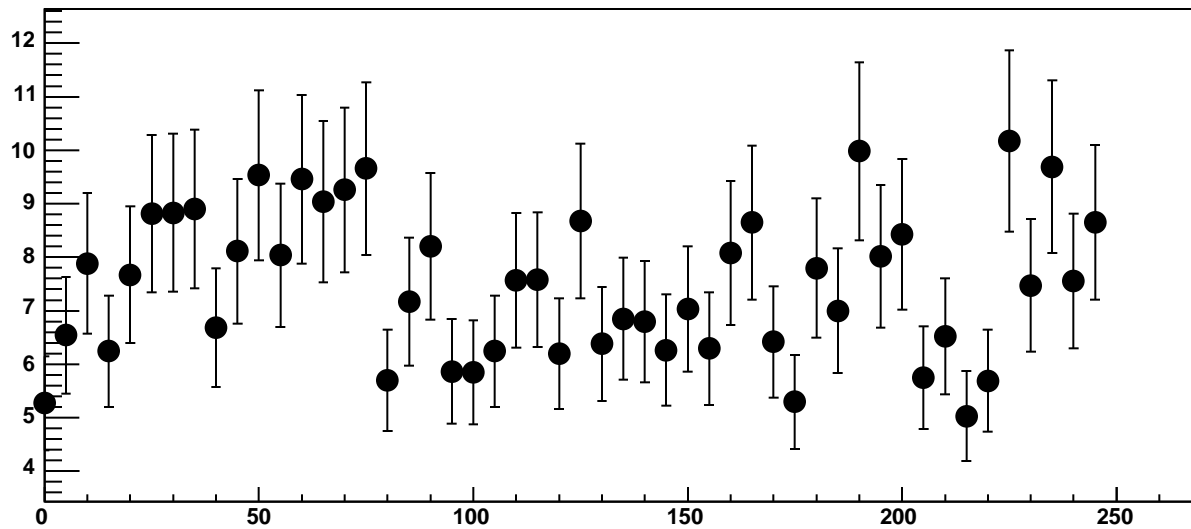


Chip 10, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold

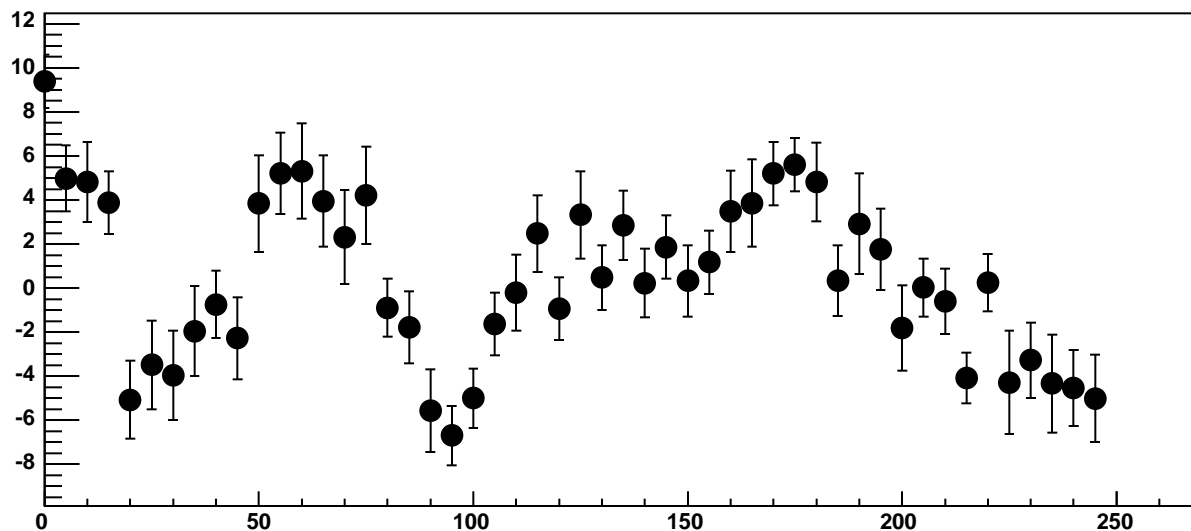


χ^2 / ndf	194.7 / 41
p0	144.3 ± 0.5418
p1	92.51 ± 0.6277
p2	-5.754e+07 ± 7.495e+05
p3	3.504e+07 ± 1.764e+05
p4	1.589 ± 0.01841

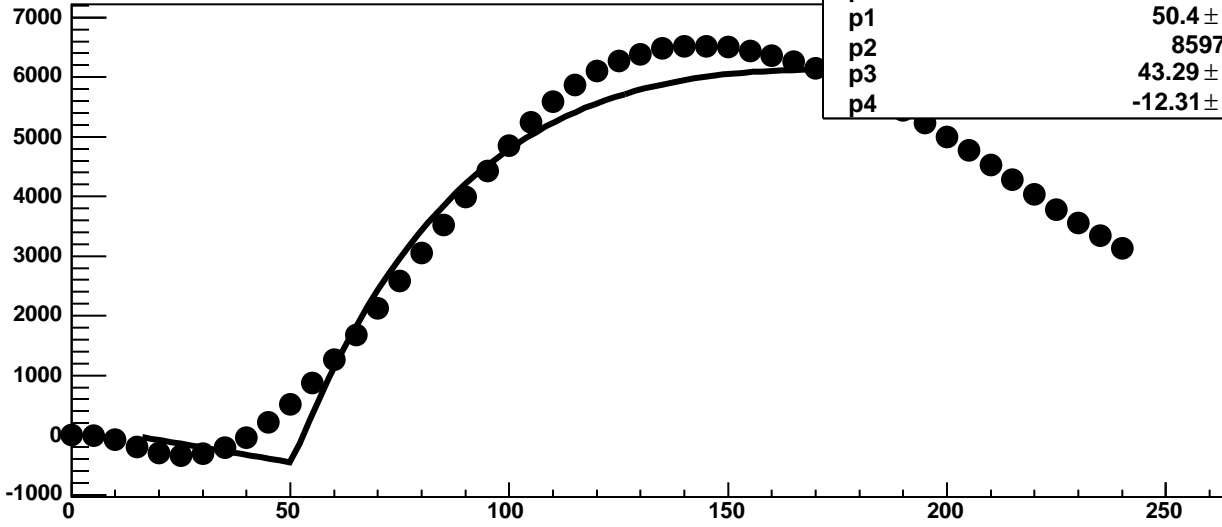
Chip 10, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold

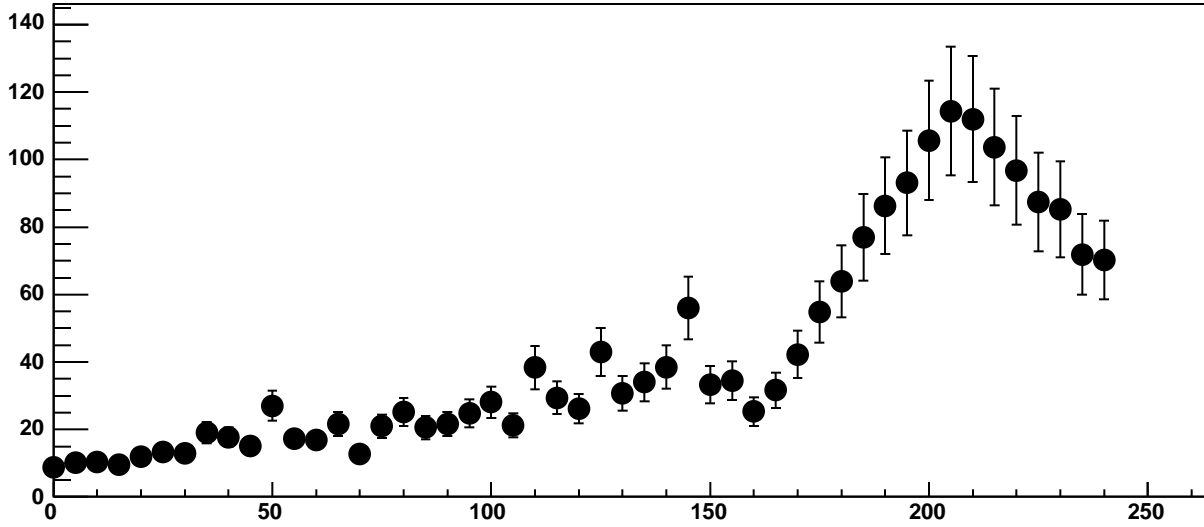


Chip 10, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

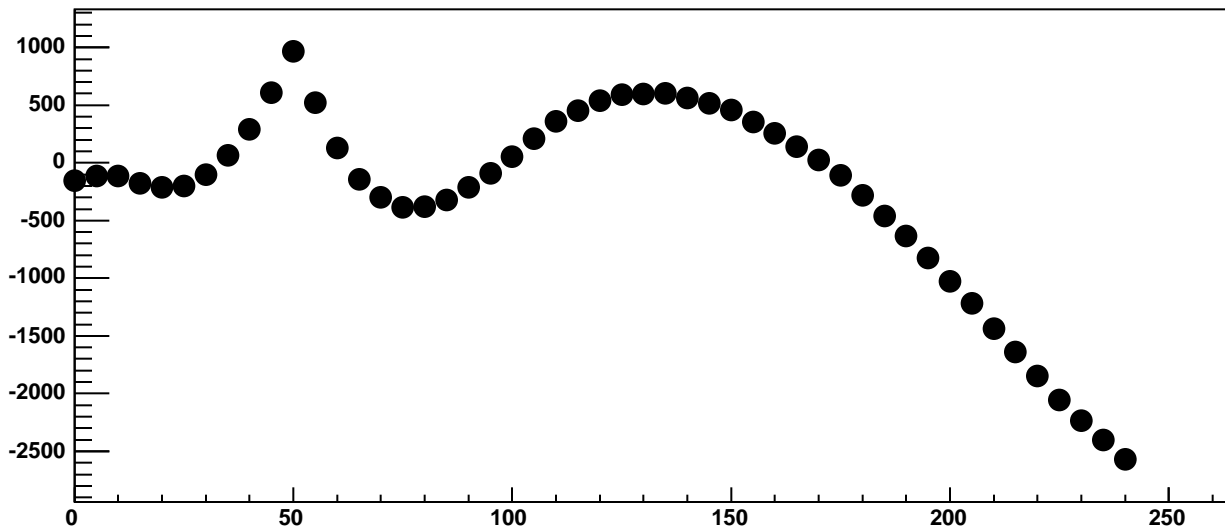


χ^2 / ndf	2.646e+05 / 41
p0	-458.8 ± 1.973
p1	50.4 ± 0.02084
p2	8597 ± 13.48
p3	43.29 ± 0.07384
p4	-12.31 ± 0.07517

Chip 10, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold

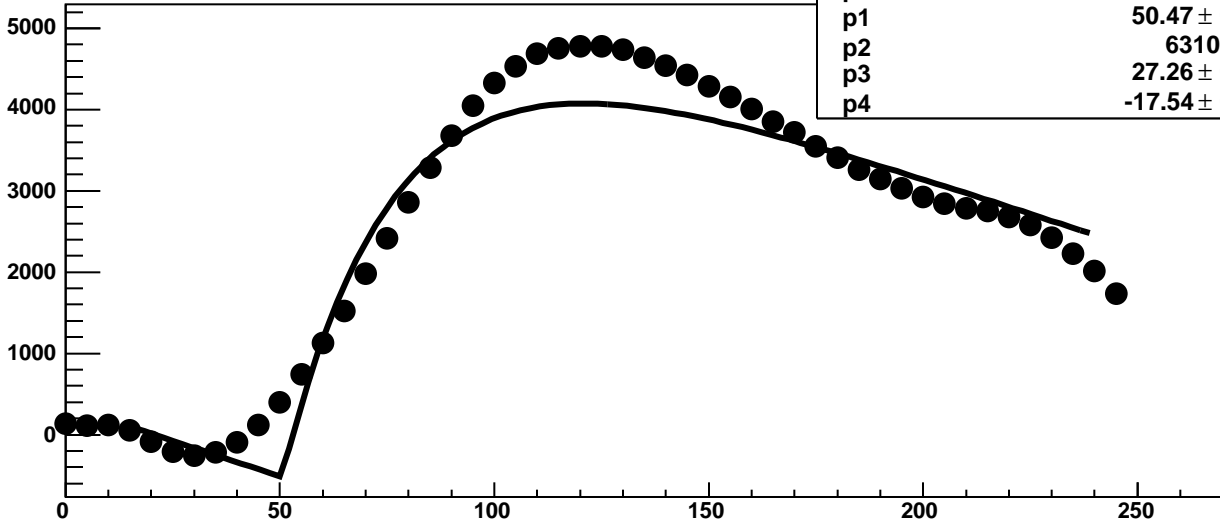


Chip 10, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

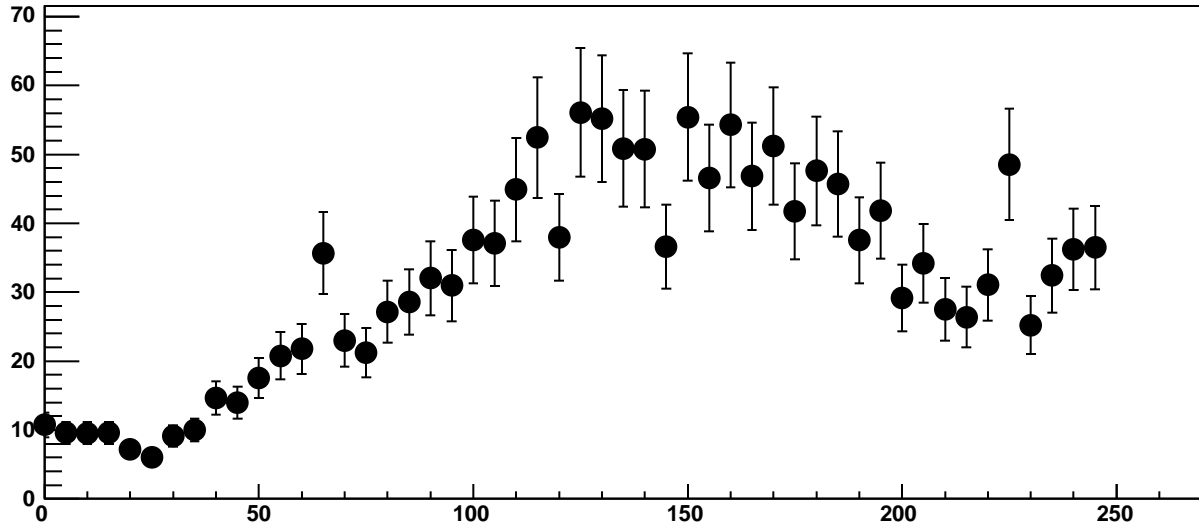


Chip 10, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

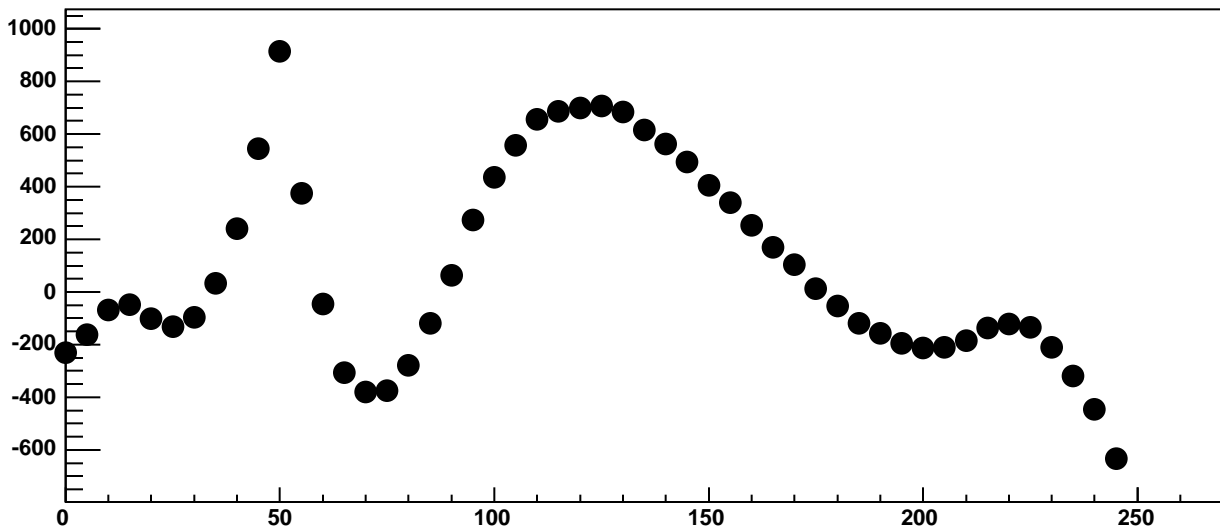
χ^2 / ndf	1.724e+05 / 41
p0	-521.3 ± 1.239
p1	50.47 ± 0.02425
p2	6310 ± 8.249
p3	27.26 ± 0.05725
p4	-17.54 ± 0.04599



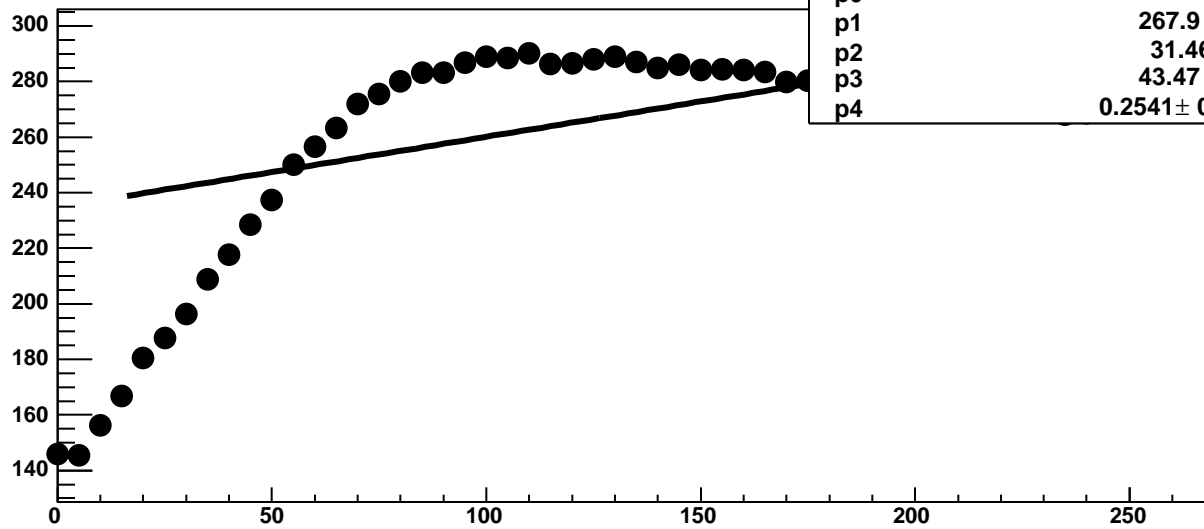
Chip 10, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold

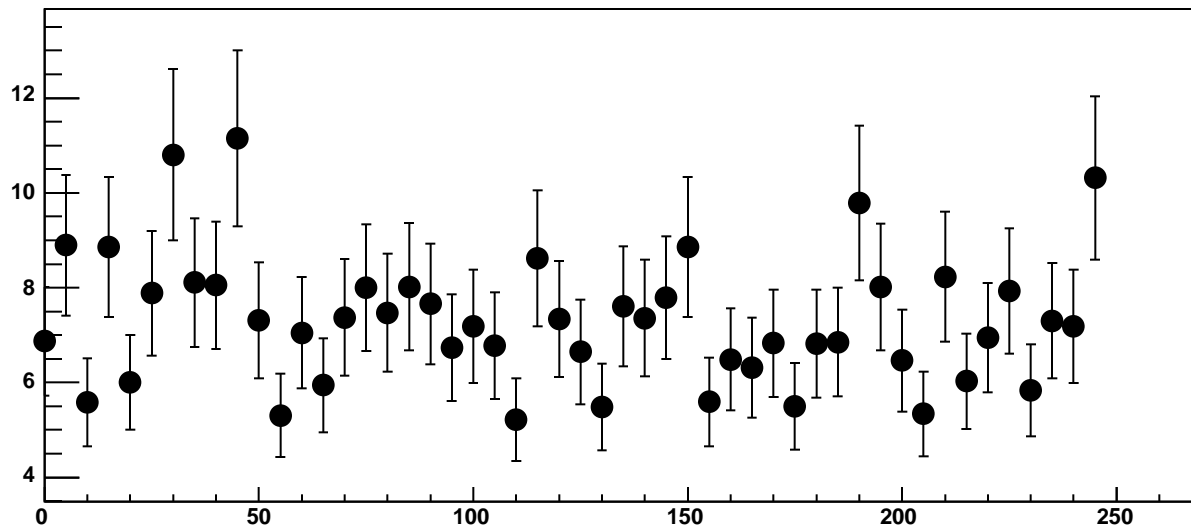


Chip 10, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

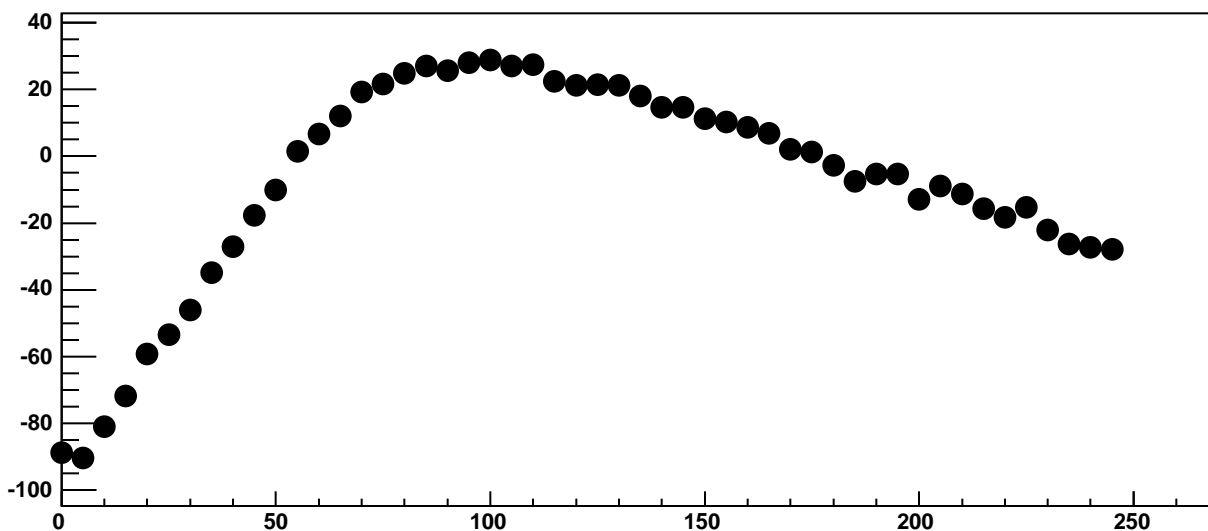


χ^2 / ndf	9941 / 41
p0	302.8 ± 0.4005
p1	267.9 ± 0.7889
p2	31.46 ± 6.858
p3	43.47 ± 0.4059
p4	0.2541 ± 0.003638

Chip 10, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold

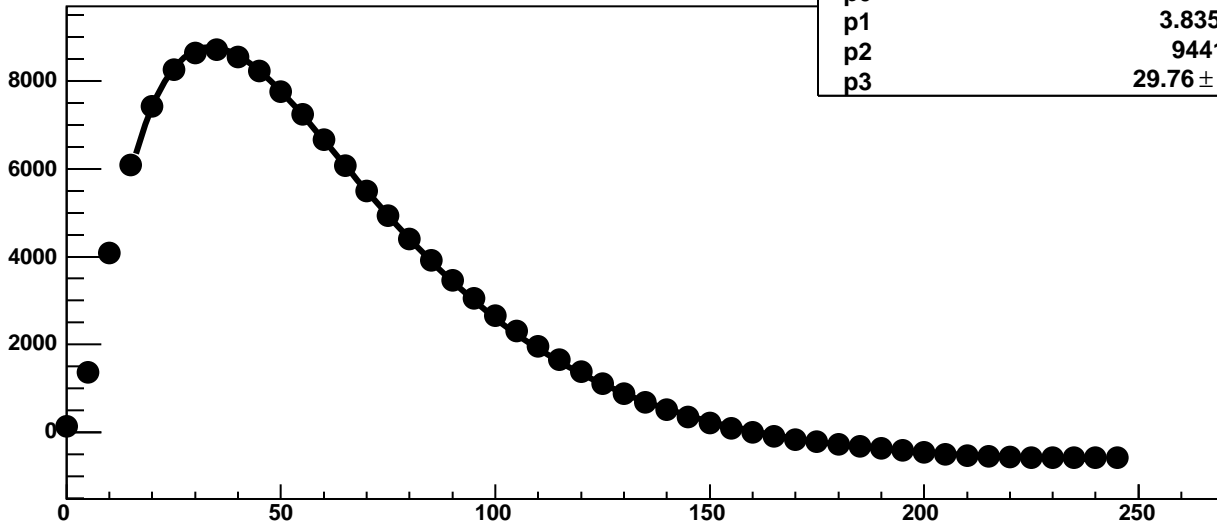


Chip 10, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

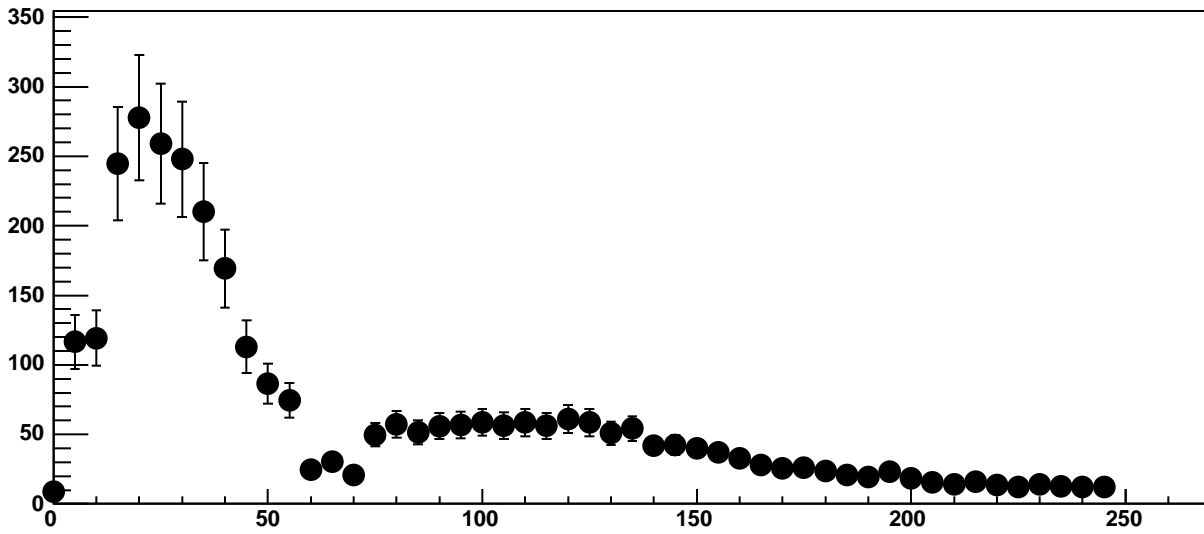


Chip 10, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold

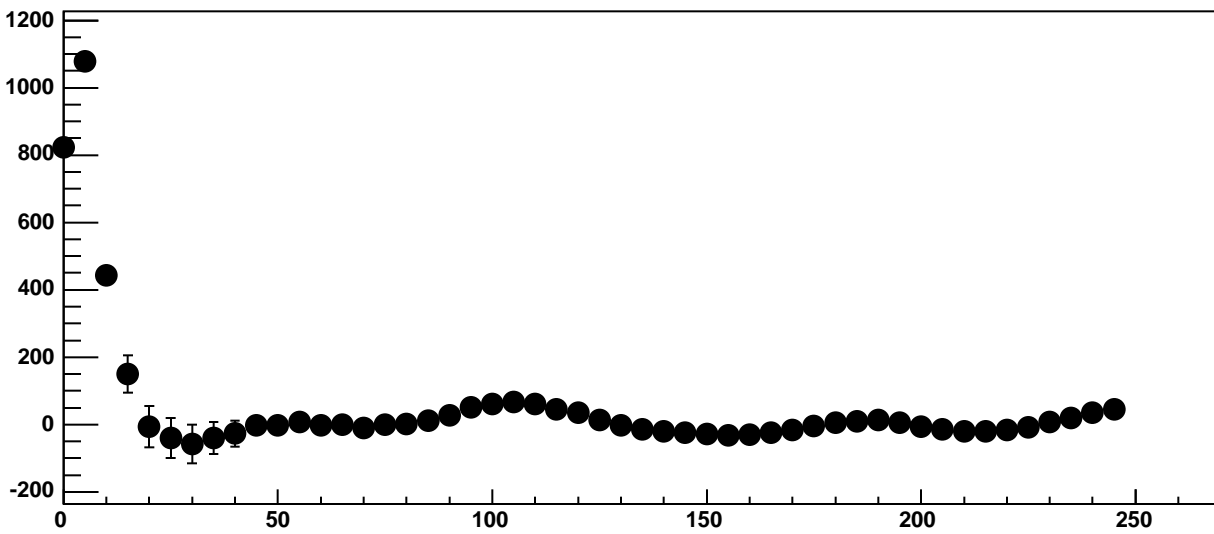
χ^2 / ndf	536.2 / 42
p0	-676.2 ± 1.429
p1	3.835 ± 0.102
p2	9441 ± 11.63
p3	29.76 ± 0.03572



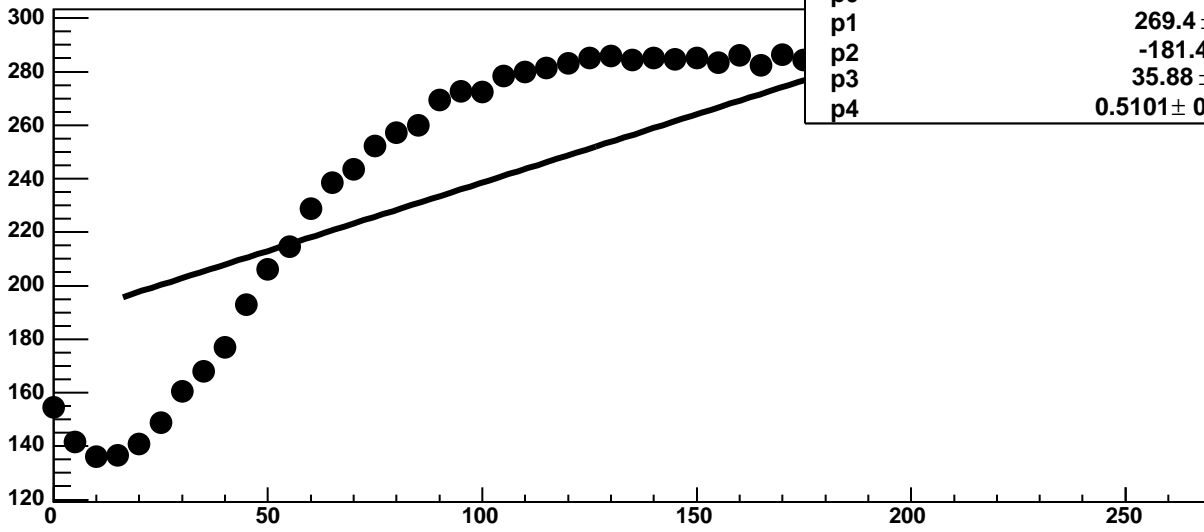
Chip 10, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold

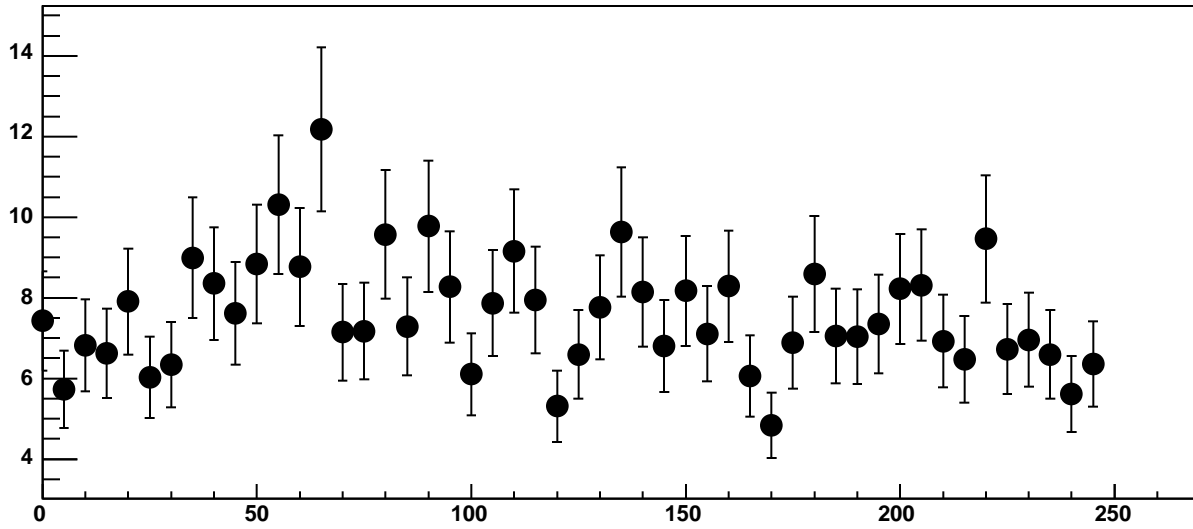


Chip 10, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

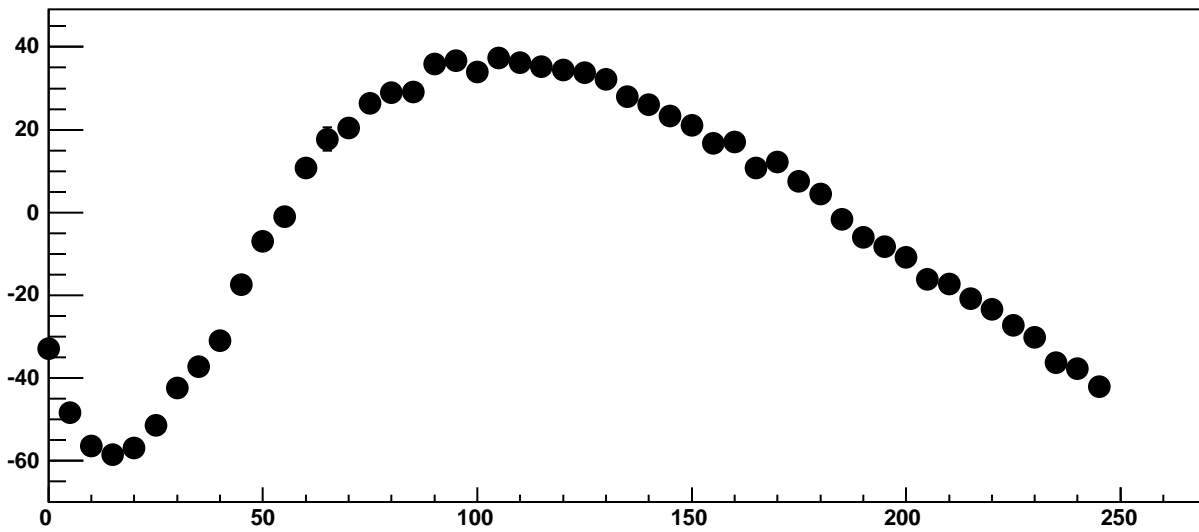


χ^2 / ndf	1.408e+04 / 41
p0	325 ± 0.3264
p1	269.4 ± 0.6379
p2	-181.4 ± 6.959
p3	35.88 ± 0.3536
p4	0.5101 ± 0.003667

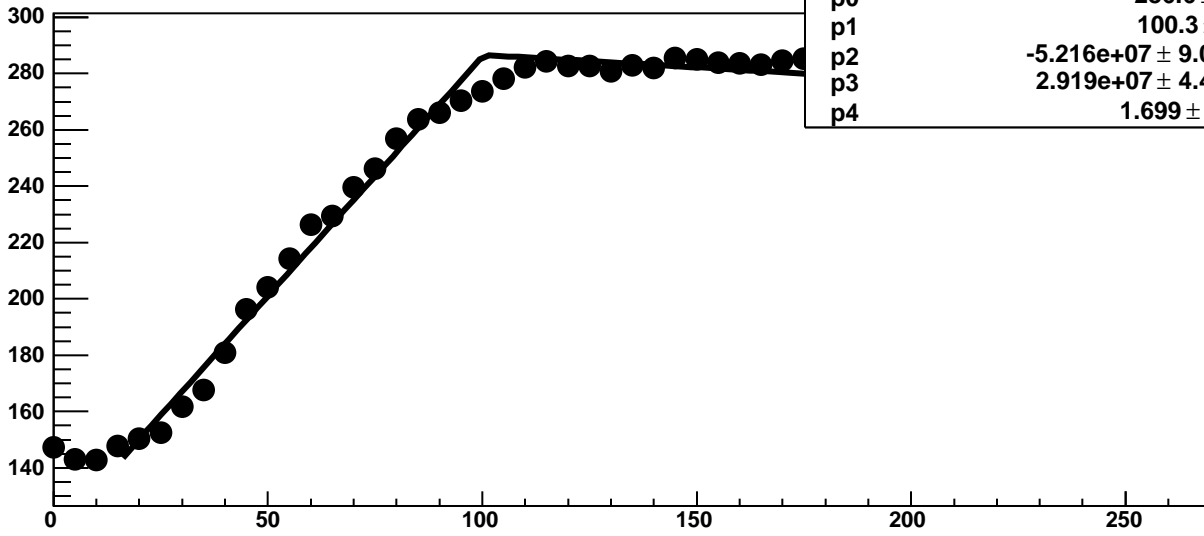
Chip 10, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

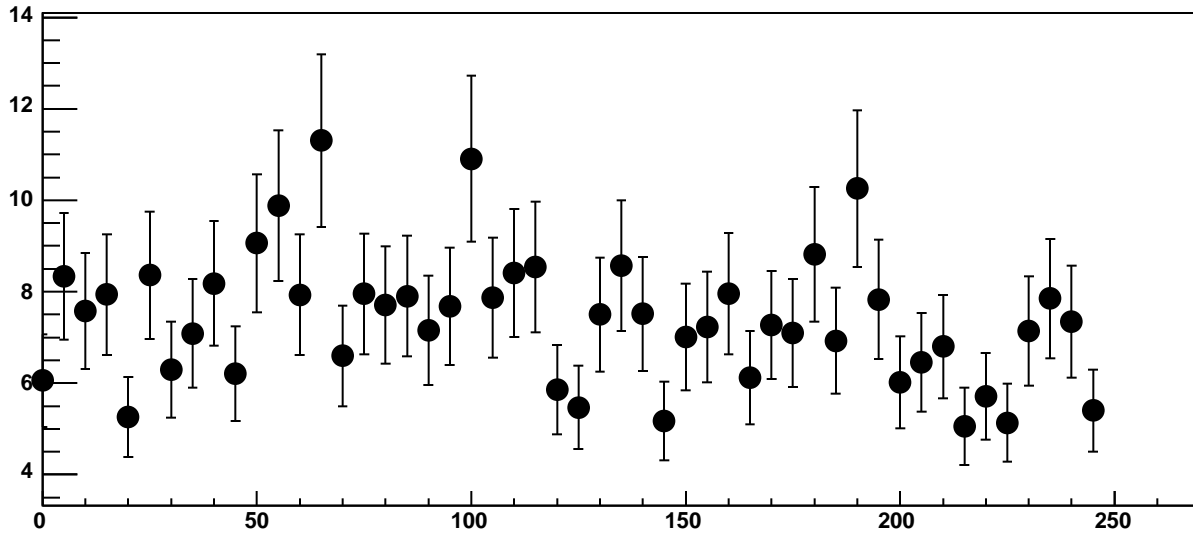


Chip 10, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

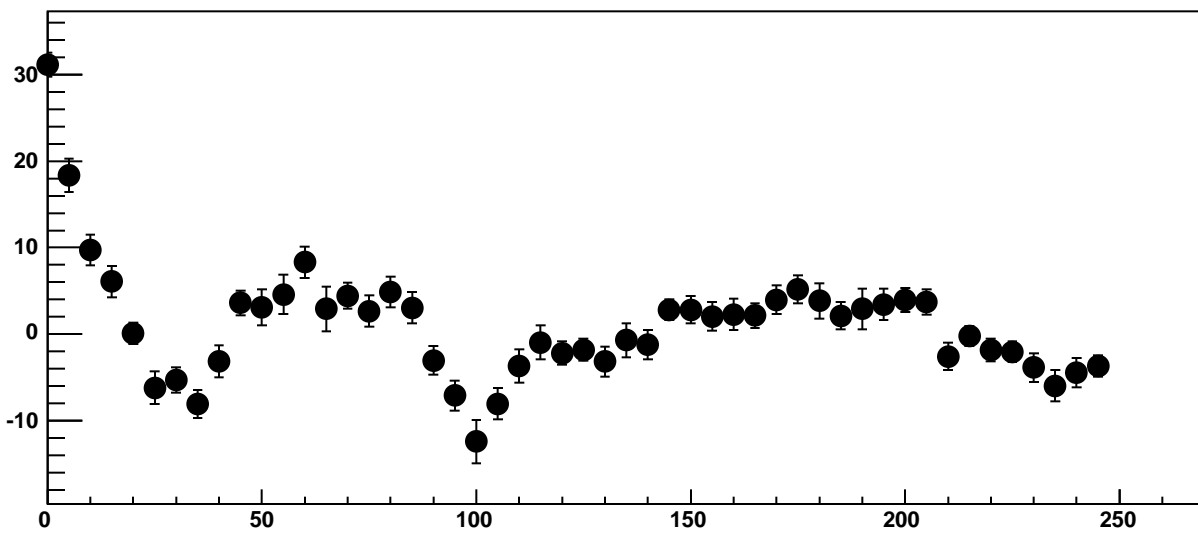


χ^2 / ndf	281.1 / 41
p0	286.6 ± 0.5938
p1	100.3 ± 0.5811
p2	$-5.216e+07 \pm 9.044e+05$
p3	$2.919e+07 \pm 4.408e+05$
p4	1.699 ± 0.01569

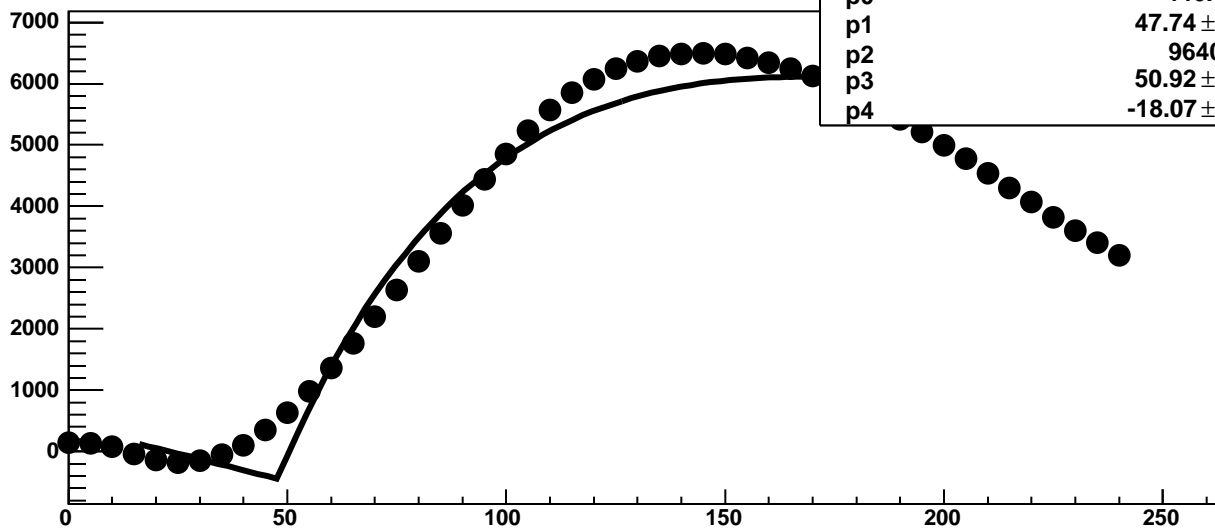
Chip 10, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

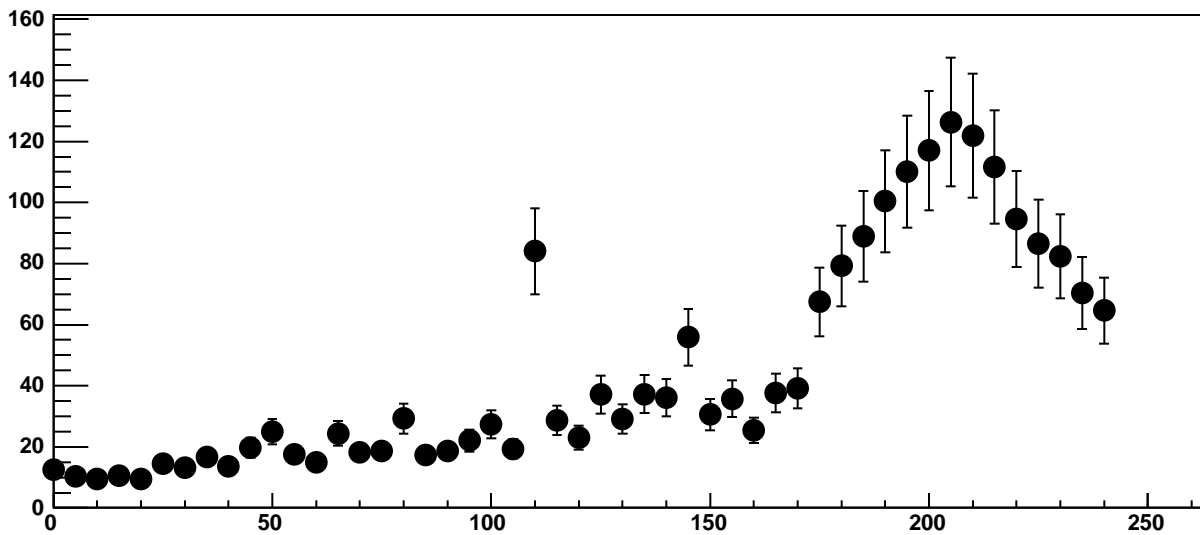


Chip 10, Channel 15, Enable 5, DAC=4000, ADC Mean vs Hold

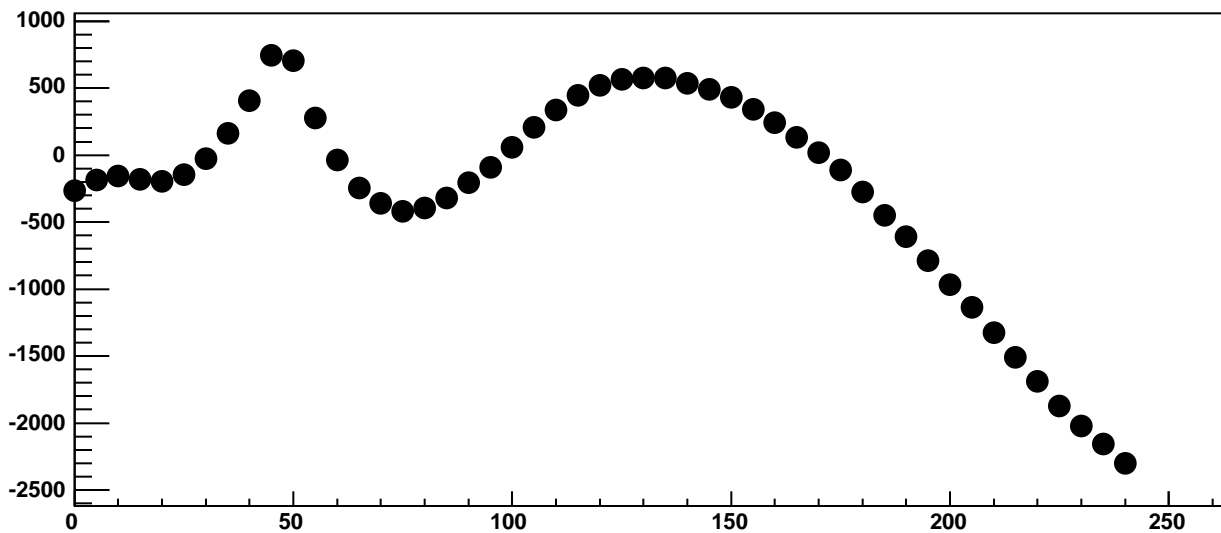


χ^2 / ndf	2.391e+05 / 41
p0	-446.9 ± 1.951
p1	47.74 ± 0.02114
p2	9640 ± 16.46
p3	50.92 ± 0.08716
p4	-18.07 ± 0.08492

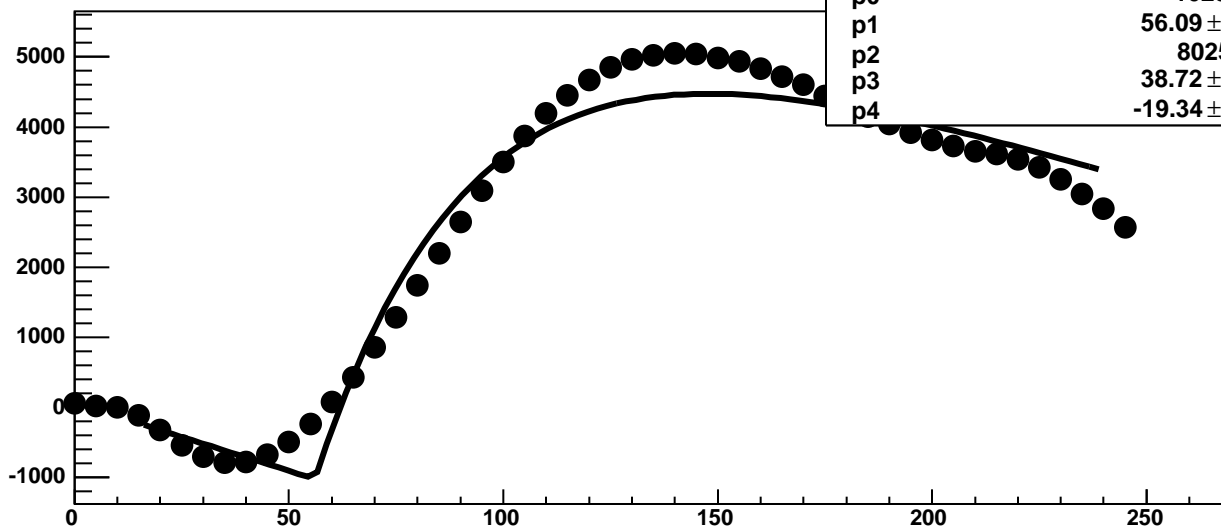
Chip 10, Channel 15, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 15, Enable 5, DAC=4000, ADC Residuals vs Hold

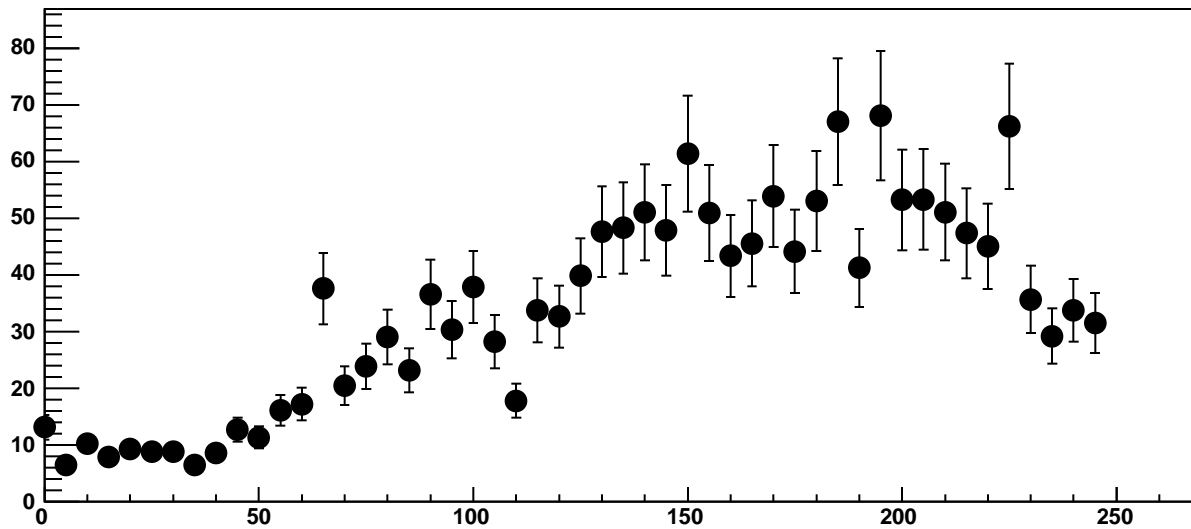


Chip 10, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

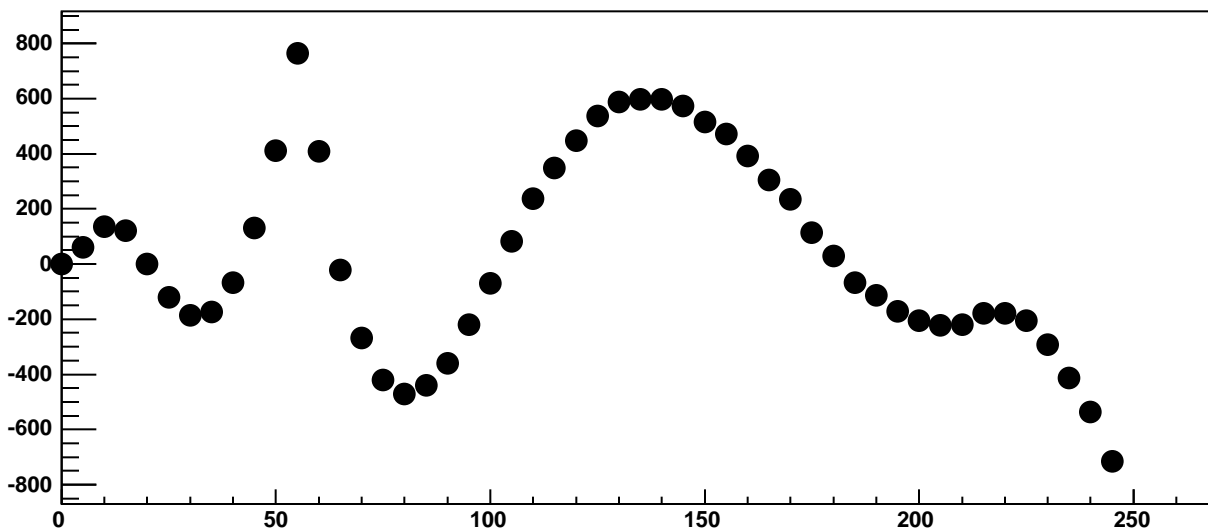


χ^2 / ndf	1.762e+05 / 41
p0	-1025 ± 1.309
p1	56.09 ± 0.02145
p2	8025 ± 9.891
p3	38.72 ± 0.06547
p4	-19.34 ± 0.05029

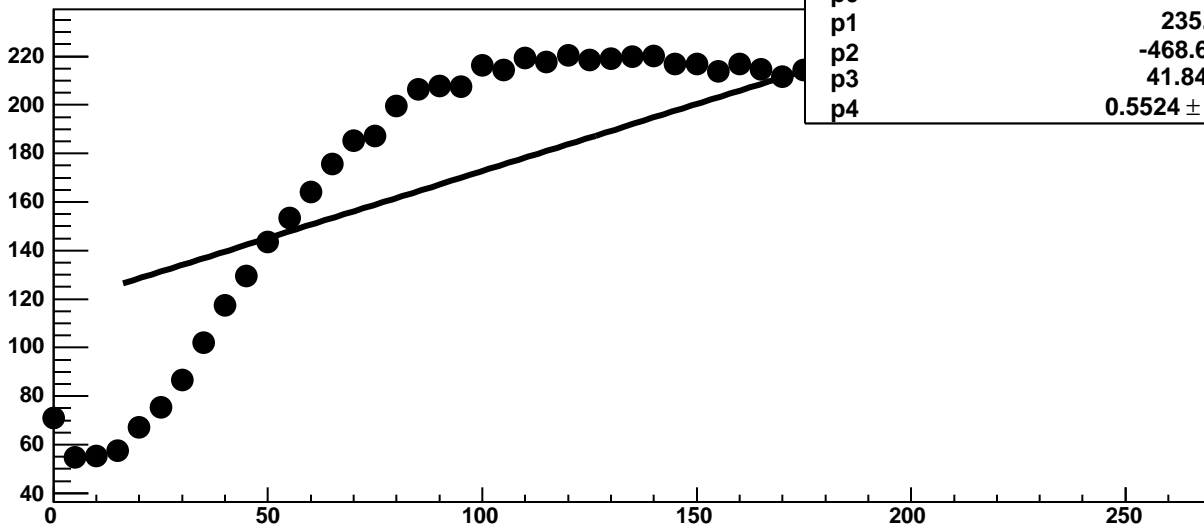
Chip 10, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

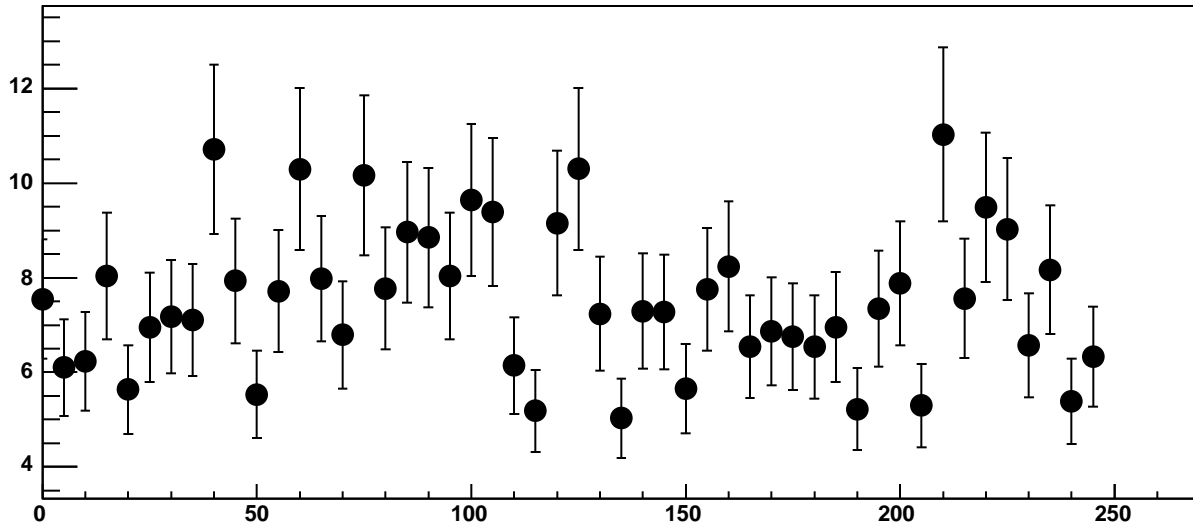


Chip 10, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold

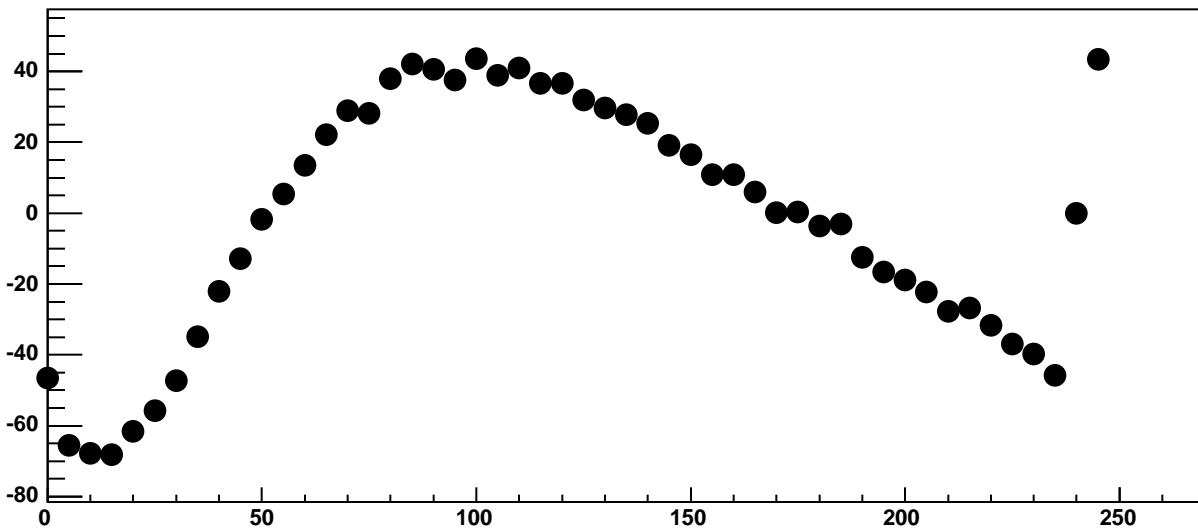


χ^2 / ndf	1.599e+04 / 41
p0	247.5 ± 1.1
p1	235.3 ± 1.8
p2	-468.6 ± 164.2
p3	41.84 ± 12.83
p4	0.5524 ± 0.00384

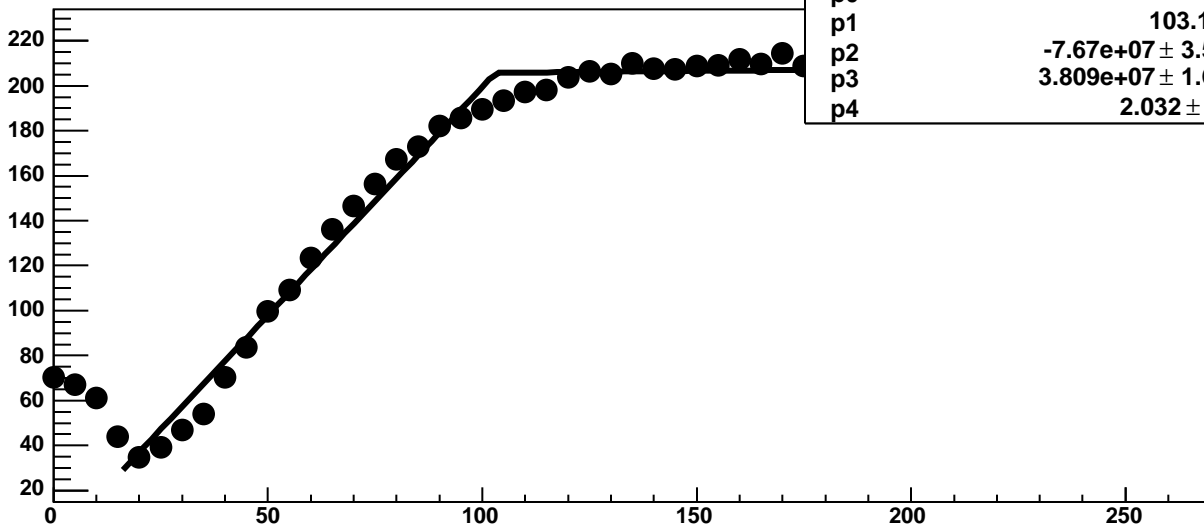
Chip 10, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold

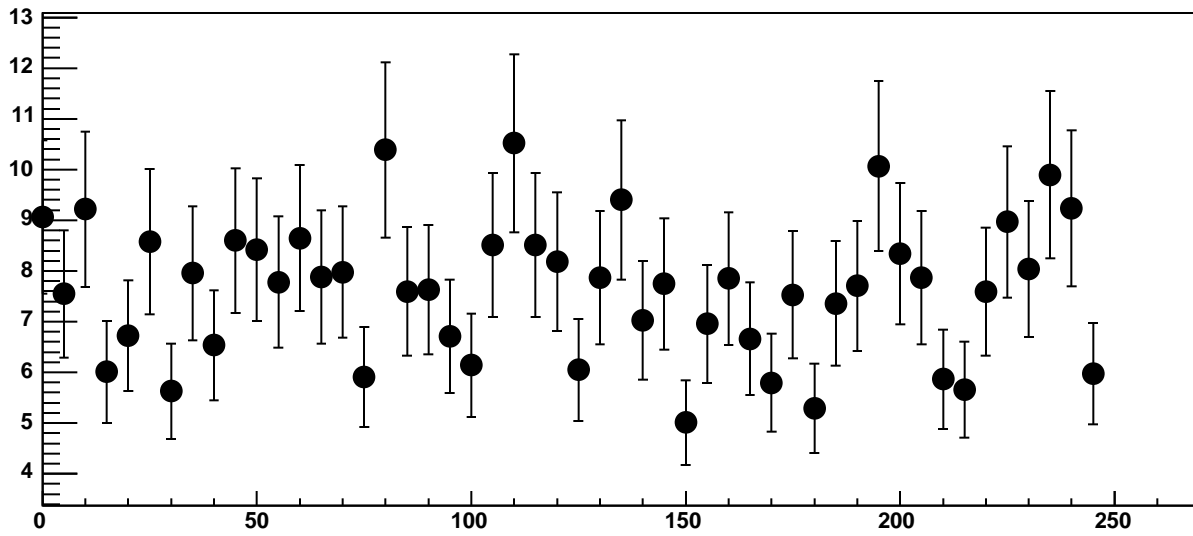


Chip 10, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold

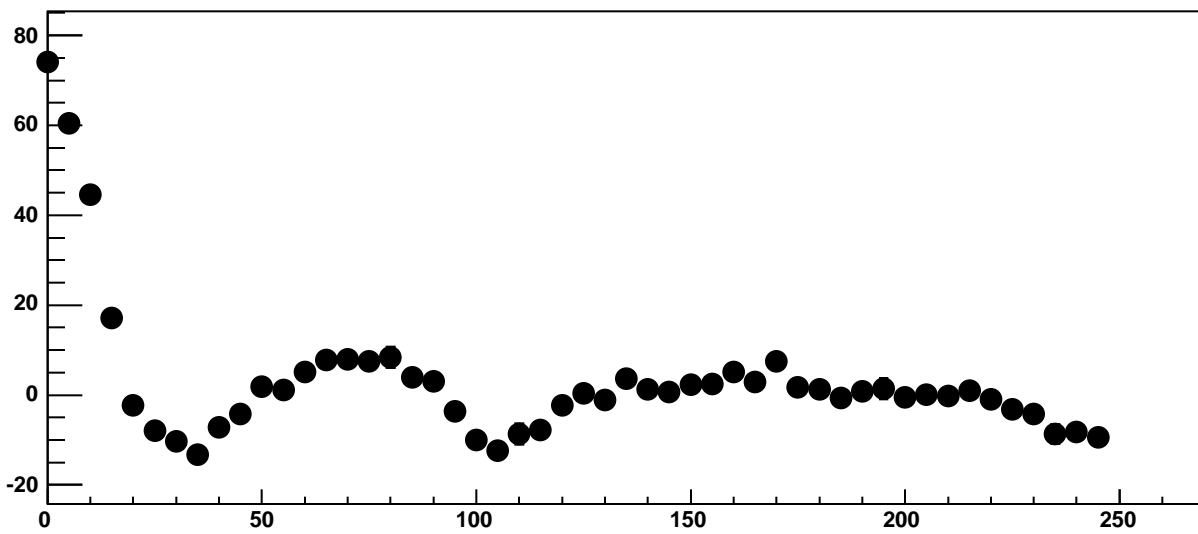


χ^2 / ndf	637.8 / 41
p0	205.8 ± 0.7011
p1	103.1 ± 0.496
p2	$-7.67\text{e}+07 \pm 3.565\text{e}+06$
p3	$3.809\text{e}+07 \pm 1.623\text{e}+06$
p4	2.032 ± 0.01417

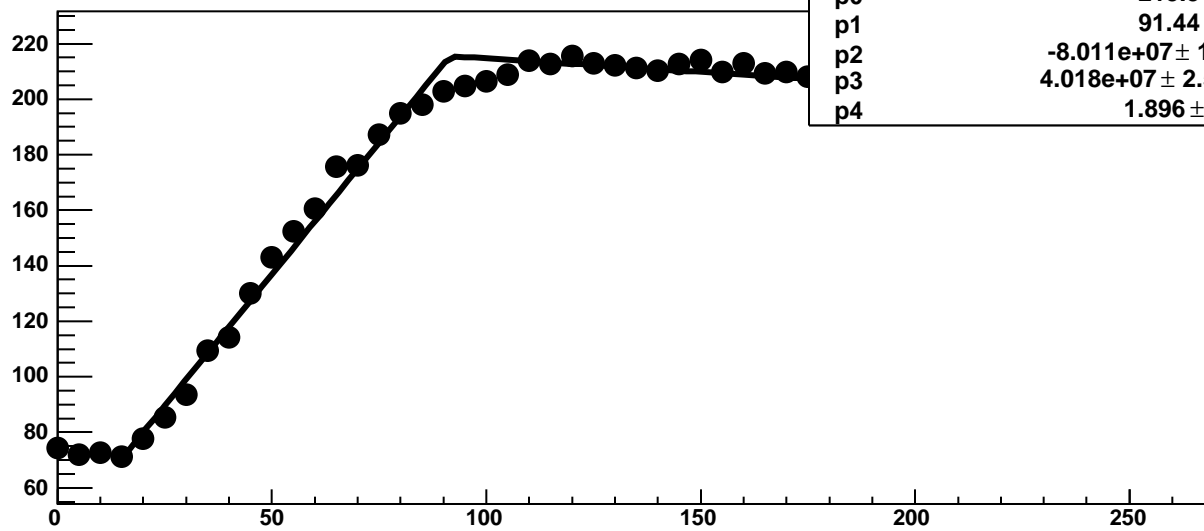
Chip 10, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold

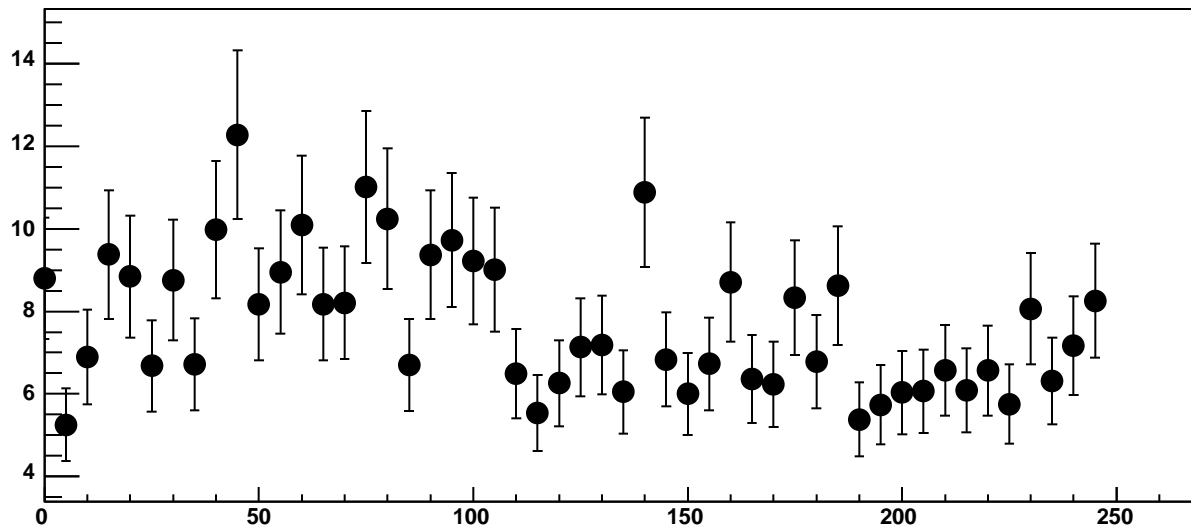


Chip 10, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

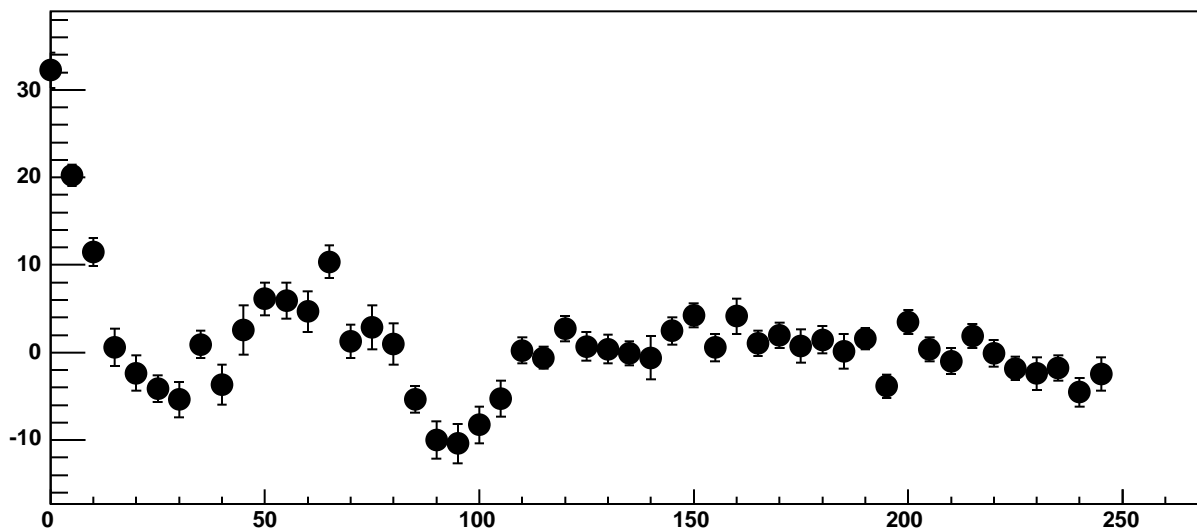


χ^2 / ndf	208 / 41
p0	215.5 ± 0.5837
p1	91.44 ± 0.5749
p2	$-8.011\text{e}+07 \pm 1.03\text{e}+06$
p3	$4.018\text{e}+07 \pm 2.545\text{e}+05$
p4	1.896 ± 0.02094

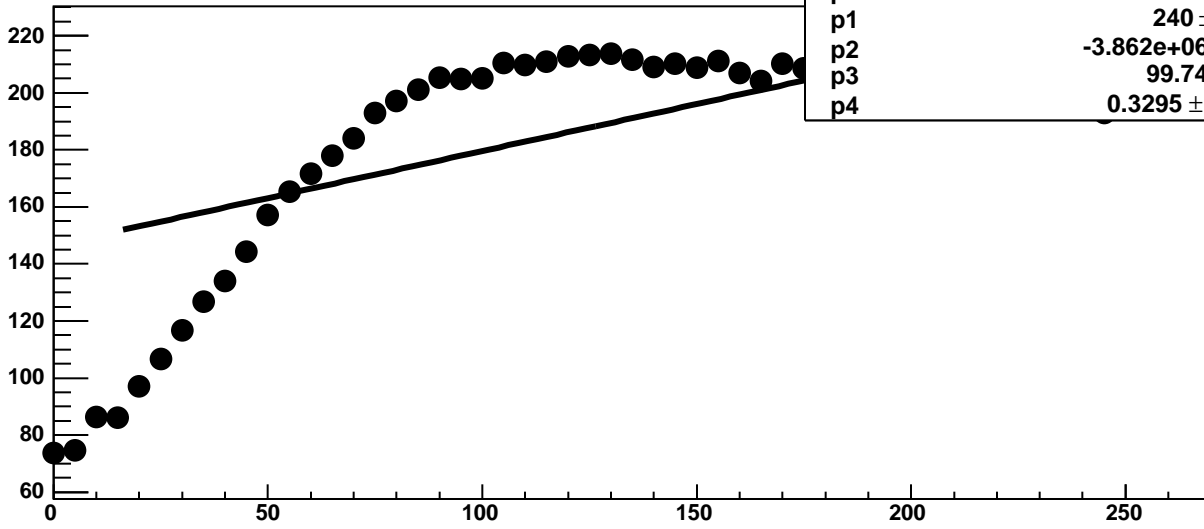
Chip 10, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

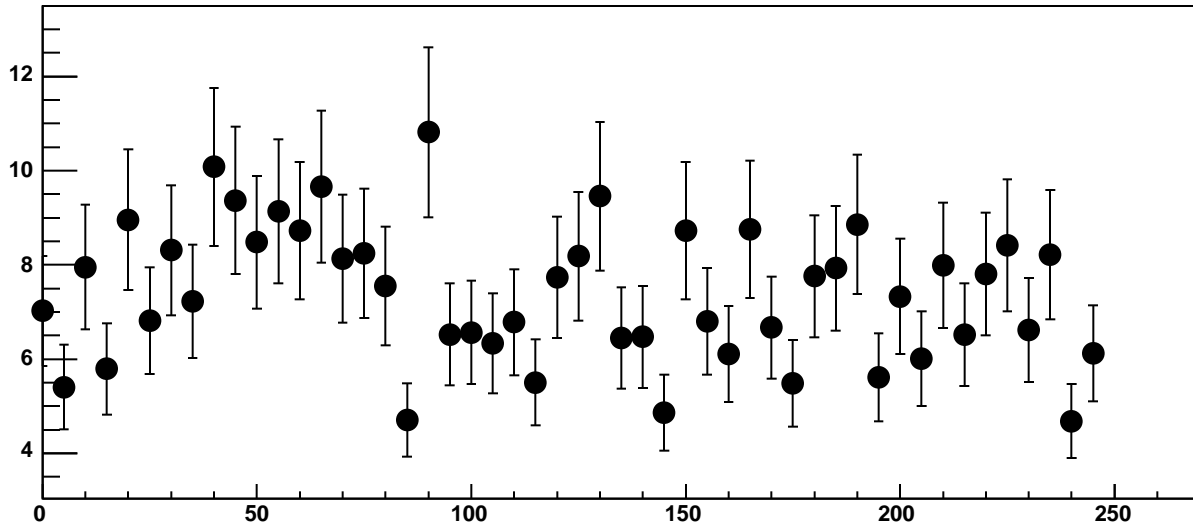


Chip 10, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

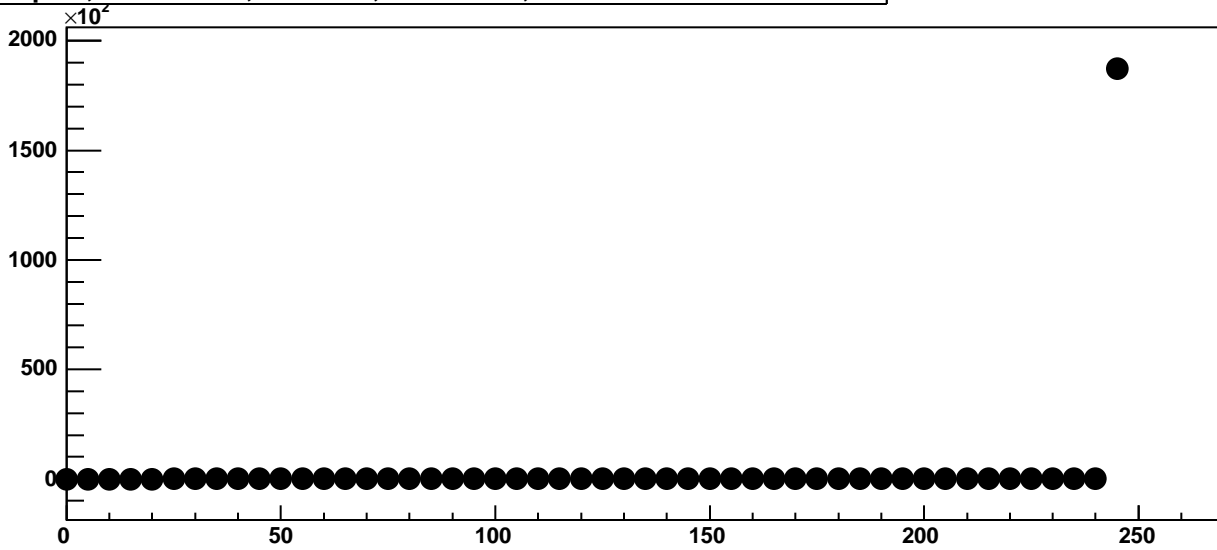


χ^2 / ndf	1.096e+04 / 41
p0	225.7 ± 0.2371
p1	240 ± 0.7196
p2	-3.862e+06 ± 84.85
p3	99.74 ± 1.414
p4	0.3295 ± 0.00191

Chip 10, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold

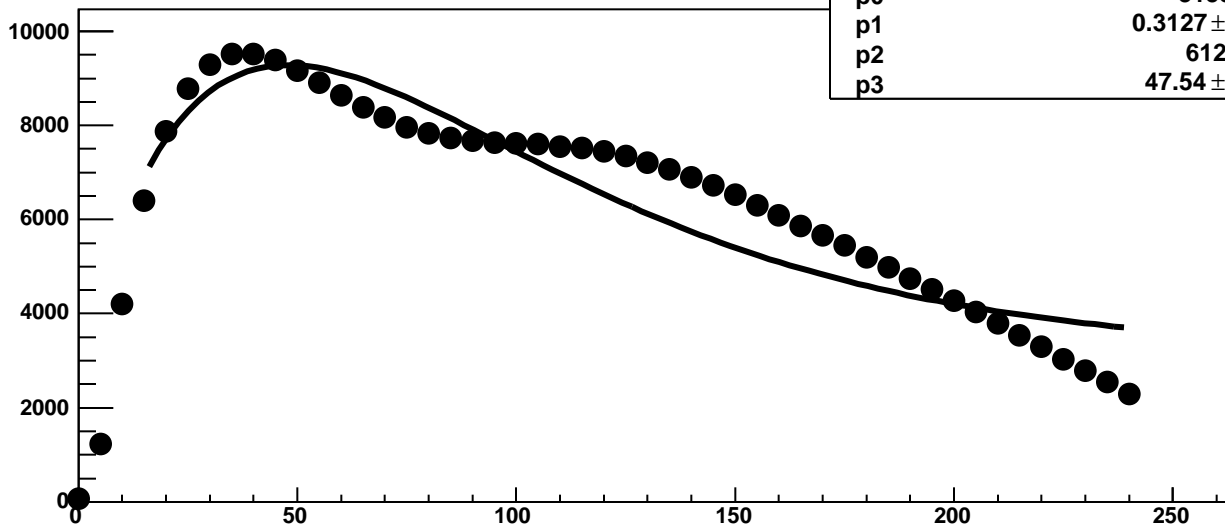


Chip 10, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

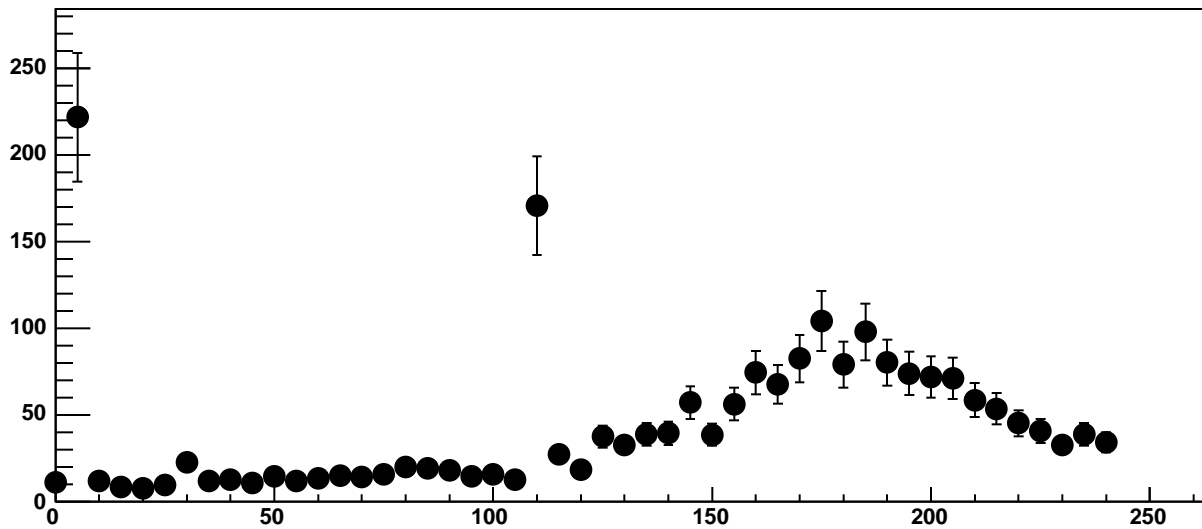


Chip 10, Channel 16, Enable 5!, DAC=4000, ADC Mean vs Hold

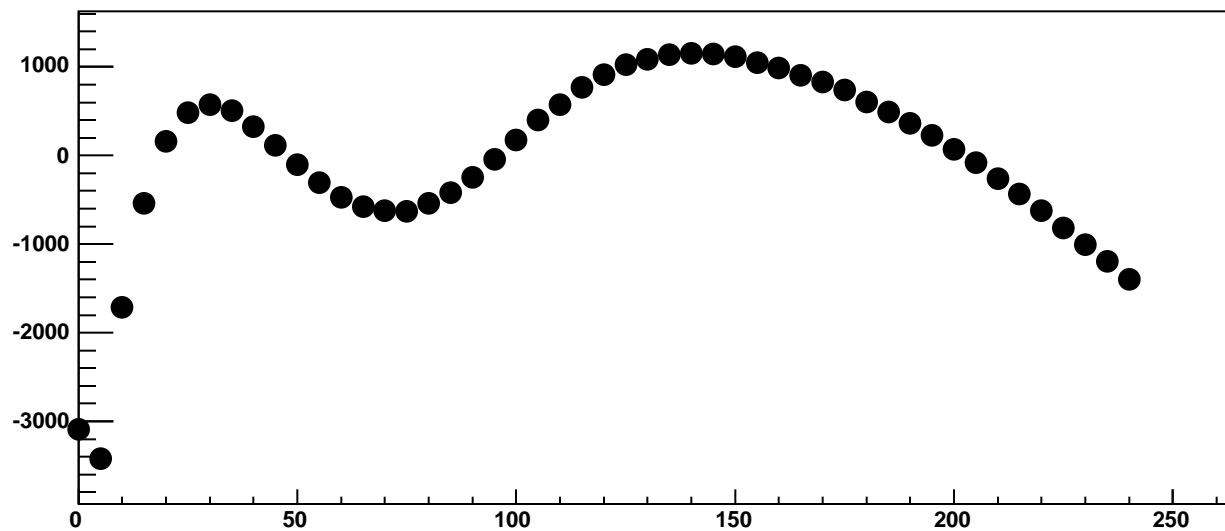
χ^2 / ndf	6.132e+05 / 42
p0	3155 ± 6.018
p1	0.3127 ± 0.03454
p2	6126 ± 5.73
p3	47.54 ± 0.04714



Chip 10, Channel 16, Enable 5!, DAC=4000, ADC Noise vs Hold

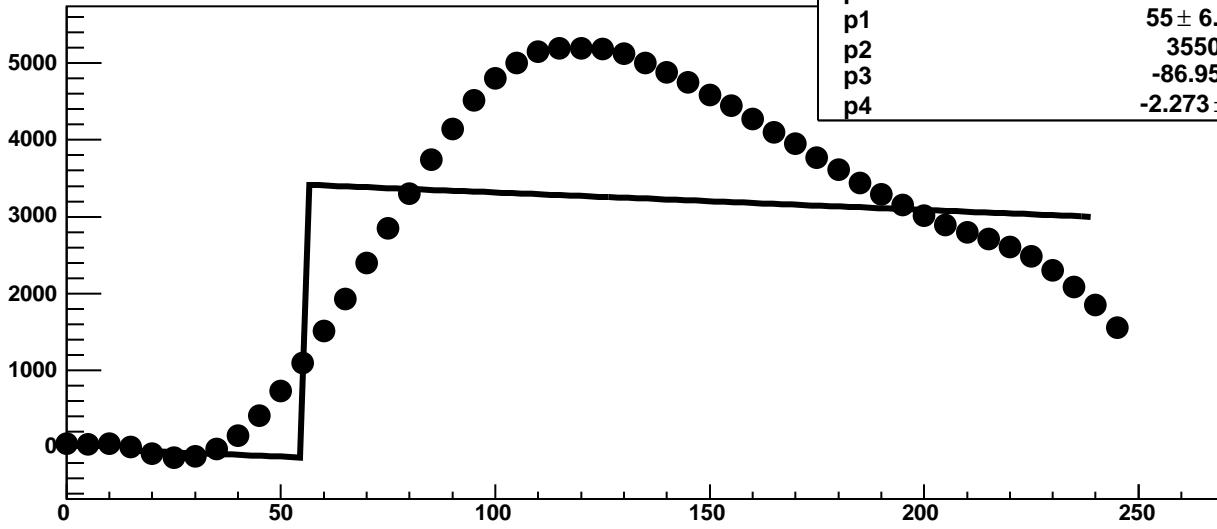


Chip 10, Channel 16, Enable 5!, DAC=4000, ADC Residuals vs Hold

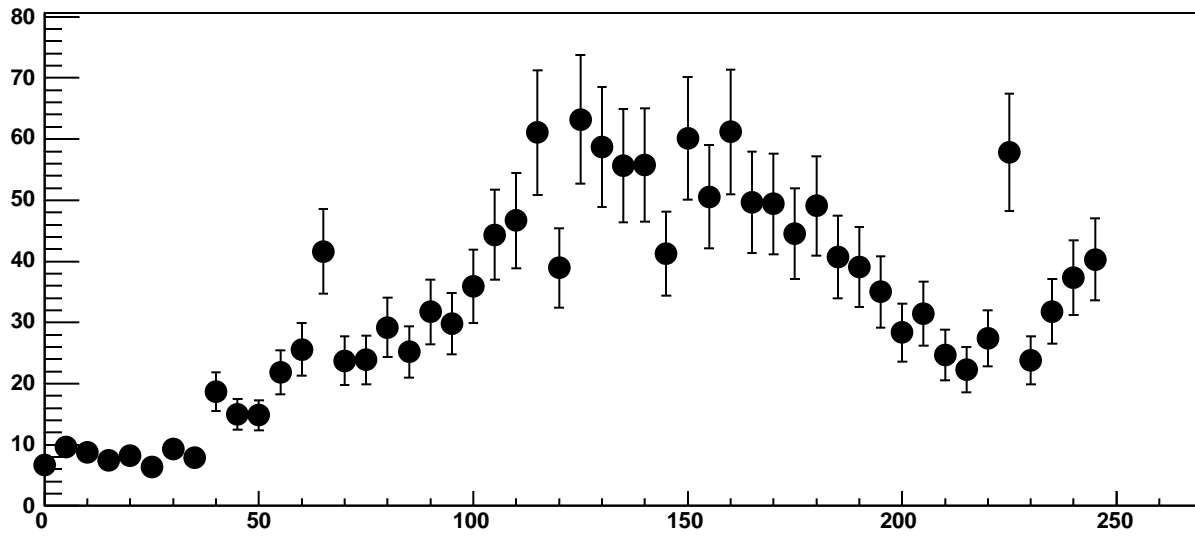


Chip 10, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

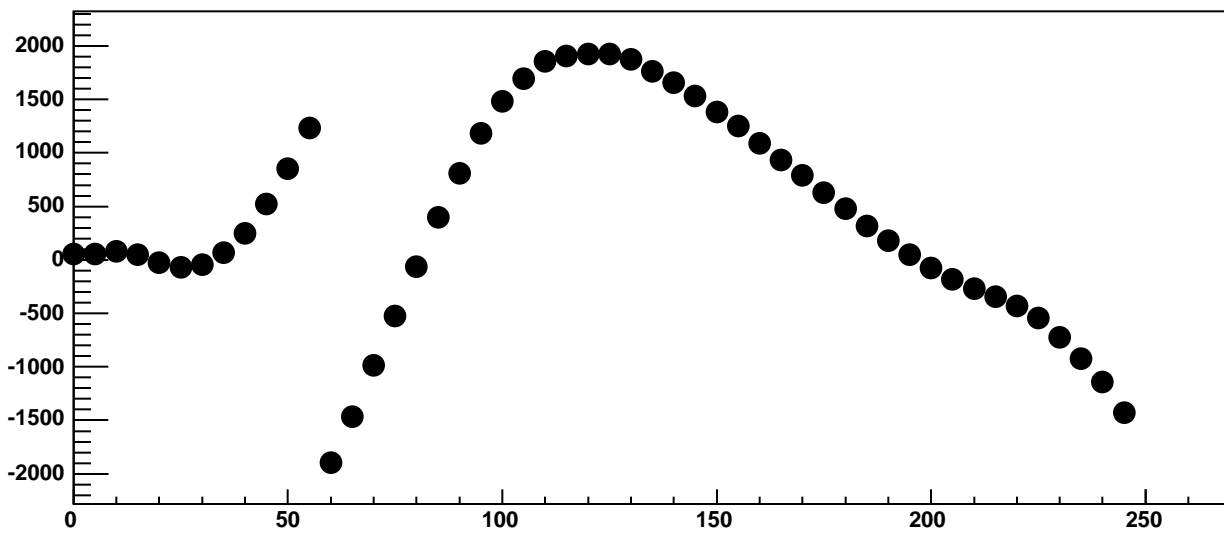
χ^2 / ndf	7.38e+05 / 41
p0	-132.2 ± 5.249
p1	55 ± 6.843e-07
p2	3550 ± 10.27
p3	-86.95 ± 2.956
p4	-2.273 ± 0.1249



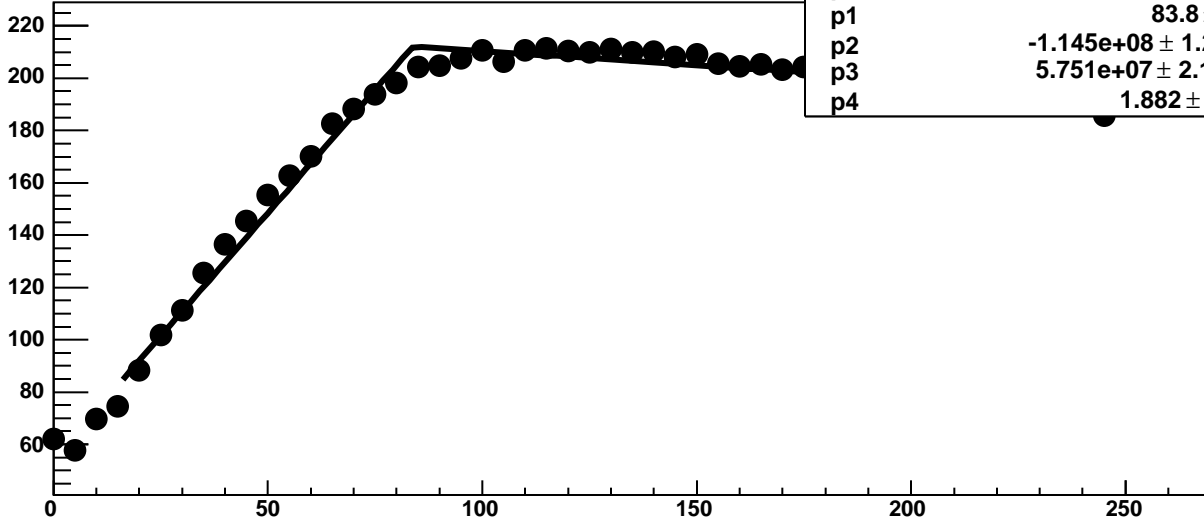
Chip 10, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

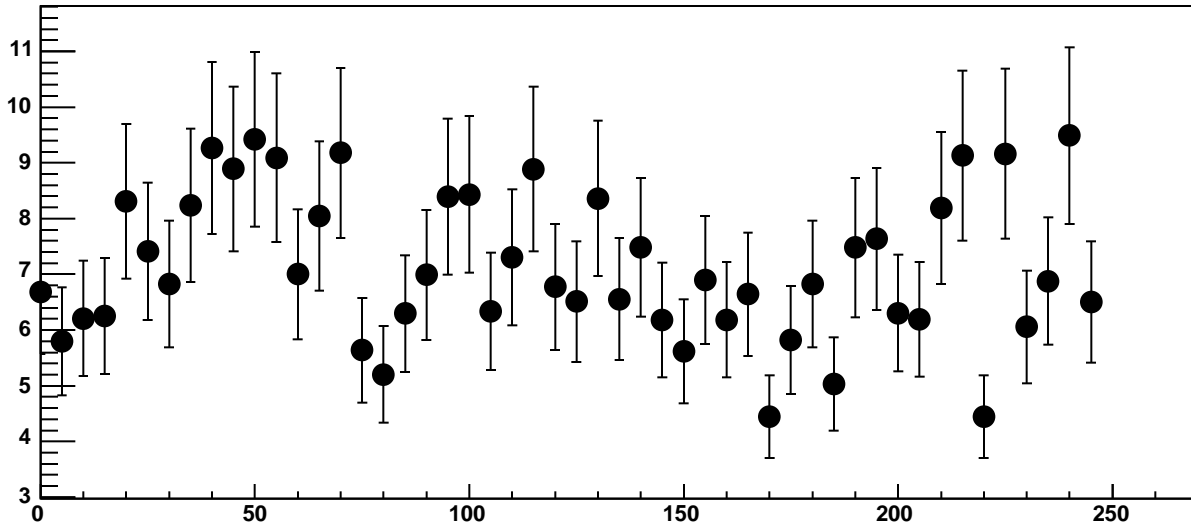


Chip 10, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

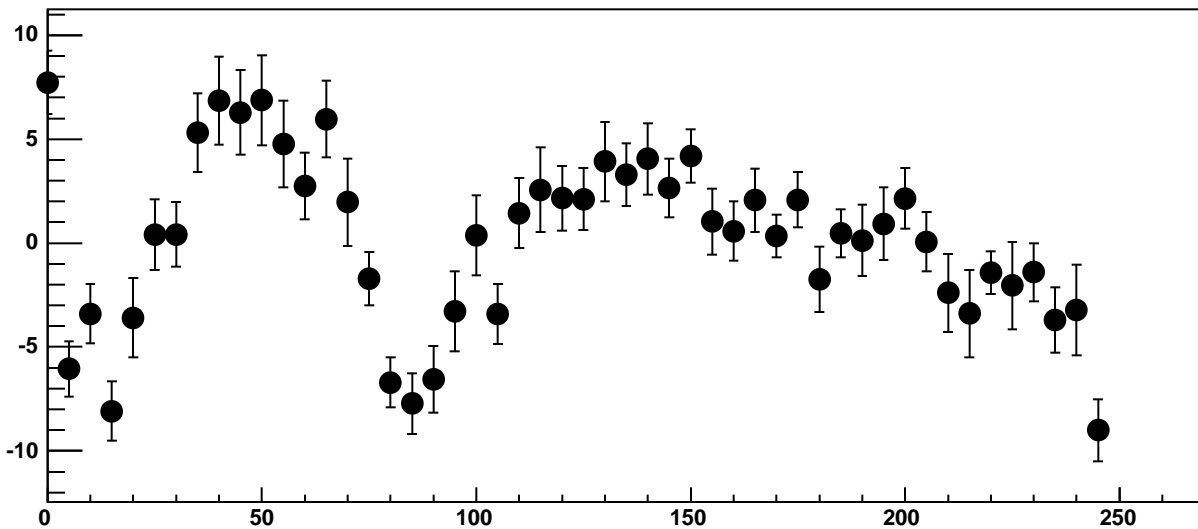


χ^2 / ndf	239.4 / 41
p0	212.1 ± 0.5388
p1	83.8 ± 0.4971
p2	$-1.145\text{e}+08 \pm 1.293\text{e}+06$
p3	$5.751\text{e}+07 \pm 2.136\text{e}+05$
p4	1.882 ± 0.01992

Chip 10, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold

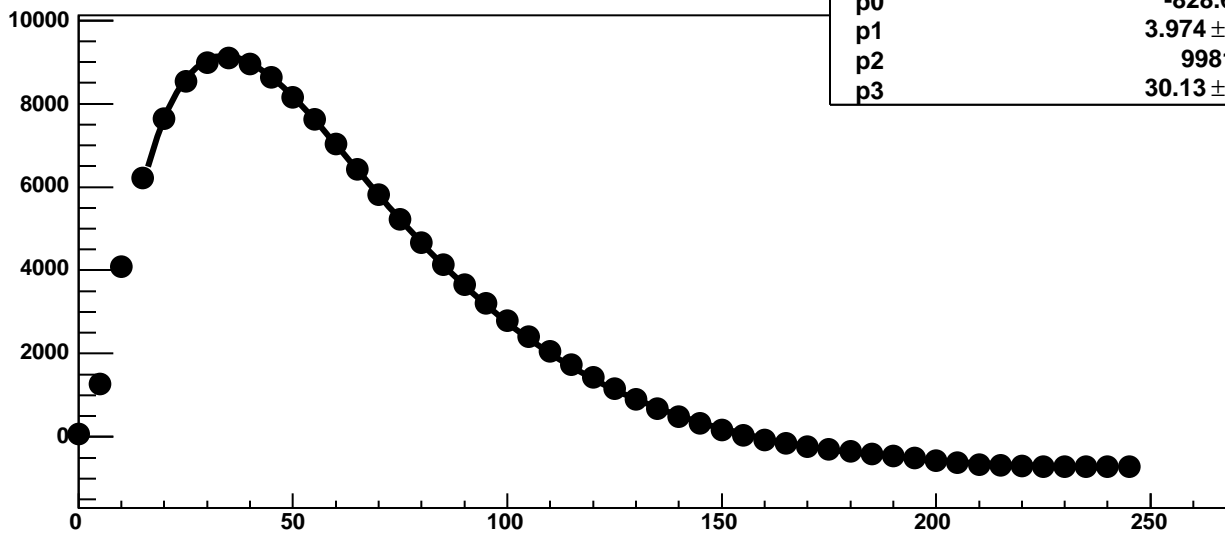


Chip 10, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

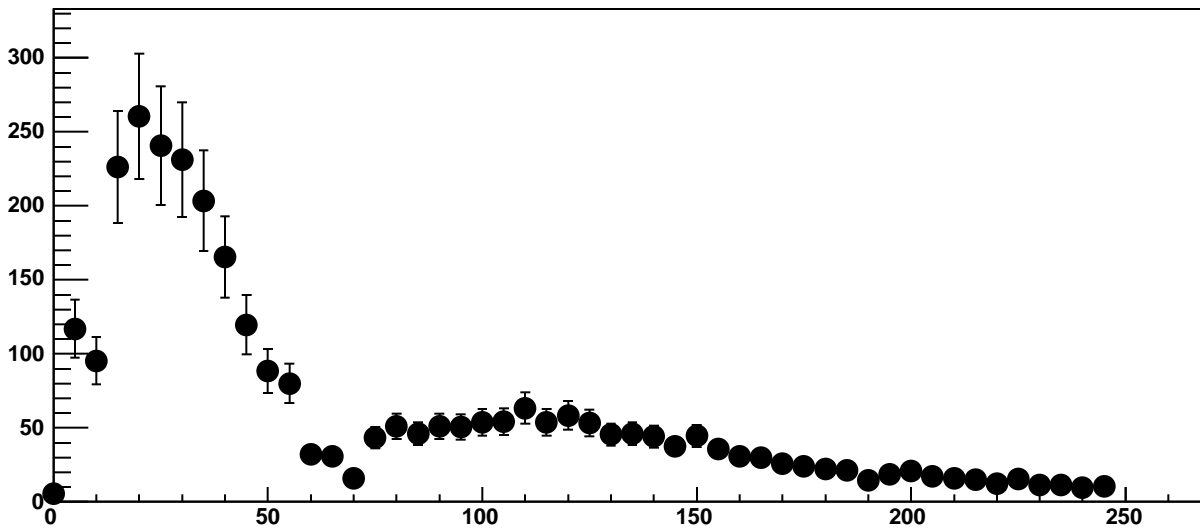


Chip 10, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold

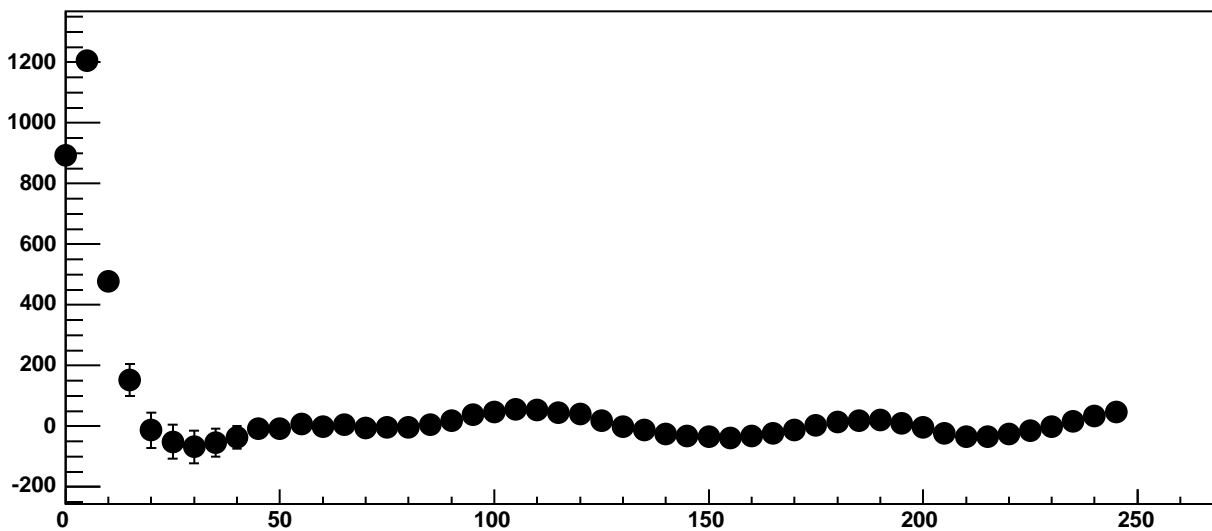
χ^2 / ndf	854.8 / 42
p0	-828.6 ± 1.345
p1	3.974 ± 0.08894
p2	9981 ± 10.87
p3	30.13 ± 0.03204



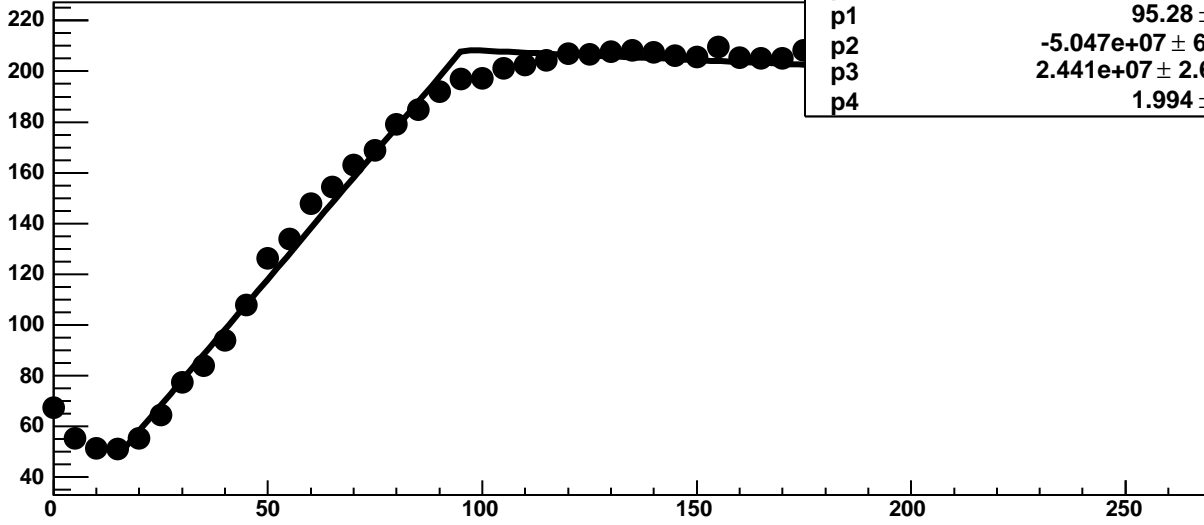
Chip 10, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold

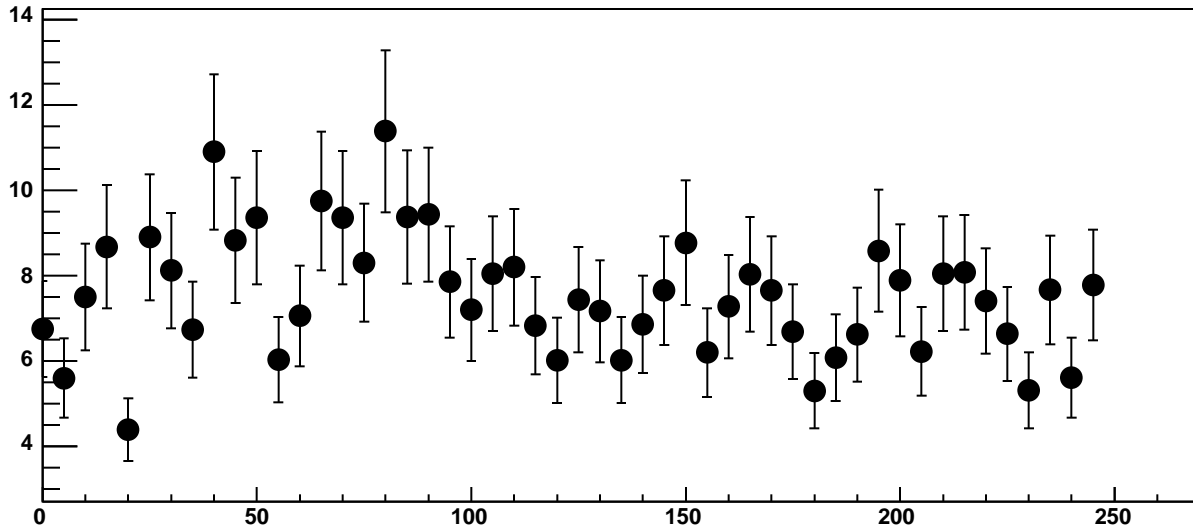


Chip 10, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold

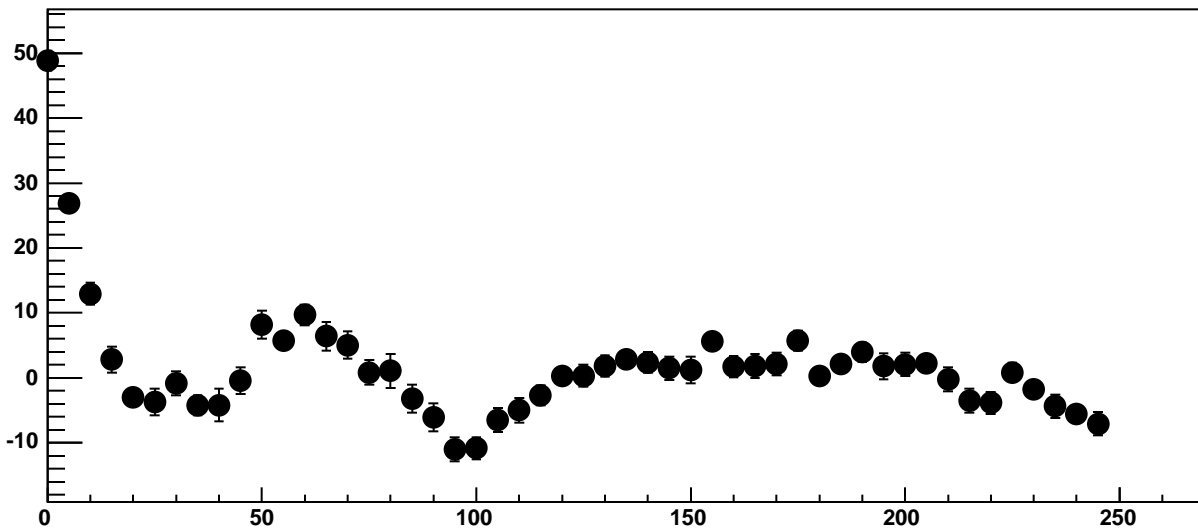


χ^2 / ndf	310.5 / 41
p0	208.4 ± 0.5962
p1	95.28 ± 0.5386
p2	$-5.047\text{e}+07 \pm 6.67\text{e}+05$
p3	$2.441\text{e}+07 \pm 2.606\text{e}+05$
p4	1.994 ± 0.0174

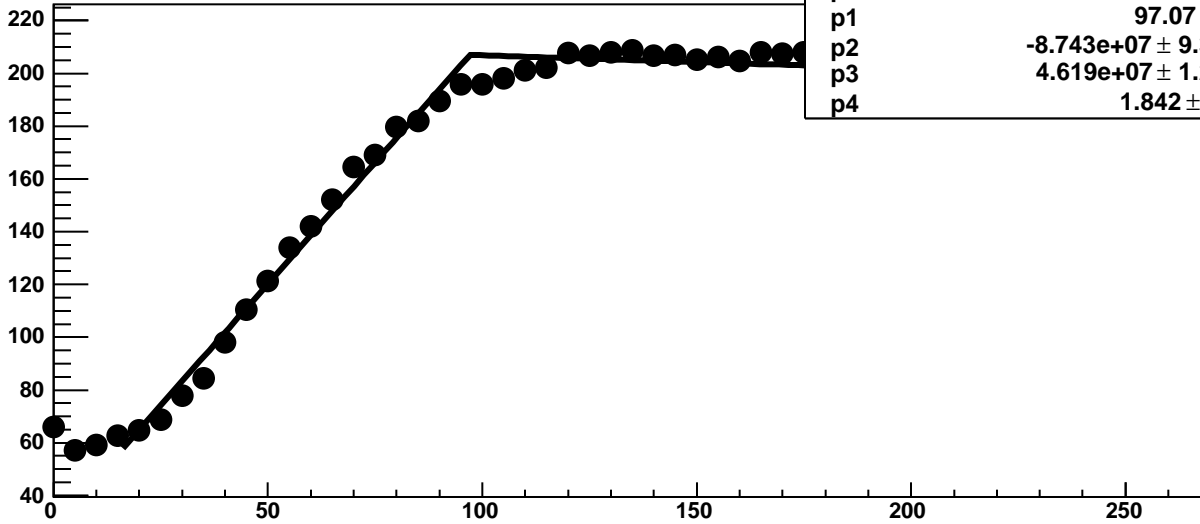
Chip 10, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold

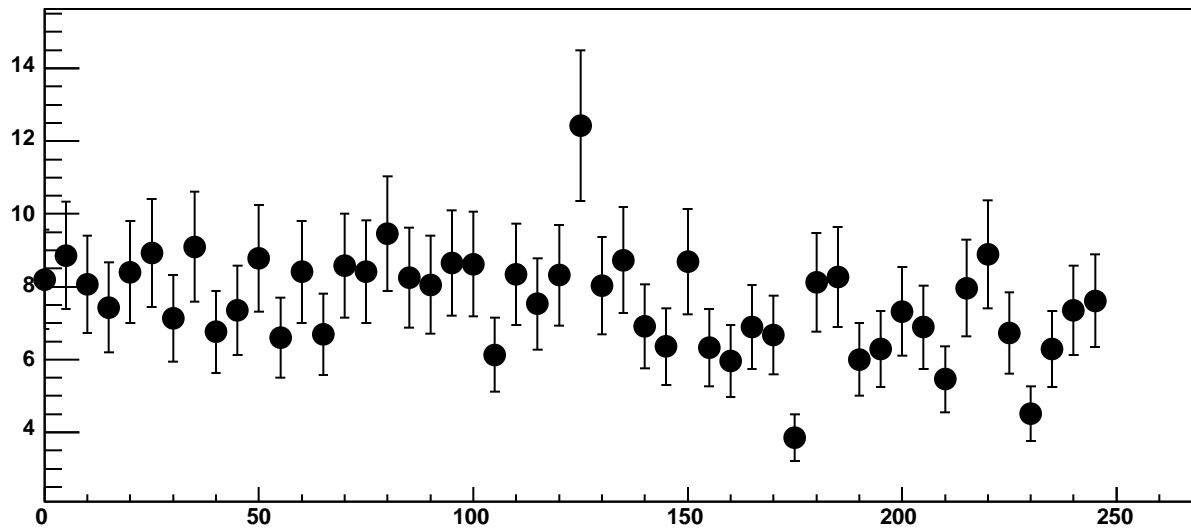


Chip 10, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold

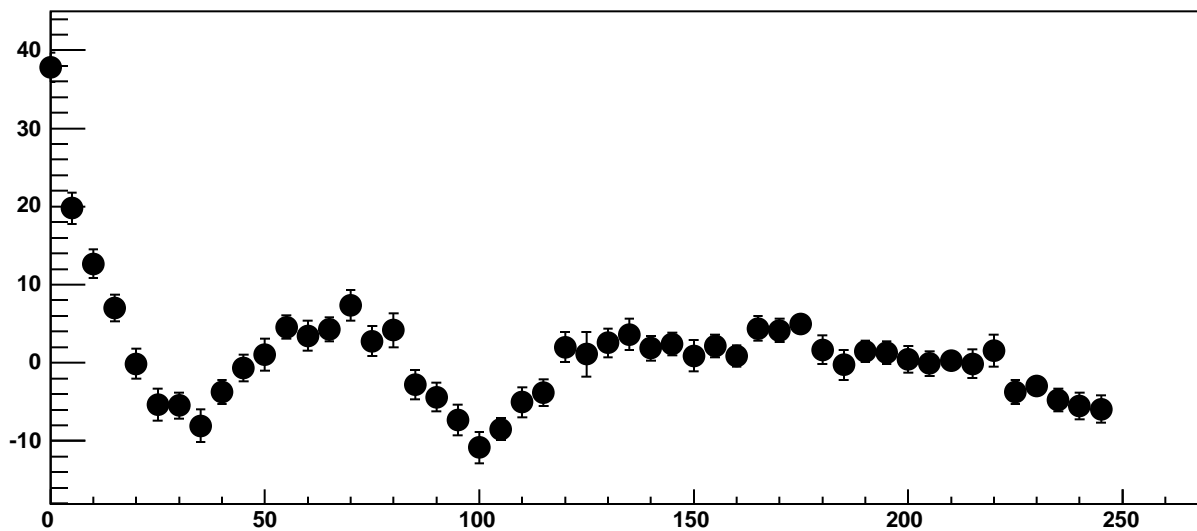


χ^2 / ndf	295.9 / 41
p0	206.9 ± 0.6182
p1	97.07 ± 0.5888
p2	$-8.743\text{e}+07 \pm 9.394\text{e}+05$
p3	$4.619\text{e}+07 \pm 1.292\text{e}+05$
p4	1.842 ± 0.01854

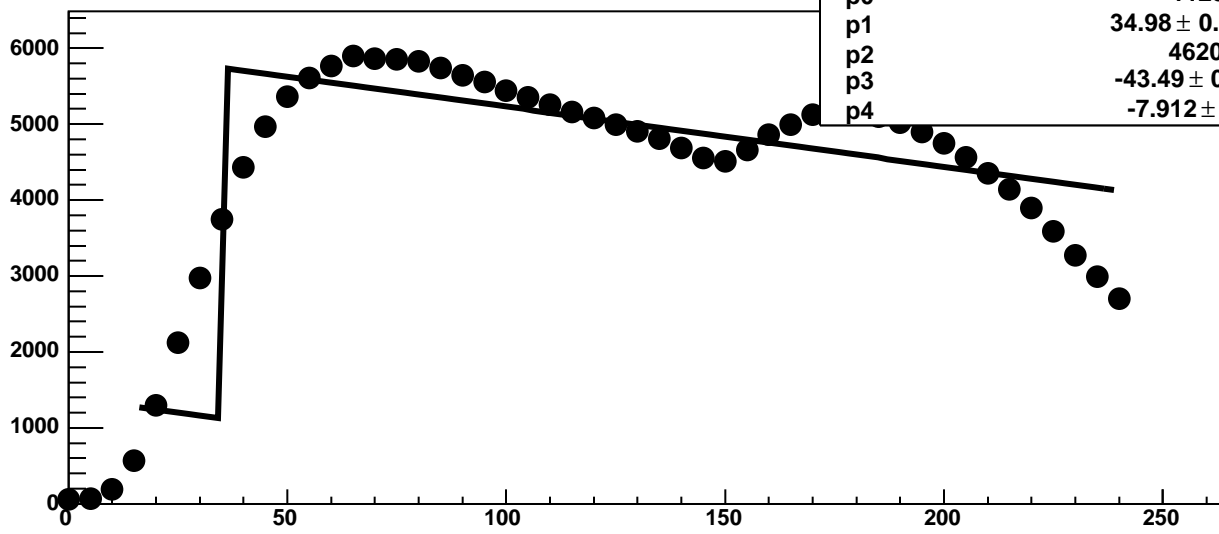
Chip 10, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

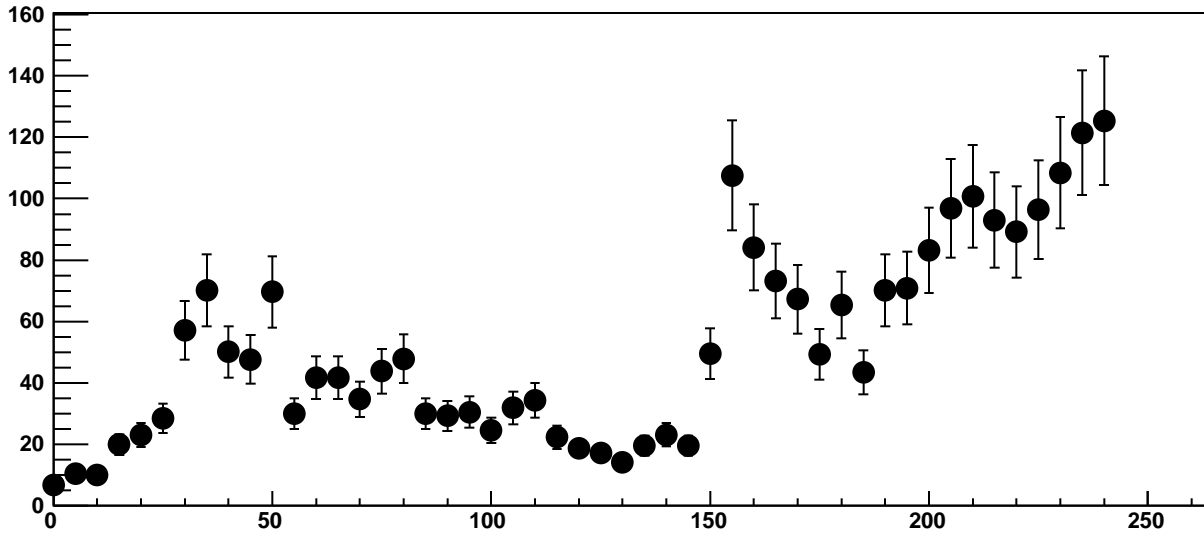


Chip 10, Channel 17, Enable 5, DAC=4000, ADC Mean vs Hold

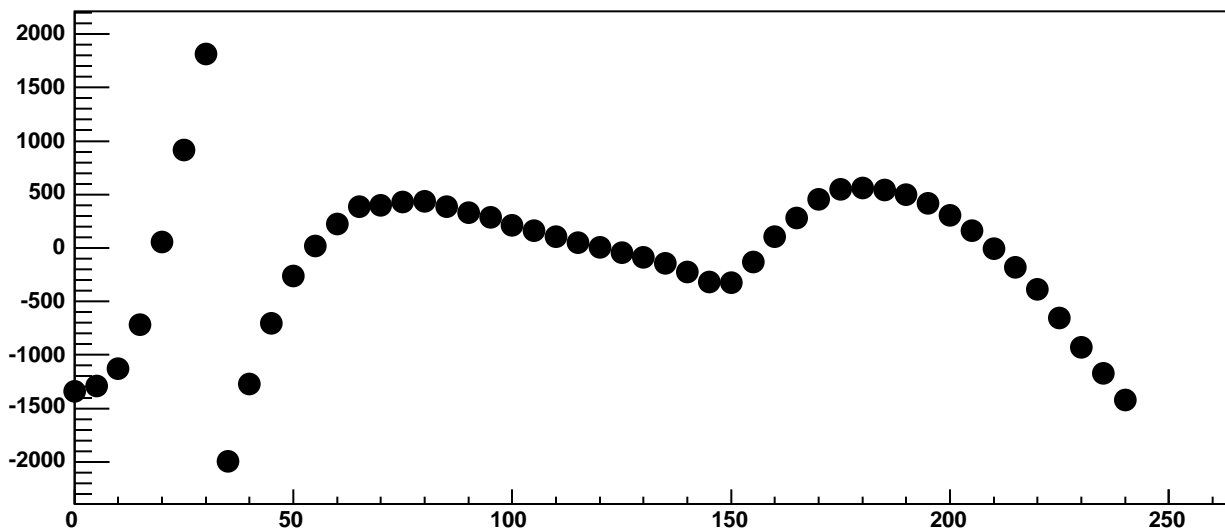


χ^2 / ndf	1.387e+05 / 41
p0	1125 ± 3.268
p1	34.98 ± 0.0003921
p2	4620 ± 4.816
p3	-43.49 ± 0.007652
p4	-7.912 ± 0.03617

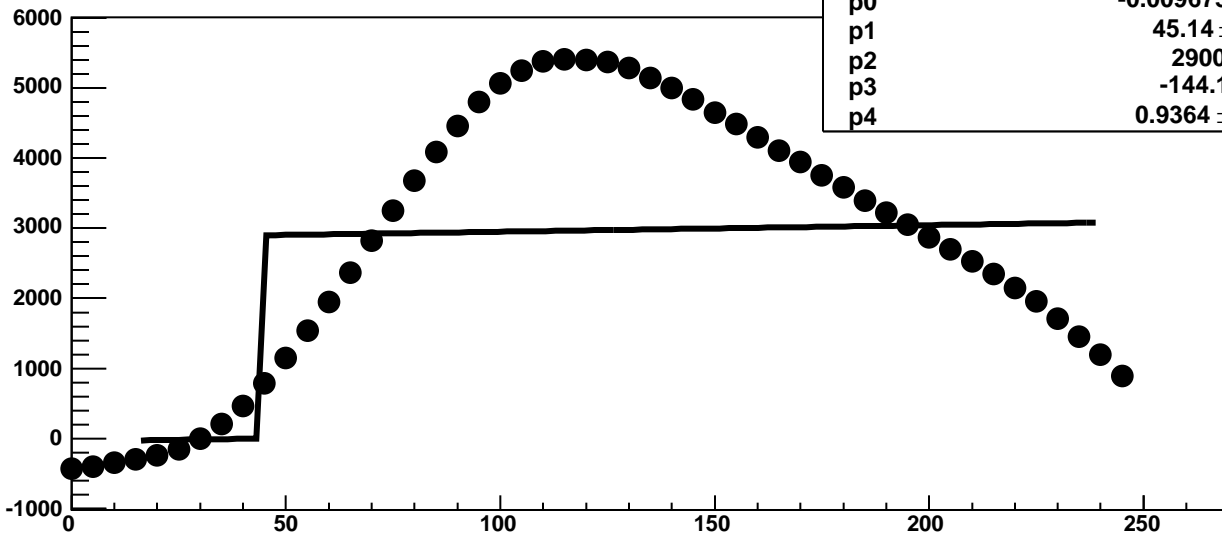
Chip 10, Channel 17, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 10, Channel 17, Enable 5, DAC=4000, ADC Residuals vs Hold

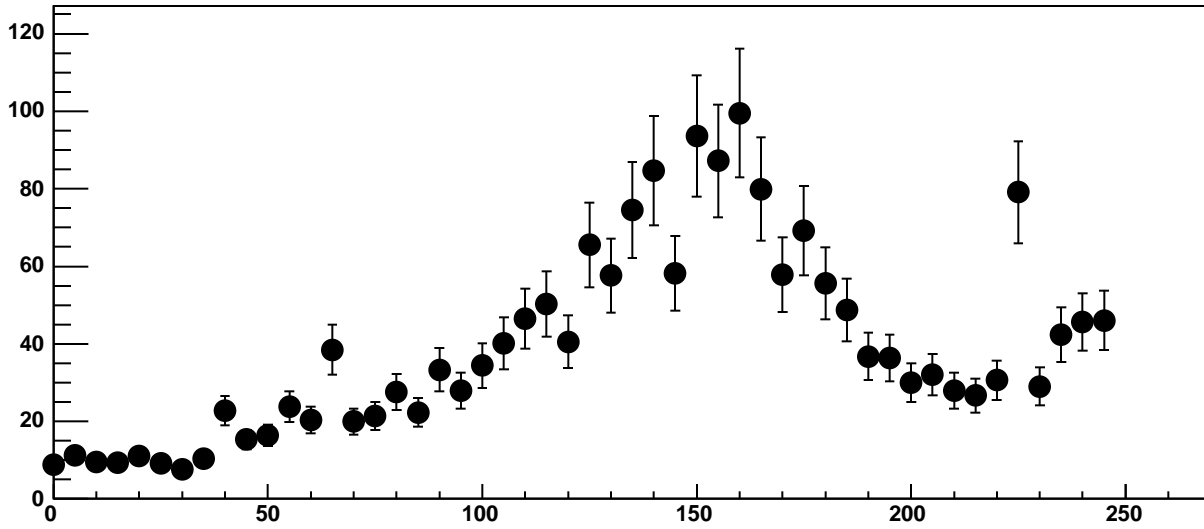


Chip 11, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold

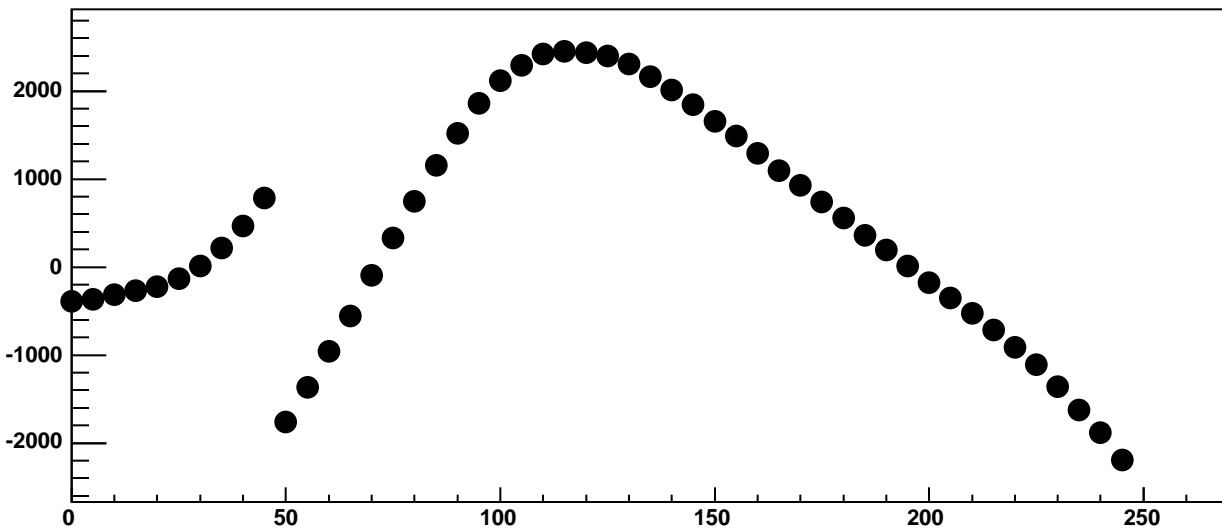


χ^2 / ndf	1.192e+06 / 41
p0	-0.009673 \pm 6.241
p1	45.14 \pm 0.0007
p2	2900 \pm 9.785
p3	-144.1 \pm 4.787
p4	0.9364 \pm 0.1234

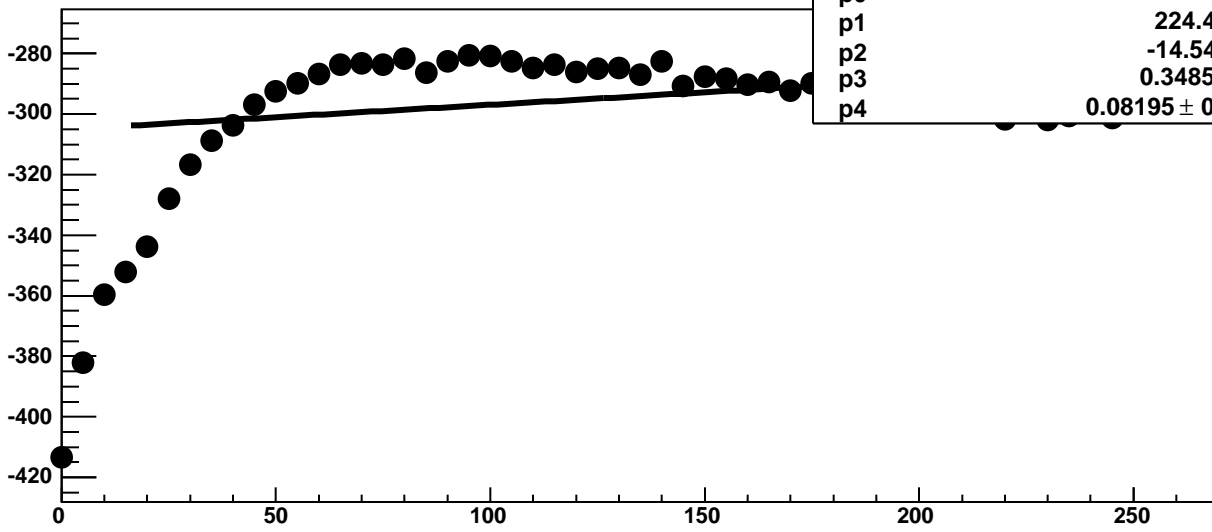
Chip 11, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold

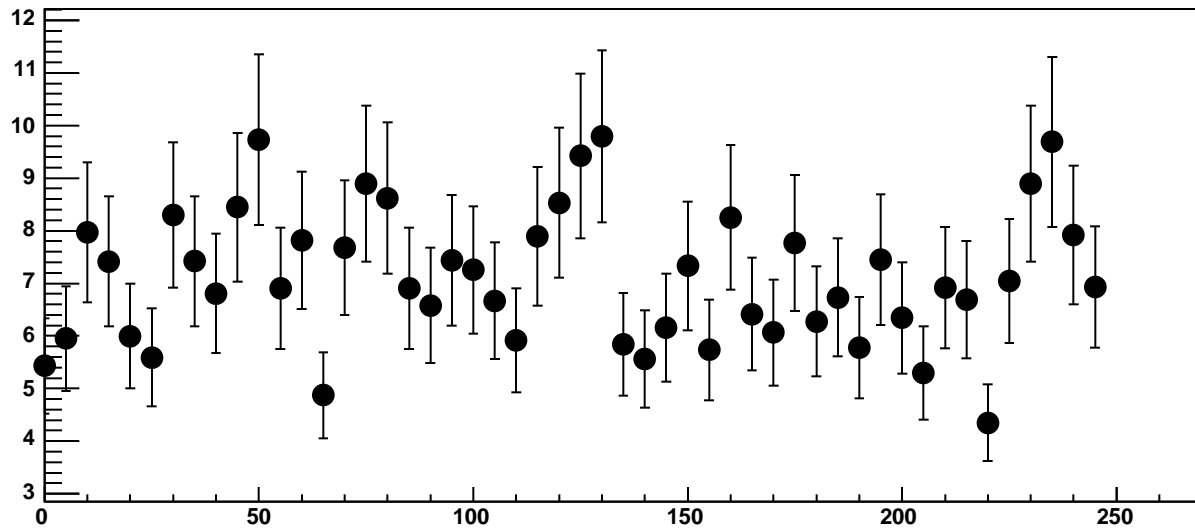


Chip 11, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

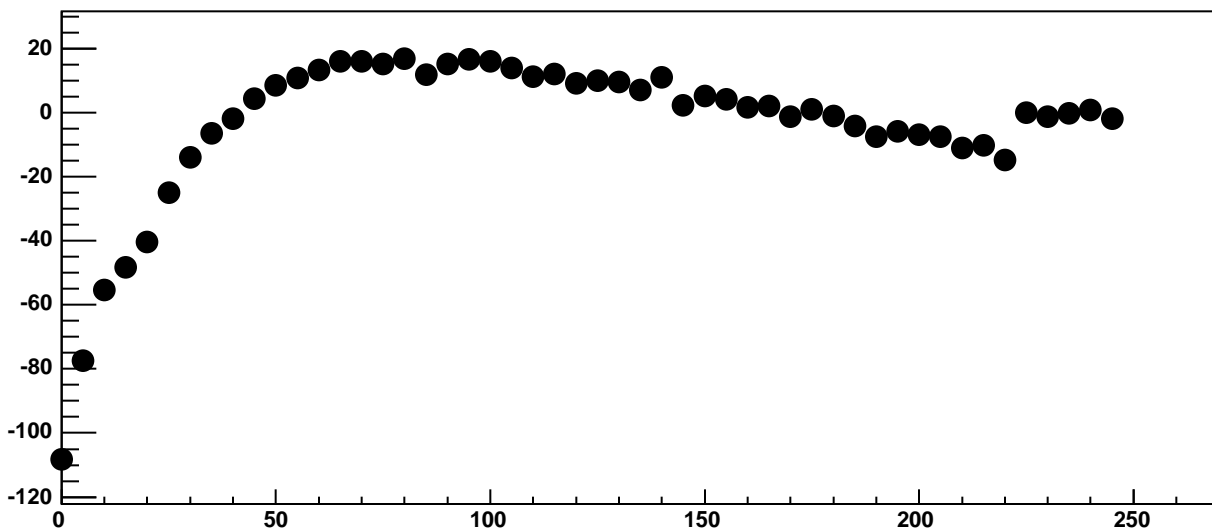


χ^2 / ndf	3813 / 41
p0	-286.7 ± 0.4753
p1	224.4 ± 3.335
p2	-14.54 ± 1.209
p3	0.3485 ± 1.858
p4	0.08195 ± 0.003464

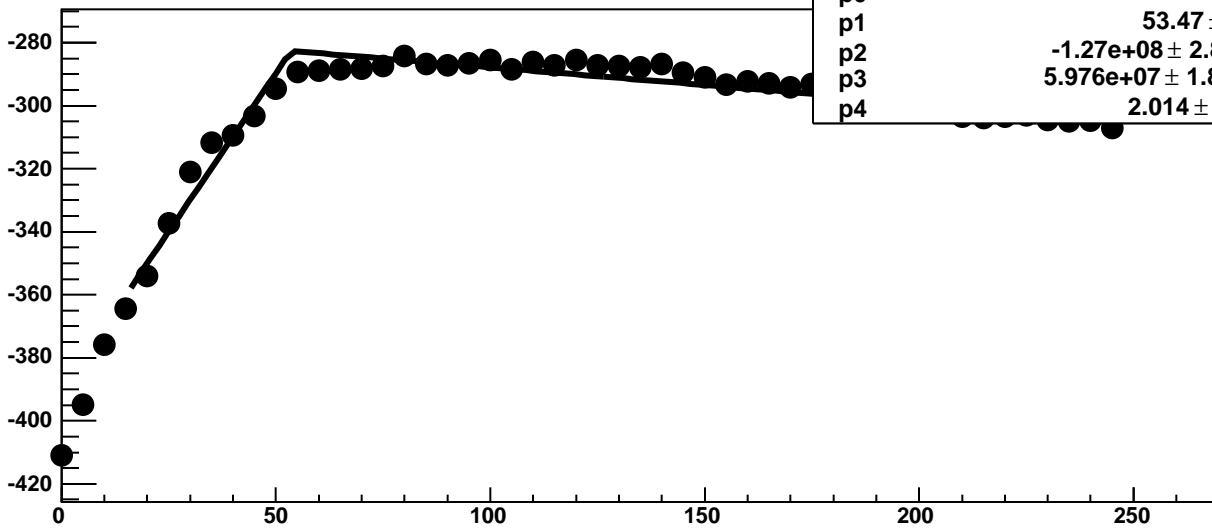
Chip 11, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold

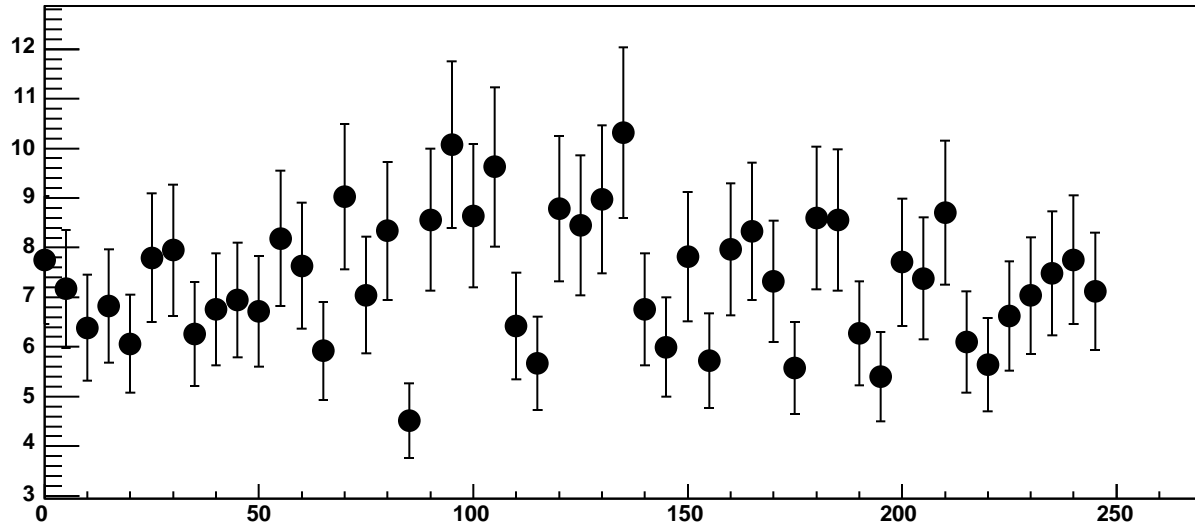


Chip 11, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

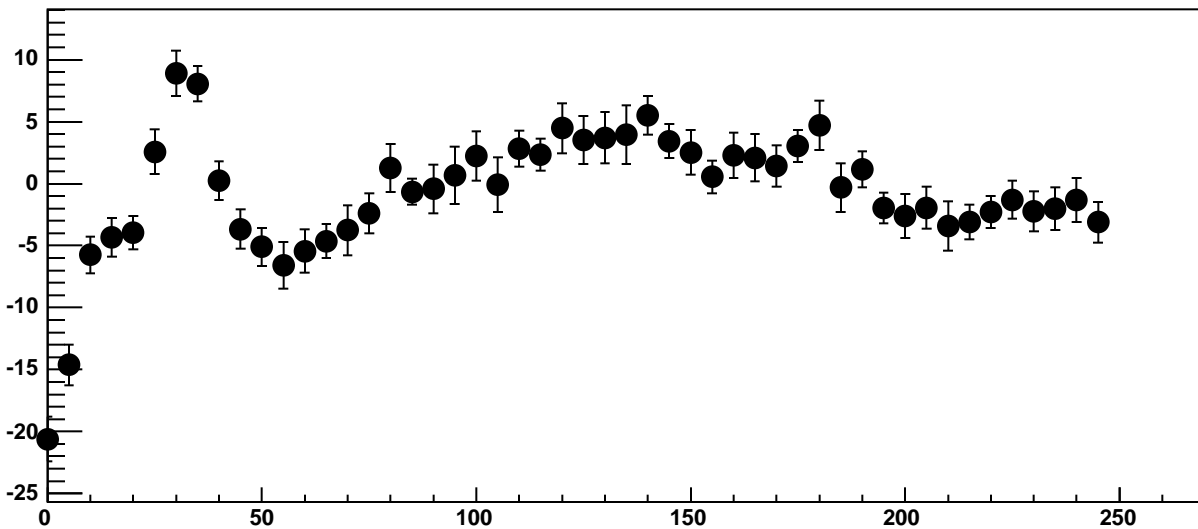


χ^2 / ndf	210.5 / 41
p0	-282.6 ± 0.51
p1	53.47 ± 0.5943
p2	-1.27e+08 ± 2.883e+06
p3	5.976e+07 ± 1.865e+05
p4	2.014 ± 0.04748

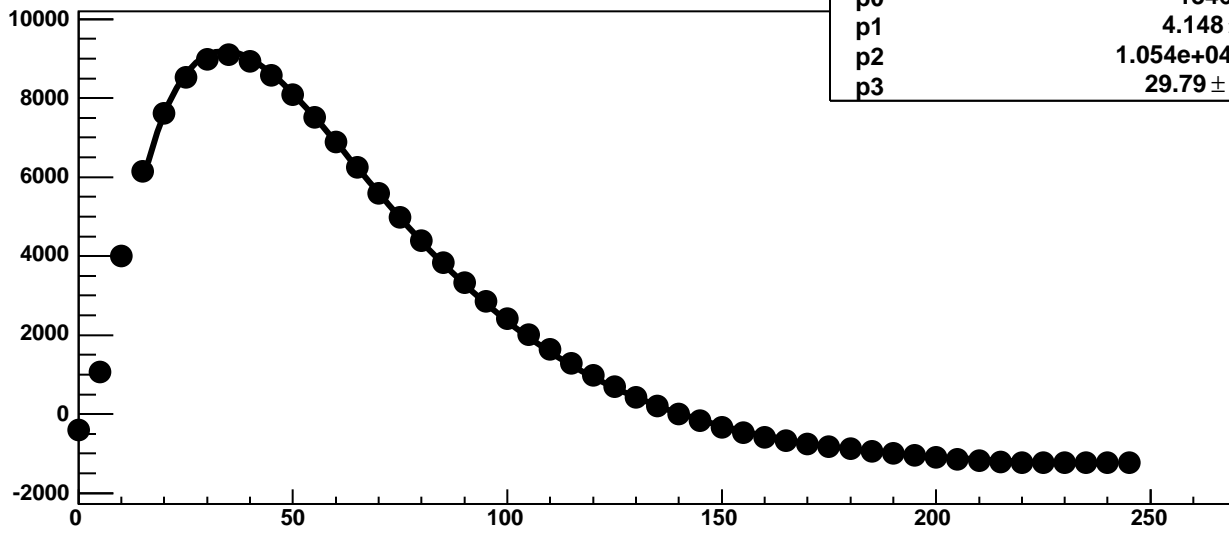
Chip 11, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

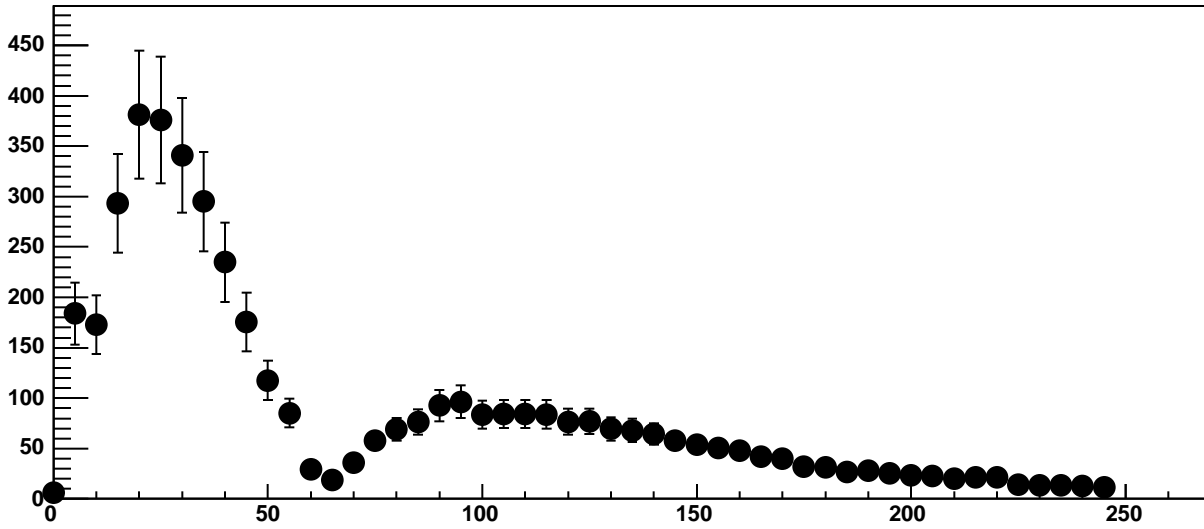


Chip 11, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold

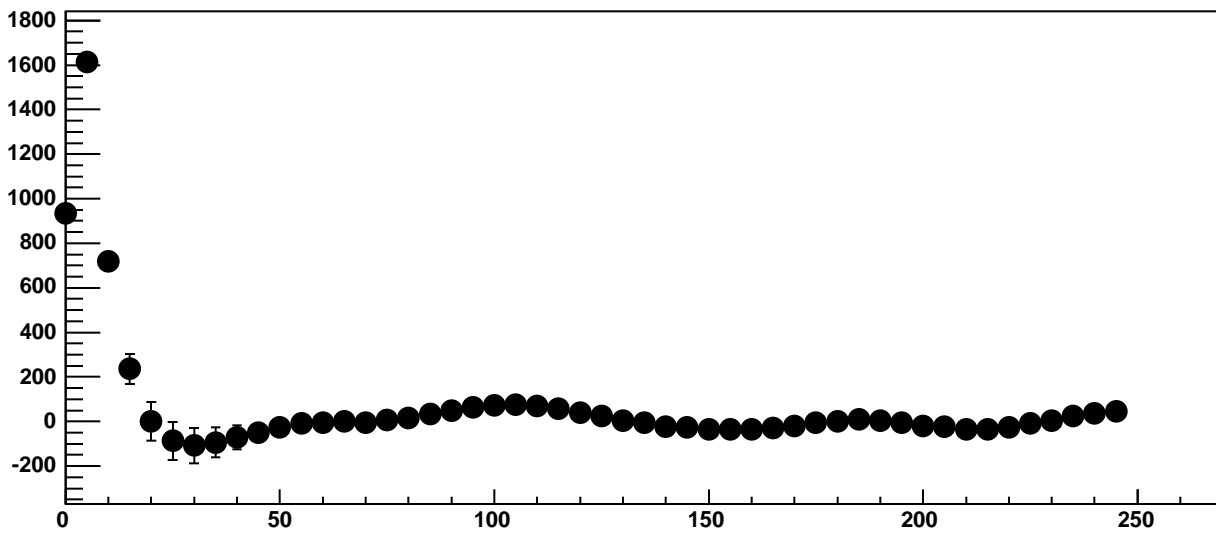


χ^2 / ndf	538.9 / 42
p0	-1346 ± 1.713
p1	4.148 ± 0.1131
p2	1.054e+04 ± 14.69
p3	29.79 ± 0.04069

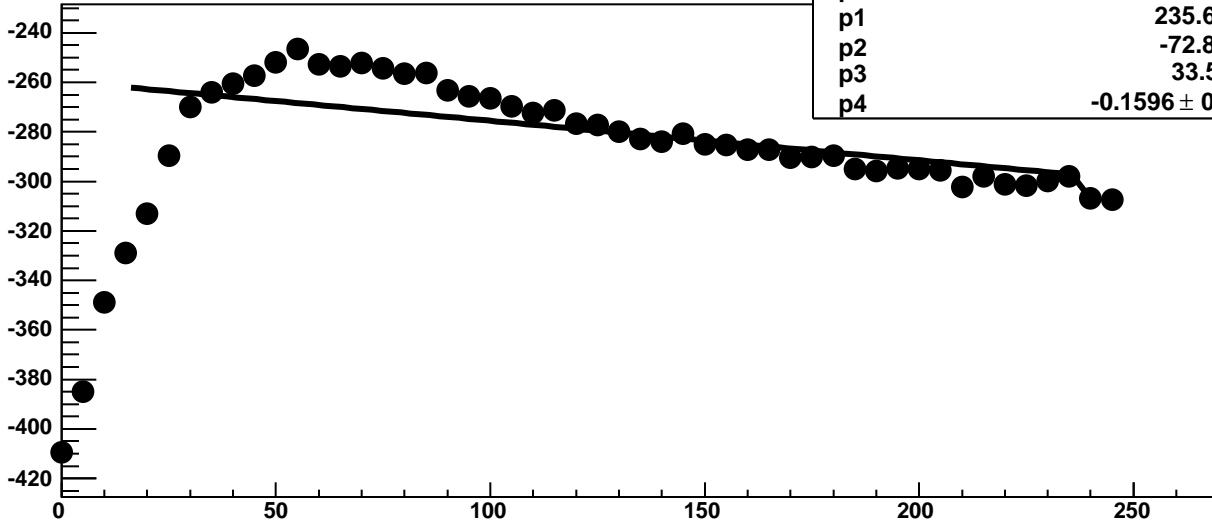
Chip 11, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold

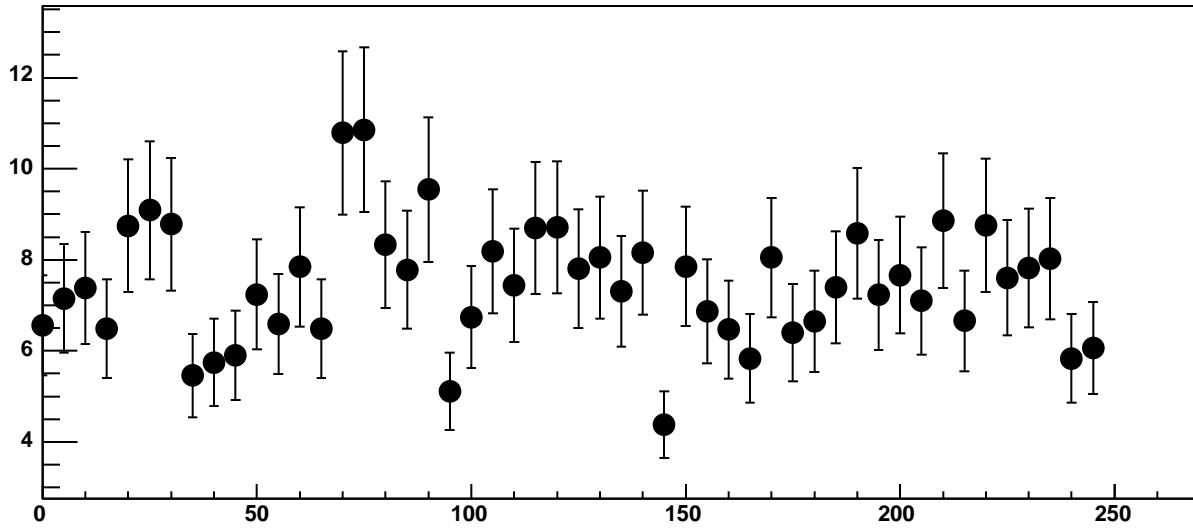


Chip 11, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

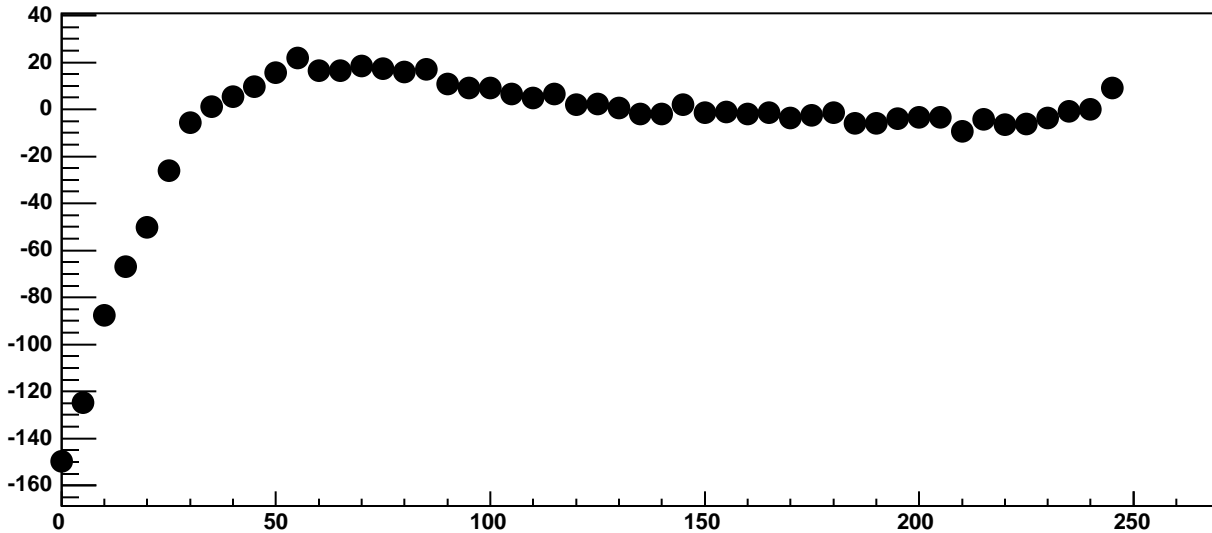


χ^2 / ndf	3906 / 41
p0	-297.2 ± 1.097
p1	235.6 ± 6.067
p2	-72.8 ± 89.34
p3	33.5 ± 36.41
p4	-0.1596 ± 0.003847

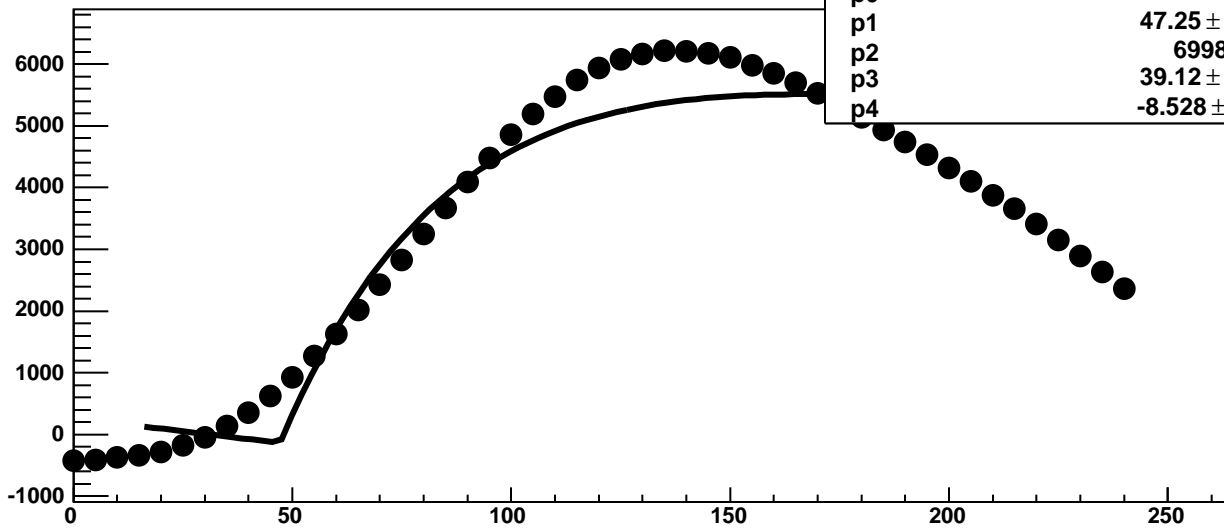
Chip 11, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

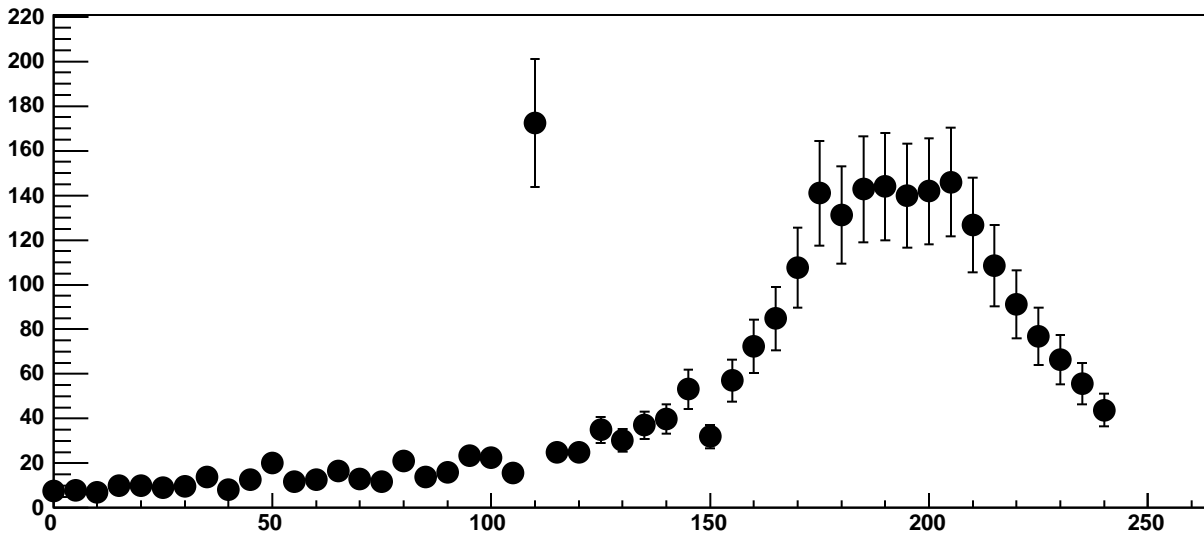


Chip 11, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold

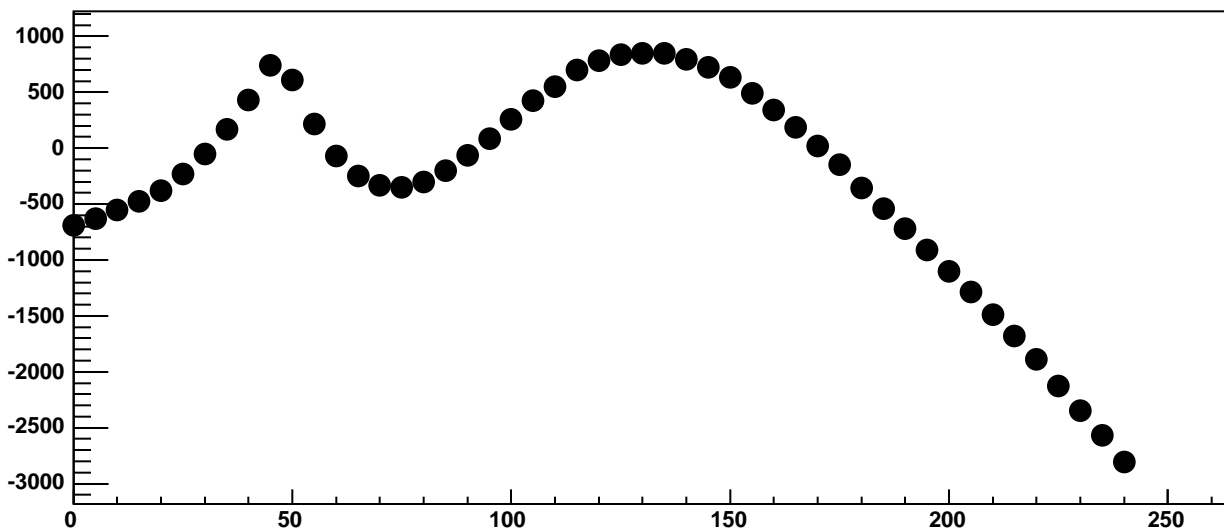


χ^2 / ndf	5.505e+05 / 41
p0	-136.9 ± 1.292
p1	47.25 ± 0.01749
p2	6998 ± 9.425
p3	39.12 ± 0.06014
p4	-8.528 ± 0.05711

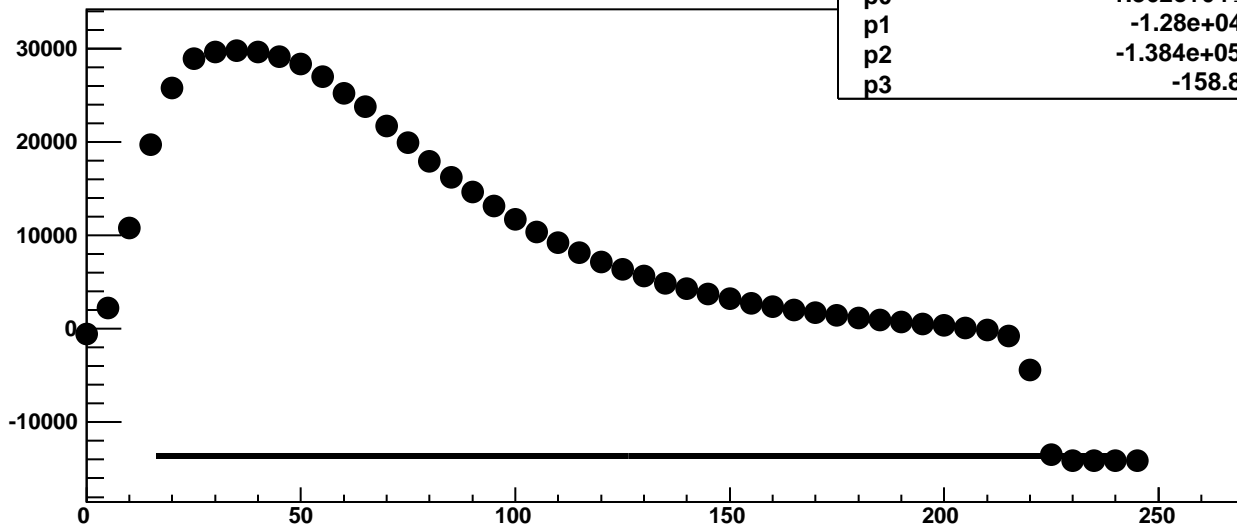
Chip 11, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold

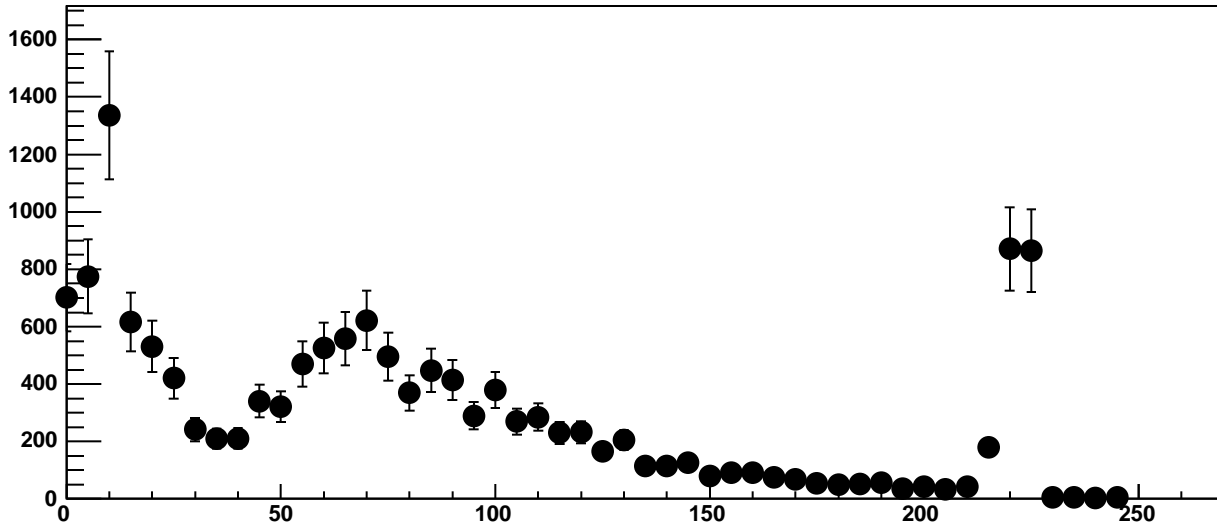


Chip 11, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold

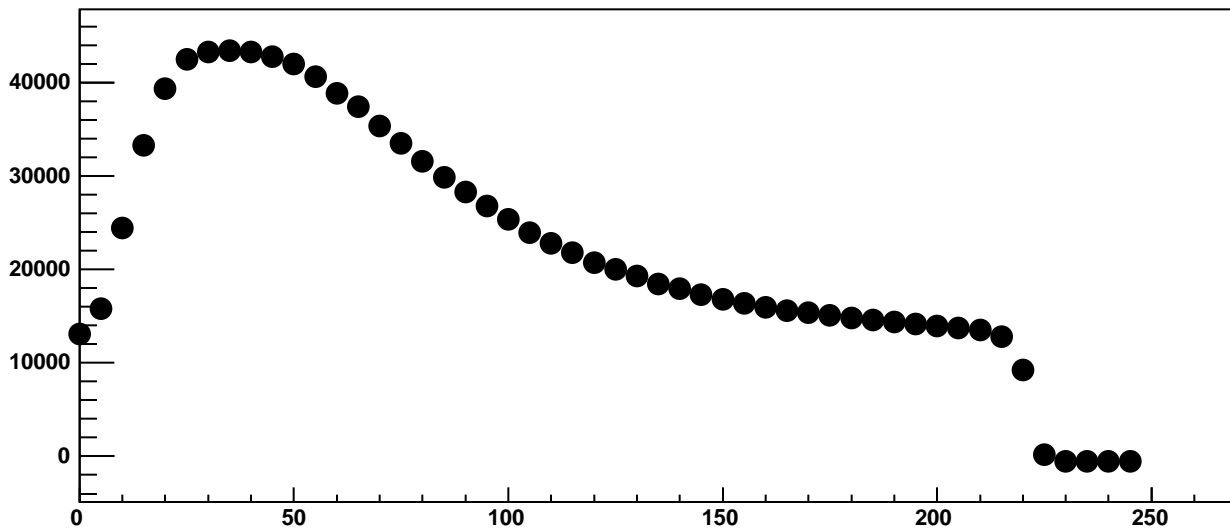


χ^2 / ndf	2.696e+07 / 42
p0	-1.362e+04 \pm 0.5559
p1	-1.28e+04 \pm 1.909
p2	-1.384e+05 \pm 1.414
p3	-158.8 \pm 1.414

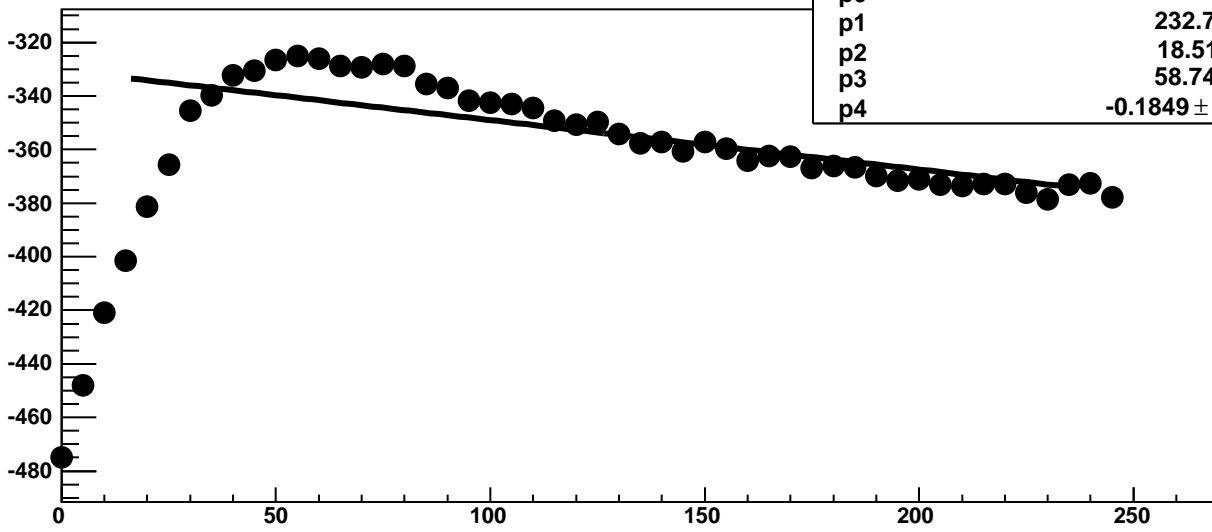
Chip 11, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold

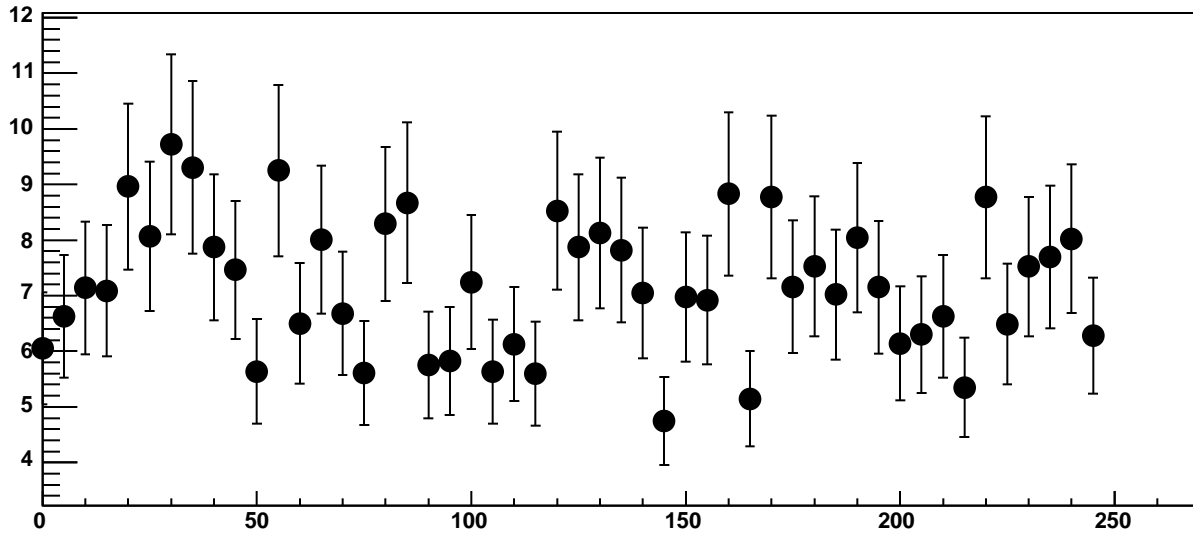


Chip 11, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

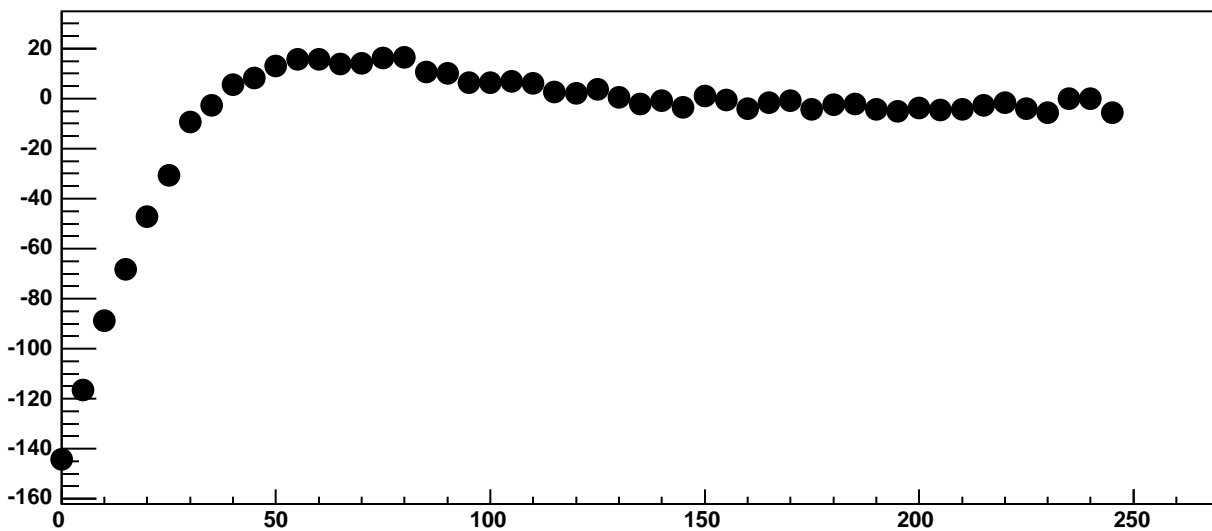


χ^2 / ndf	3532 / 41
p0	-373.5 ± 1.866
p1	232.7 ± 10.23
p2	18.51 ± 87.32
p3	58.74 ± 245.7
p4	-0.1849 ± 0.00392

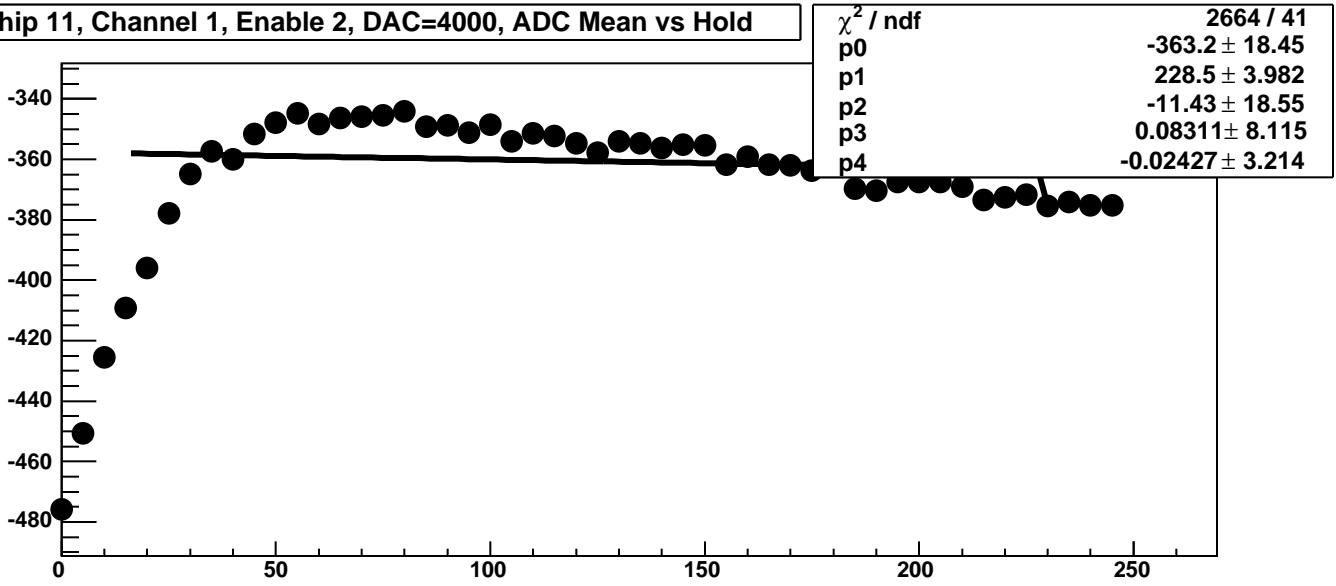
Chip 11, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



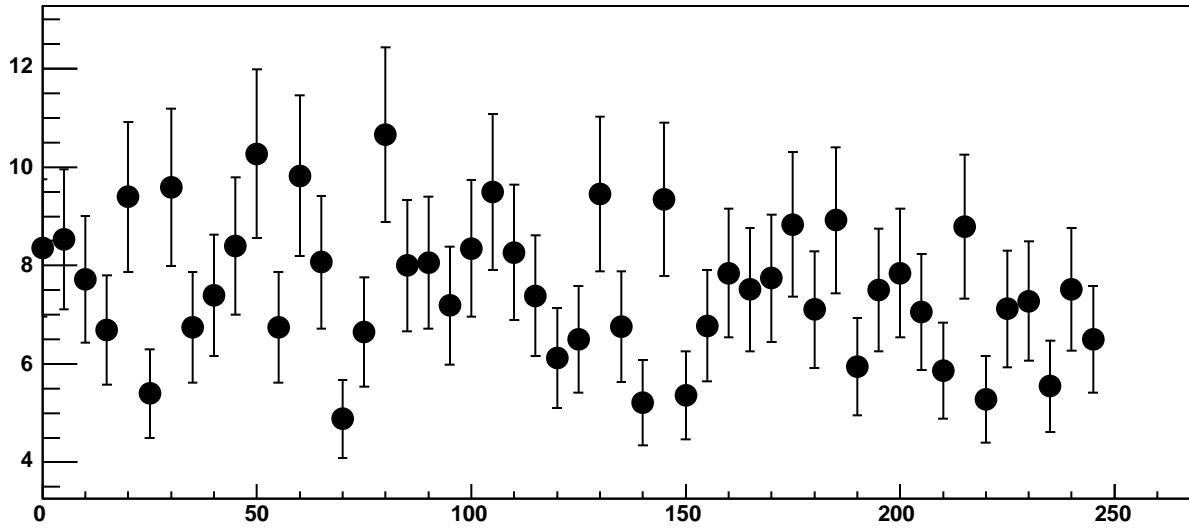
Chip 11, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold



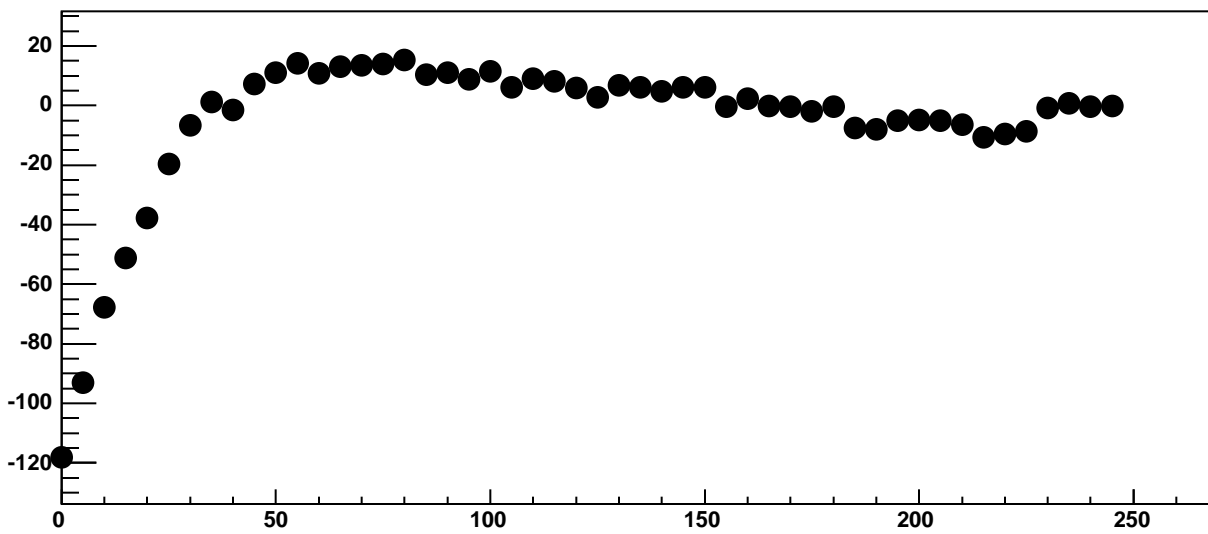
Chip 11, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold



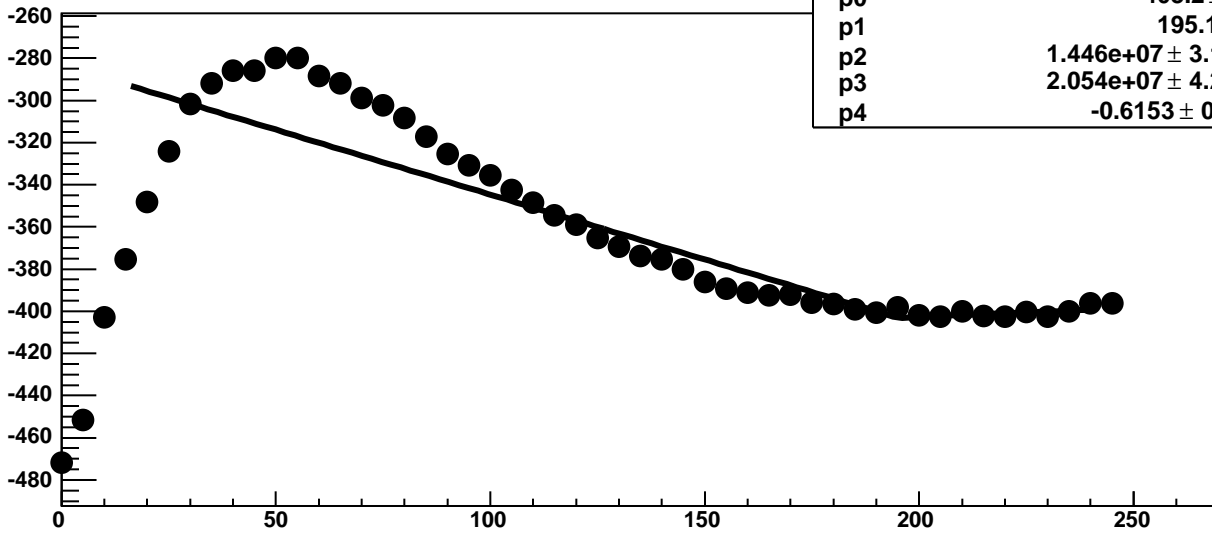
Chip 11, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

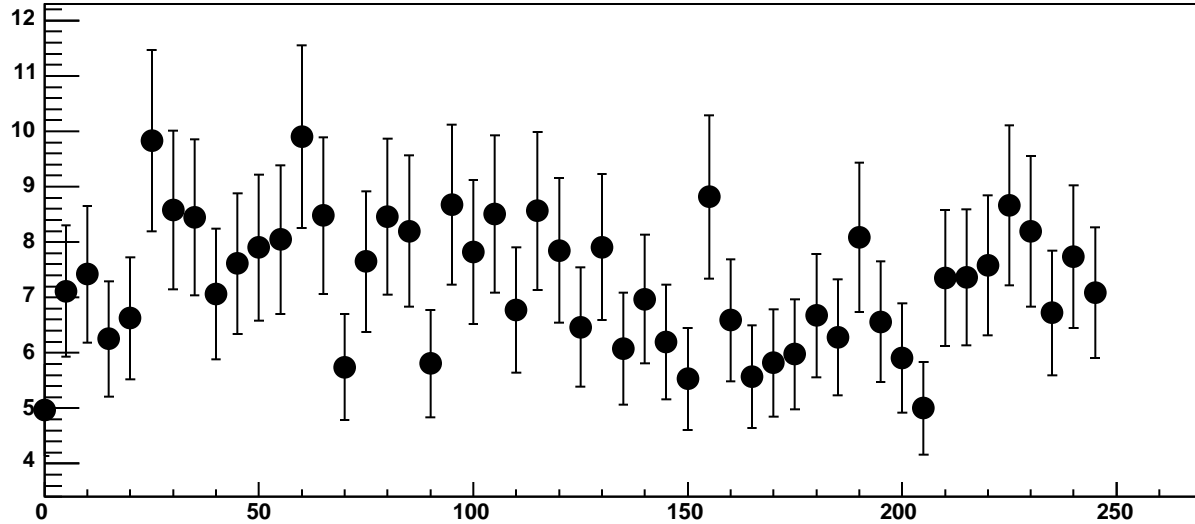


Chip 11, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold

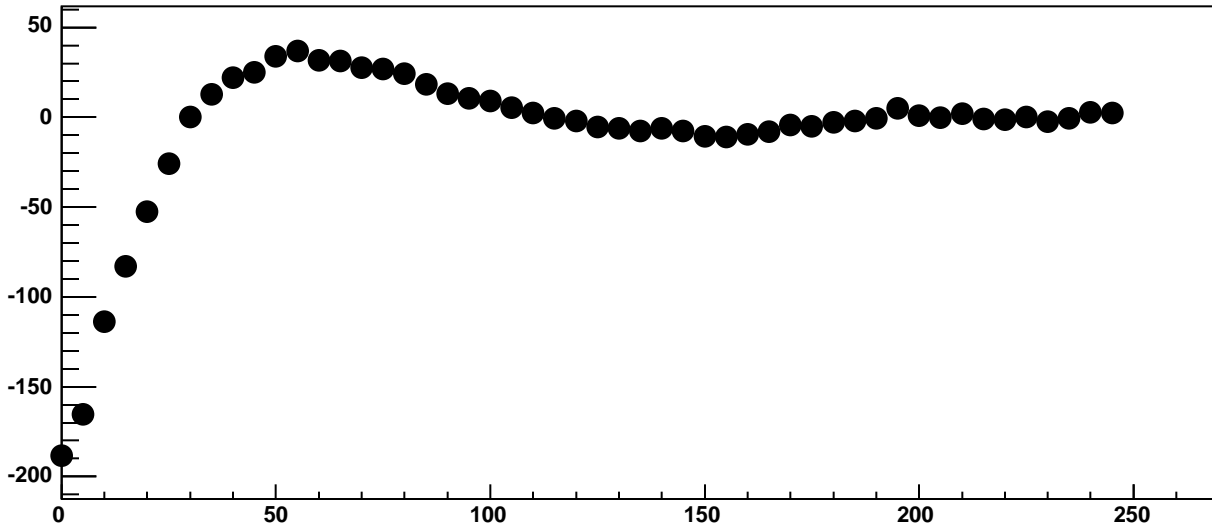


χ^2 / ndf	7713 / 41
p0	-403.2 ± 0.8813
p1	195.1 ± 1.588
p2	$1.446\text{e}+07 \pm 3.166\text{e}+06$
p3	$2.054\text{e}+07 \pm 4.231\text{e}+06$
p4	-0.6153 ± 0.004953

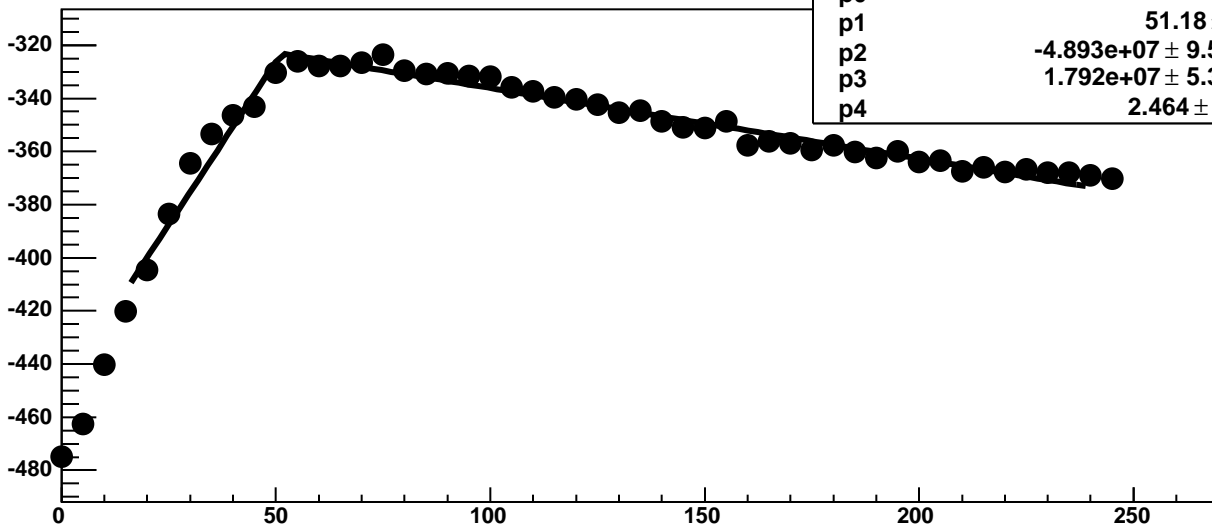
Chip 11, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold

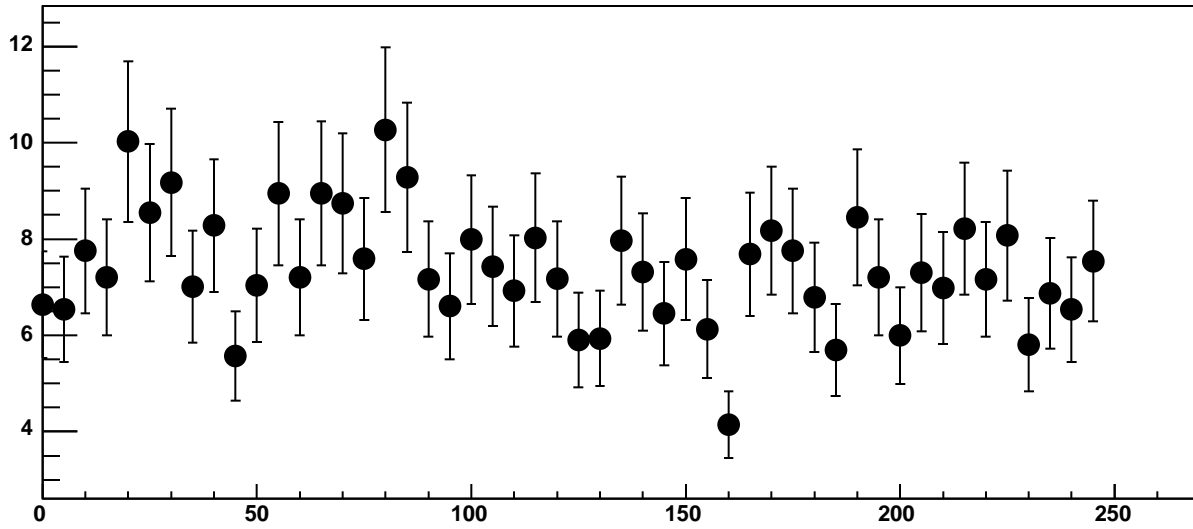


Chip 11, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

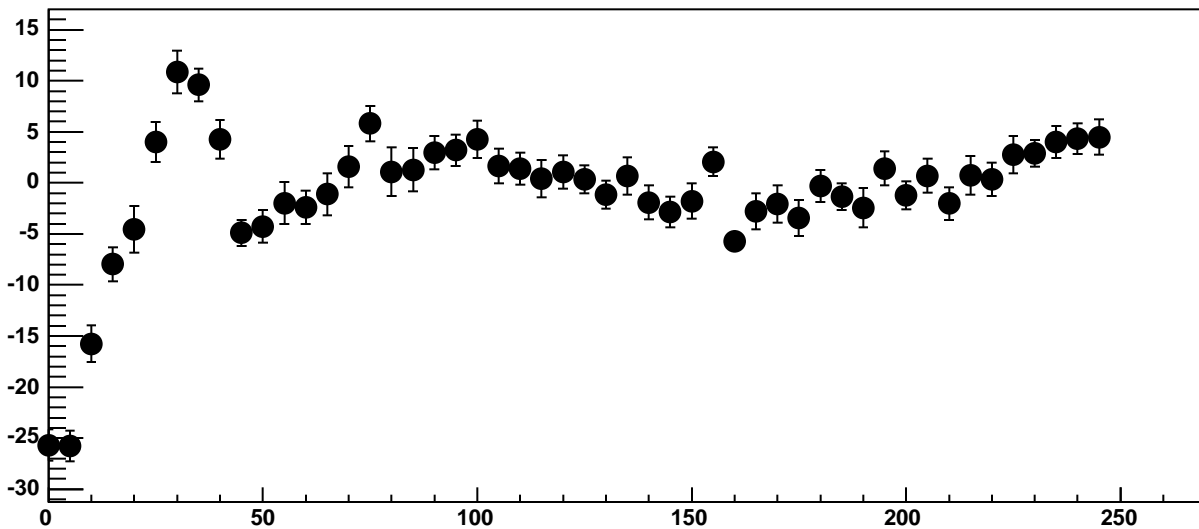


χ^2 / ndf	231.9 / 41
p0	-323.1 ± 0.5371
p1	51.18 ± 0.4391
p2	$-4.893e+07 \pm 9.522e+05$
p3	$1.792e+07 \pm 5.383e+04$
p4	2.464 ± 0.05189

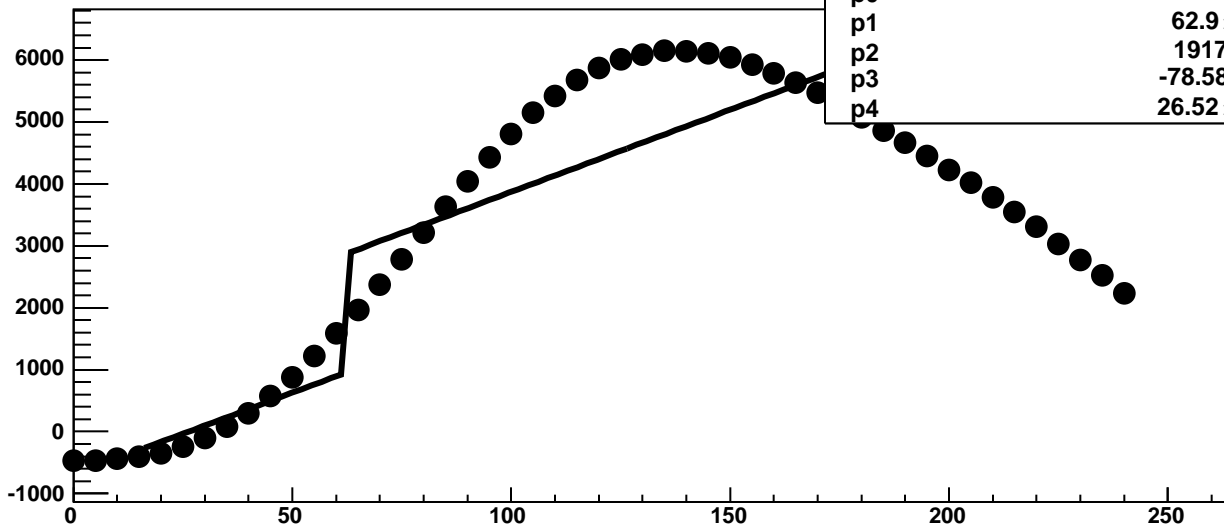
Chip 11, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

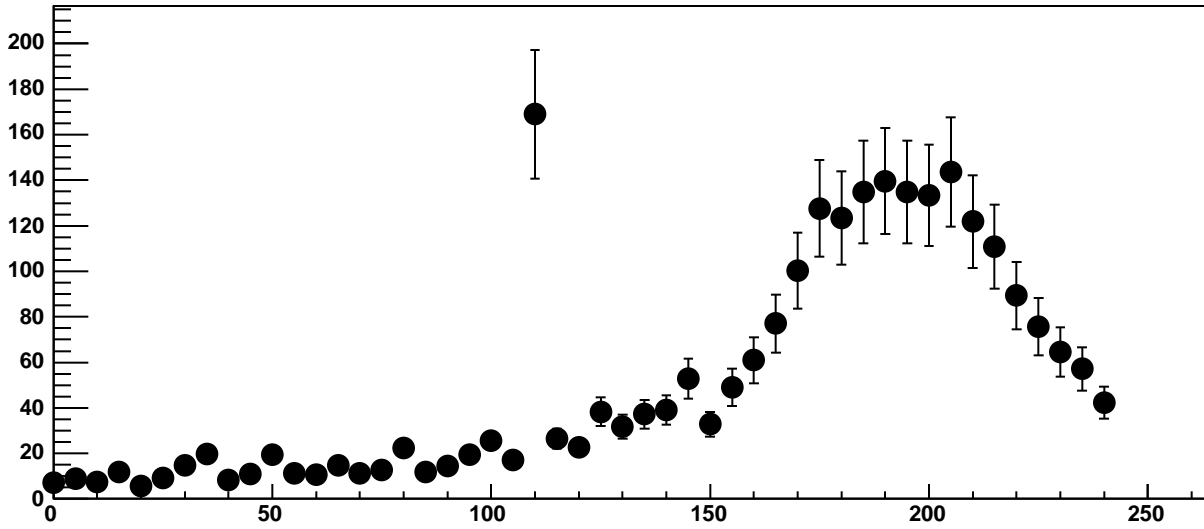


Chip 11, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

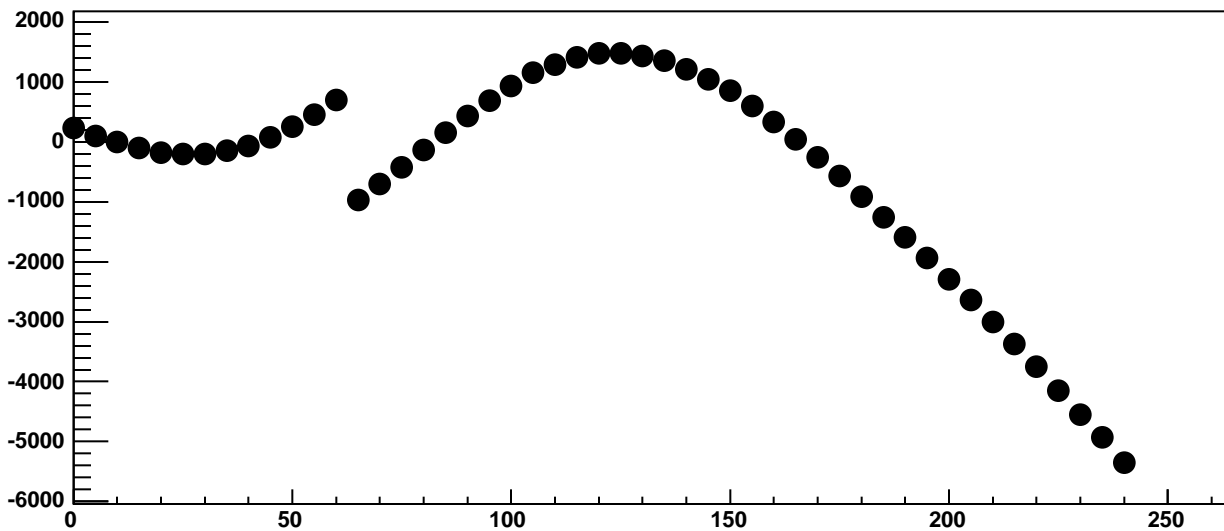


χ^2 / ndf	1.433e+06 / 41
p0	971 ± 2.991
p1	62.9 ± 0.1671
p2	1917 ± 7.287
p3	-78.58 ± 4.764
p4	26.52 ± 0.1351

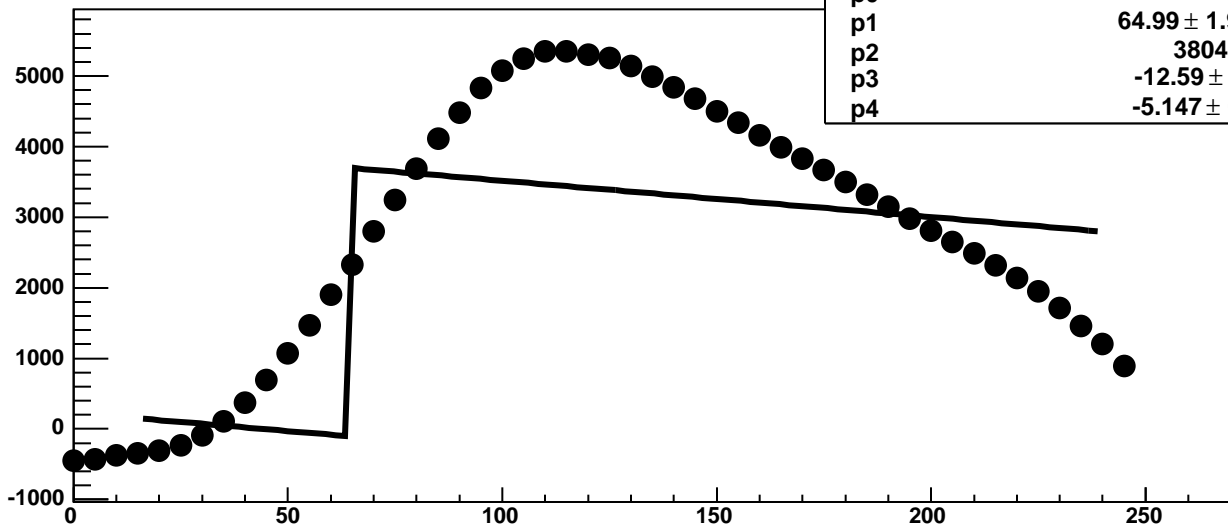
Chip 11, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold

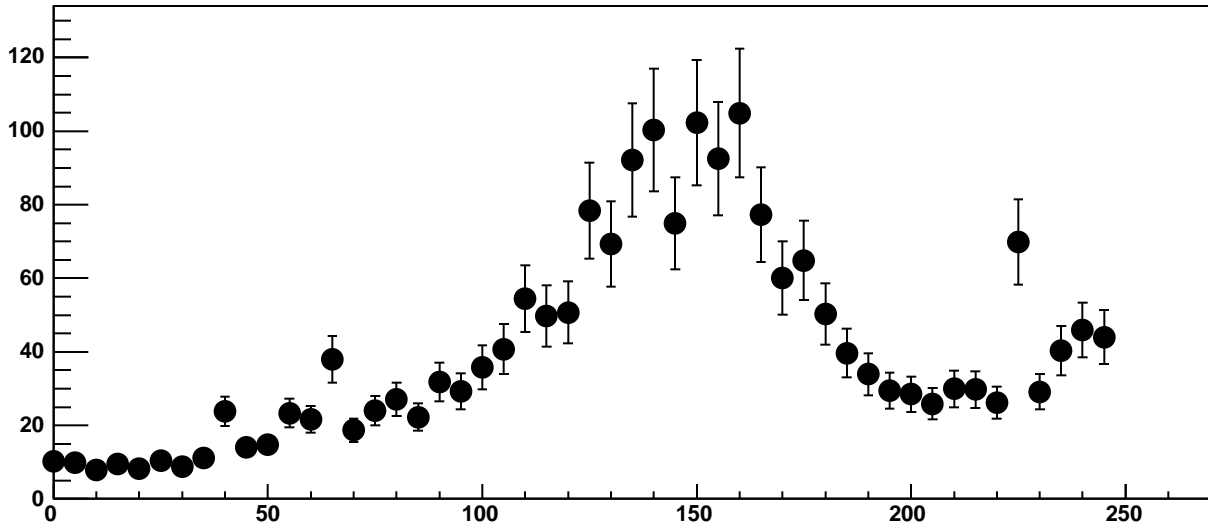


Chip 11, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold

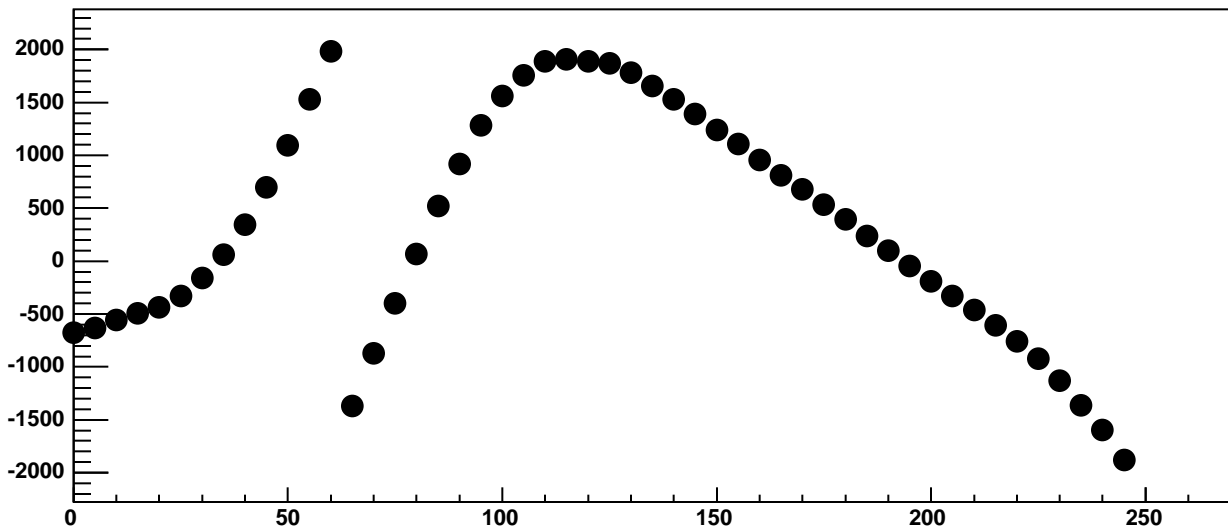


χ^2 / ndf	9.753e+05 / 41
p0	-106.8 ± 1.558
p1	64.99 ± 1.915e-05
p2	3804 ± 3.642
p3	-12.59 ± 0.01591
p4	-5.147 ± 0.02669

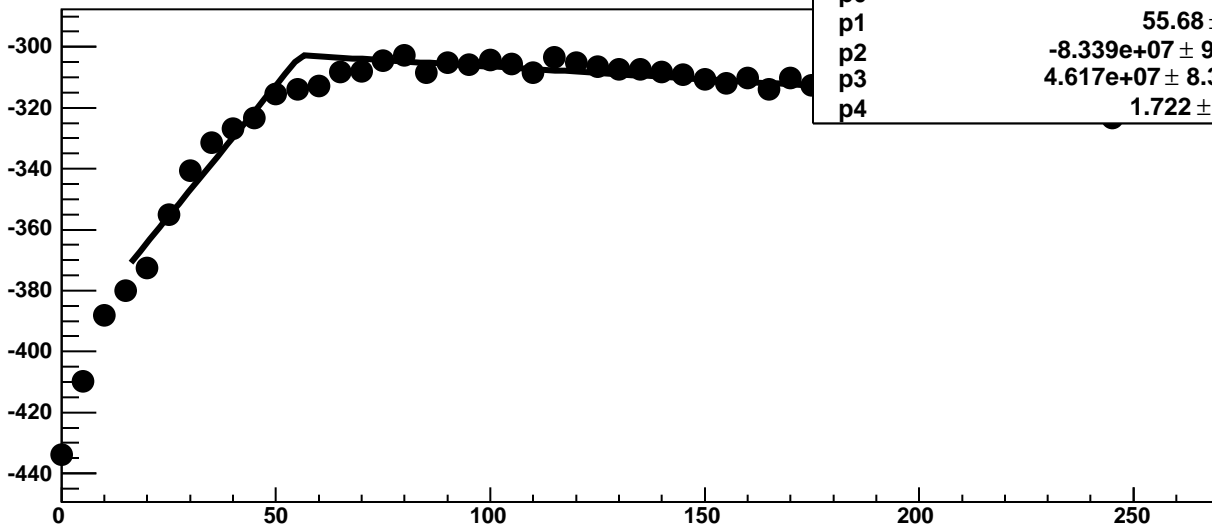
Chip 11, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold

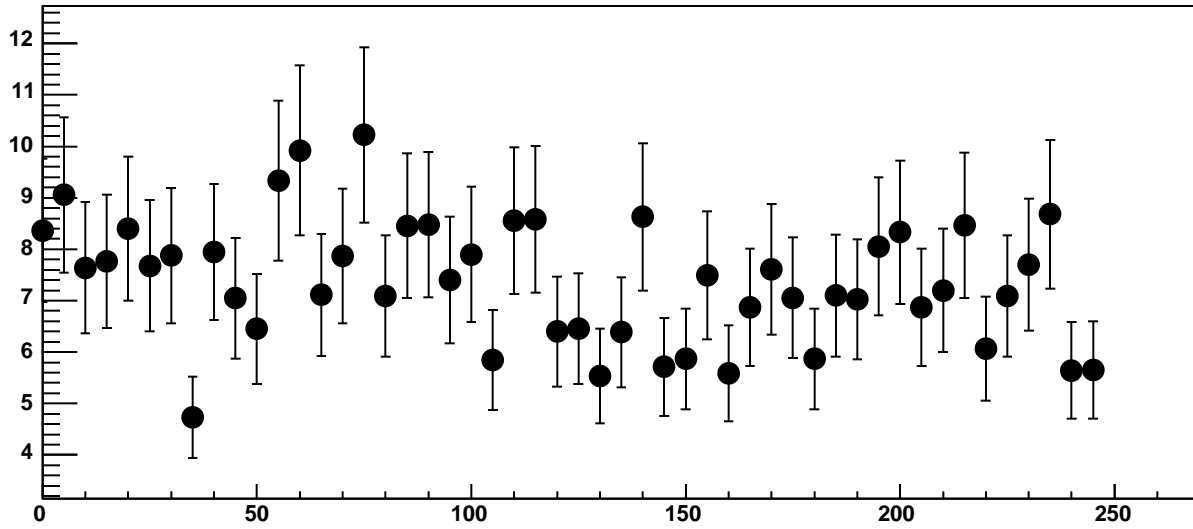


Chip 11, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

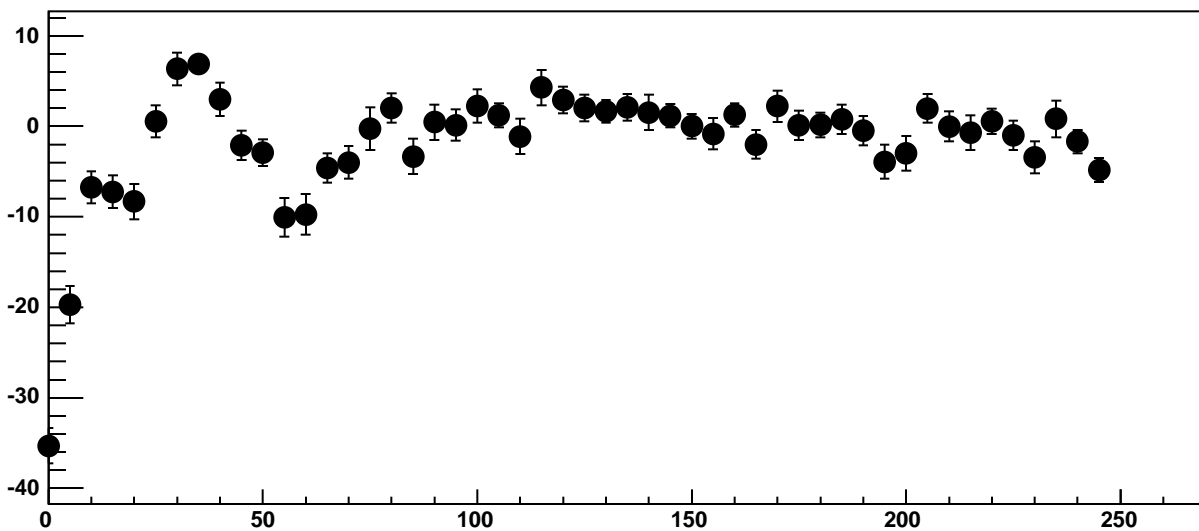


χ^2 / ndf	192.3 / 41
p0	-302.7 ± 0.524
p1	55.68 ± 0.4789
p2	$-8.339\text{e}+07 \pm 9.71\text{e}+05$
p3	$4.617\text{e}+07 \pm 8.393\text{e}+05$
p4	1.722 ± 0.01971

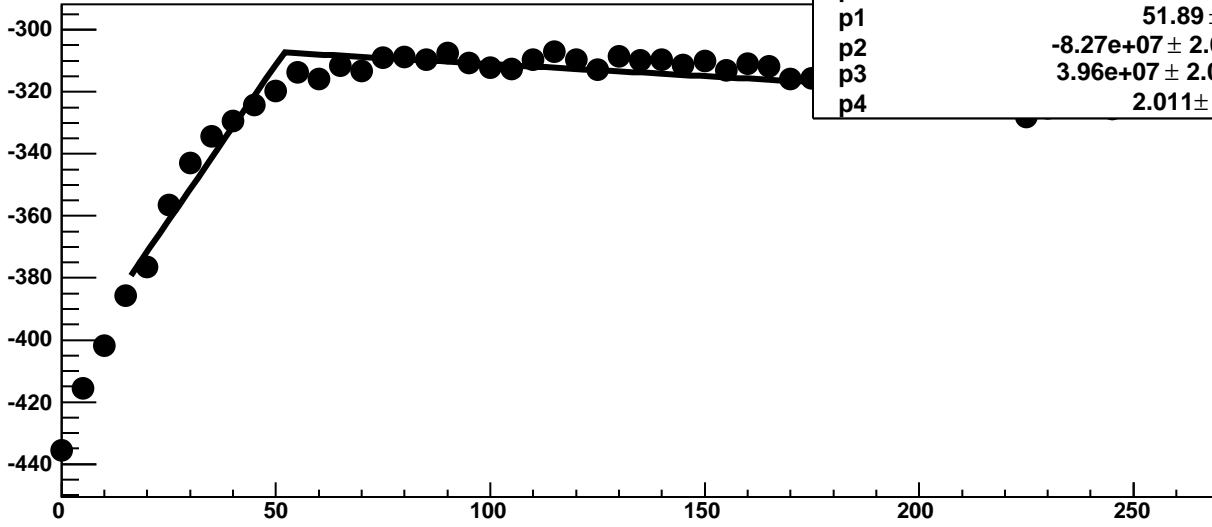
Chip 11, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold

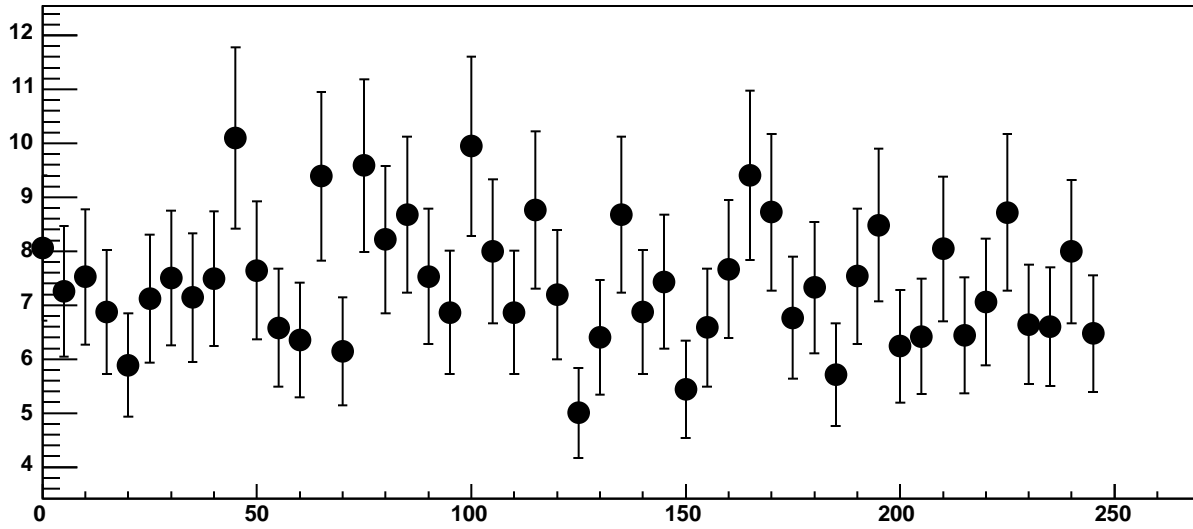


Chip 11, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

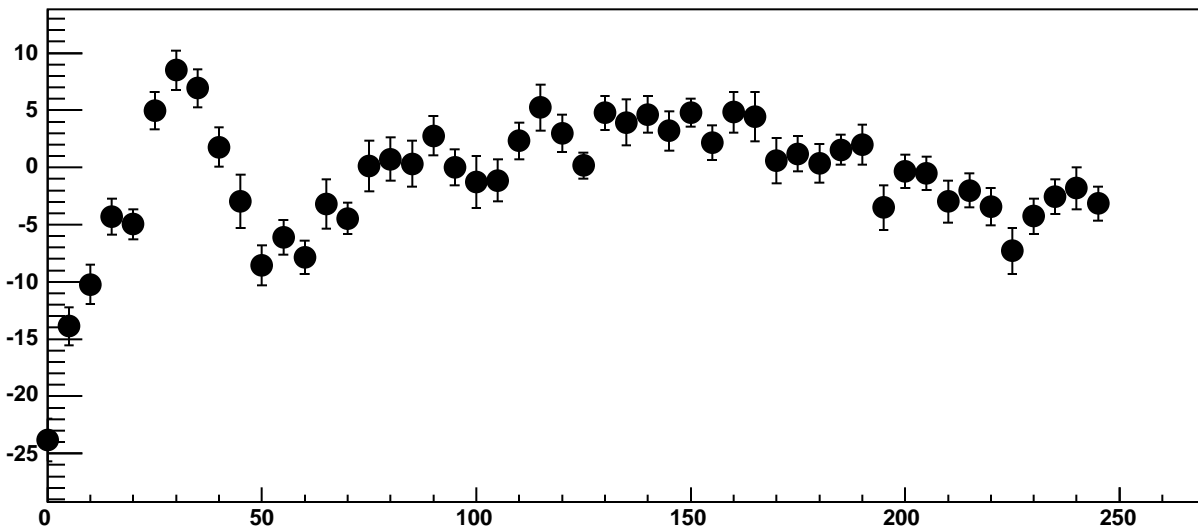


χ^2 / ndf	268.9 / 41
p0	-307.3 ± 0.5269
p1	51.89 ± 0.6557
p2	$-8.27\text{e}+07 \pm 2.084\text{e}+06$
p3	$3.96\text{e}+07 \pm 2.042\text{e}+05$
p4	2.011 ± 0.05165

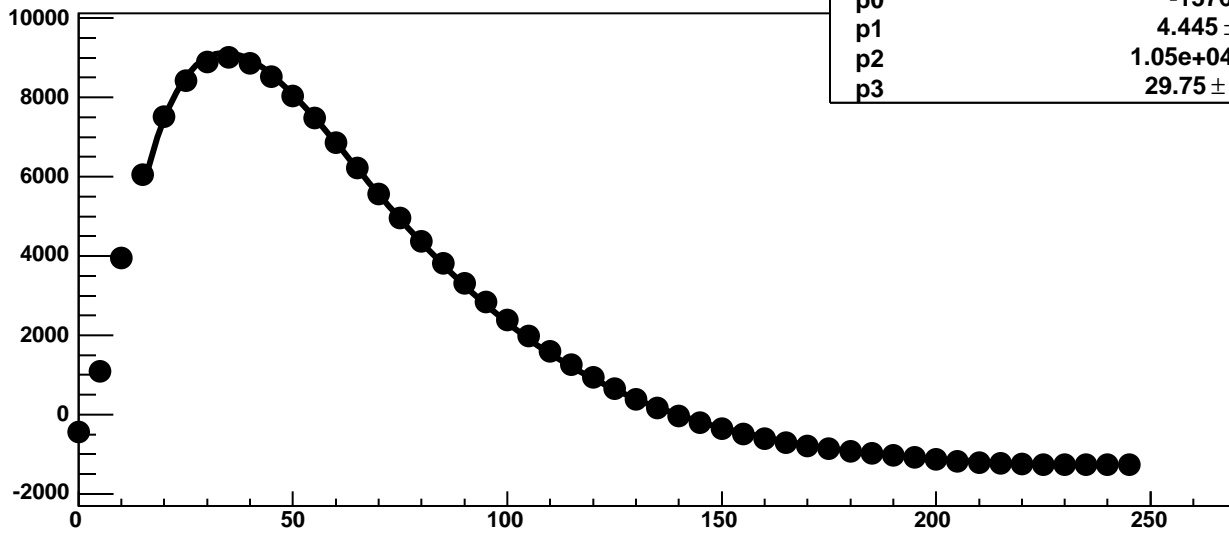
Chip 11, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



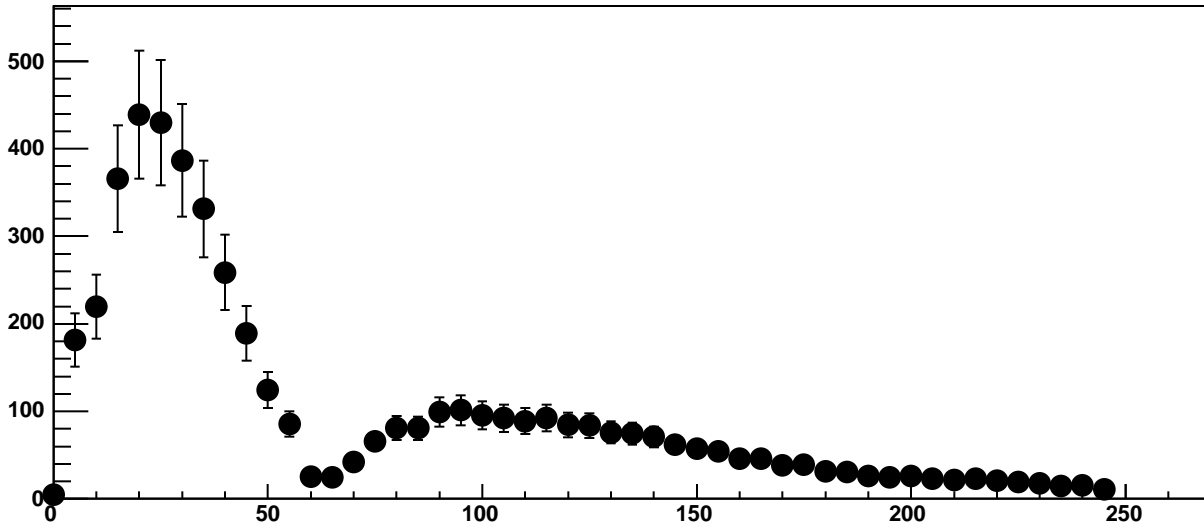
Chip 11, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold



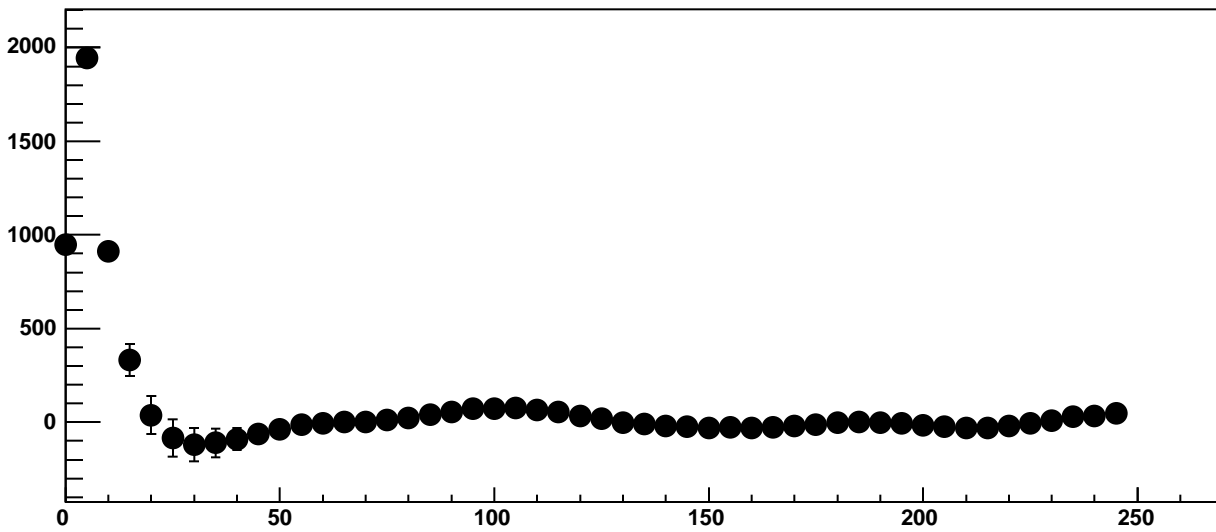
Chip 11, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold



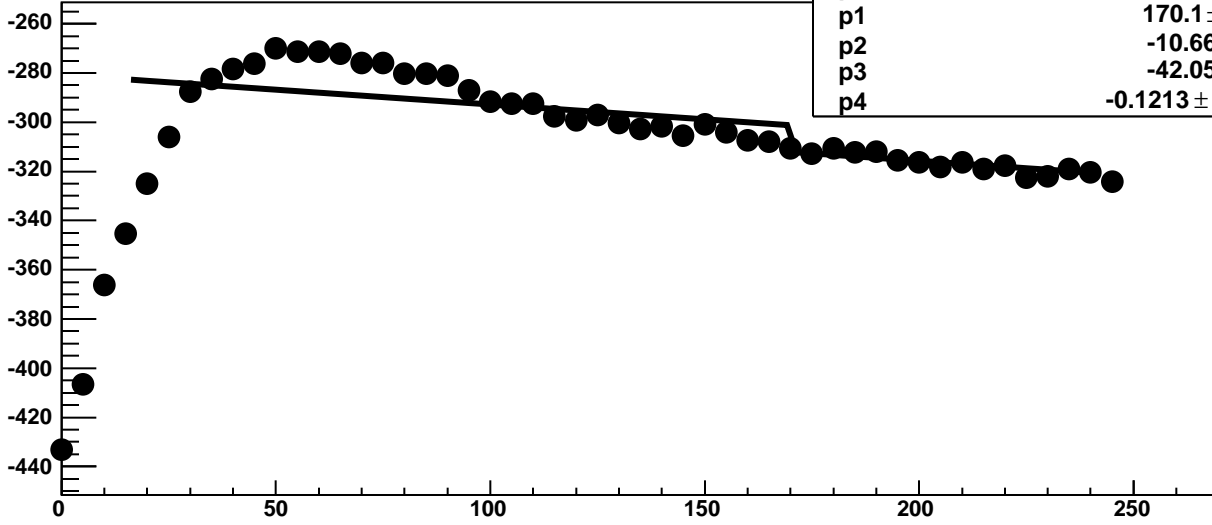
Chip 11, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold

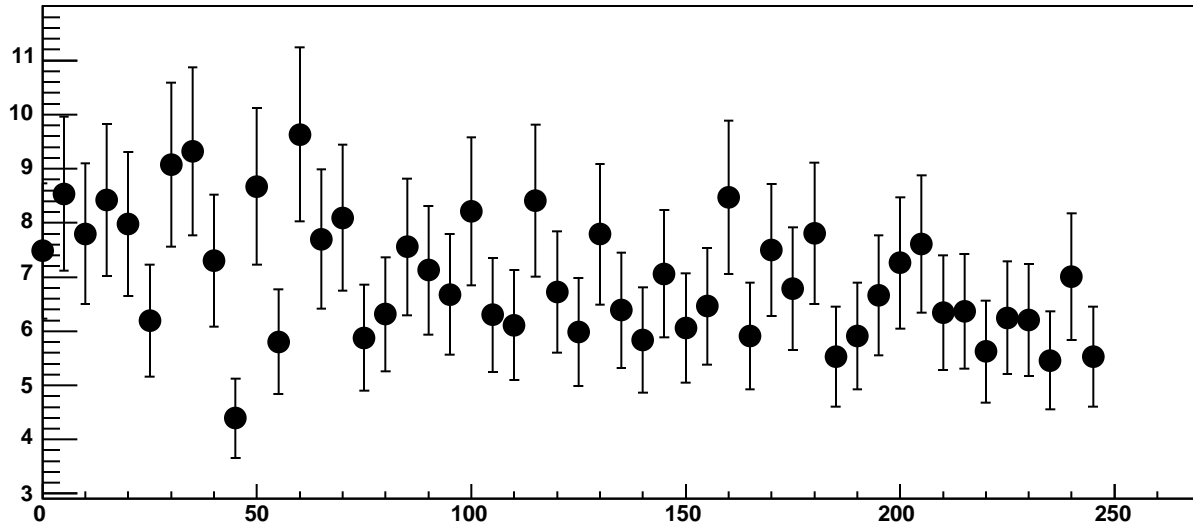


Chip 11, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold

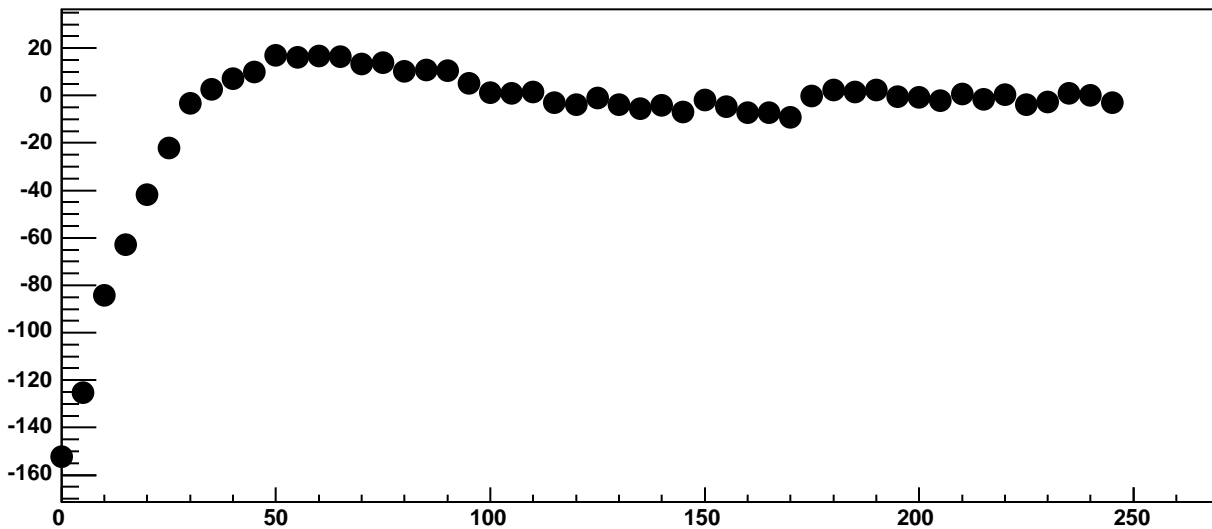


χ^2 / ndf	2767 / 41
p0	-301.3 ± 1.82
p1	170.1 ± 0.1305
p2	-10.66 ± 3.225
p3	-42.05 ± 59.42
p4	-0.1213 ± 0.02123

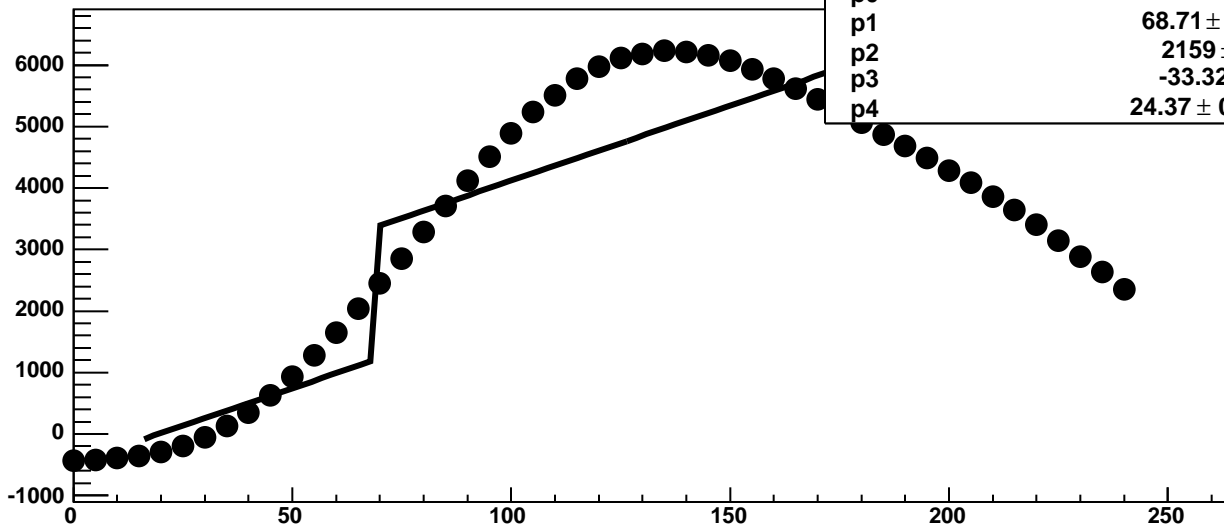
Chip 11, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

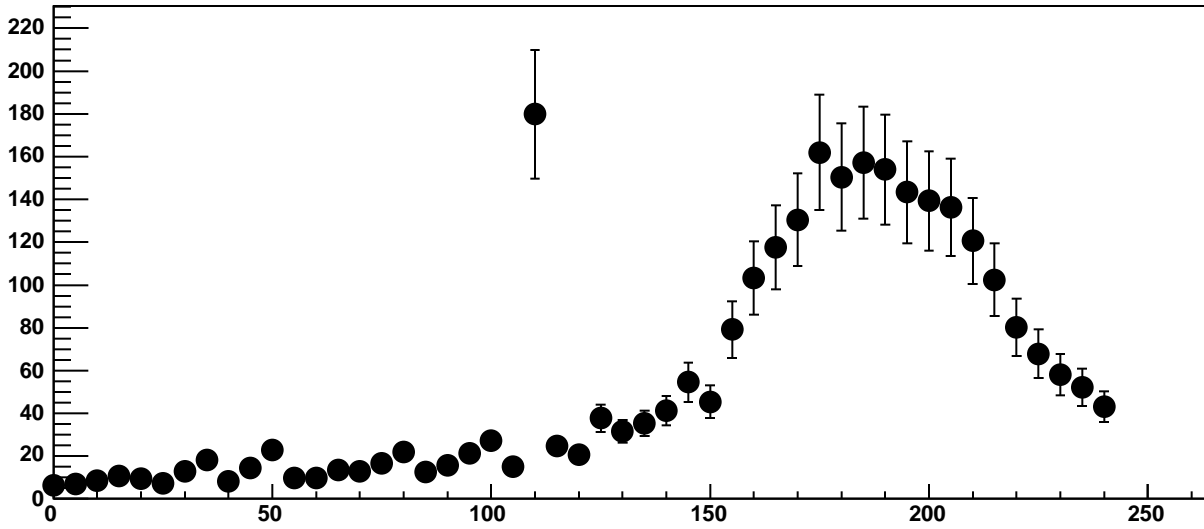


Chip 11, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

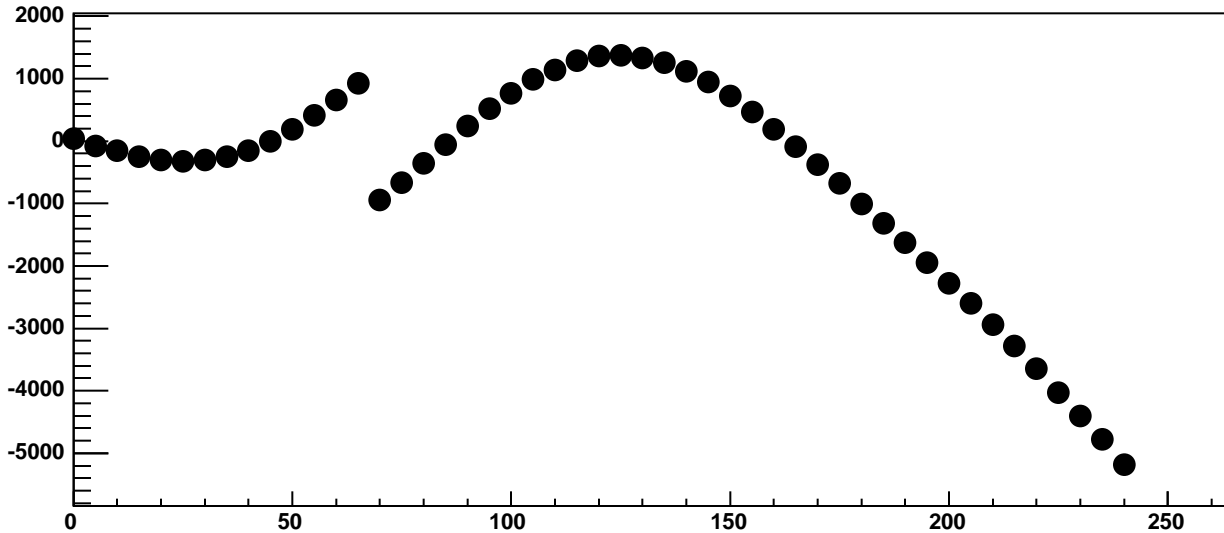


χ^2 / ndf	1.491e+06 / 41
p0	1200 ± 0.1203
p1	68.71 ± 0.09713
p2	2159 ± 0.1152
p3	-33.32 ± 5.463
p4	24.37 ± 0.003611

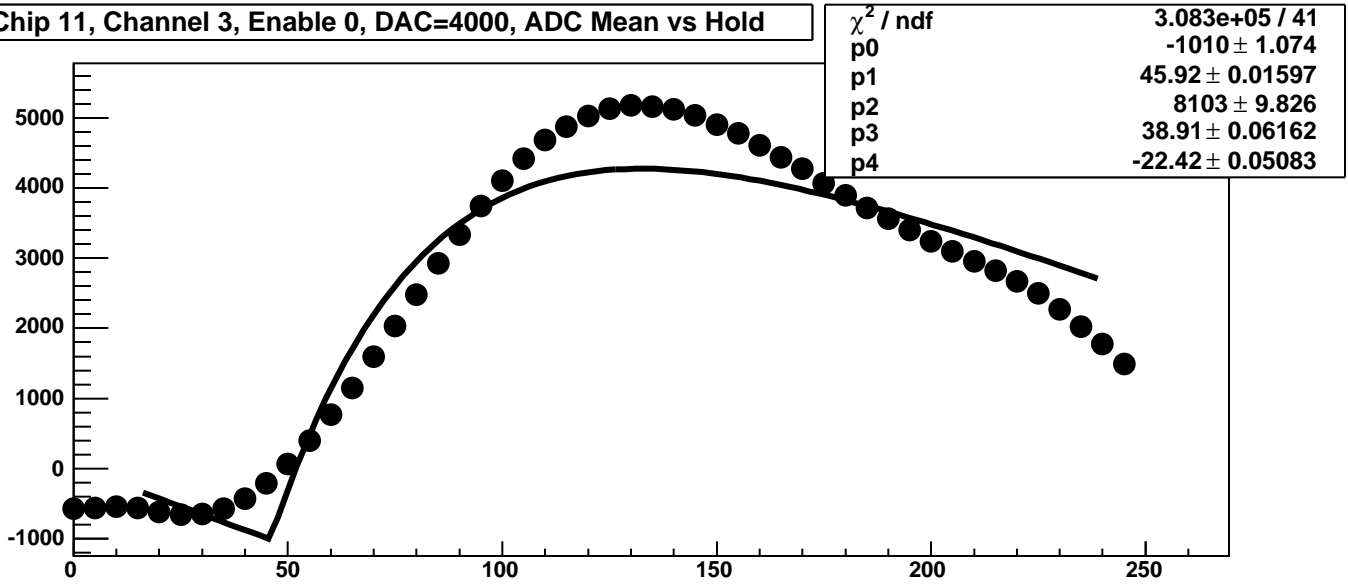
Chip 11, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold



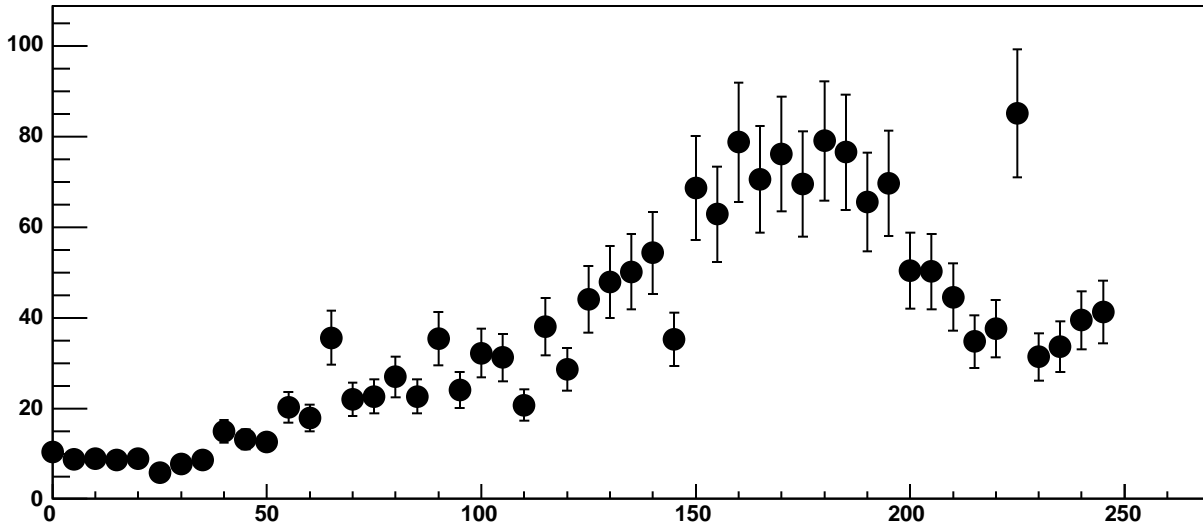
Chip 11, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold



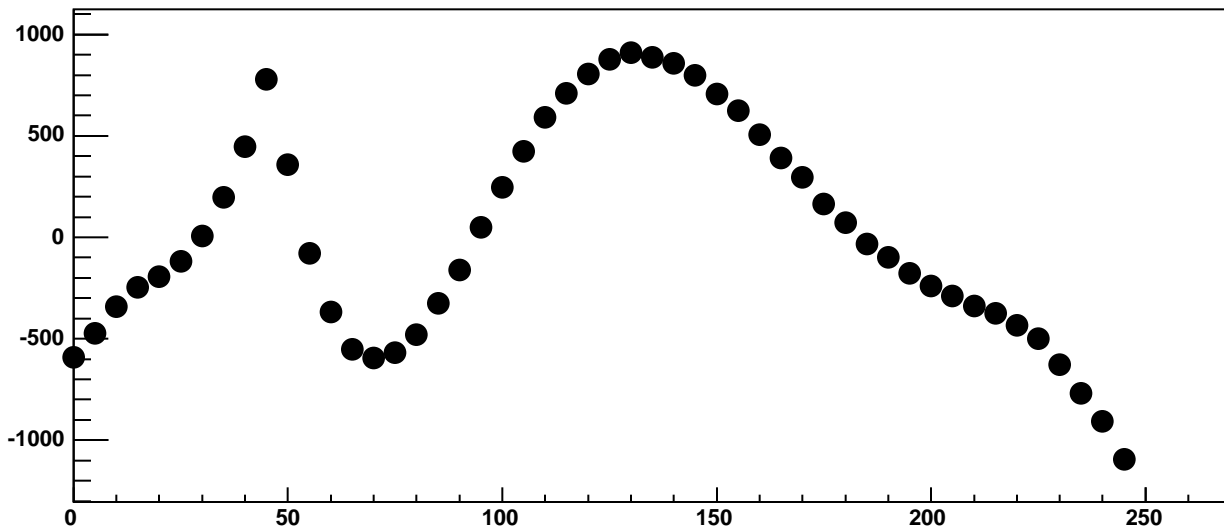
Chip 11, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold



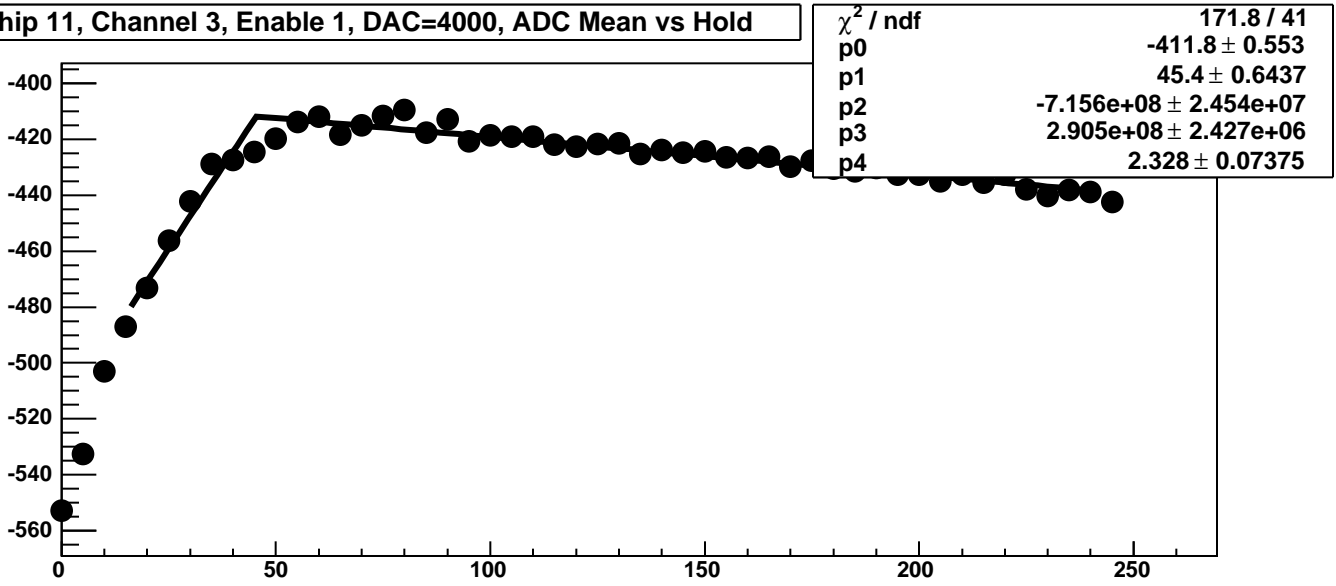
Chip 11, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



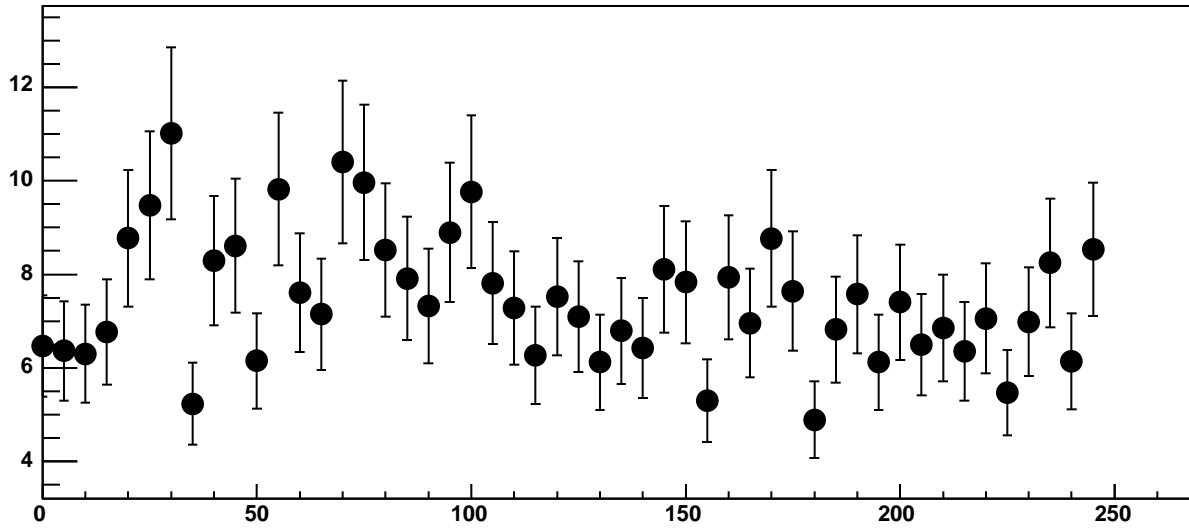
Chip 11, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold



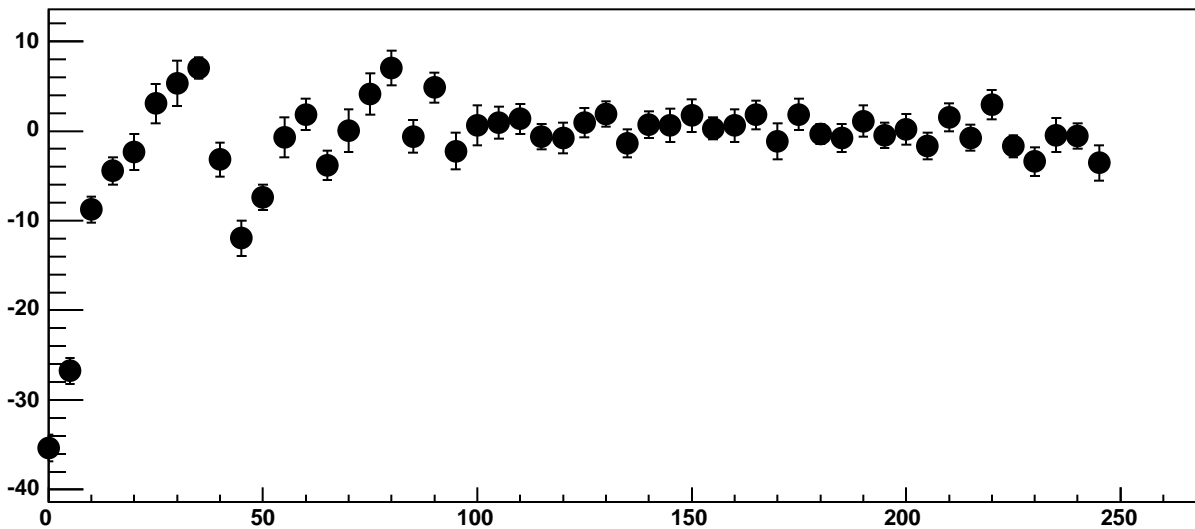
Chip 11, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold



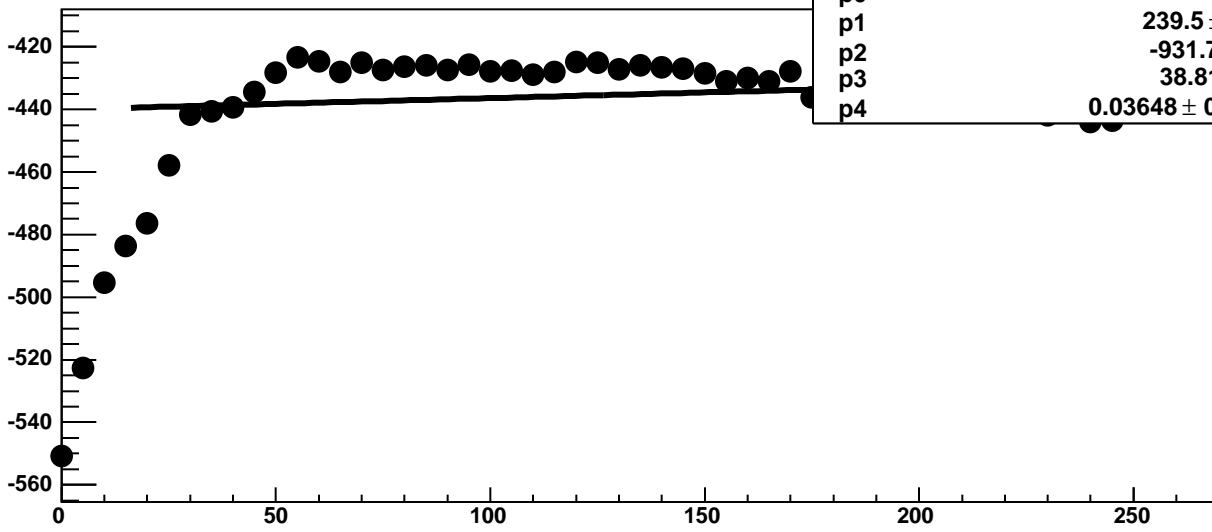
Chip 11, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold

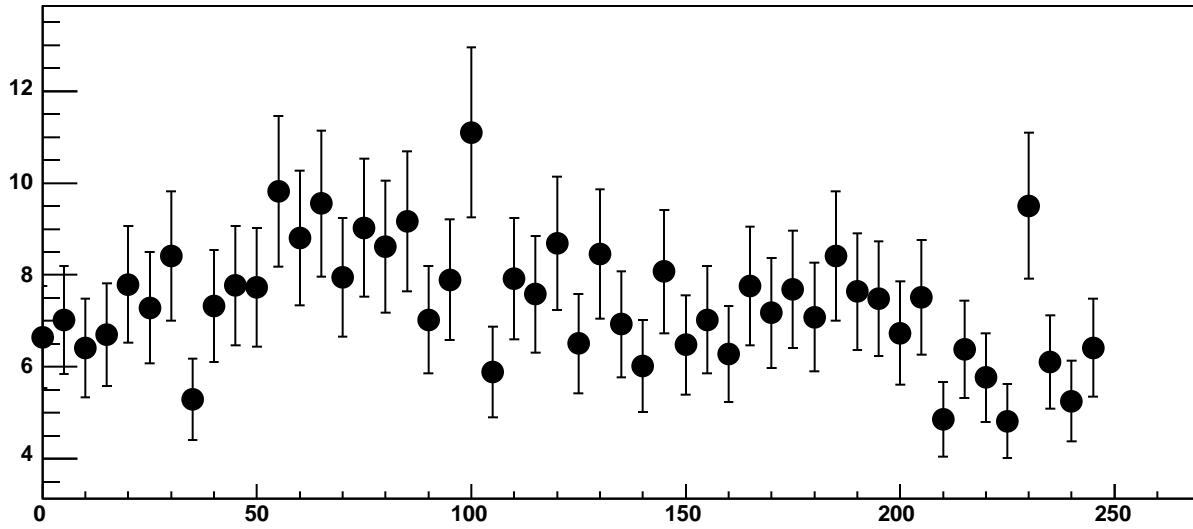


Chip 11, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

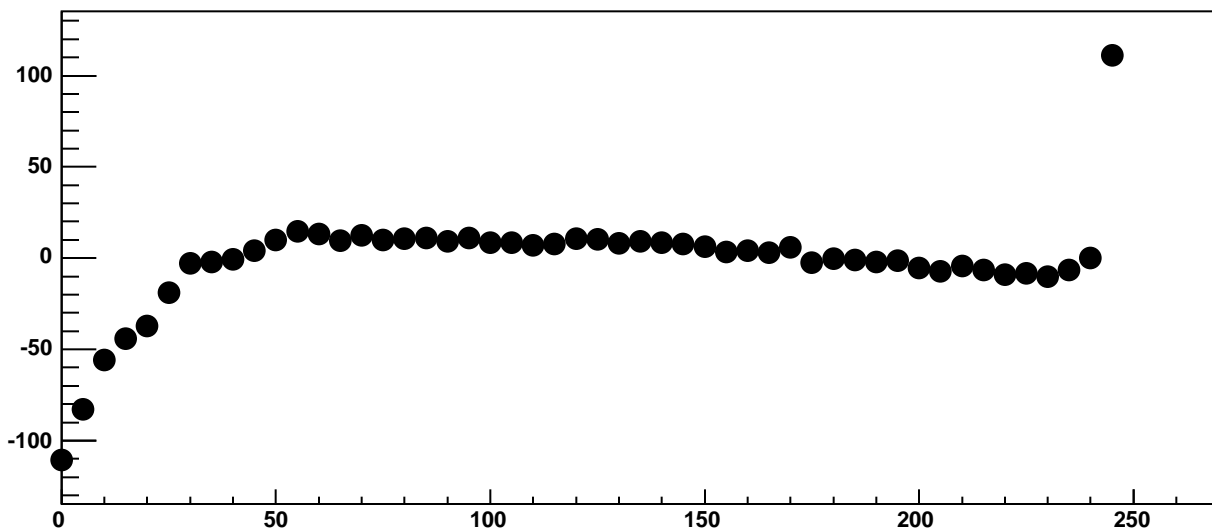


χ^2 / ndf	2281 / 41
p0	-431.3 ± 0.4543
p1	239.5 ± 0.5949
p2	-931.7 ± 1031
p3	38.81 ± 35.41
p4	0.03648 ± 0.003622

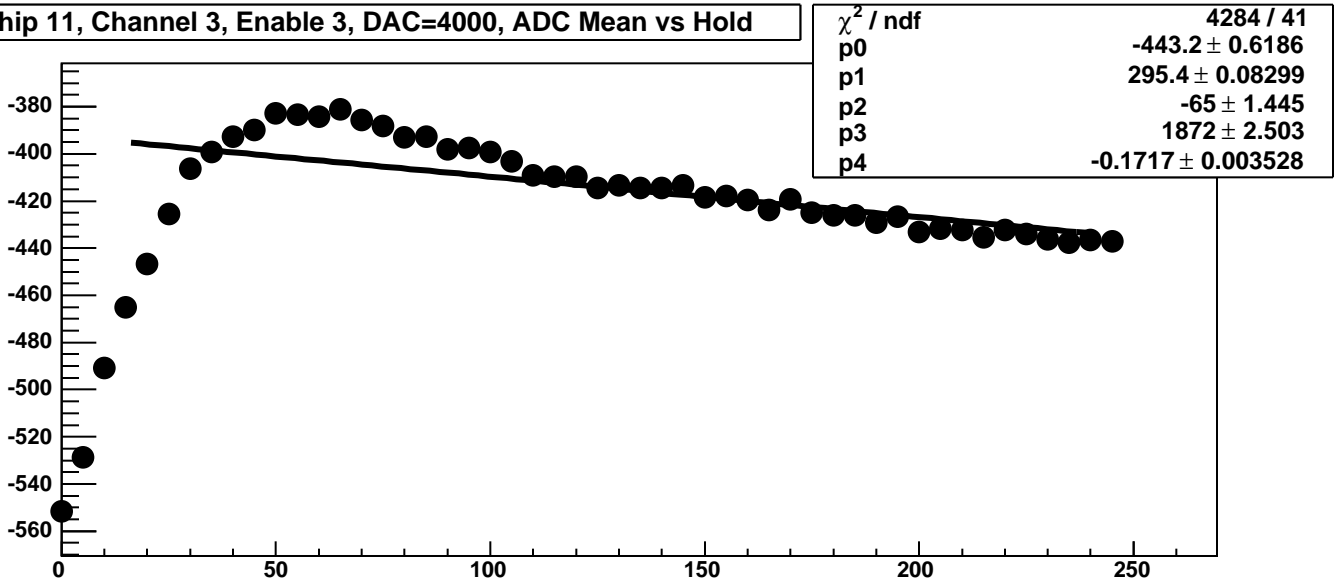
Chip 11, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



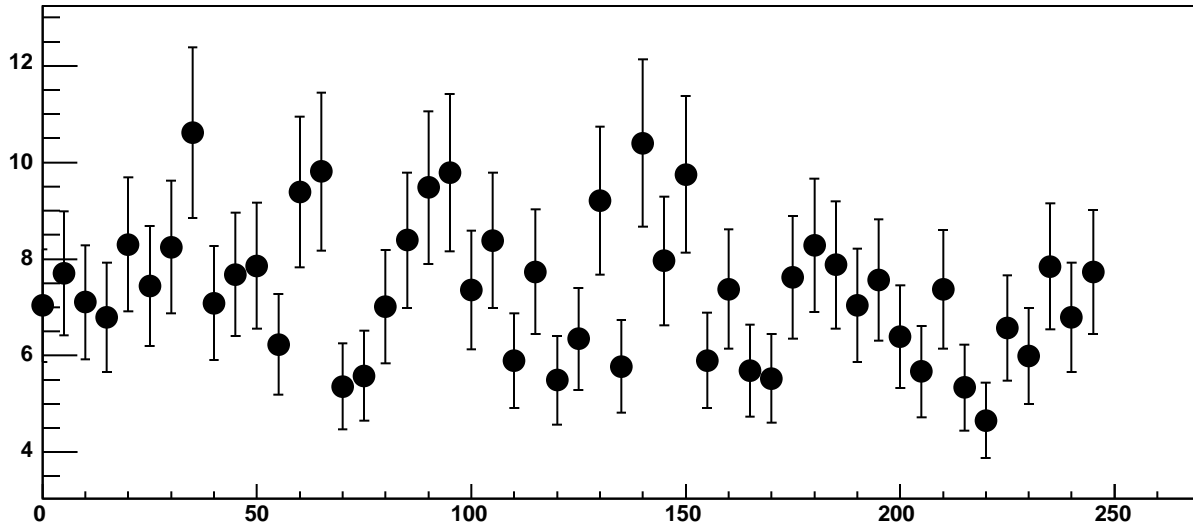
Chip 11, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold



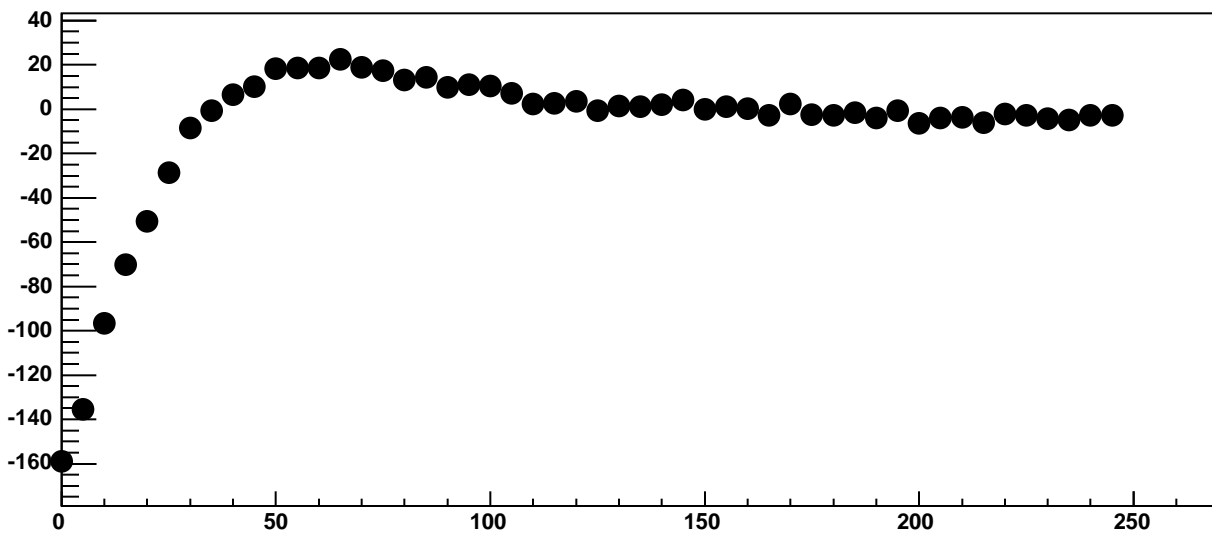
Chip 11, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold



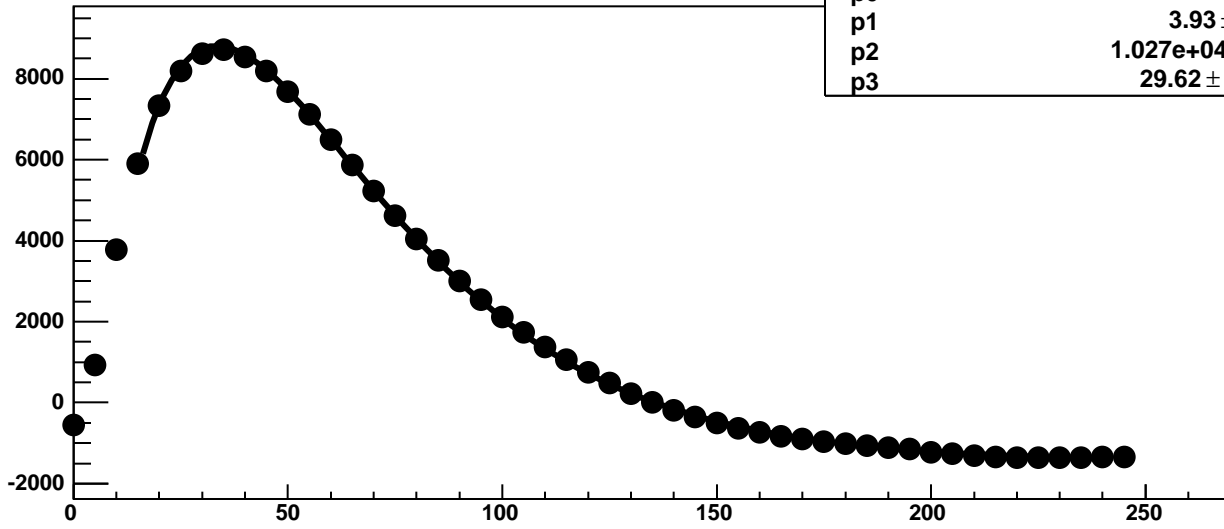
Chip 11, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

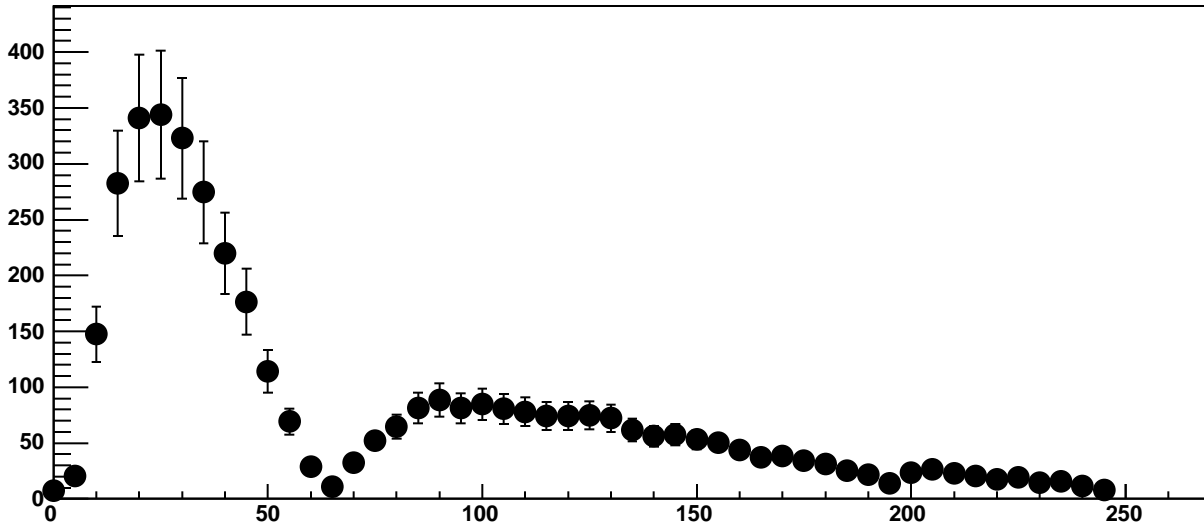


Chip 11, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold

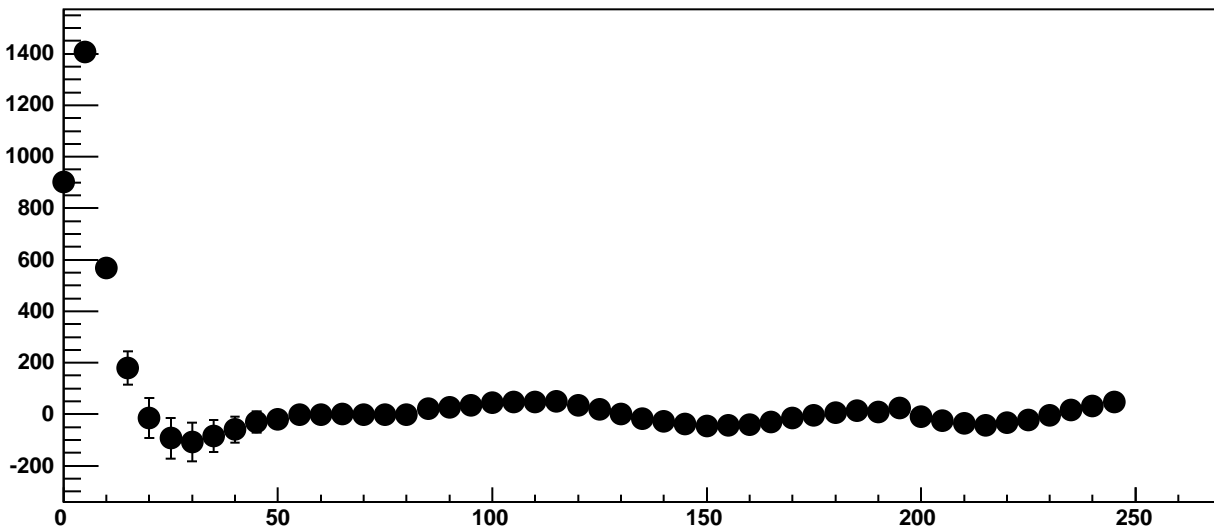


χ^2 / ndf	613 / 42
p0	-1456 ± 1.696
p1	3.93 ± 0.1109
p2	1.027e+04 ± 14.07
p3	29.62 ± 0.04018

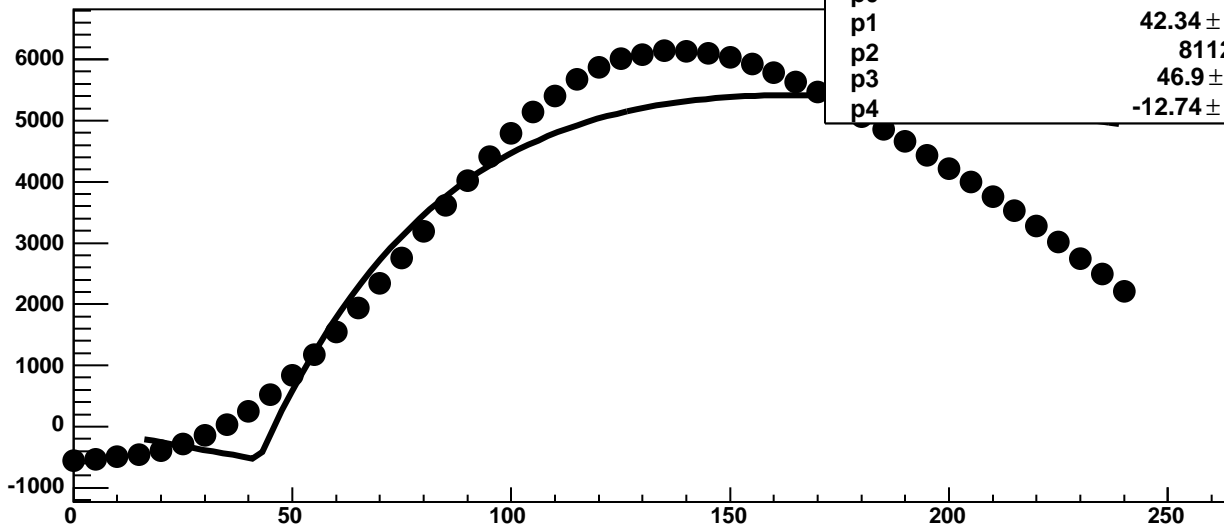
Chip 11, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold

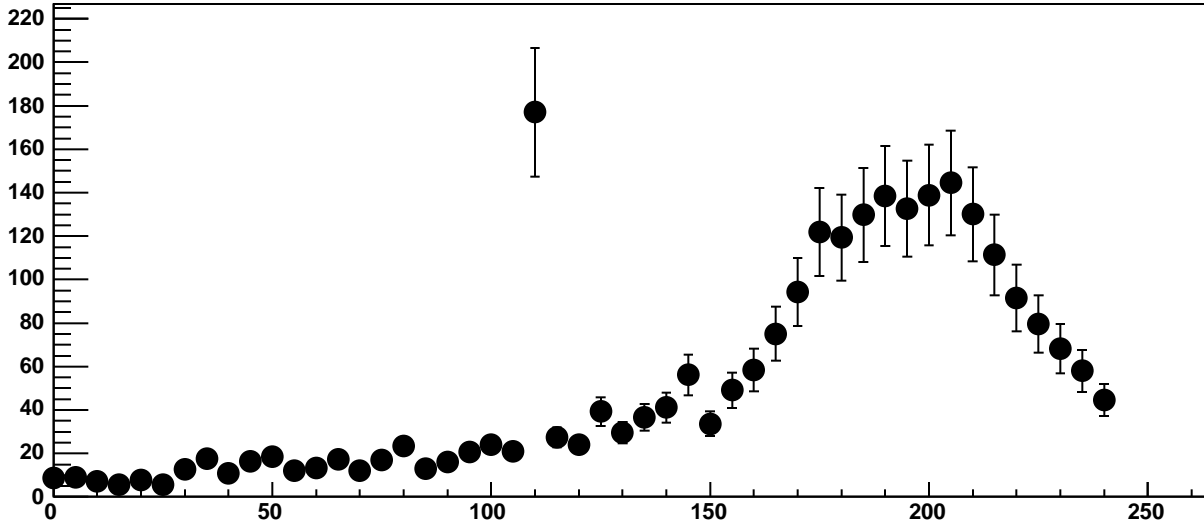


Chip 11, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold

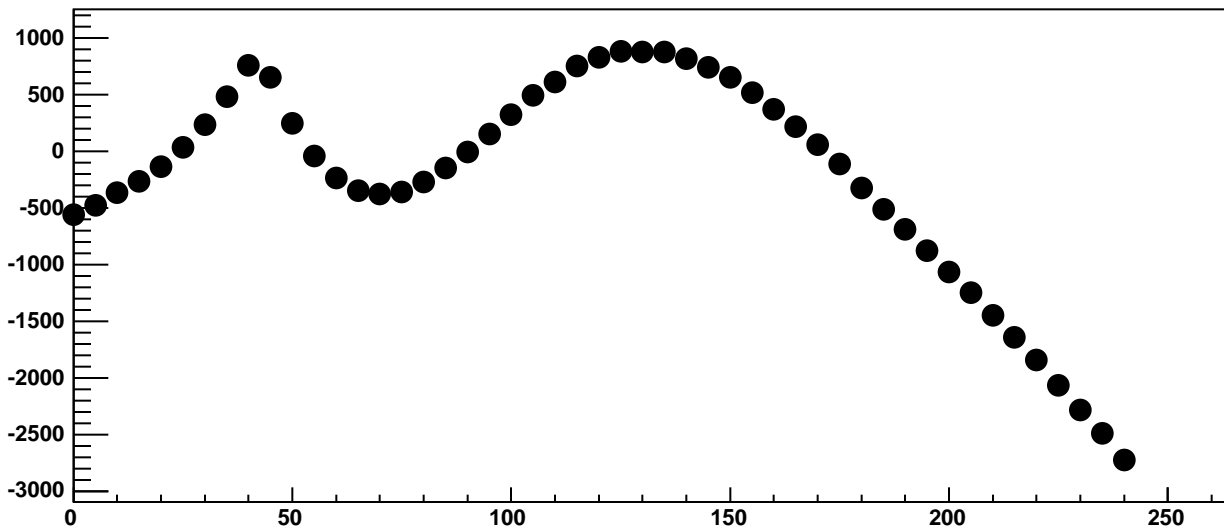


χ^2 / ndf	5.09e+05 / 41
p0	-539.3 ± 1.35
p1	42.34 ± 0.01766
p2	8112 ± 11.65
p3	46.9 ± 0.06941
p4	-12.74 ± 0.06342

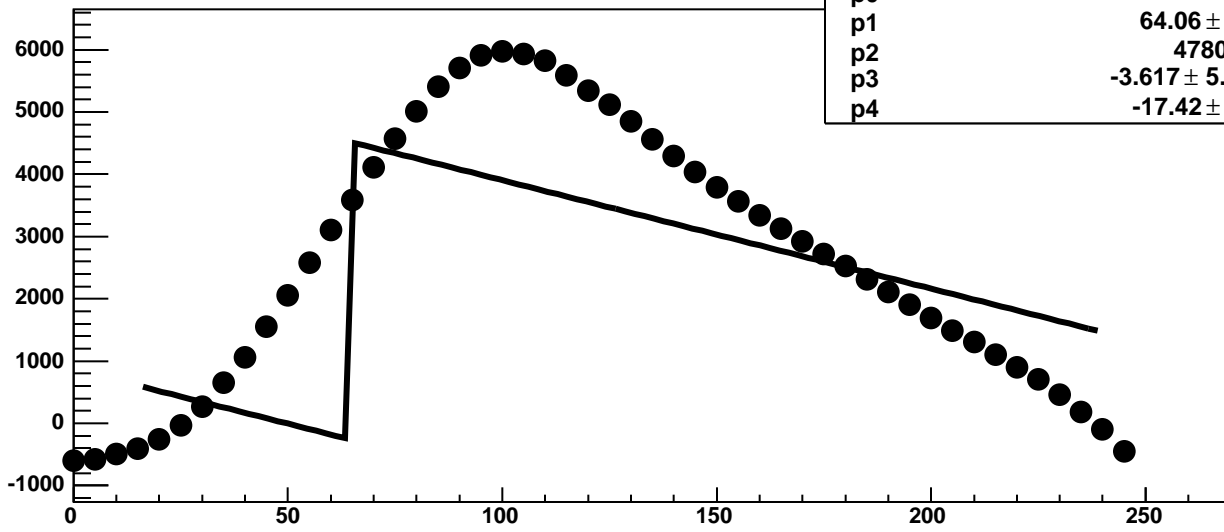
Chip 11, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold

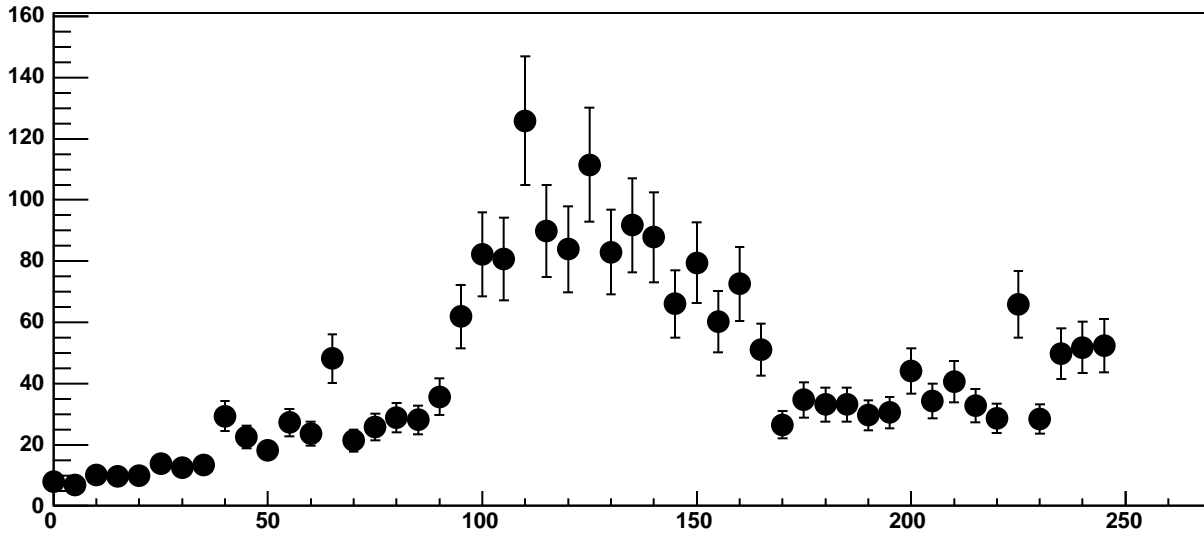


Chip 11, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

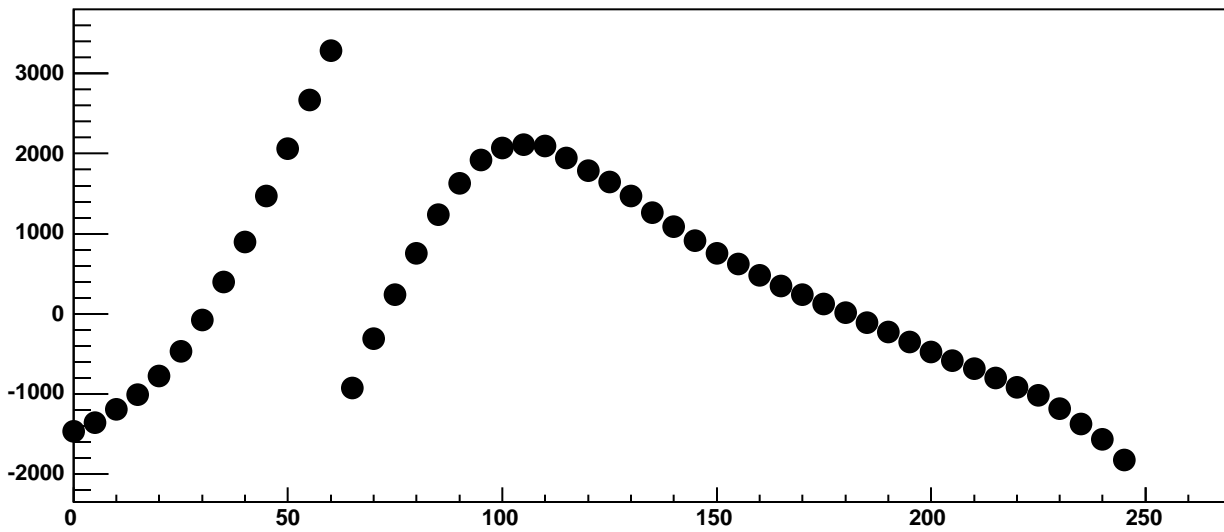


χ^2 / ndf	1.577e+06 / 41
p0	-249.8 ± 1.573
p1	64.06 ± 0.07377
p2	4780 ± 3.575
p3	-3.617 ± 5.862e-06
p4	-17.42 ± 0.02032

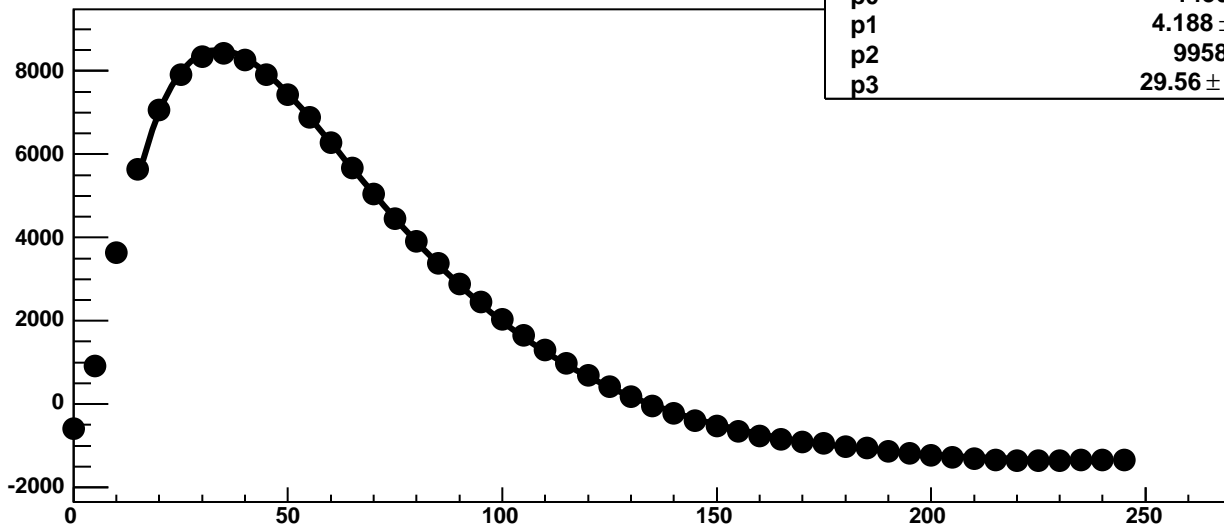
Chip 11, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

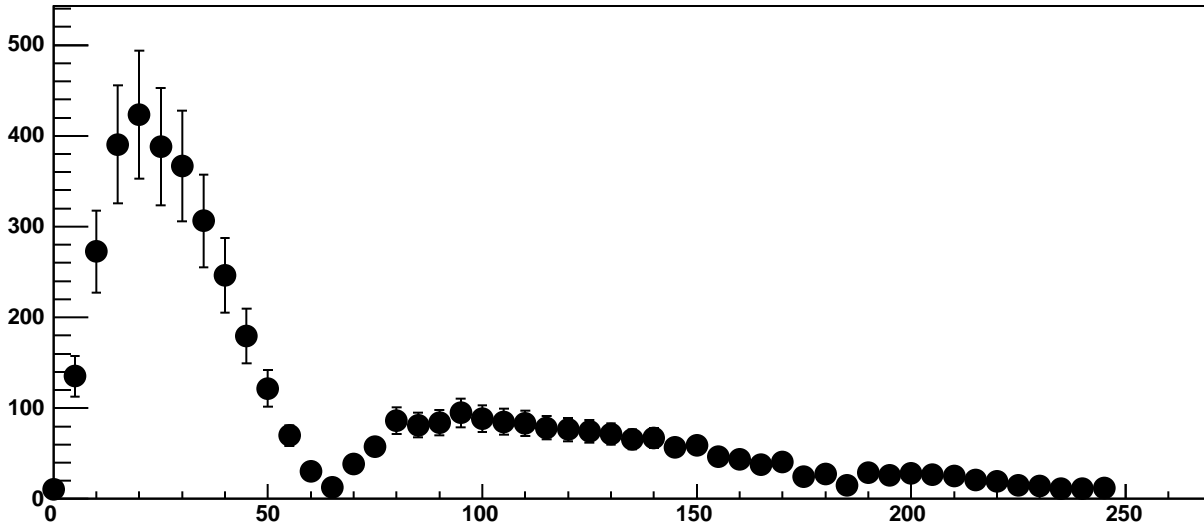


Chip 11, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold

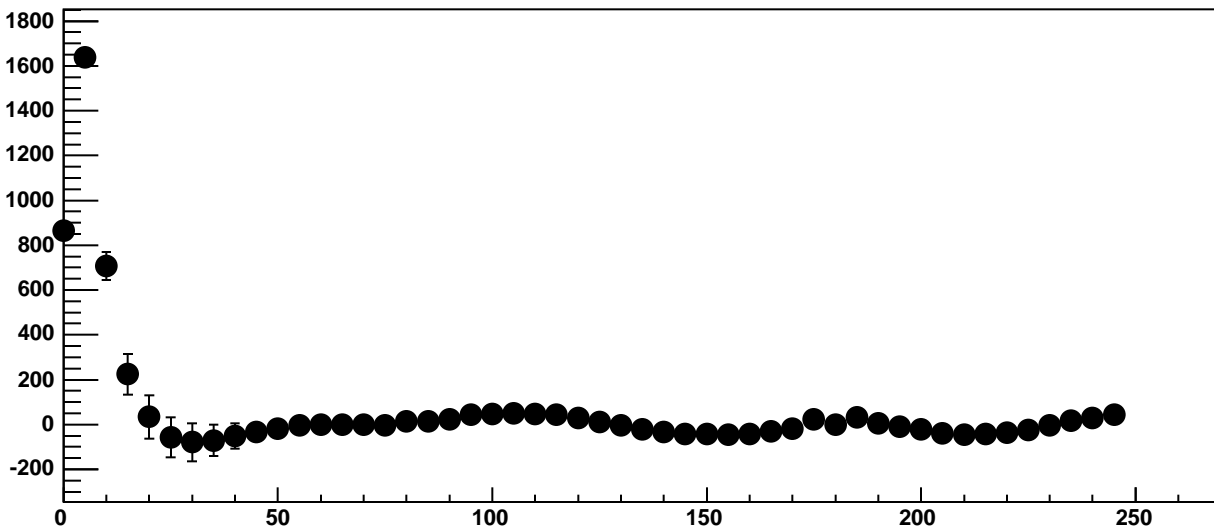


χ^2 / ndf	711.8 / 42
p0	-1450 ± 1.649
p1	4.188 ± 0.1394
p2	9958 ± 17.04
p3	29.56 ± 0.04334

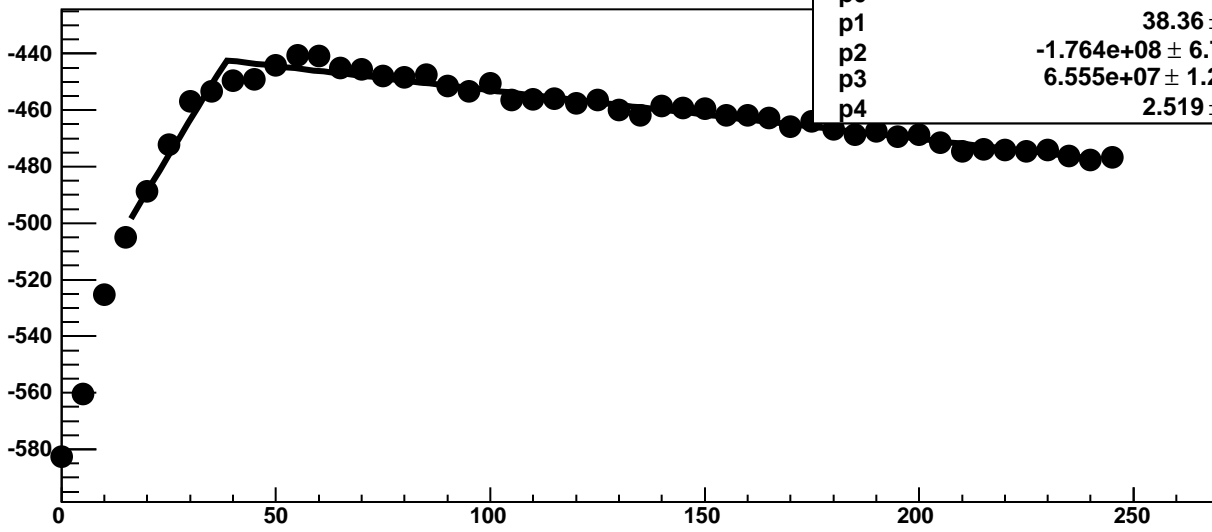
Chip 11, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold

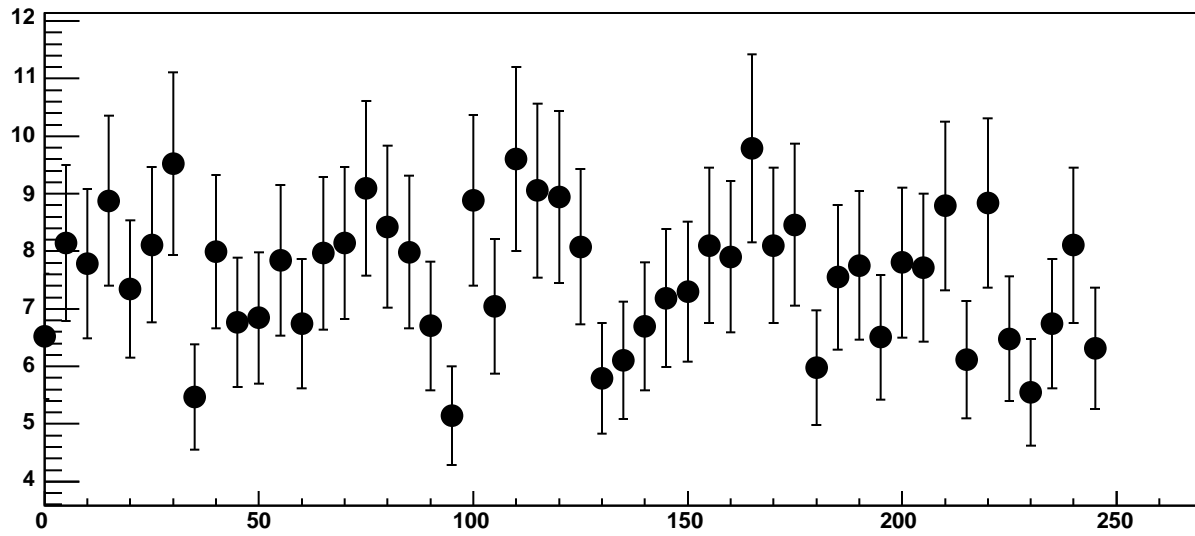


Chip 11, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

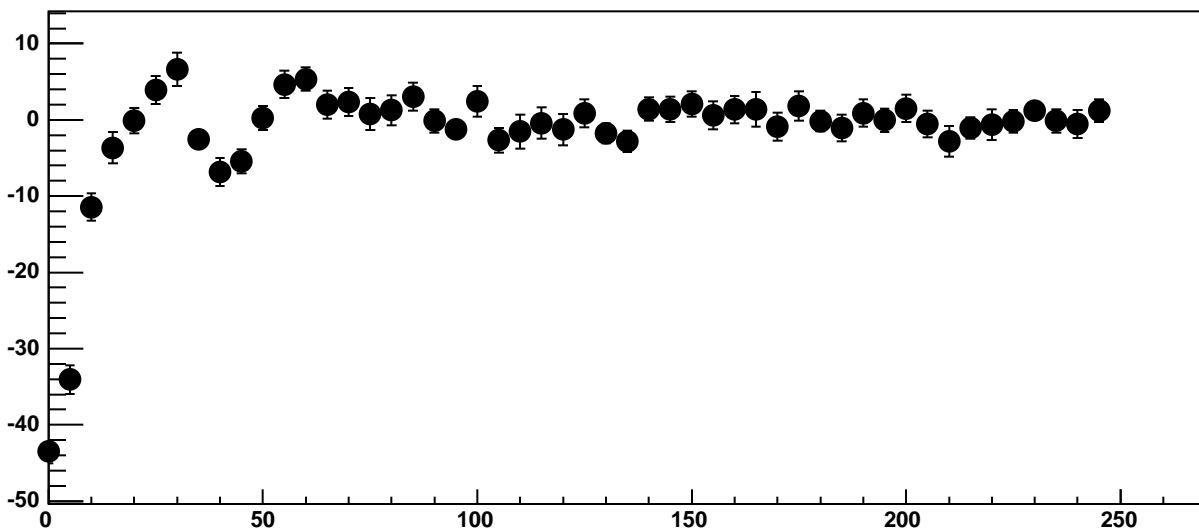


χ^2 / ndf	94.53 / 41
p0	-442.4 ± 0.4971
p1	38.36 ± 0.5602
p2	-1.764e+08 ± 6.763e+06
p3	6.555e+07 ± 1.222e+05
p4	2.519 ± 0.1019

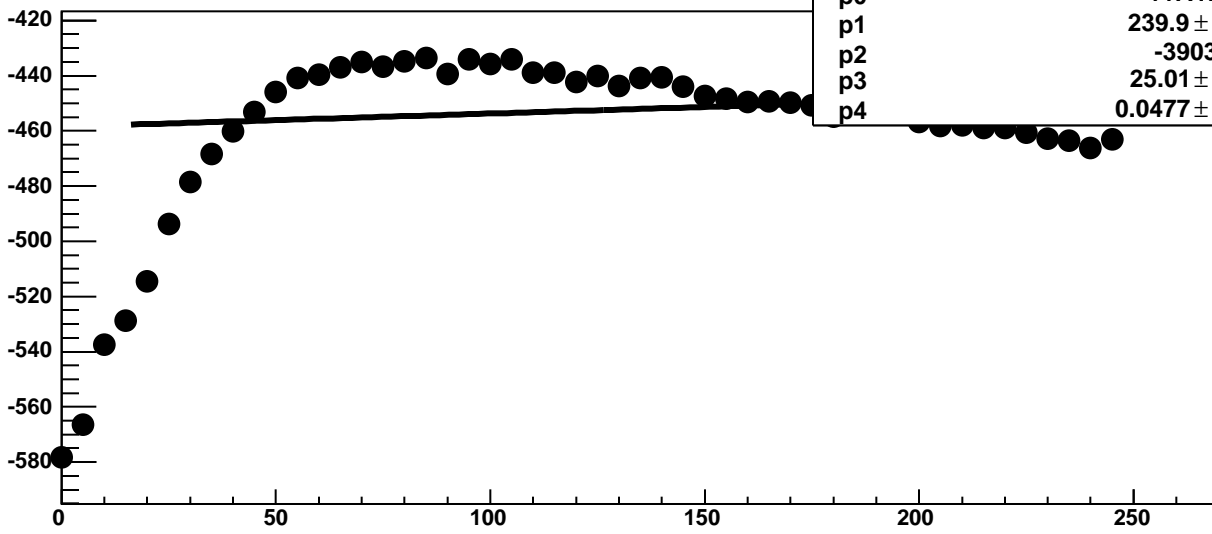
Chip 11, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

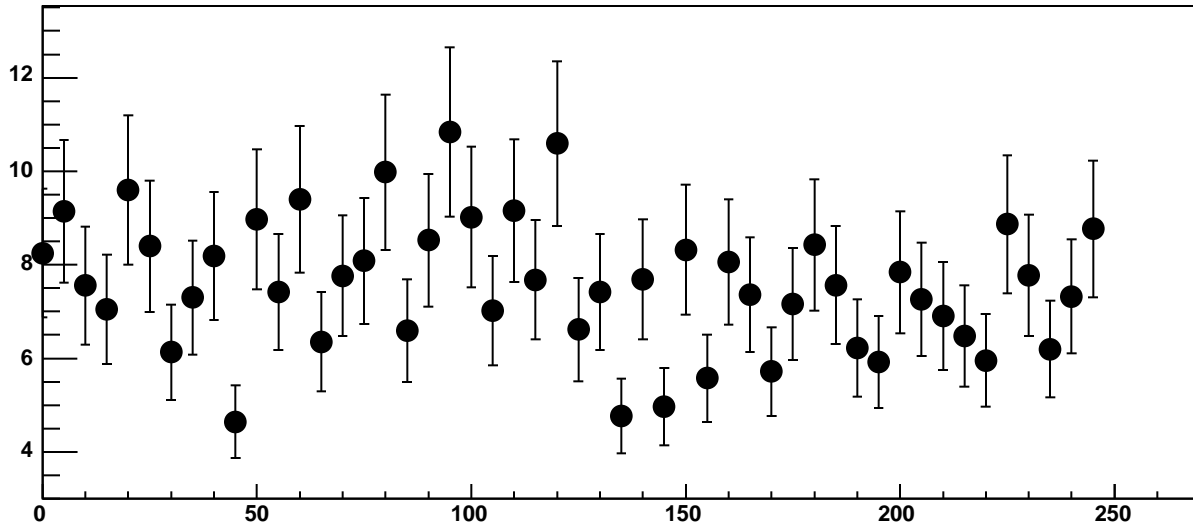


Chip 11, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

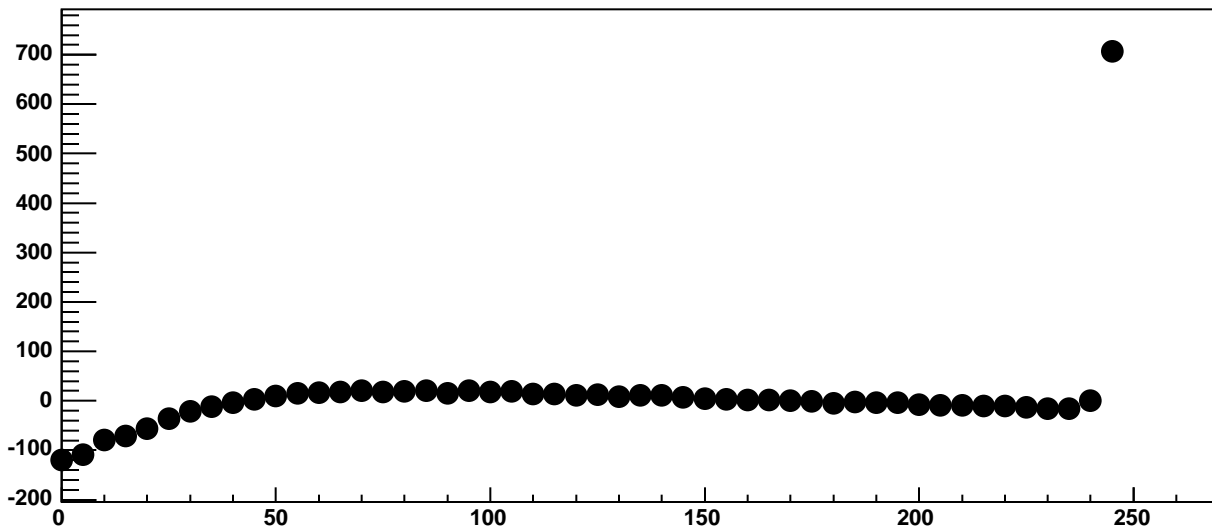


χ^2 / ndf	5284 / 41
p0	-447.1 ± 0.4777
p1	239.9 ± 0.01145
p2	-3903 ± 10.74
p3	25.01 ± 0.09938
p4	0.0477 ± 0.00373

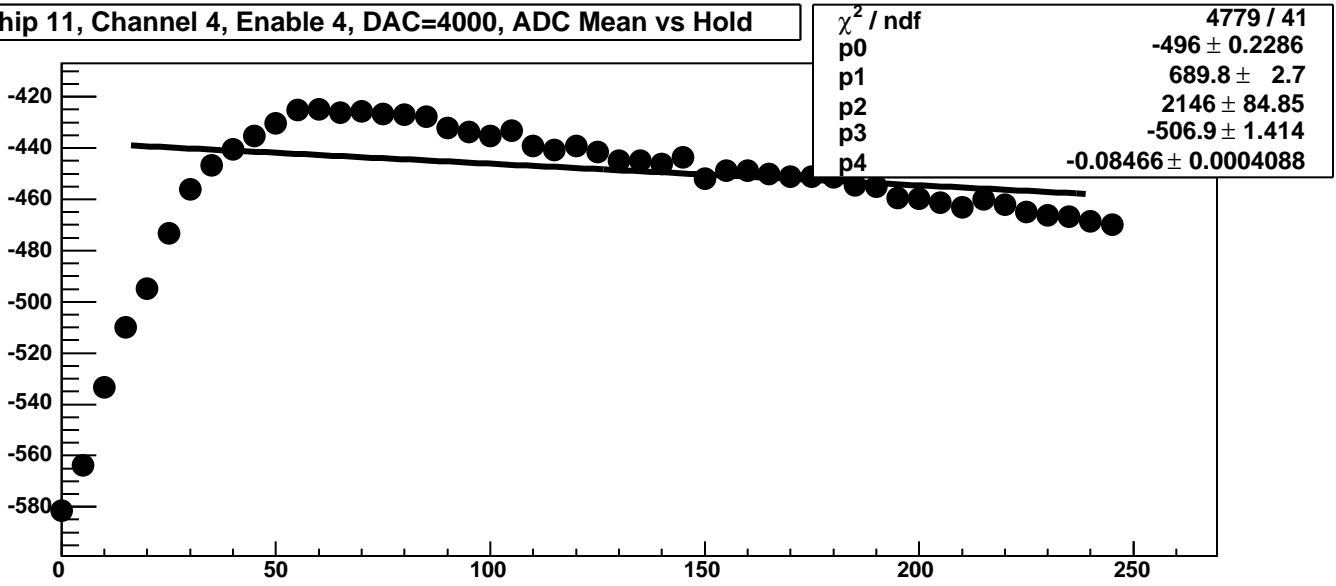
Chip 11, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



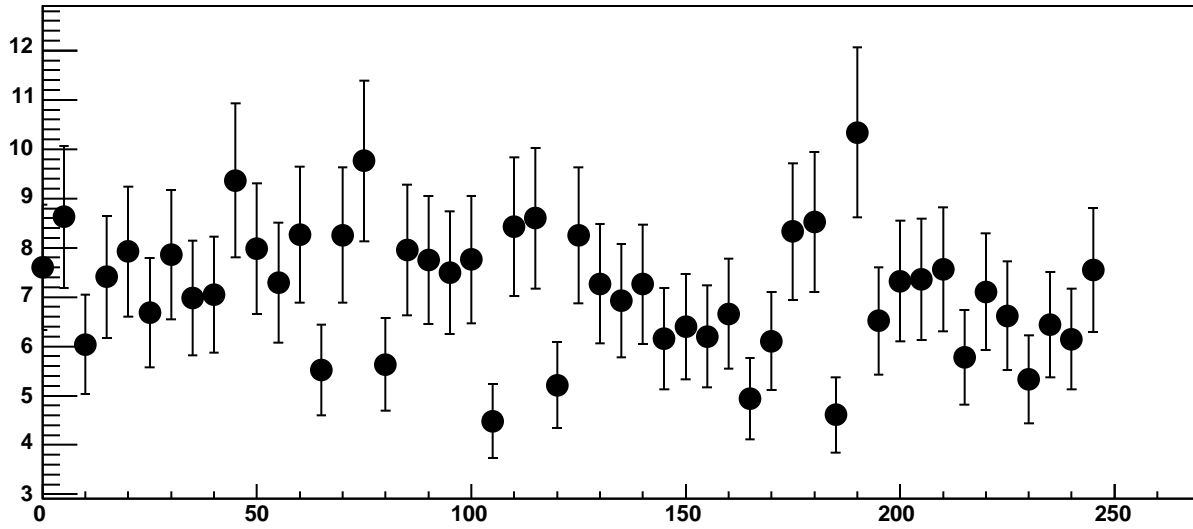
Chip 11, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold



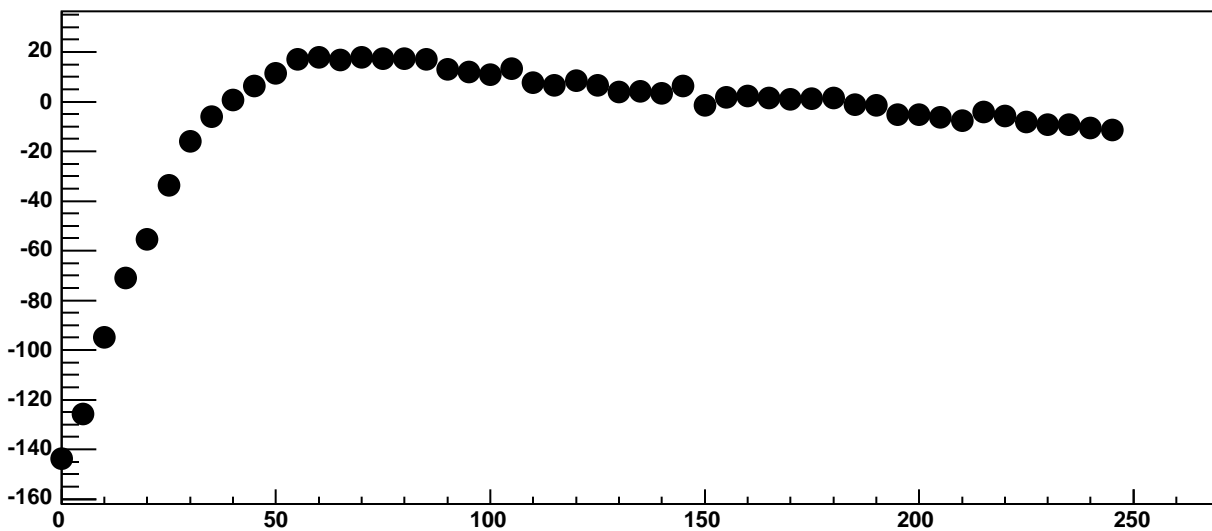
Chip 11, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold



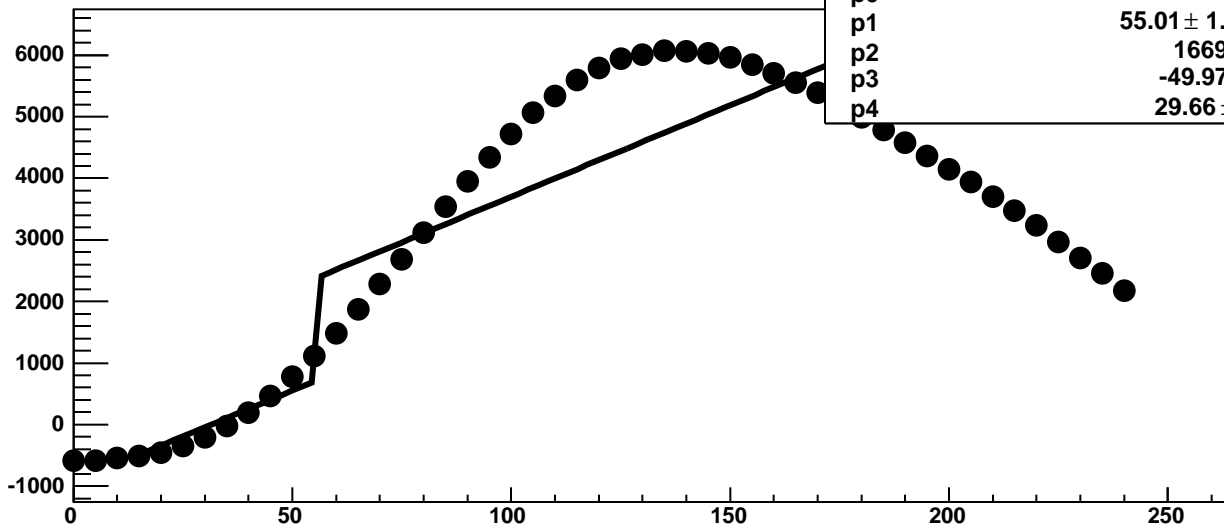
Chip 11, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold

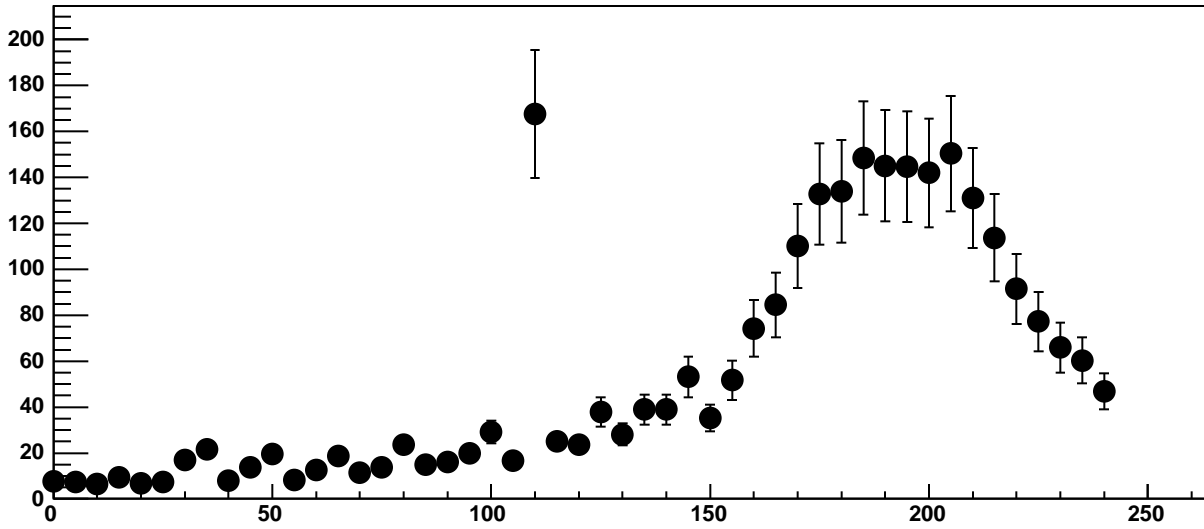


Chip 11, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold

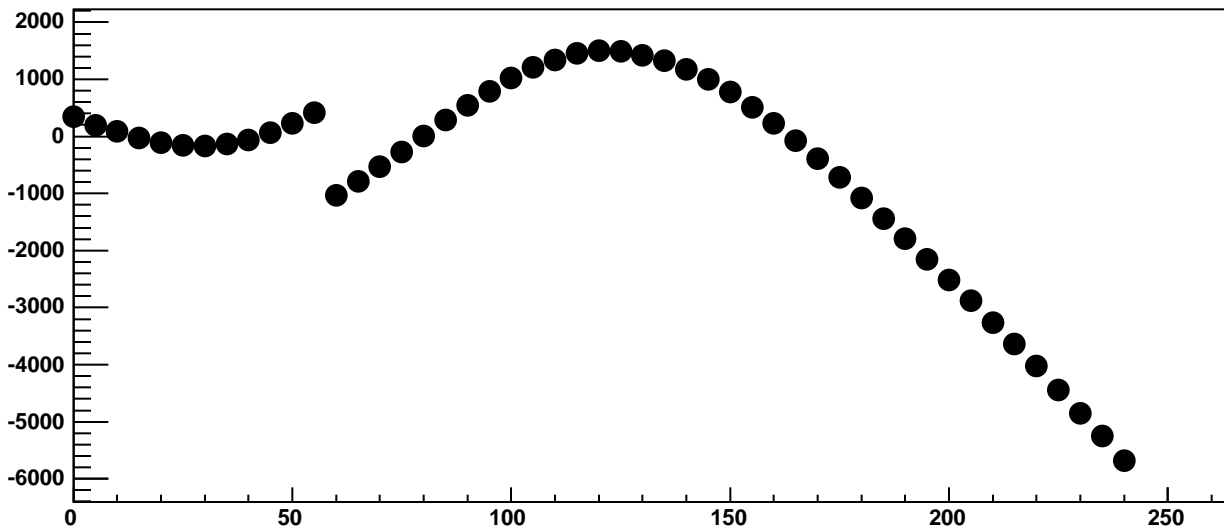


χ^2 / ndf	1.422e+06 / 41
p0	698.4 ± 1.528
p1	55.01 ± 1.463e-05
p2	1669 ± 1.378
p3	-49.97 ± 15.19
p4	29.66 ± 0.1023

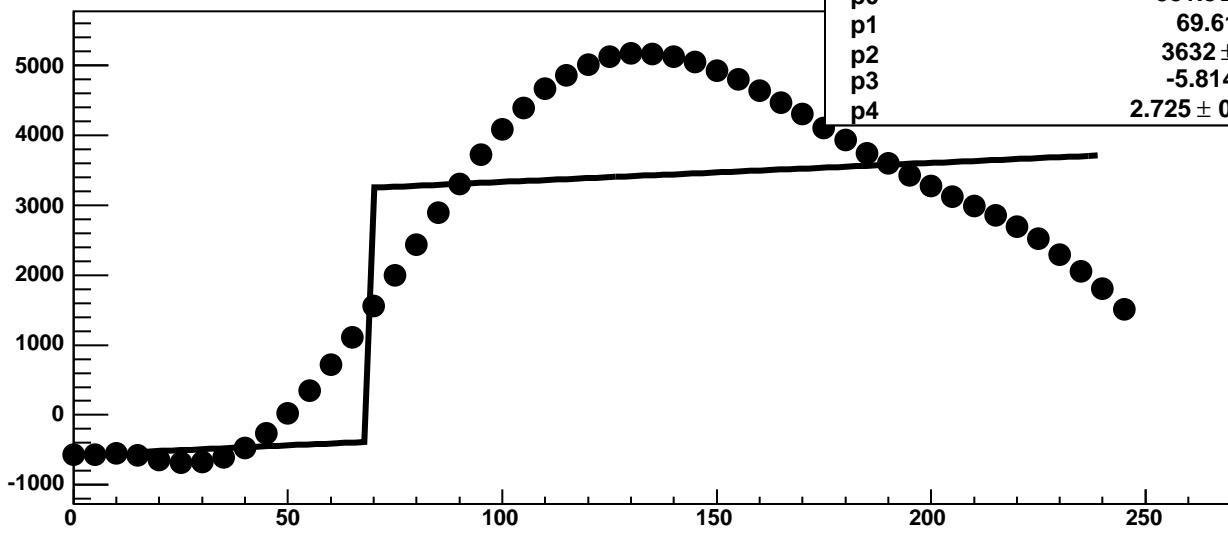
Chip 11, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold

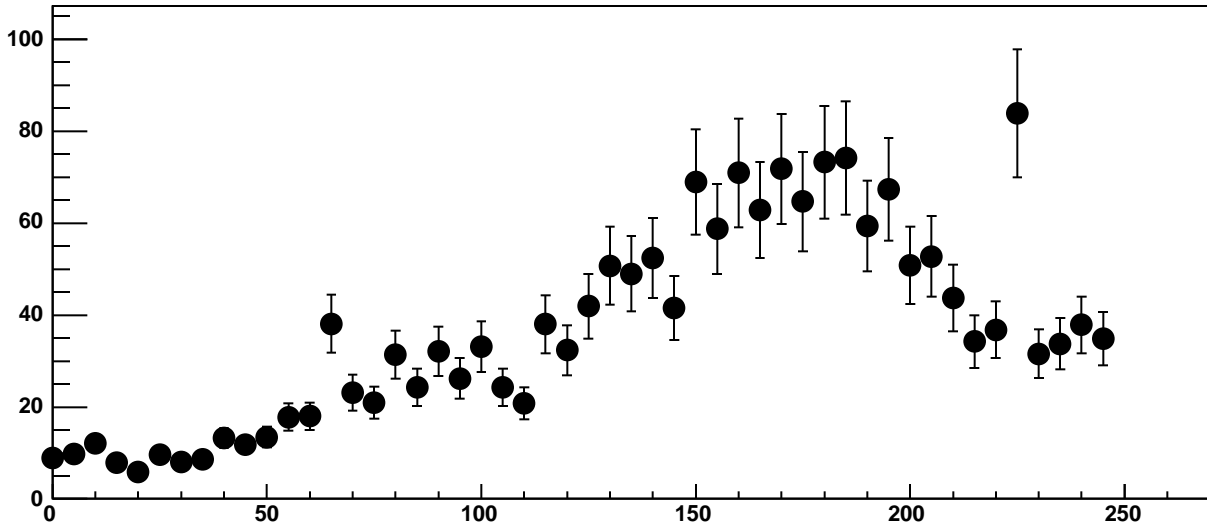


Chip 11, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold

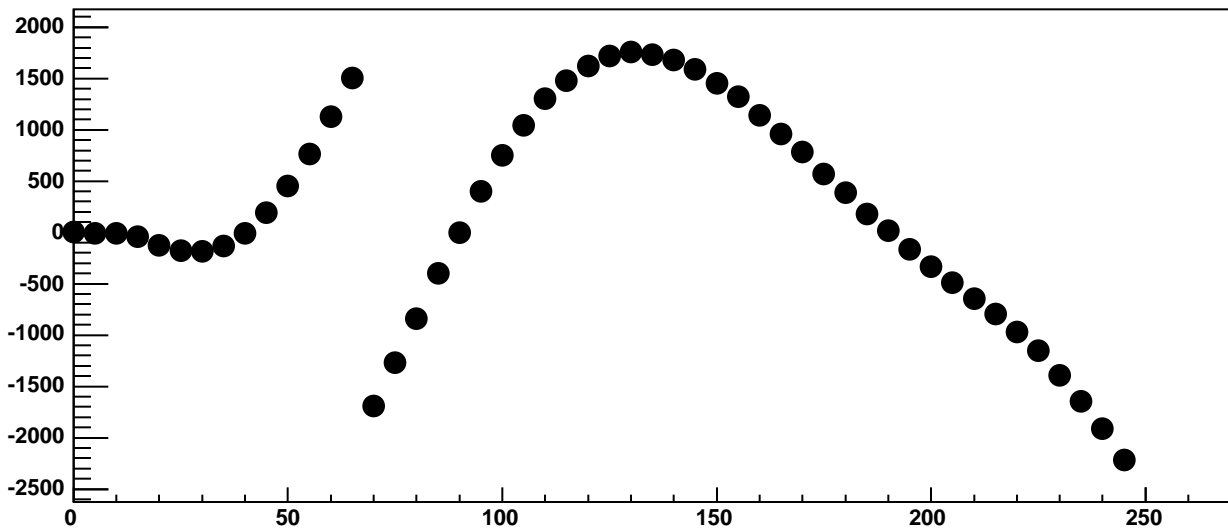


χ^2 / ndf	9.115e+05 / 41
p0	-381.8 ± 0.1368
p1	69.61 ± 1.05
p2	3632 ± 0.3333
p3	-5.814 ± 0.28
p4	2.725 ± 0.002821

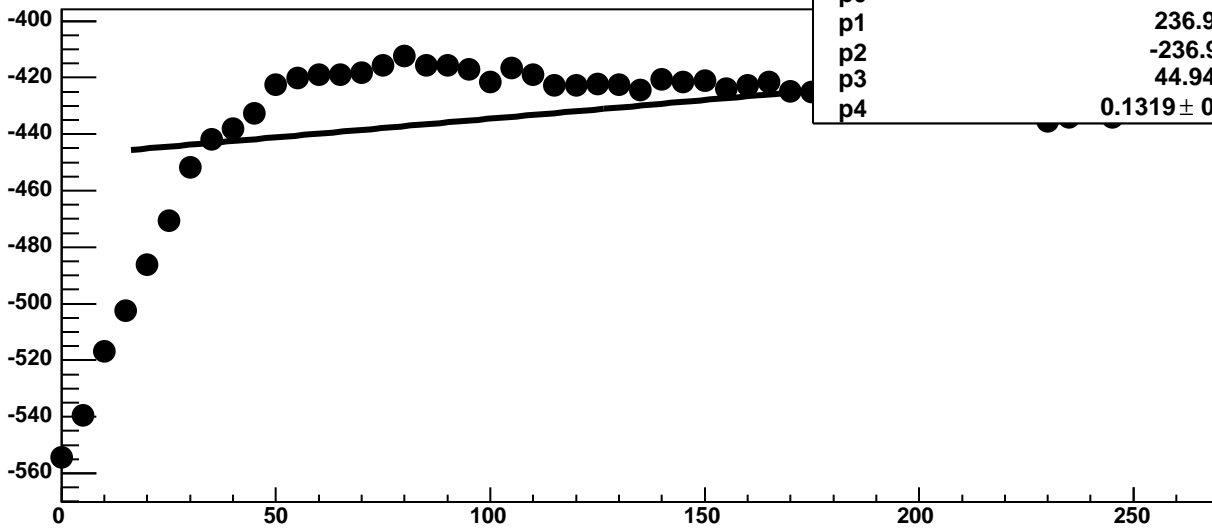
Chip 11, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

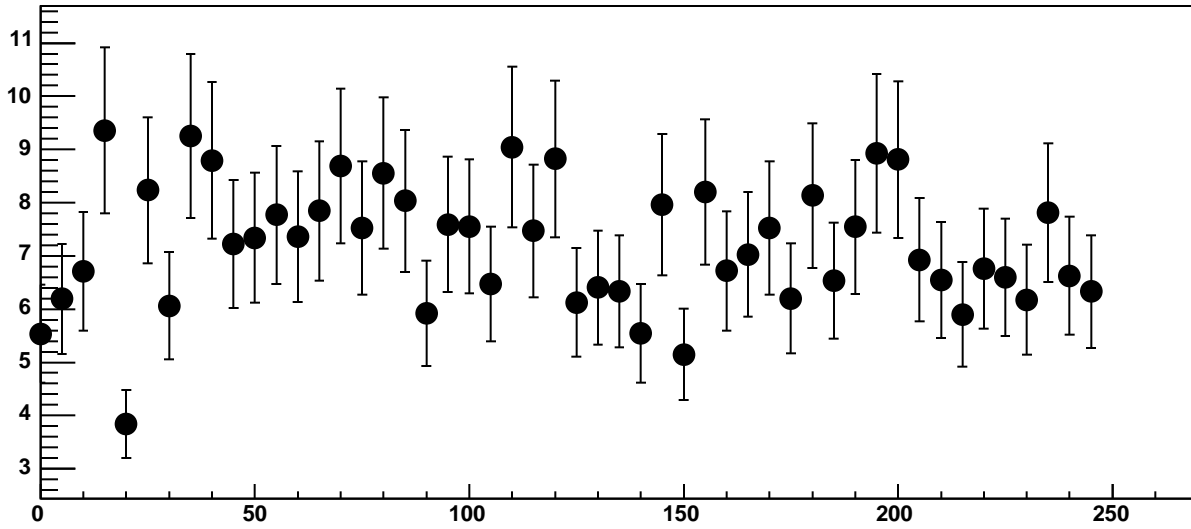


Chip 11, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold

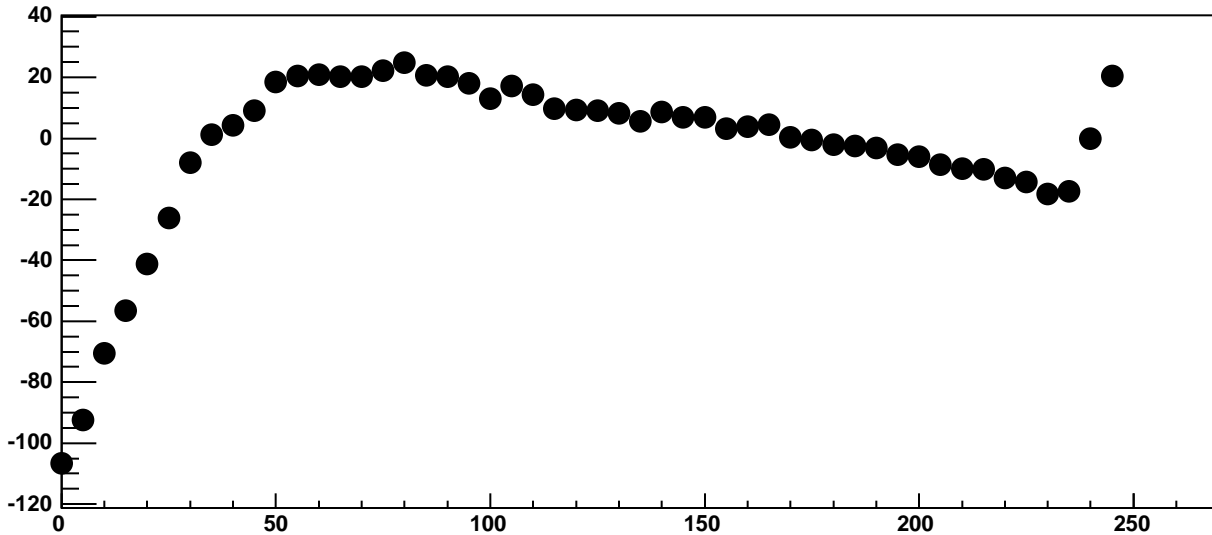


χ^2 / ndf	5611 / 41
p0	-416.5 ± 0.5992
p1	236.9 ± 2.917
p2	-236.9 ± 211.6
p3	44.94 ± 34.27
p4	0.1319 ± 0.003573

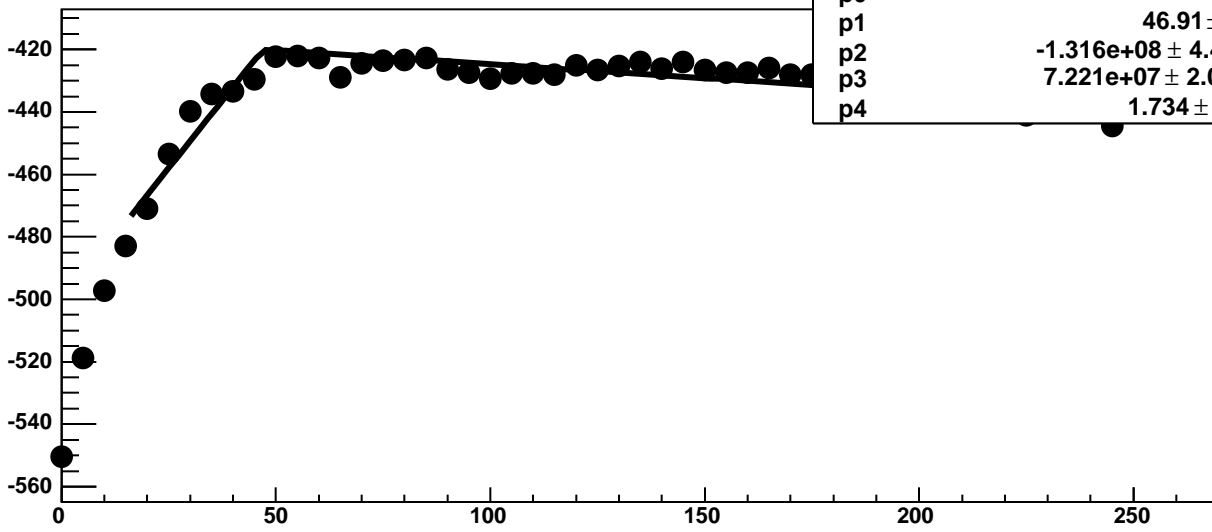
Chip 11, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold

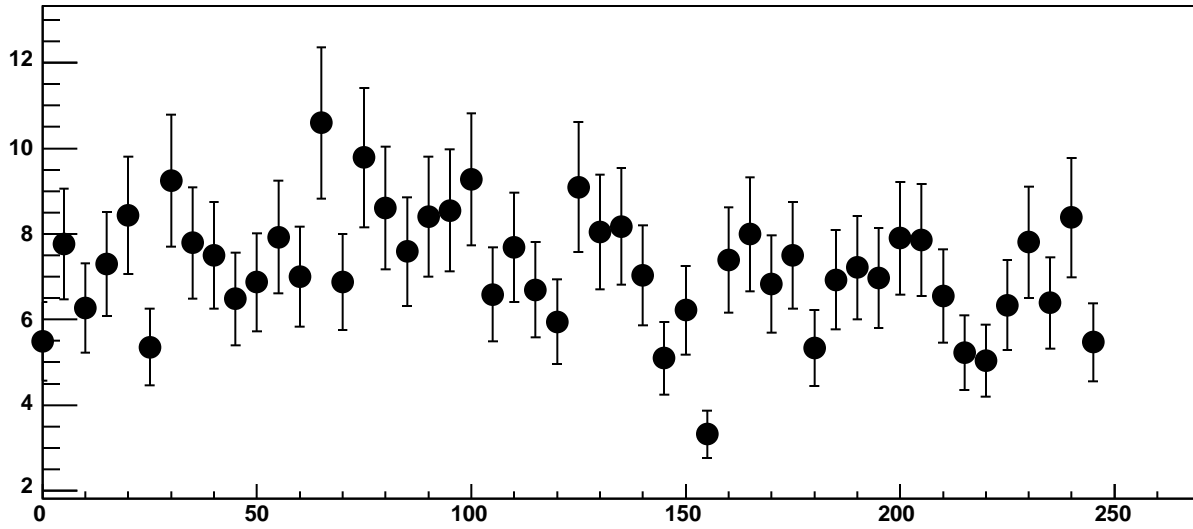


Chip 11, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

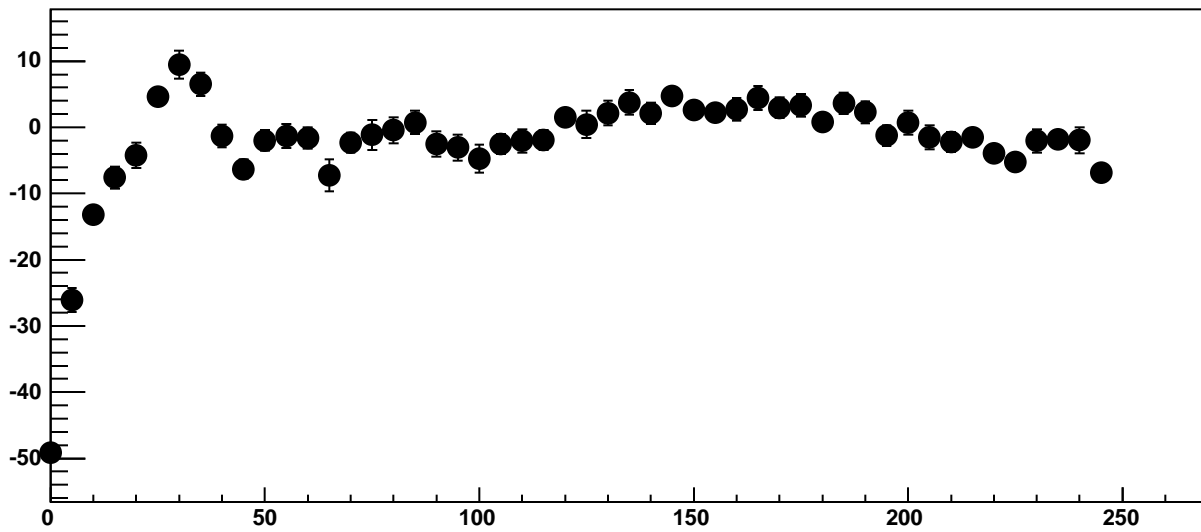


χ^2 / ndf	214.4 / 41
p0	-420 ± 0.531
p1	46.91 ± 0.6864
p2	$-1.316\text{e}+08 \pm 4.407\text{e}+06$
p3	$7.221\text{e}+07 \pm 2.048\text{e}+05$
p4	1.734 ± 0.05959

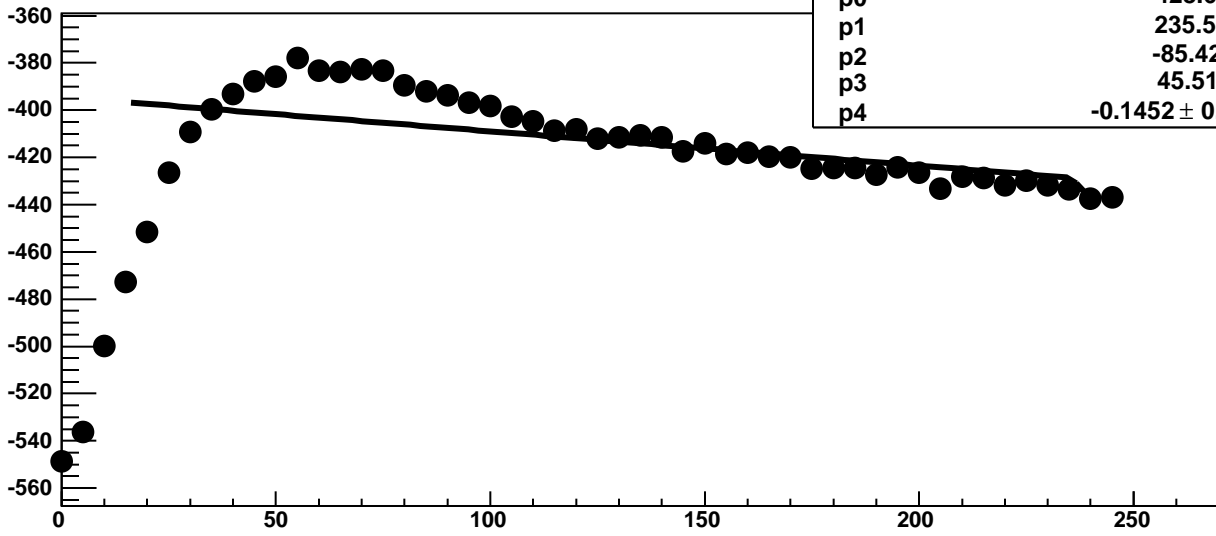
Chip 11, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



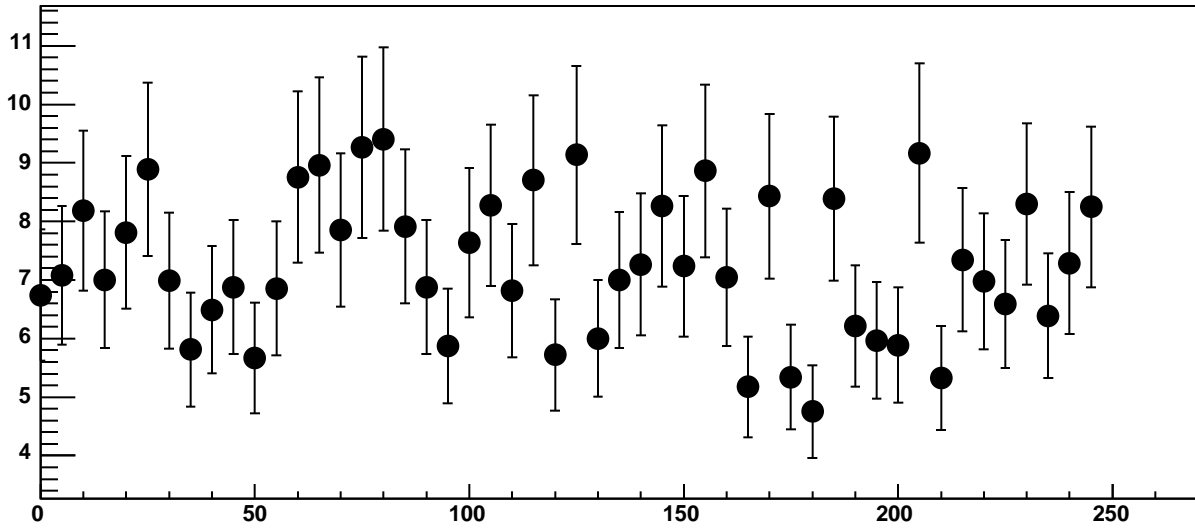
Chip 11, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold



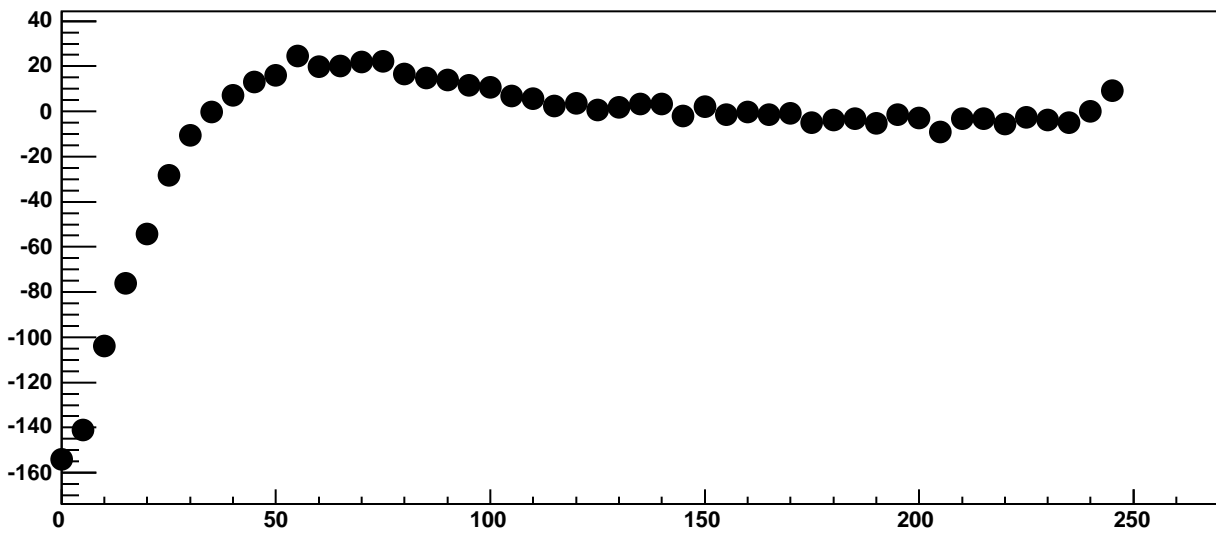
Chip 11, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold



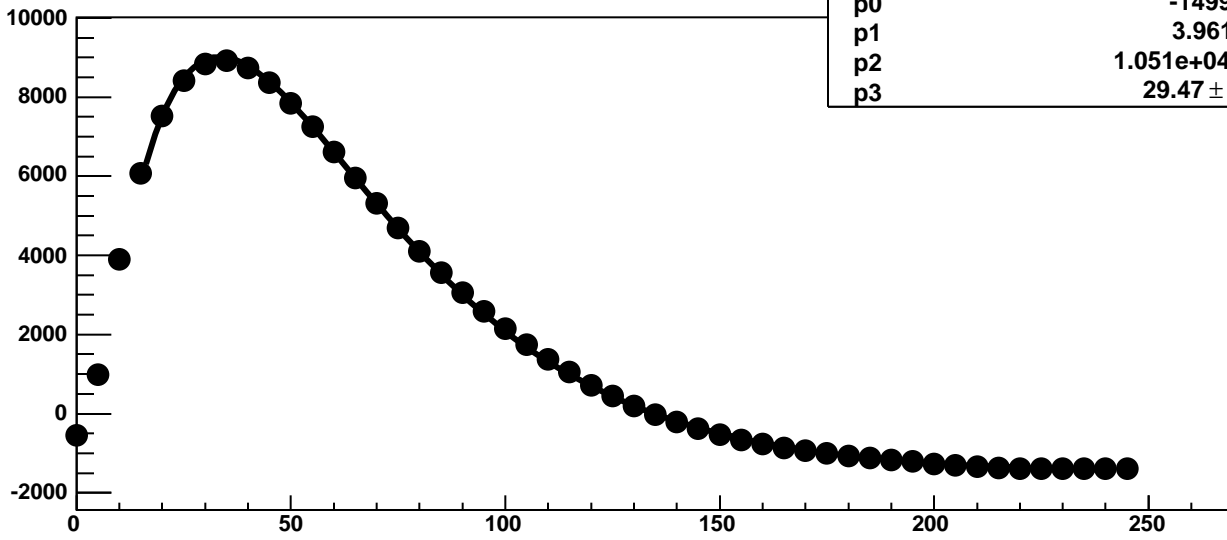
Chip 11, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

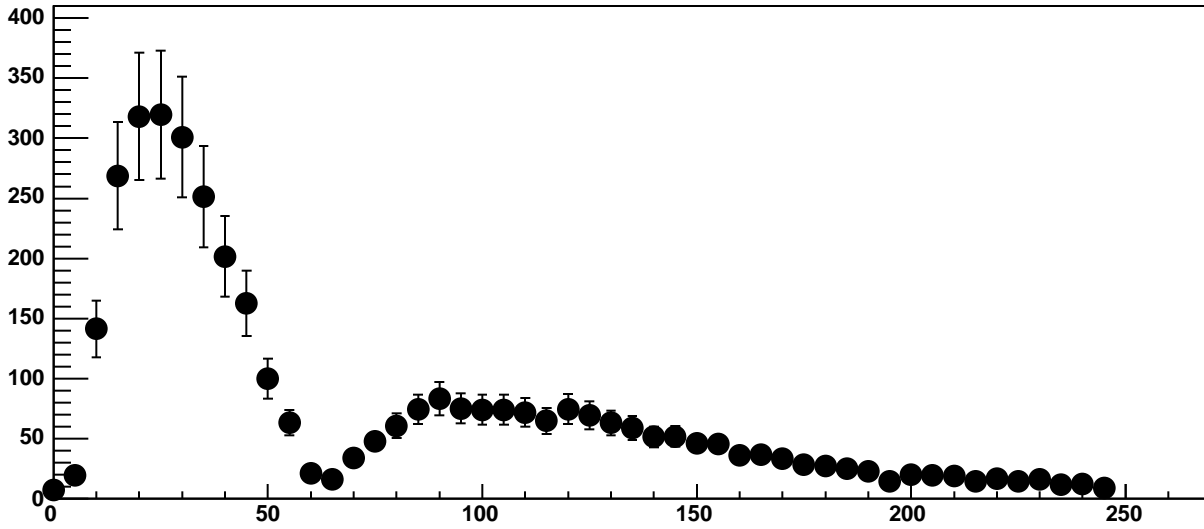


Chip 11, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold

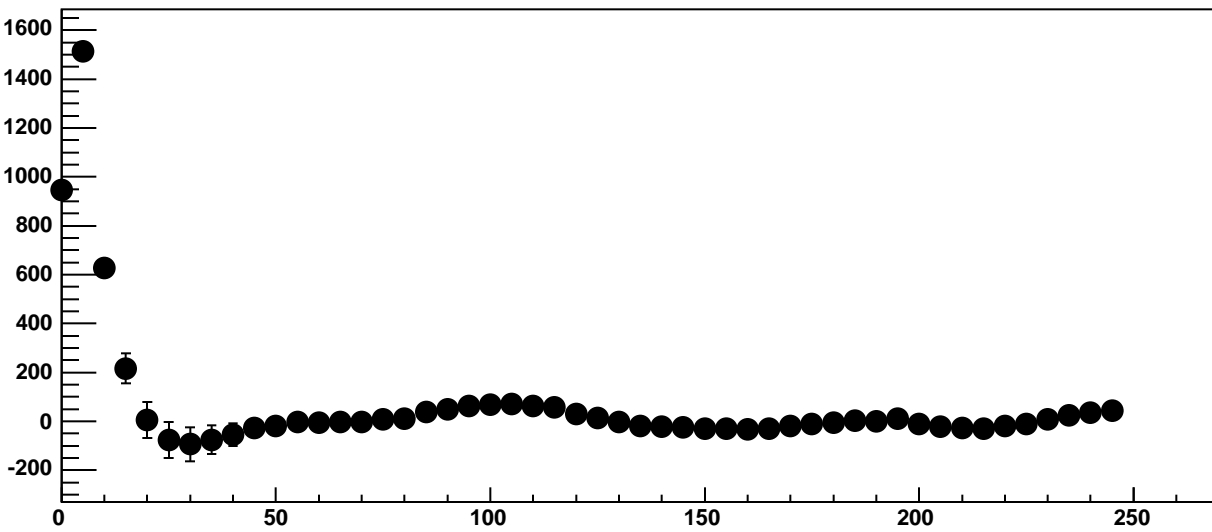


χ^2 / ndf	595.2 / 42
p0	-1499 ± 1.535
p1	3.961 ± 0.102
p2	$1.051\text{e}+04 \pm 13.19$
p3	29.47 ± 0.03594

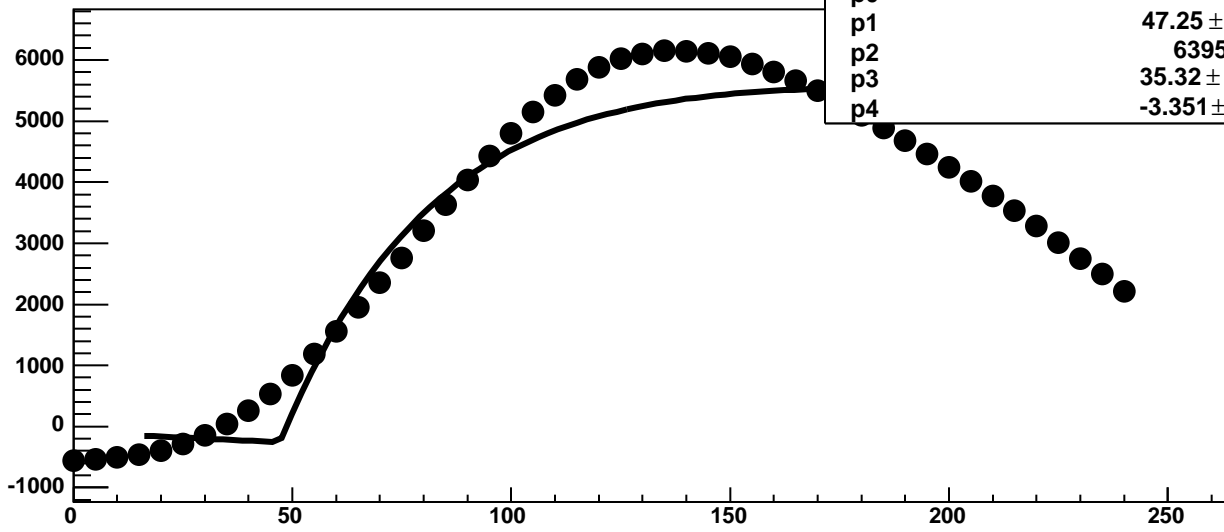
Chip 11, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold



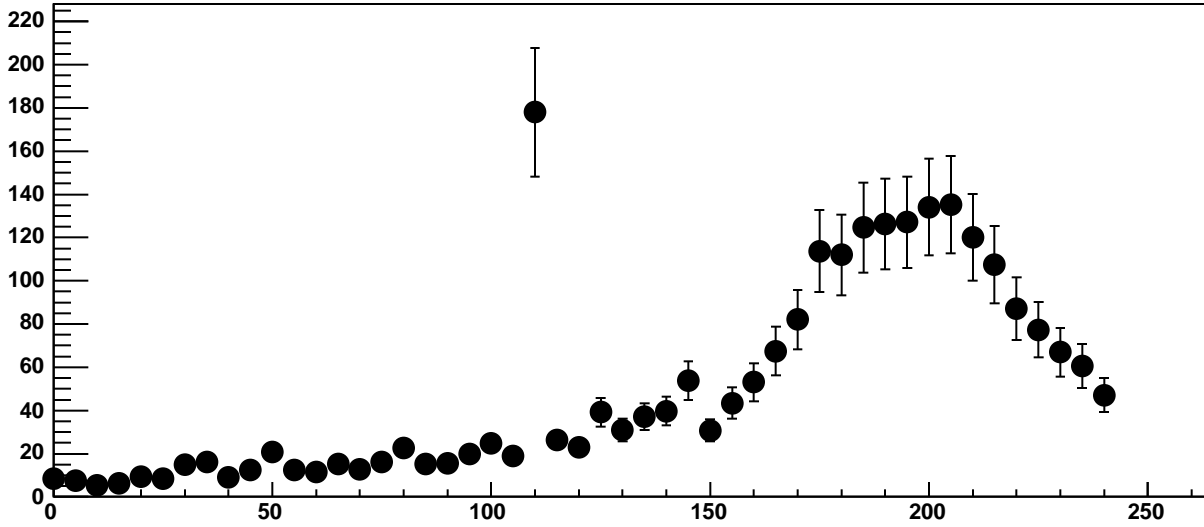
Chip 11, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold



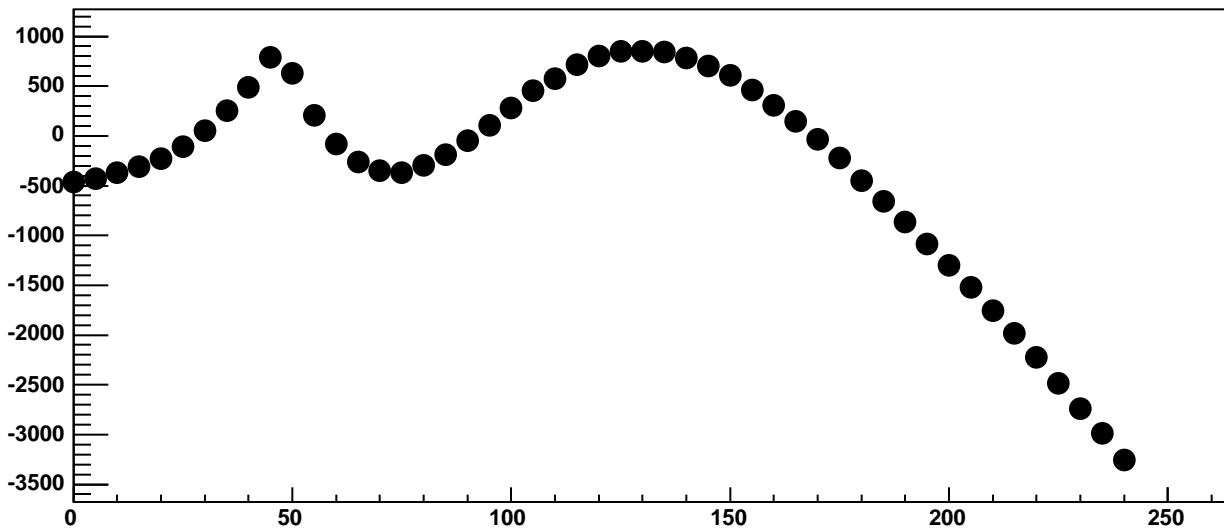
Chip 11, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold



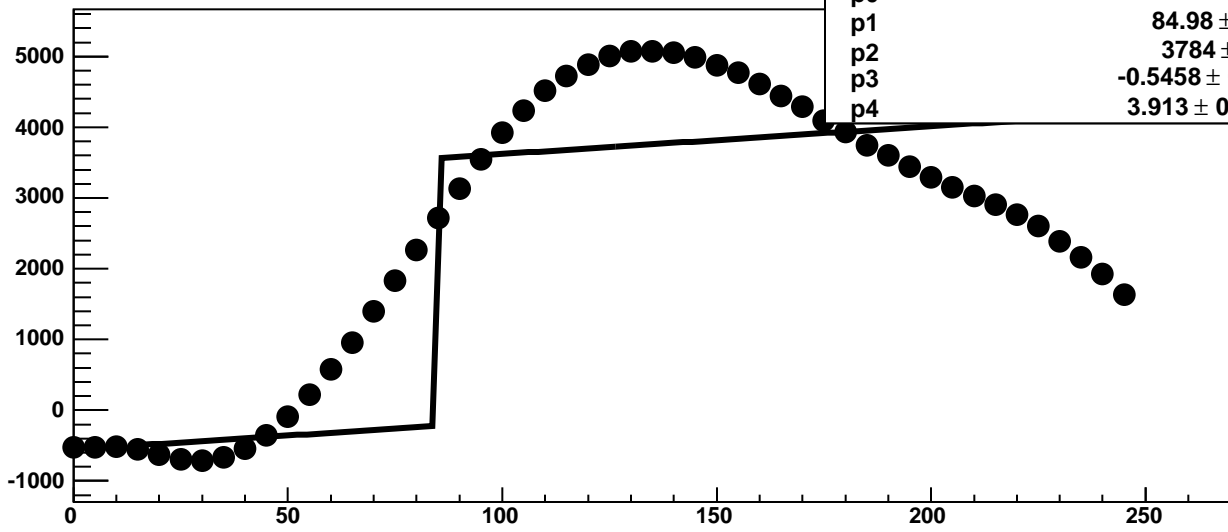
Chip 11, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

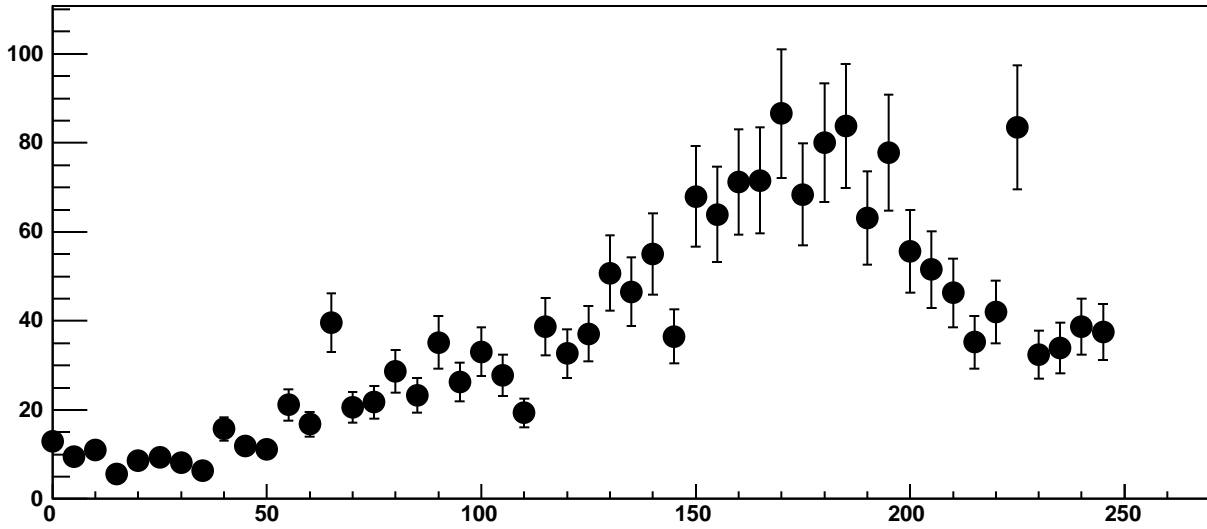


Chip 11, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

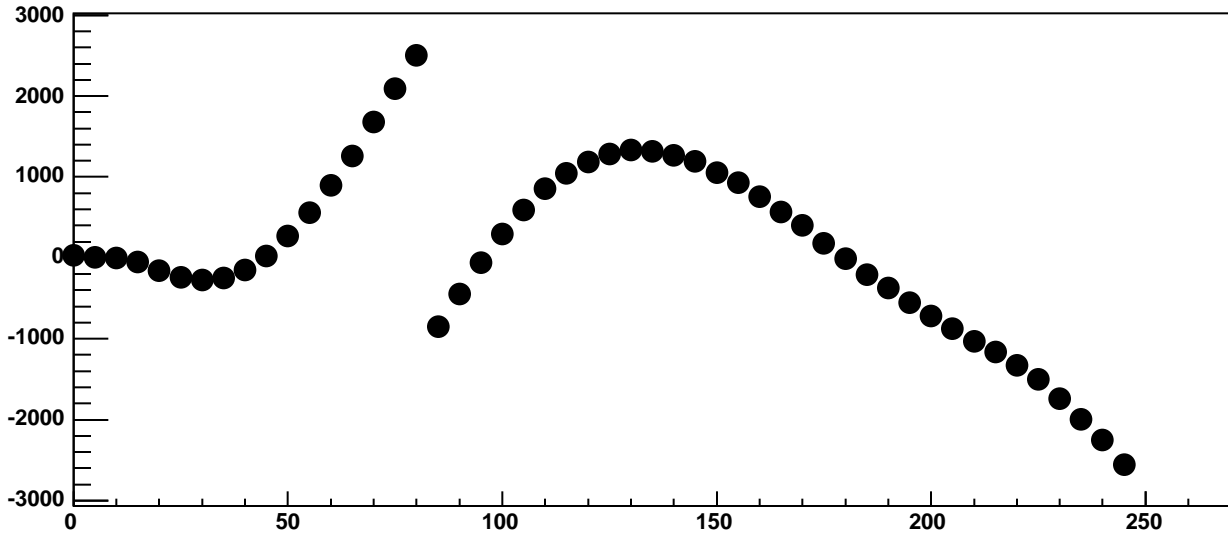


χ^2 / ndf	1.08e+06 / 41
p0	-221.1 ± 0.05961
p1	84.98 ± 0.3346
p2	3784 ± 0.1583
p3	-0.5458 ± 0.01257
p4	3.913 ± 0.001002

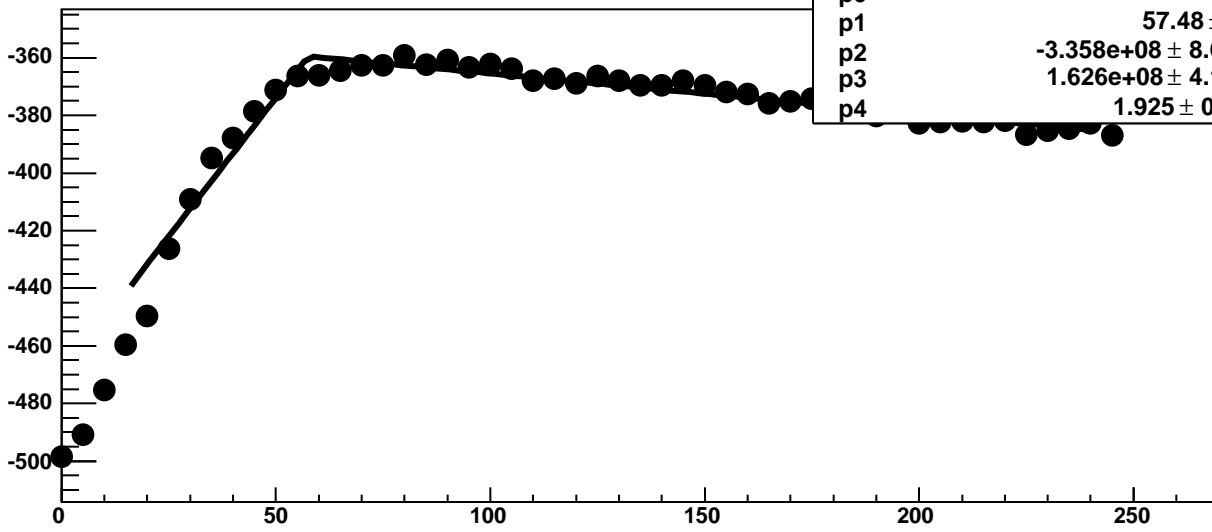
Chip 11, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



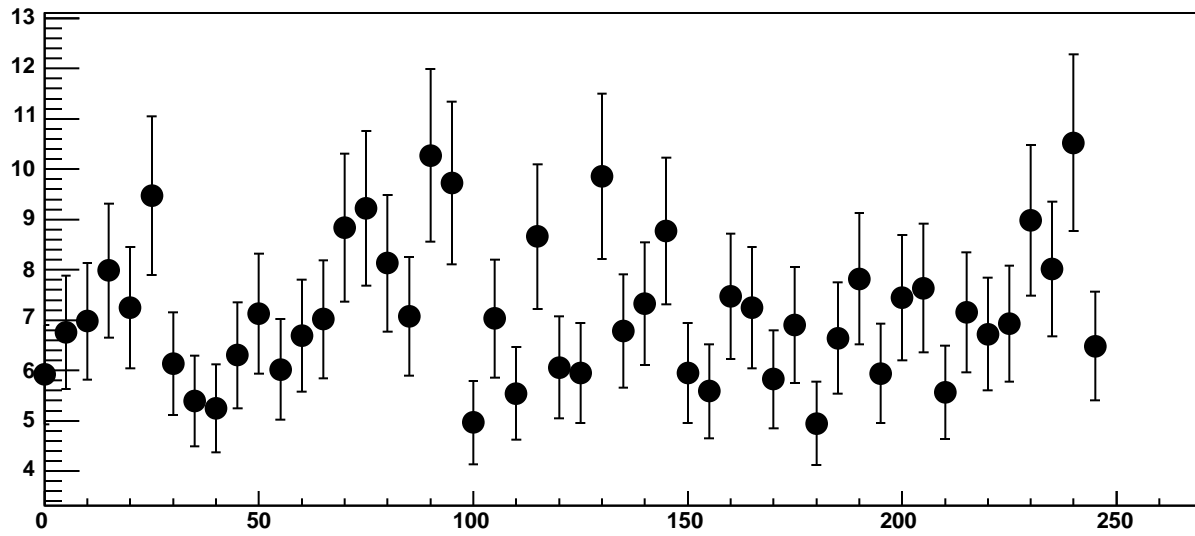
Chip 11, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold



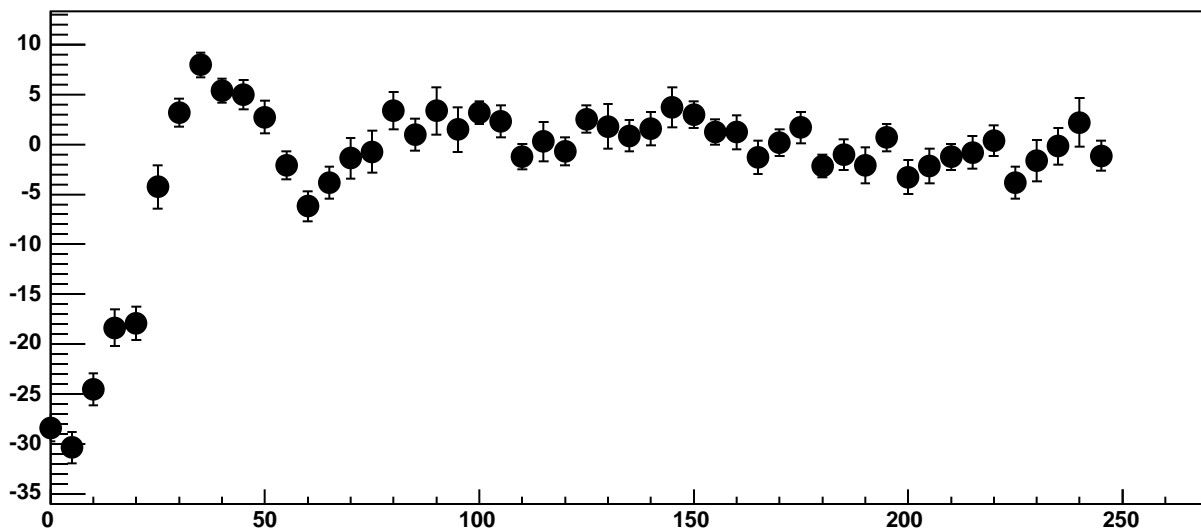
Chip 11, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold



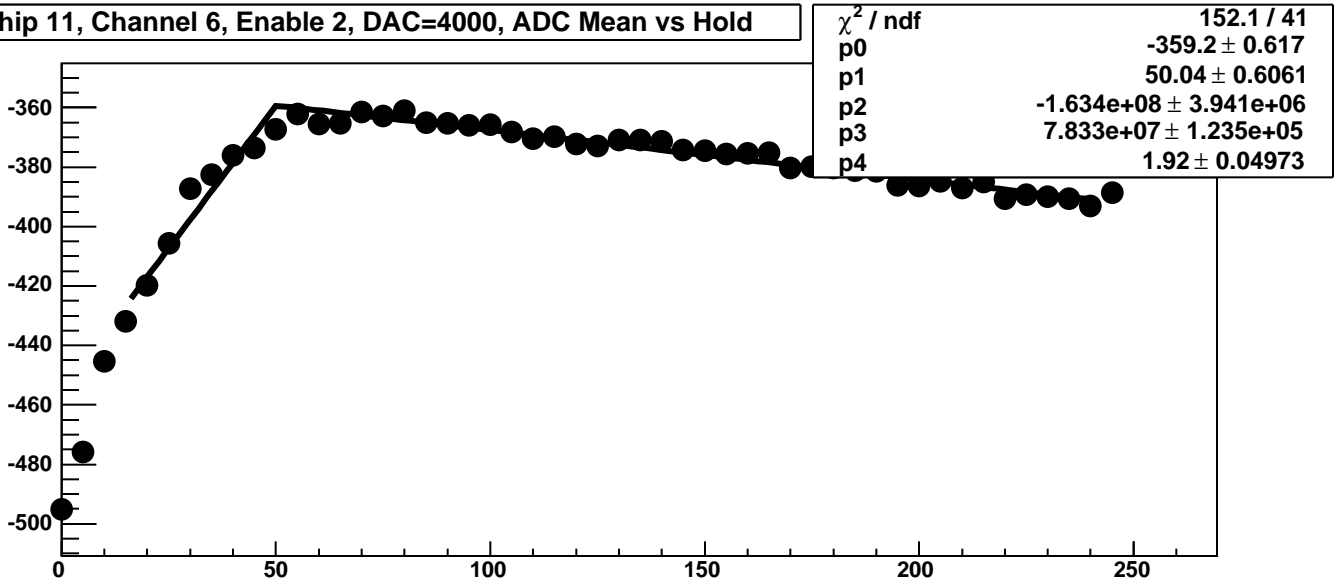
Chip 11, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



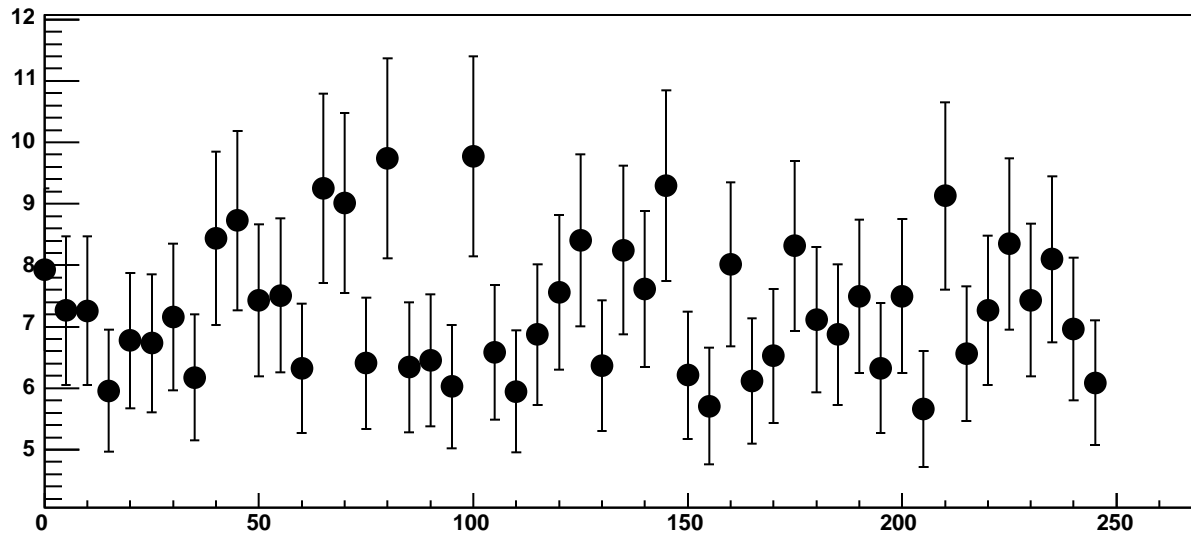
Chip 11, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold



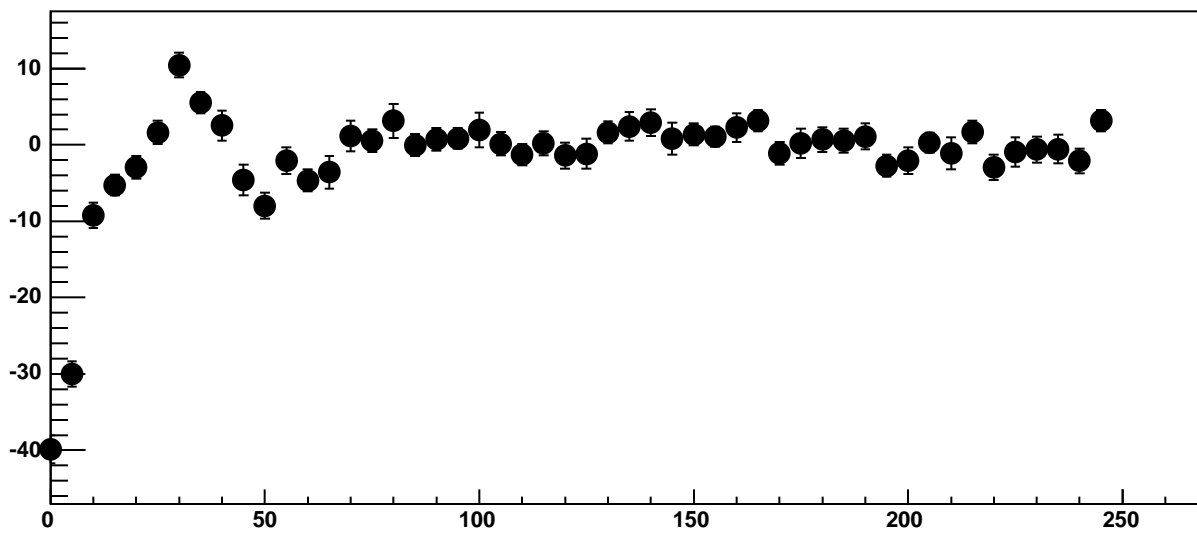
Chip 11, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold



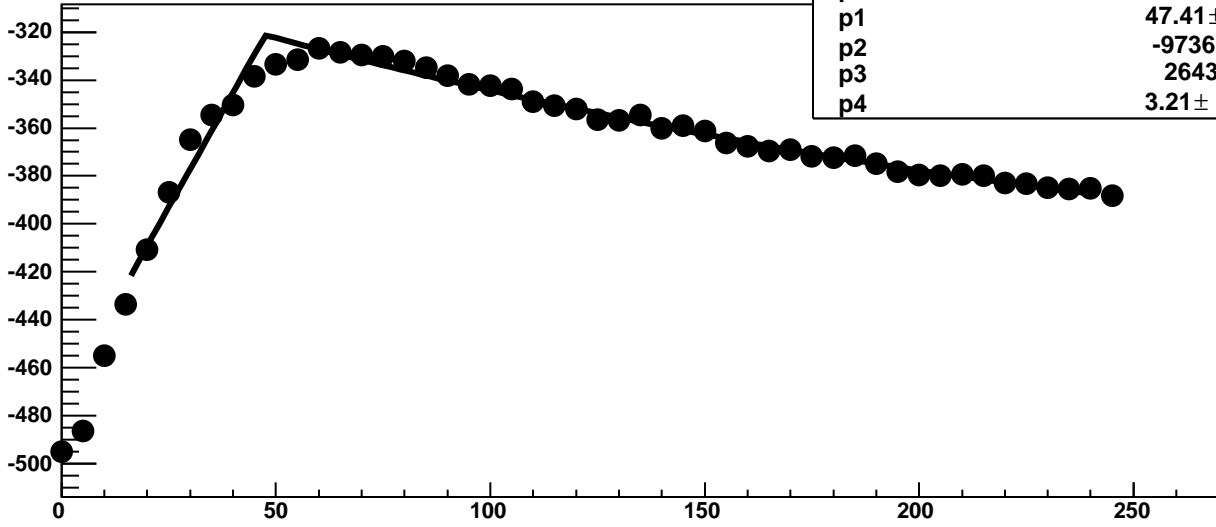
Chip 11, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold

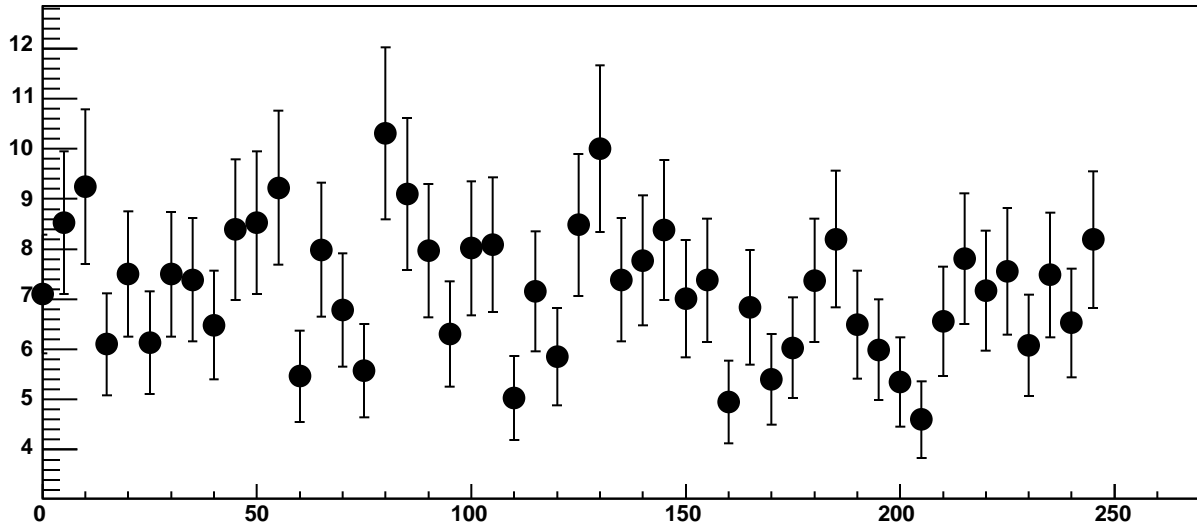


Chip 11, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

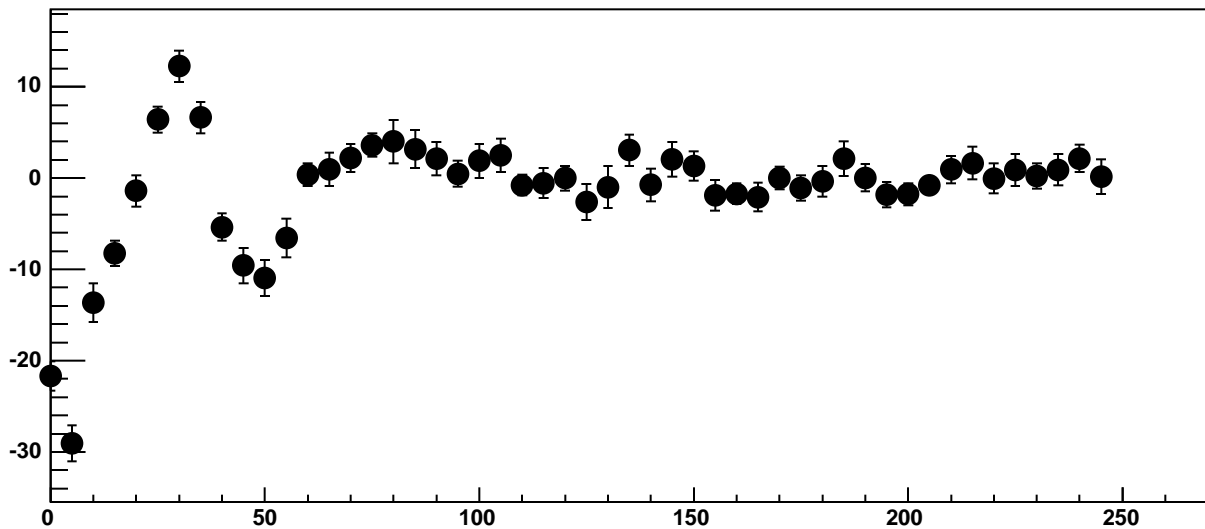


χ^2 / ndf	243.6 / 41
p0	-321.3 ± 0.6133
p1	47.41 ± 0.3768
p2	-9736 ± 854.7
p3	2643 ± 234.1
p4	3.21 ± 0.06024

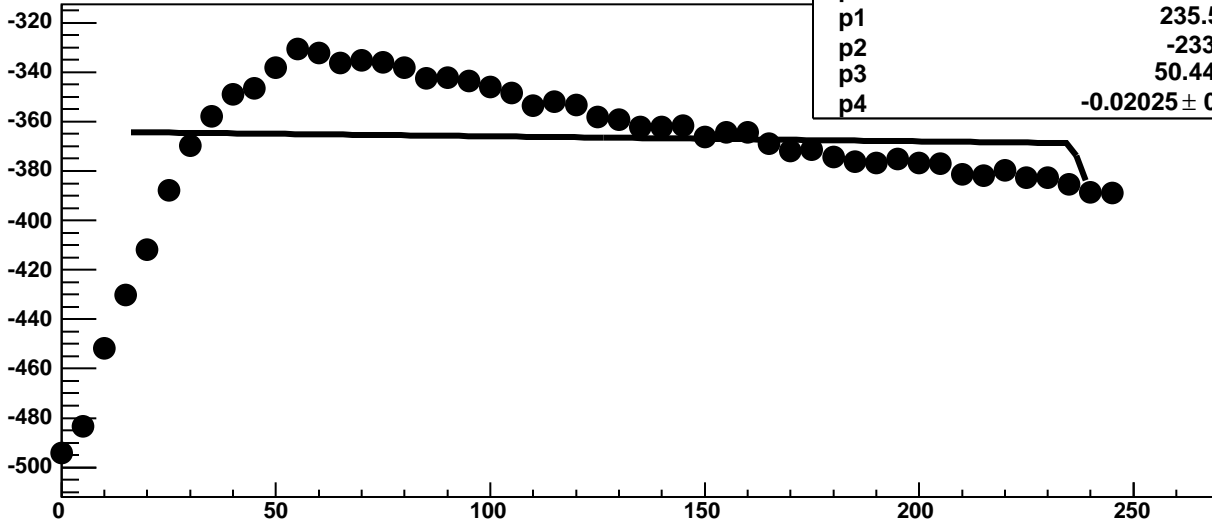
Chip 11, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

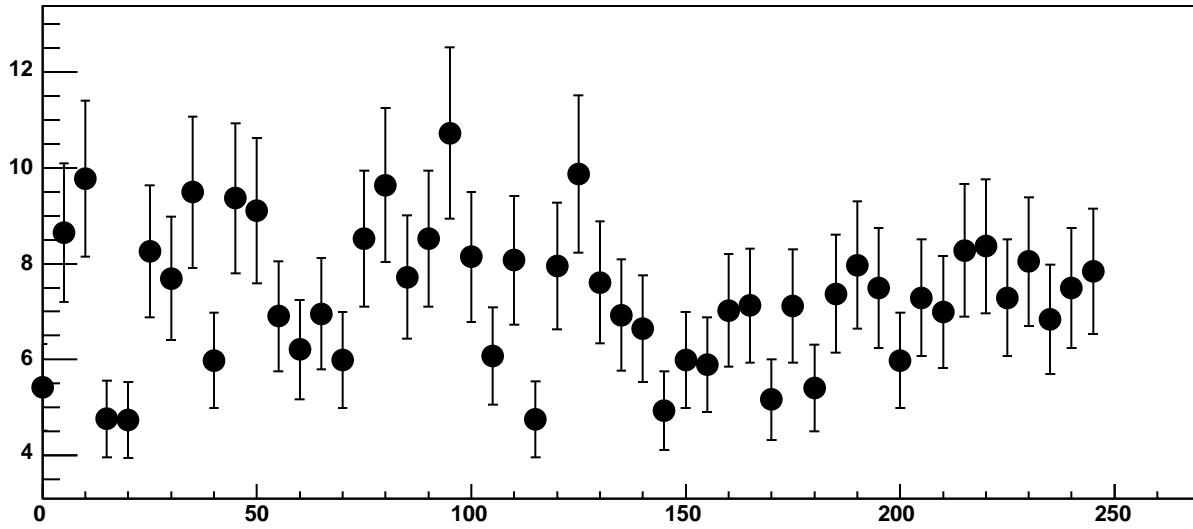


Chip 11, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

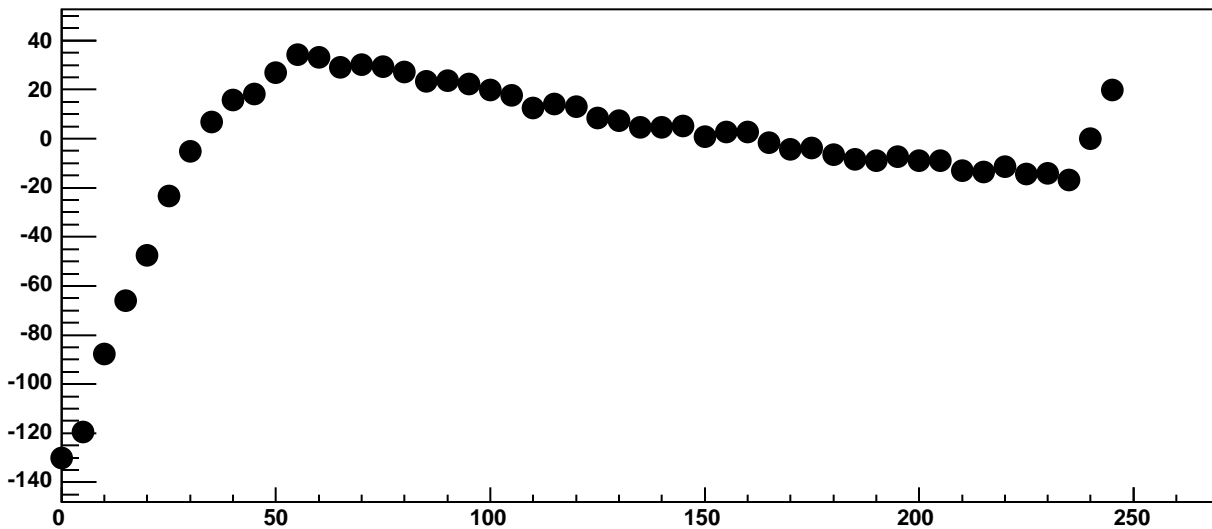


χ^2 / ndf	9893 / 41
p0	-368.7 ± 0.4703
p1	235.5 ± 3.511
p2	-233 ± 171.3
p3	50.44 ± 32.12
p4	-0.02025 ± 0.003572

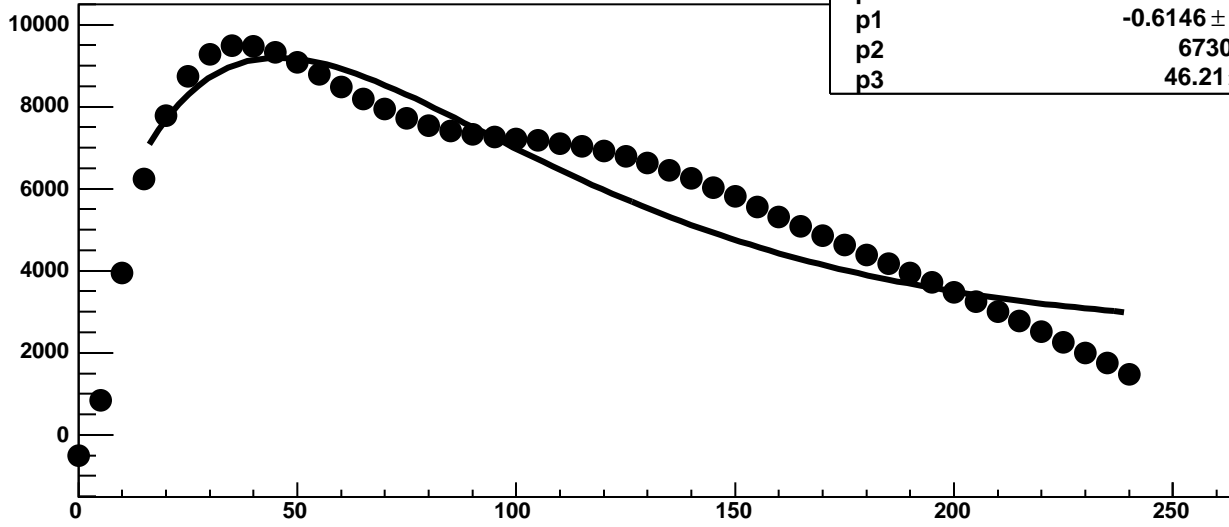
Chip 11, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

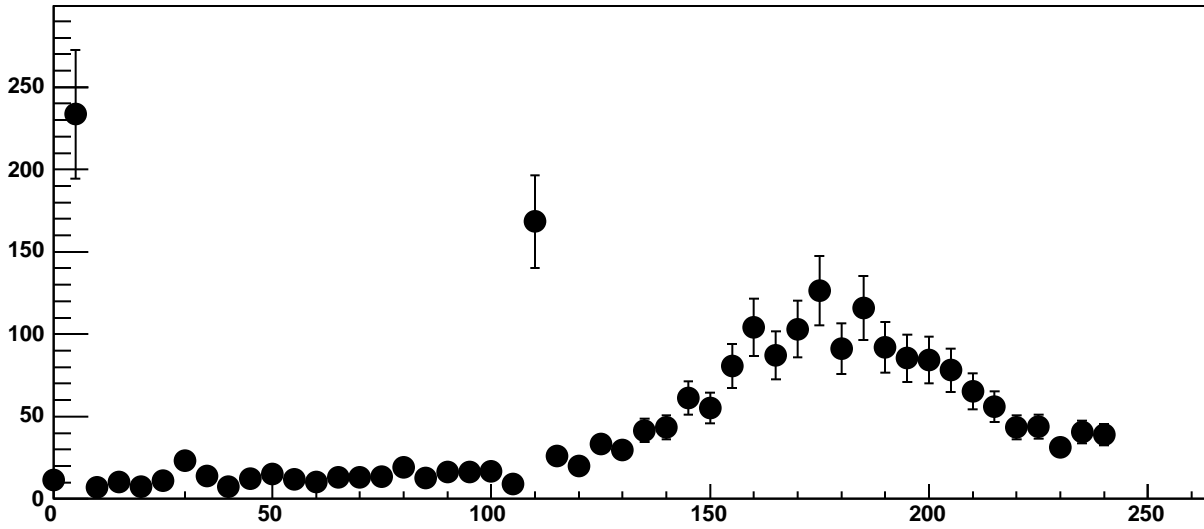


Chip 11, Channel 6, Enable 5!, DAC=4000, ADC Mean vs Hold

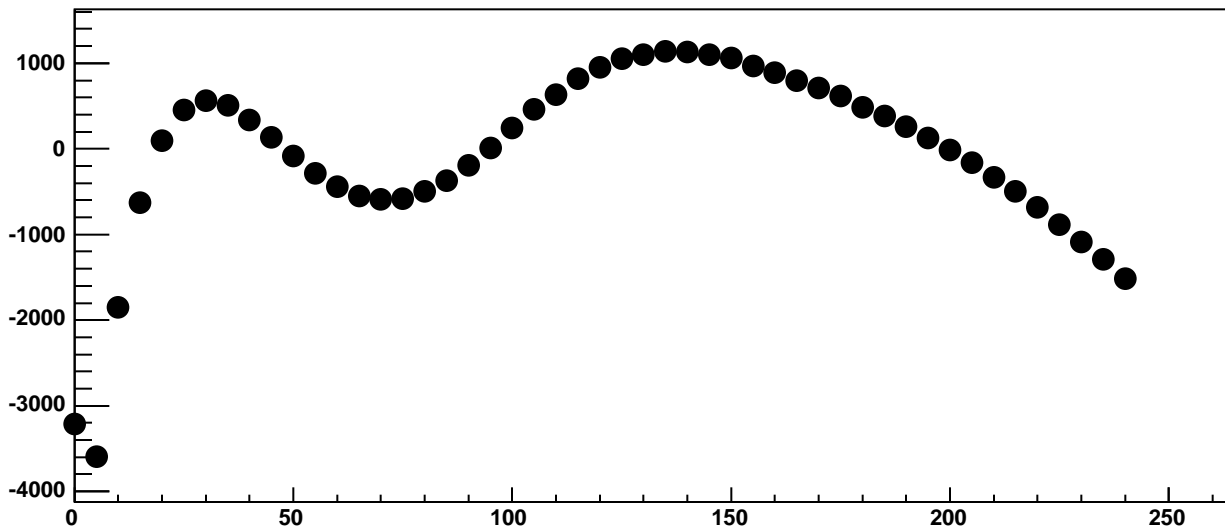


χ^2 / ndf	6.645e+05 / 42
p0	2463 ± 5.922
p1	-0.6146 ± 0.03146
p2	6730 ± 5.638
p3	46.21 ± 0.0401

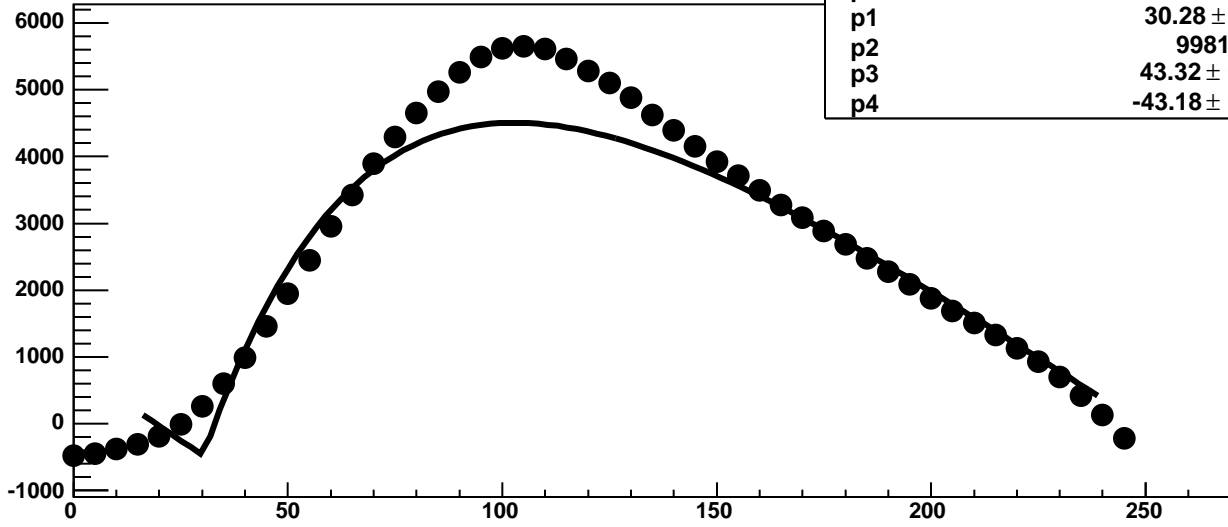
Chip 11, Channel 6, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 6, Enable 5!, DAC=4000, ADC Residuals vs Hold

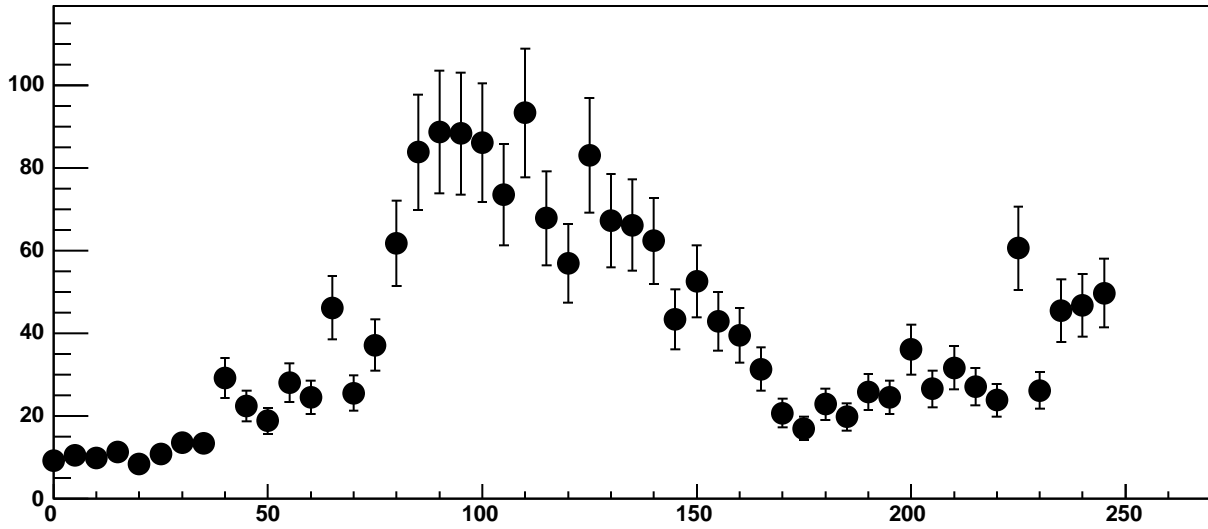


Chip 11, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

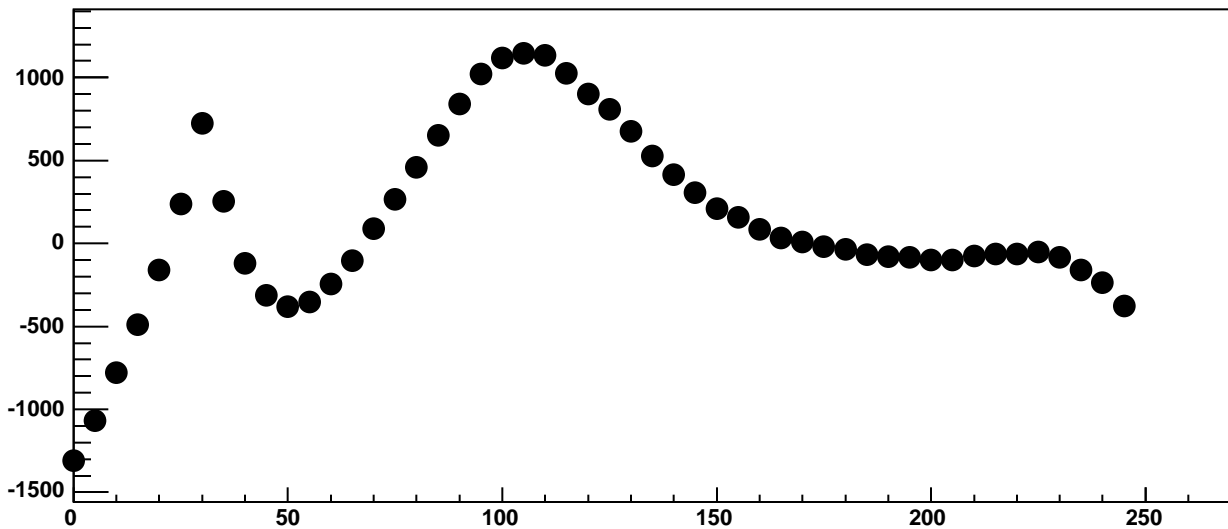


χ^2 / ndf	1.667e+05 / 41
p0	-474.5 \pm 1.253
p1	30.28 \pm 0.01711
p2	9981 \pm 14.96
p3	43.32 \pm 0.08138
p4	-43.18 \pm 0.07654

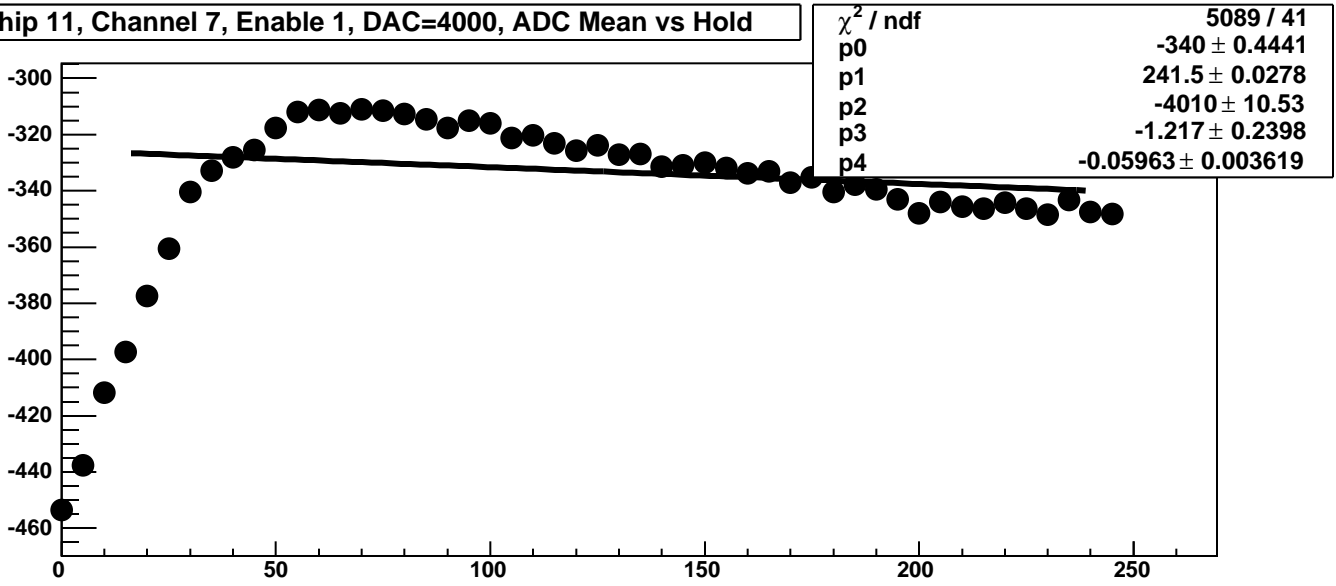
Chip 11, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



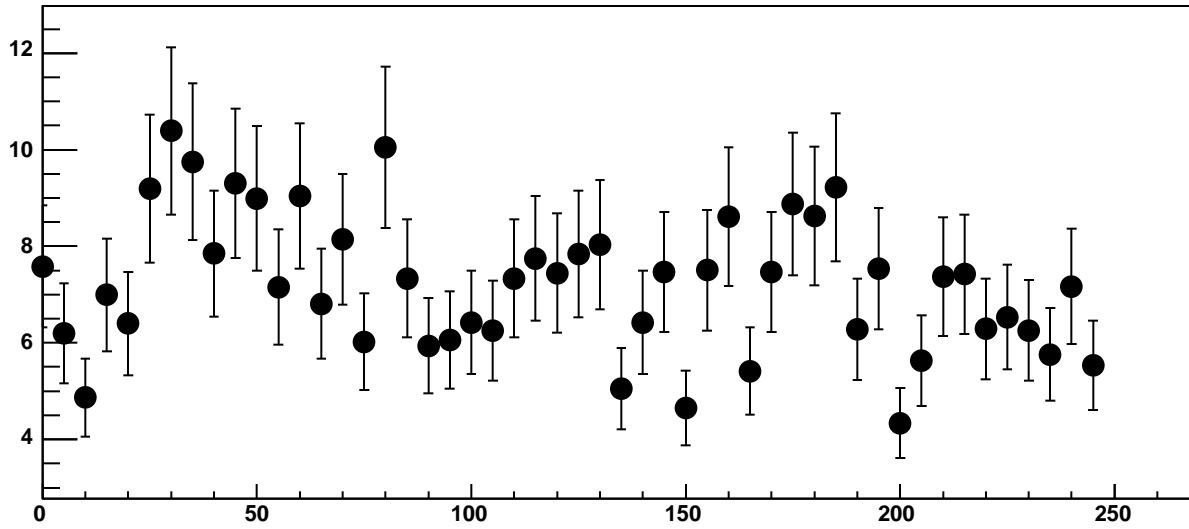
Chip 11, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold



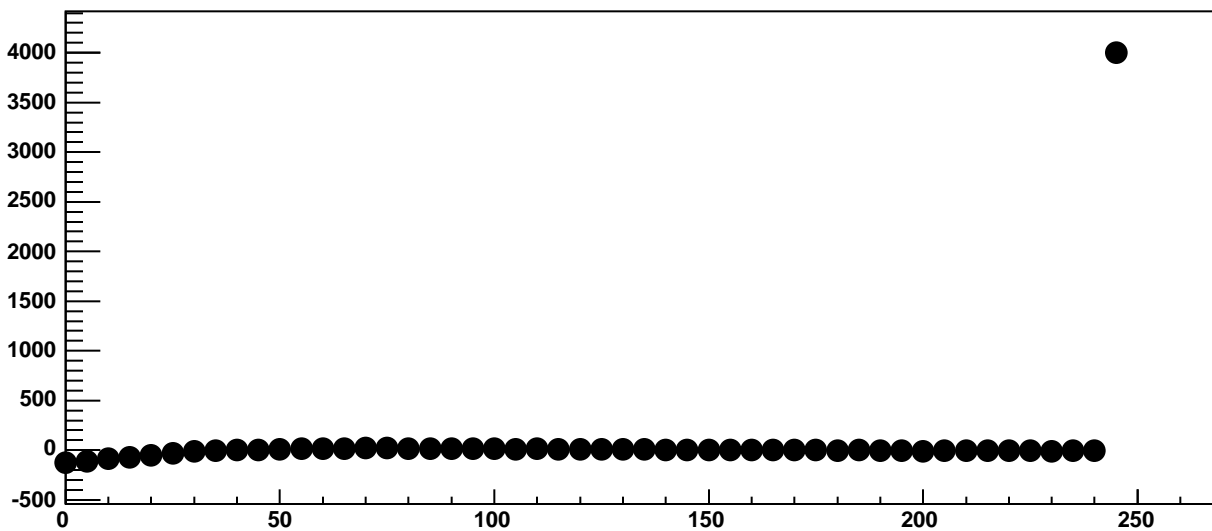
Chip 11, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold



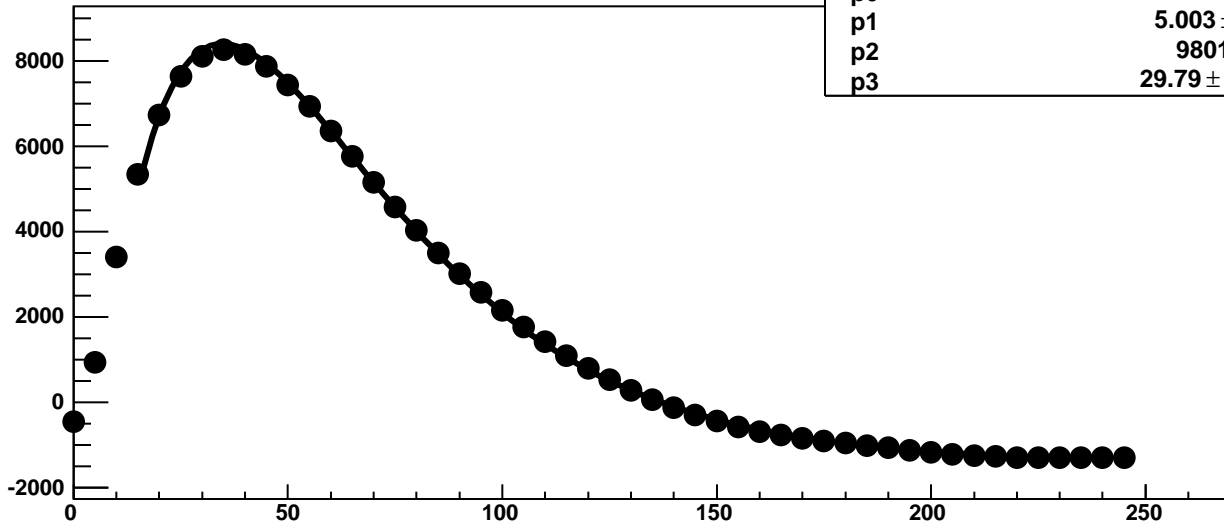
Chip 11, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

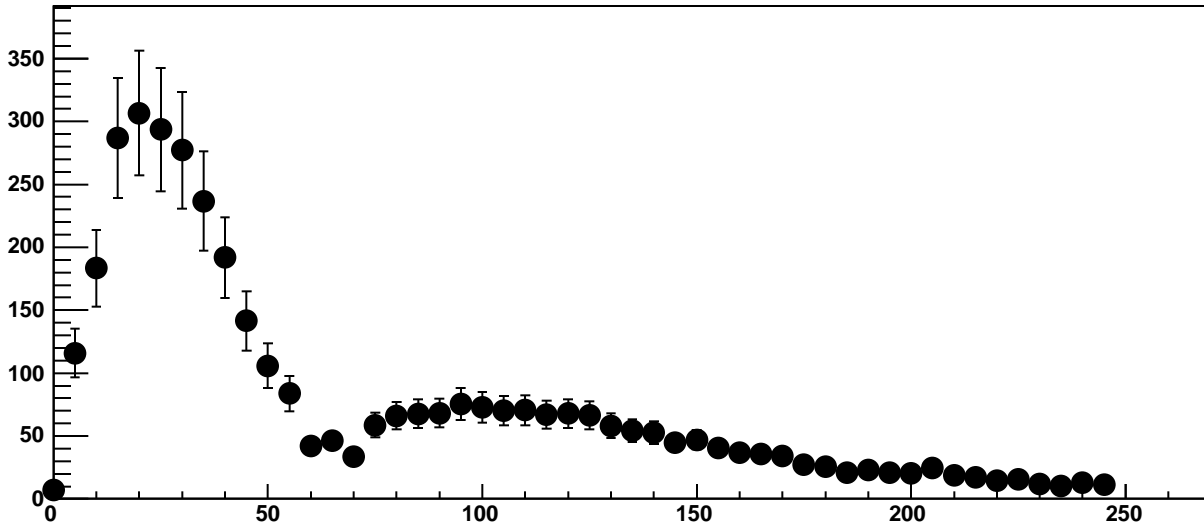


Chip 11, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold

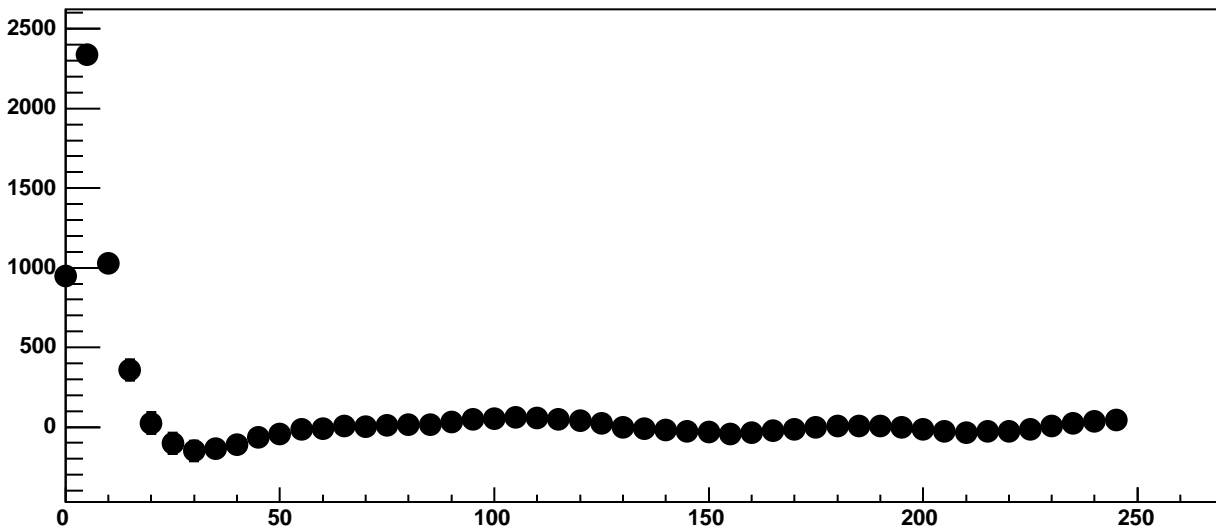


χ^2 / ndf	666 / 42
p0	-1403 ± 1.5
p1	5.003 ± 0.1048
p2	9801 ± 12.86
p3	29.79 ± 0.03808

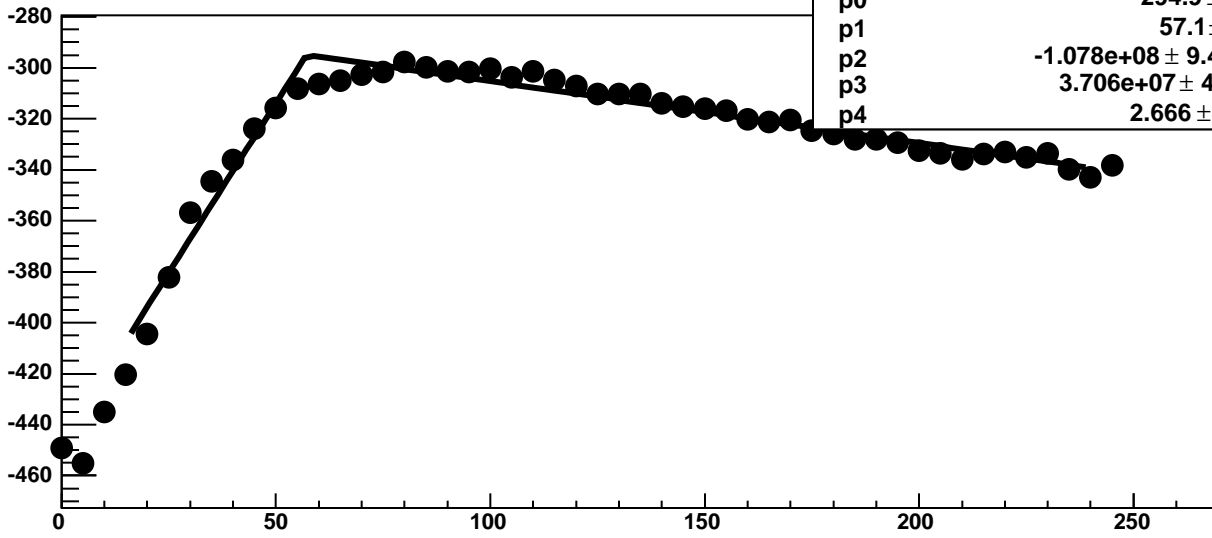
Chip 11, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold

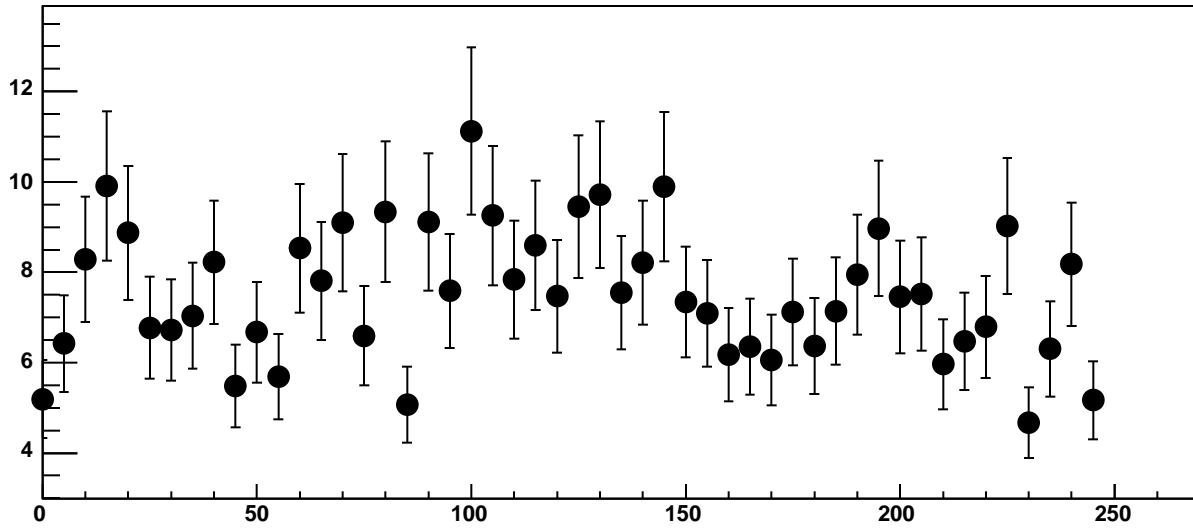


Chip 11, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

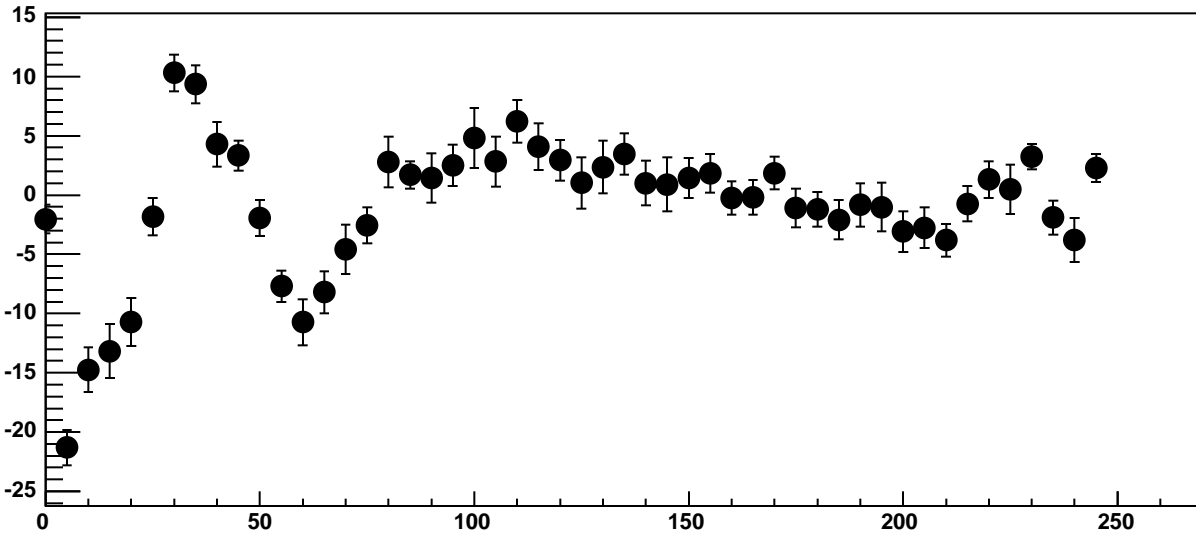


χ^2 / ndf	321.7 / 41
p0	-294.9 ± 0.5235
p1	57.1 ± 0.2982
p2	$-1.078\text{e}+08 \pm 9.454\text{e}+05$
p3	$3.706\text{e}+07 \pm 4.15\text{e}+05$
p4	2.666 ± 0.02161

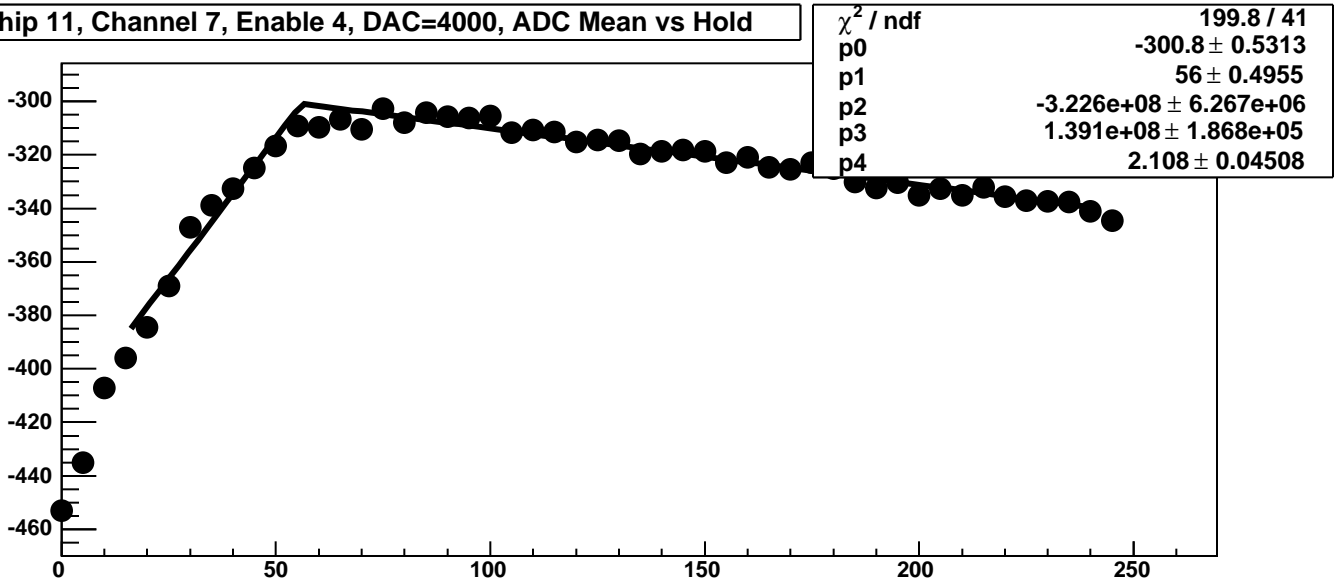
Chip 11, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



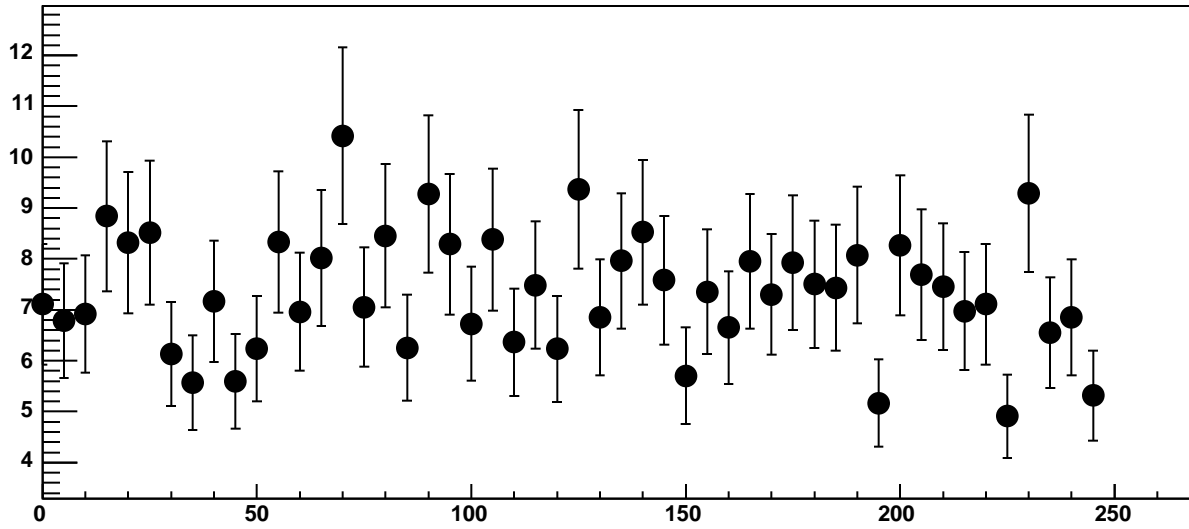
Chip 11, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold



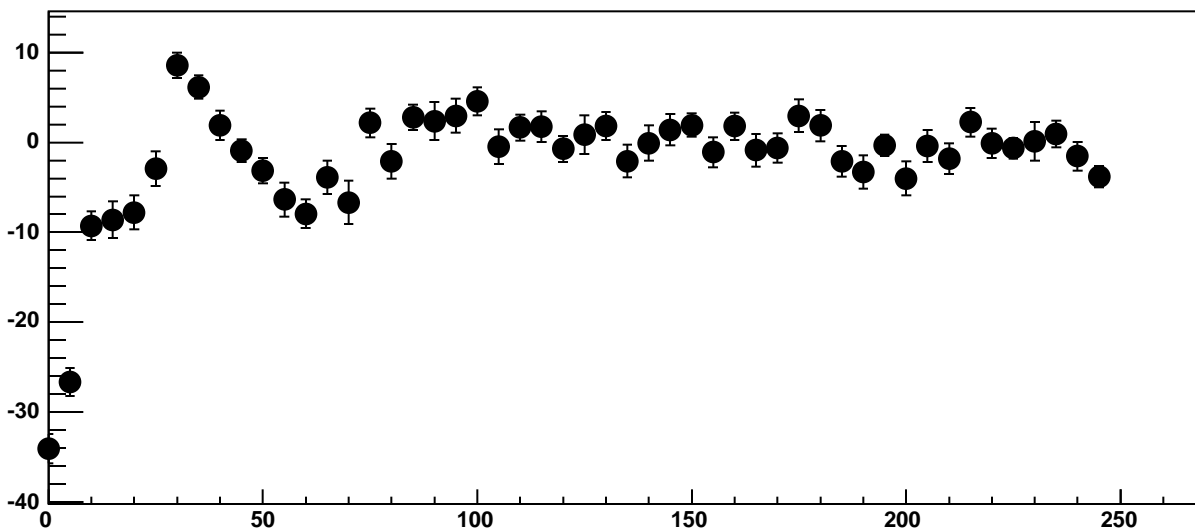
Chip 11, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold



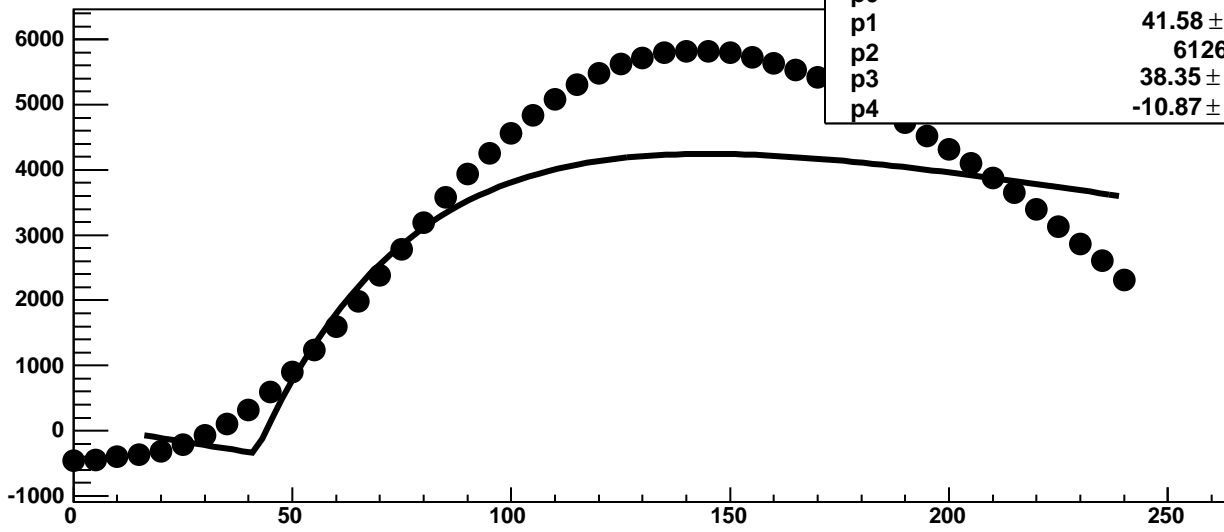
Chip 11, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

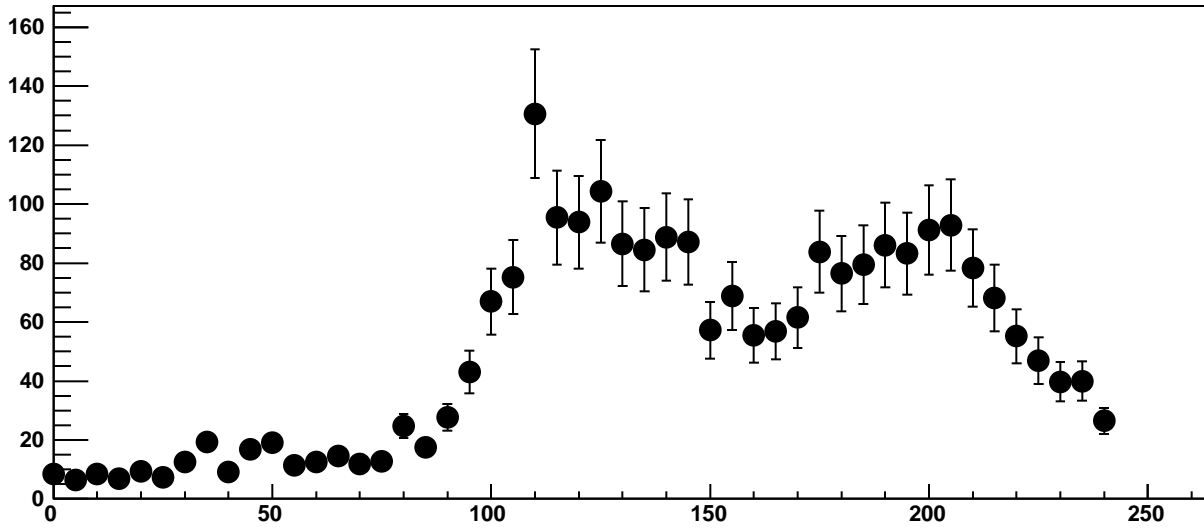


Chip 11, Channel 7, Enable 5, DAC=4000, ADC Mean vs Hold

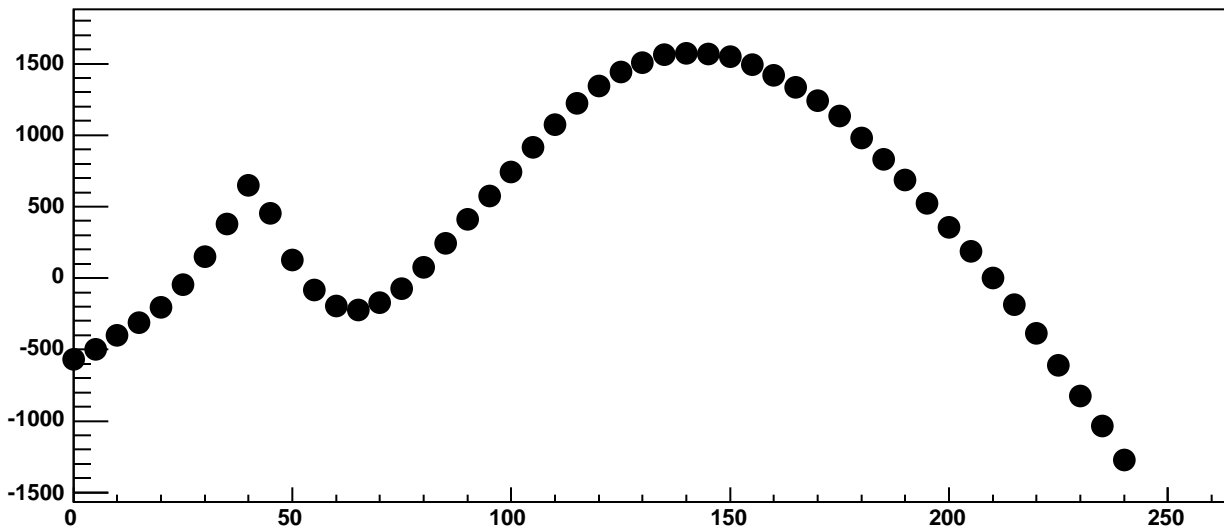


χ^2 / ndf	3.713e+05 / 41
p0	-343.9 ± 1.204
p1	41.58 ± 0.02191
p2	6126 ± 10.75
p3	38.35 ± 0.07632
p4	-10.87 ± 0.05555

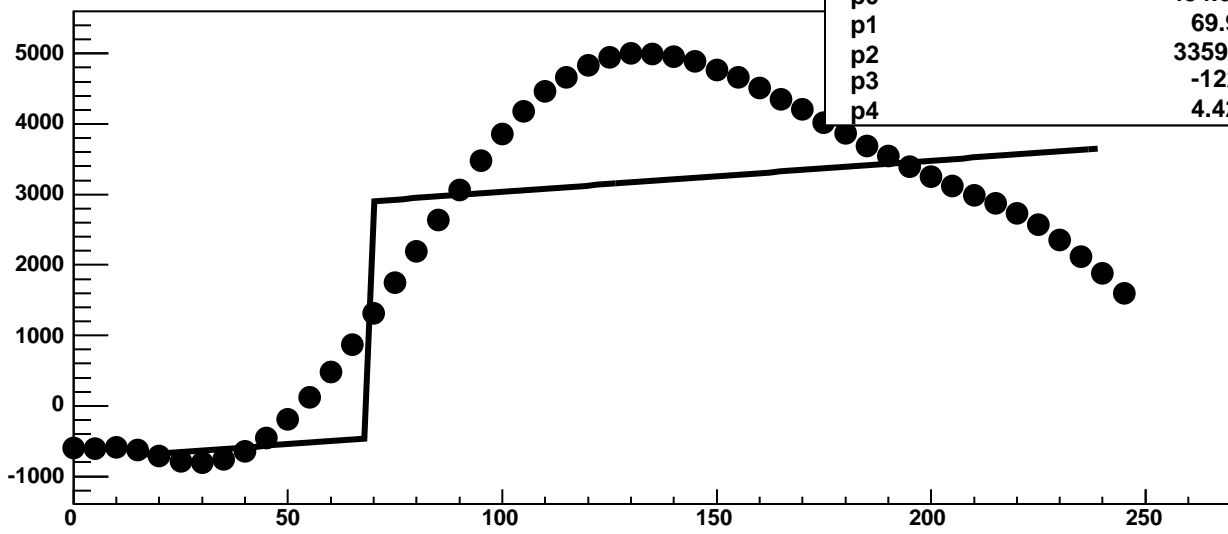
Chip 11, Channel 7, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 7, Enable 5, DAC=4000, ADC Residuals vs Hold

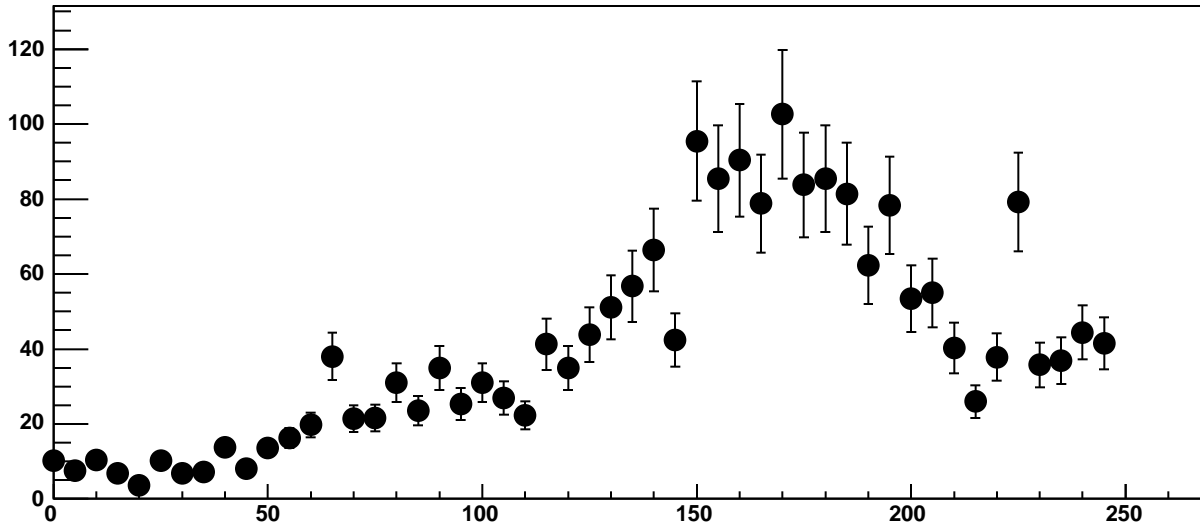


Chip 11, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

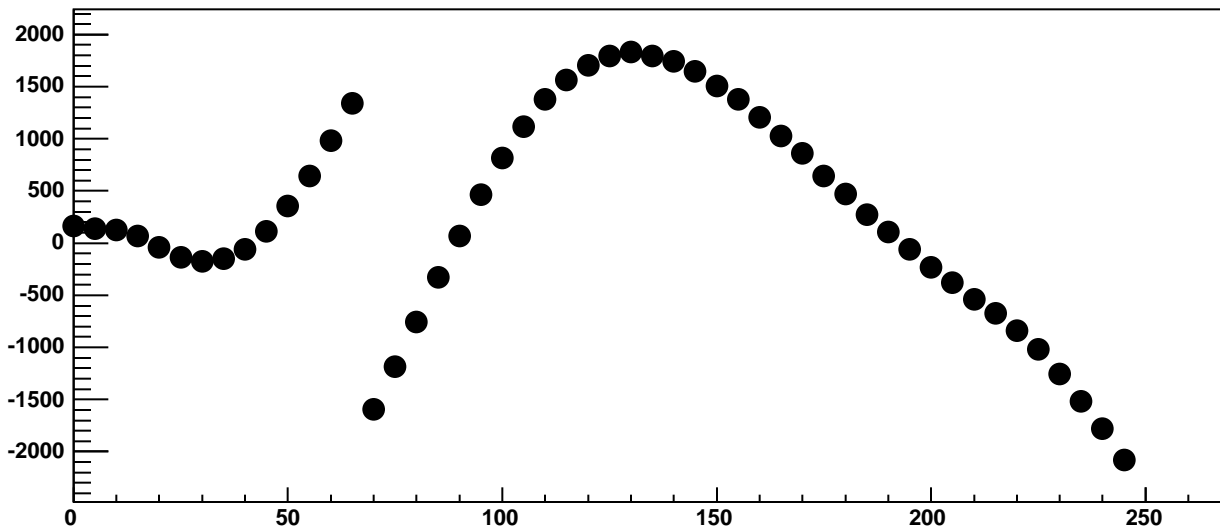


χ^2 / ndf	7.733e+05 / 41
p0	-454.6 ± 1.431
p1	69.96 ± 1
p2	3359 ± 2.964
p3	-1220 ± 1
p4	4.422 ± 1

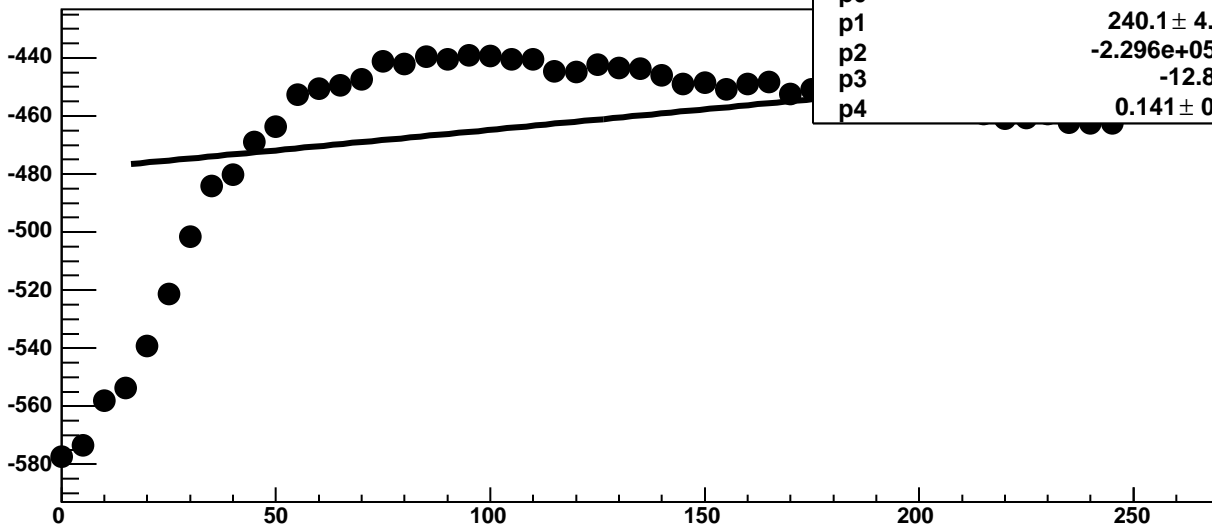
Chip 11, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

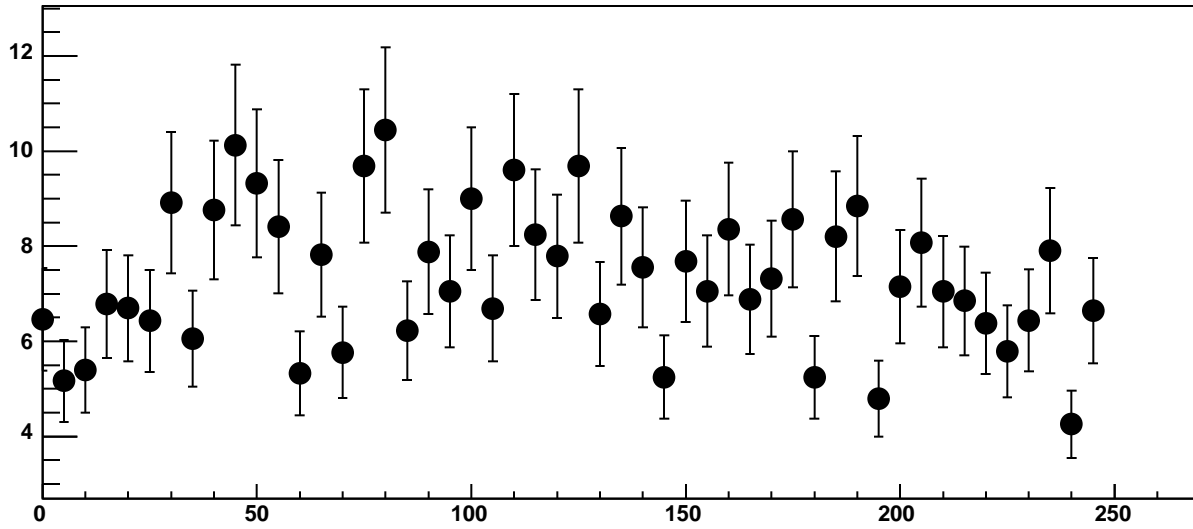


Chip 11, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

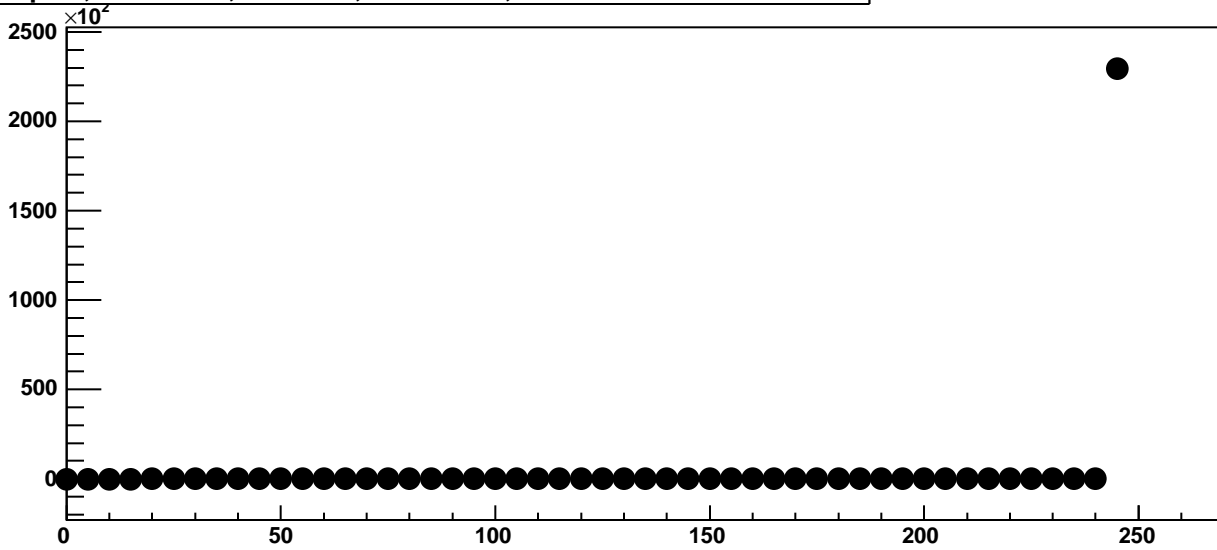


χ^2 / ndf	9143 / 41
p0	-445 ± 0.2371
p1	240.1 ± 4.679e-07
p2	-2.296e+05 ± 84.85
p3	-12.8 ± 1.414
p4	0.141 ± 0.001895

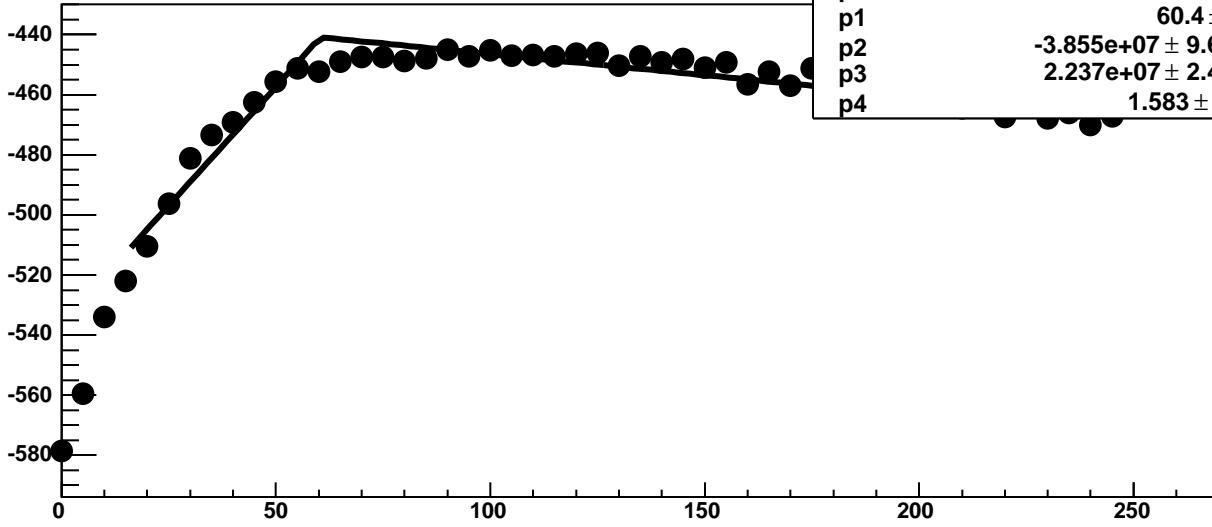
Chip 11, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

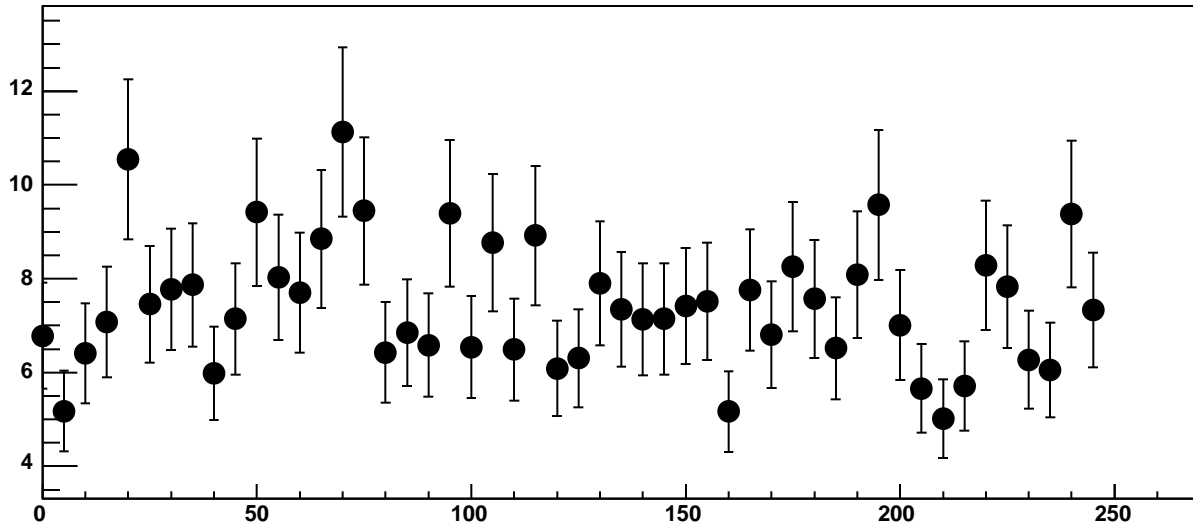


Chip 11, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold

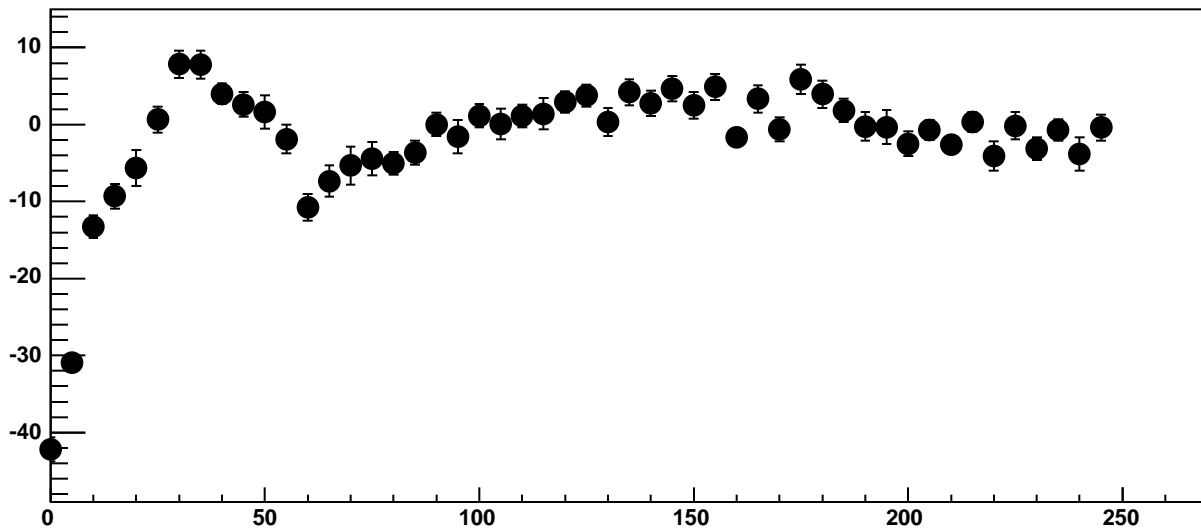


χ^2 / ndf	248.8 / 41
p0	-440.9 ± 0.5413
p1	60.4 ± 0.7072
p2	$-3.855e+07 \pm 9.699e+05$
p3	$2.237e+07 \pm 2.489e+05$
p4	1.583 ± 0.03977

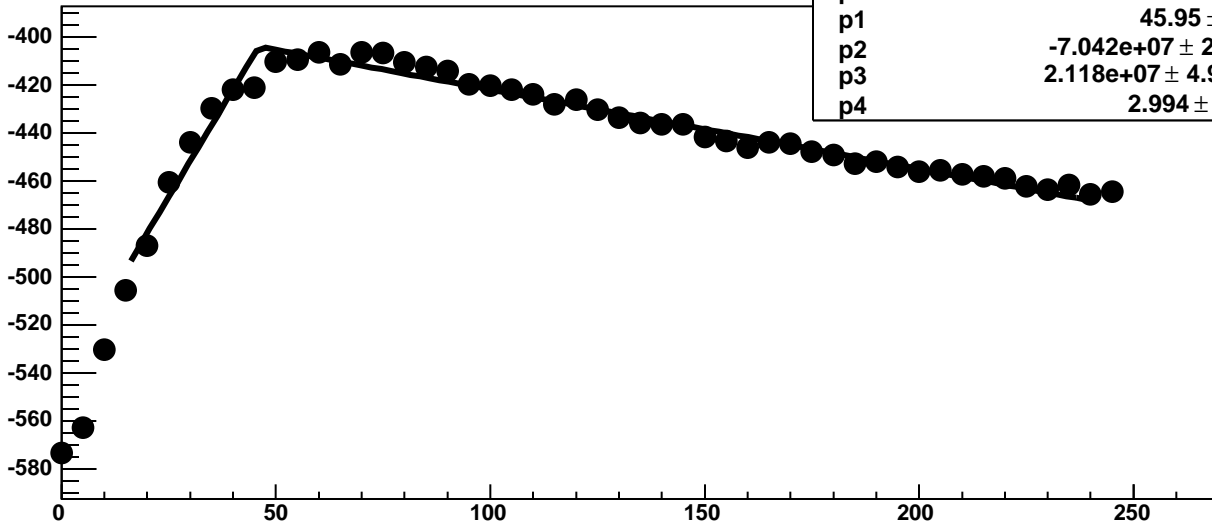
Chip 11, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold

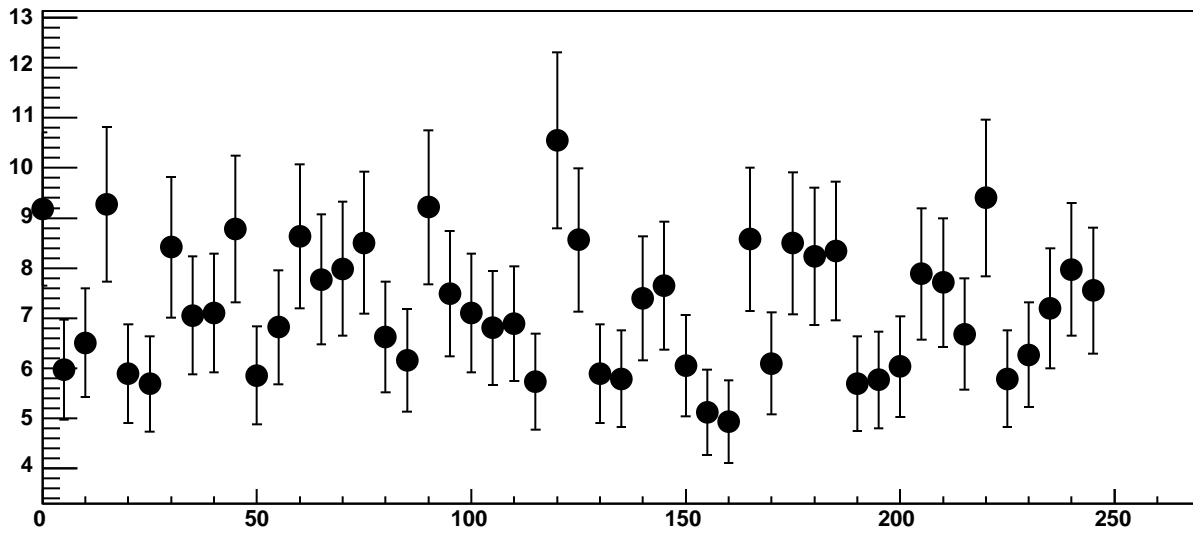


Chip 11, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

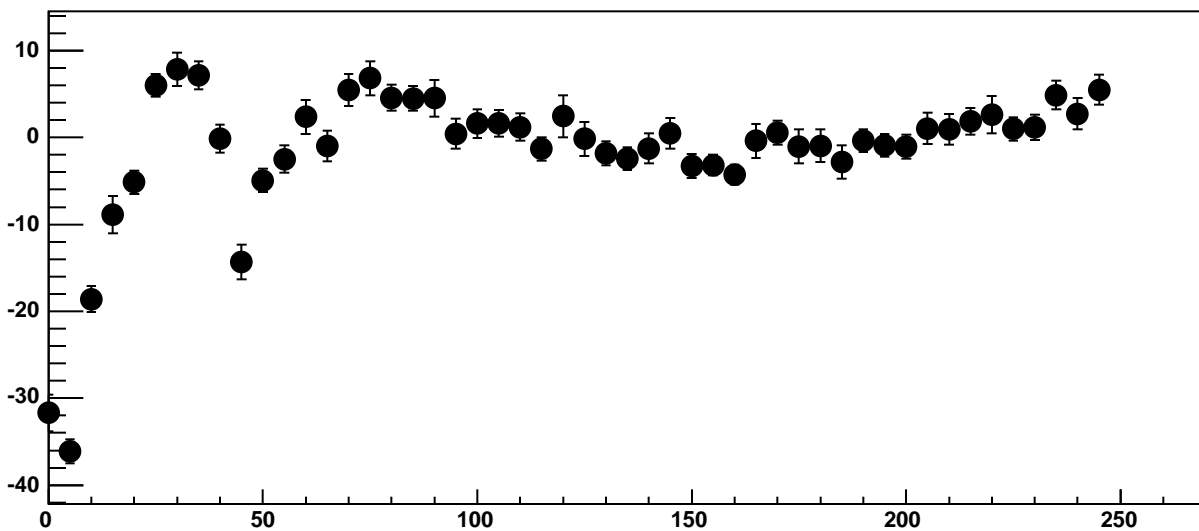


χ^2 / ndf	259.8 / 41
p0	-404 ± 0.4919
p1	45.95 ± 0.4215
p2	-7.042e+07 ± 2.09e+06
p3	2.118e+07 ± 4.933e+05
p4	2.994 ± 0.06763

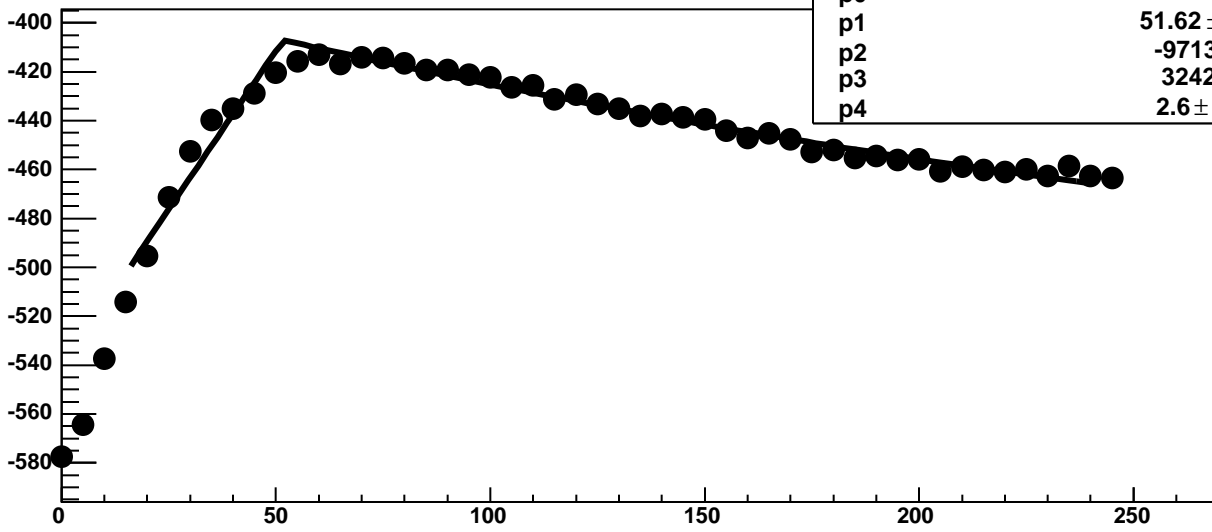
Chip 11, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

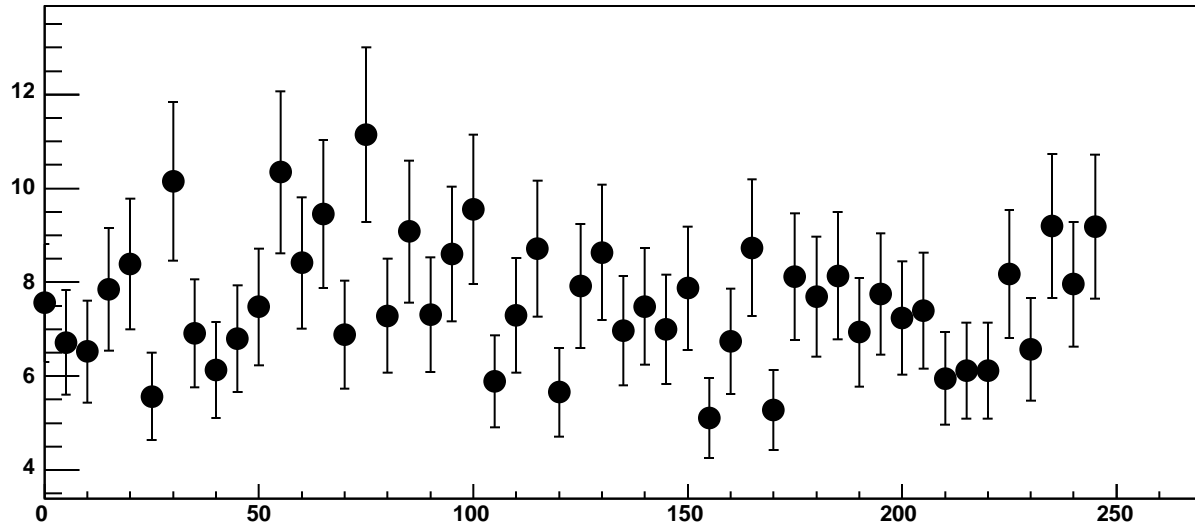


Chip 11, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

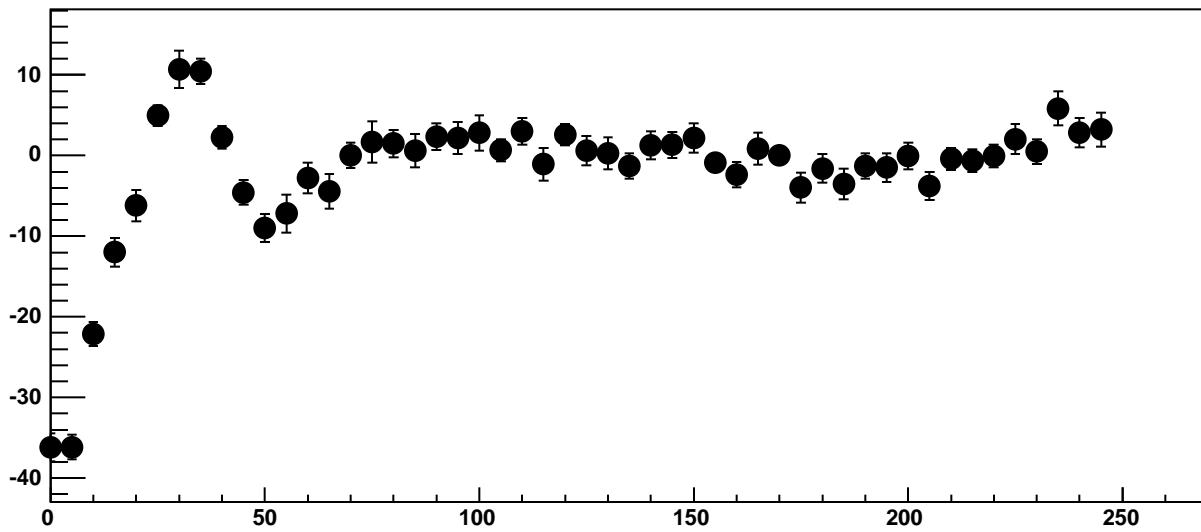


χ^2 / ndf	235.6 / 41
p0	-407 ± 0.6478
p1	51.62 ± 0.4286
p2	-9713 ± 1134
p3	3242 ± 385.7
p4	2.6 ± 0.05184

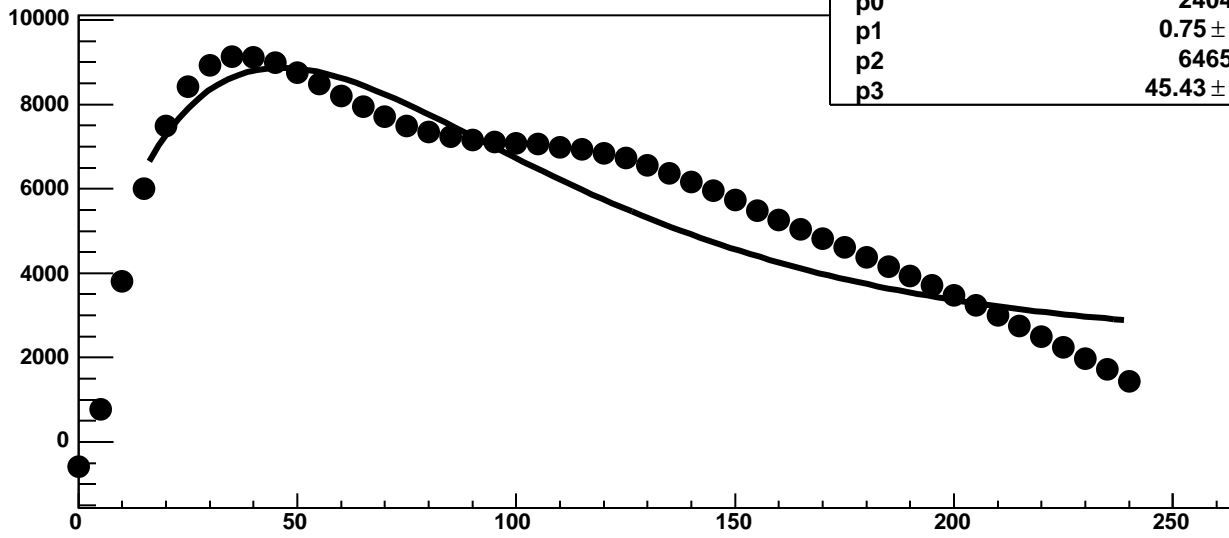
Chip 11, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

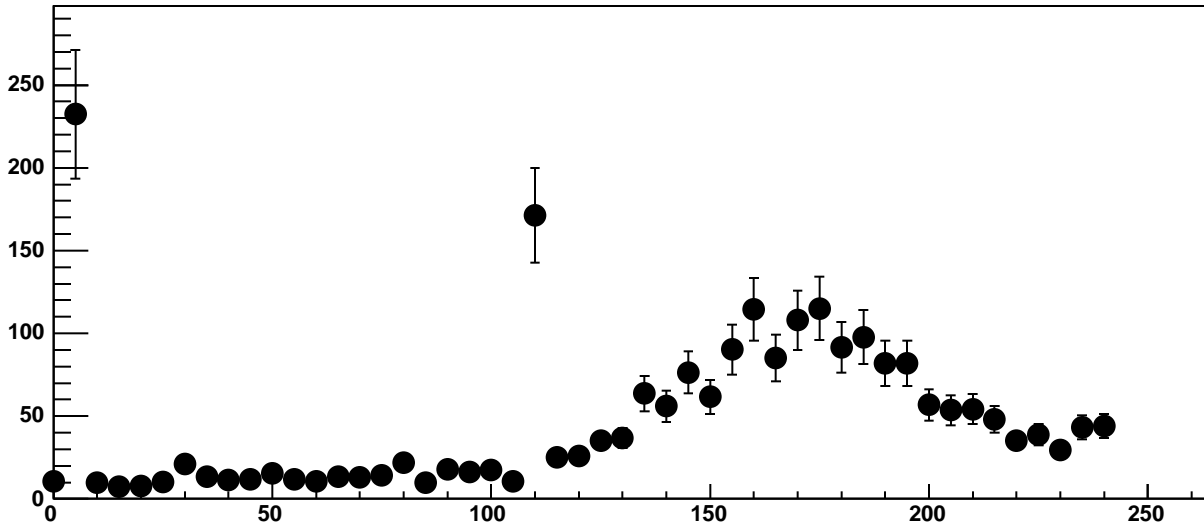


Chip 11, Channel 8, Enable 5!, DAC=4000, ADC Mean vs Hold

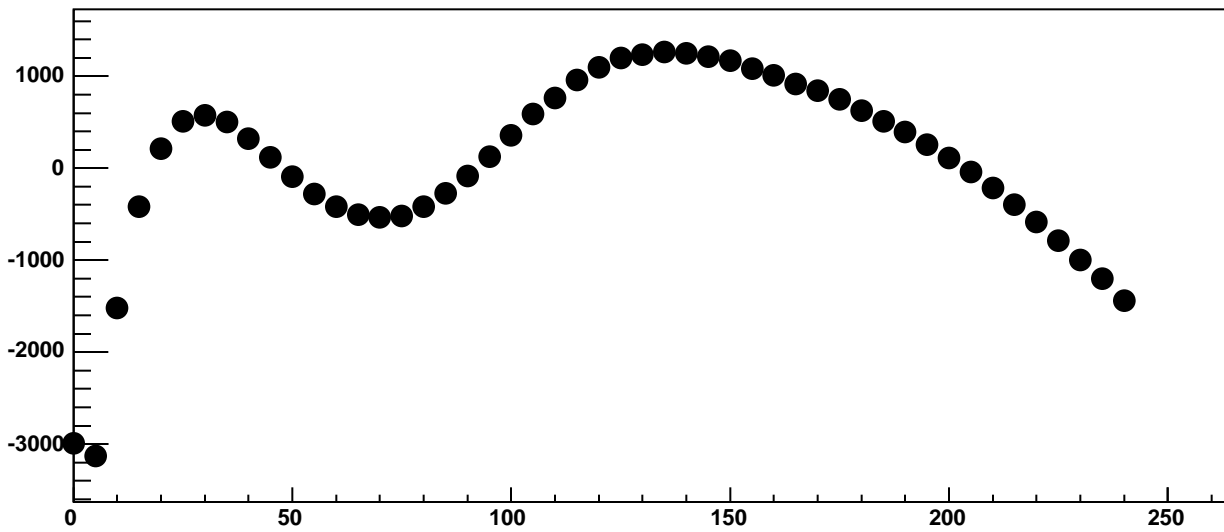


χ^2 / ndf	6.082e+05 / 42
p0	2404 ± 5.228
p1	0.75 ± 0.02734
p2	6465 ± 4.957
p3	45.43 ± 0.03712

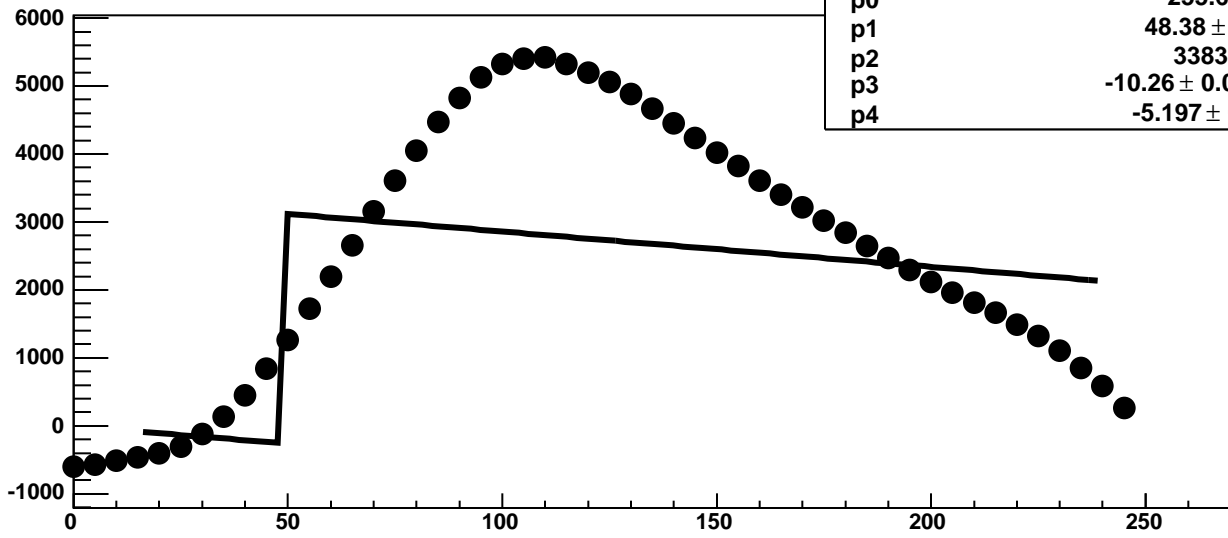
Chip 11, Channel 8, Enable 5!, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 8, Enable 5!, DAC=4000, ADC Residuals vs Hold

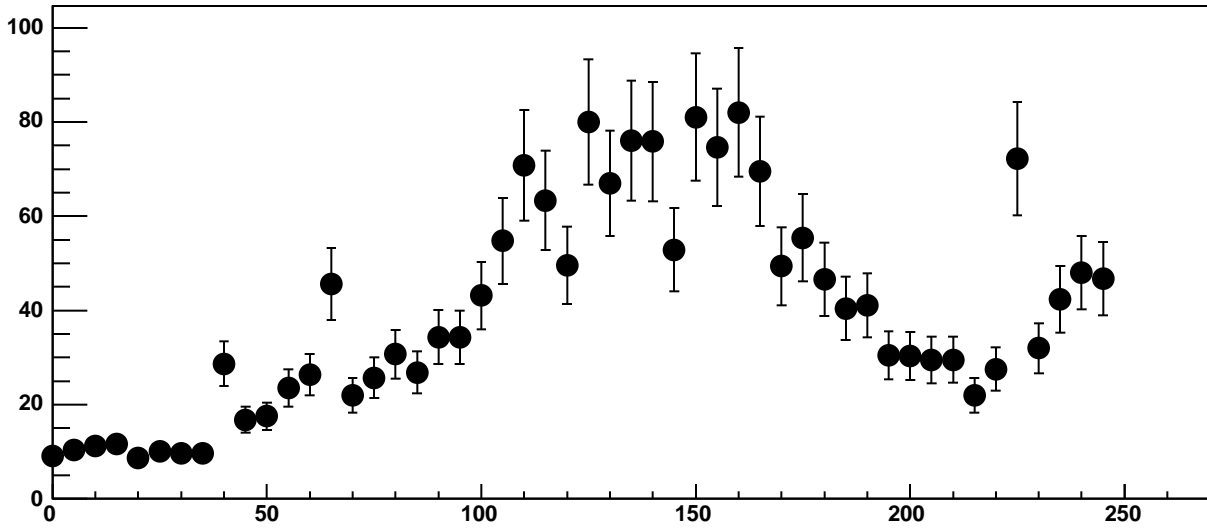


Chip 11, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold

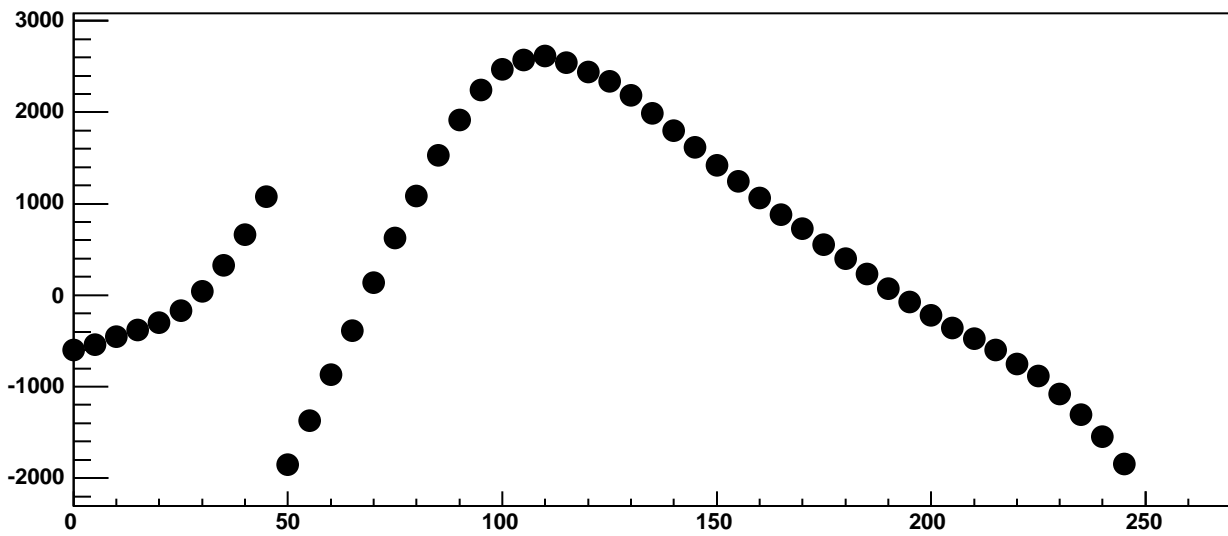


χ^2 / ndf	1.104e+06 / 41
p0	-255.6 ± 1.246
p1	48.38 ± 0.01411
p2	3383 ± 2.718
p3	-10.26 ± 0.0001084
p4	-5.197 ± 0.01953

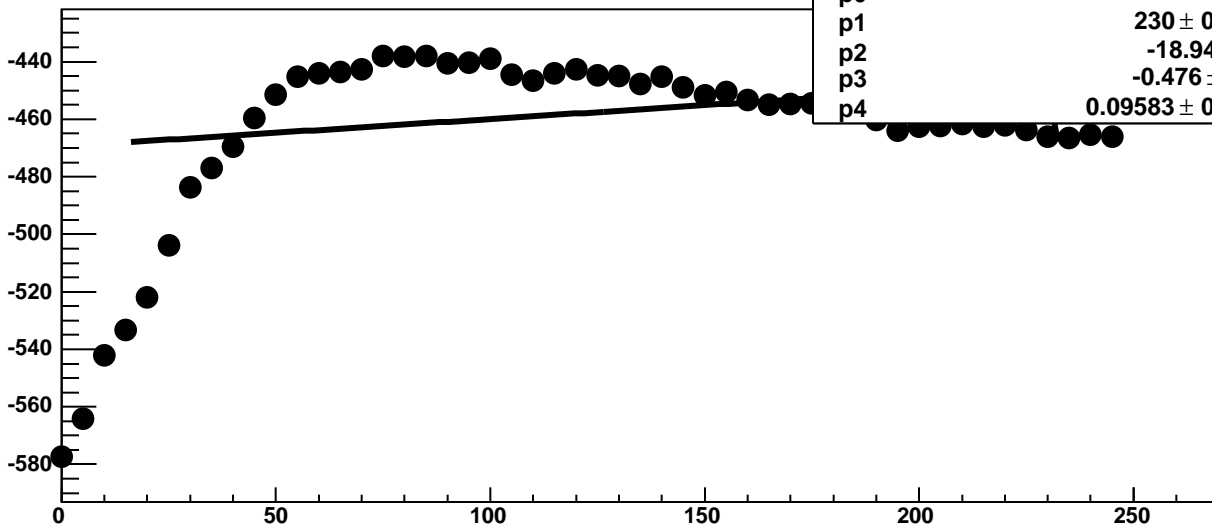
Chip 11, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold



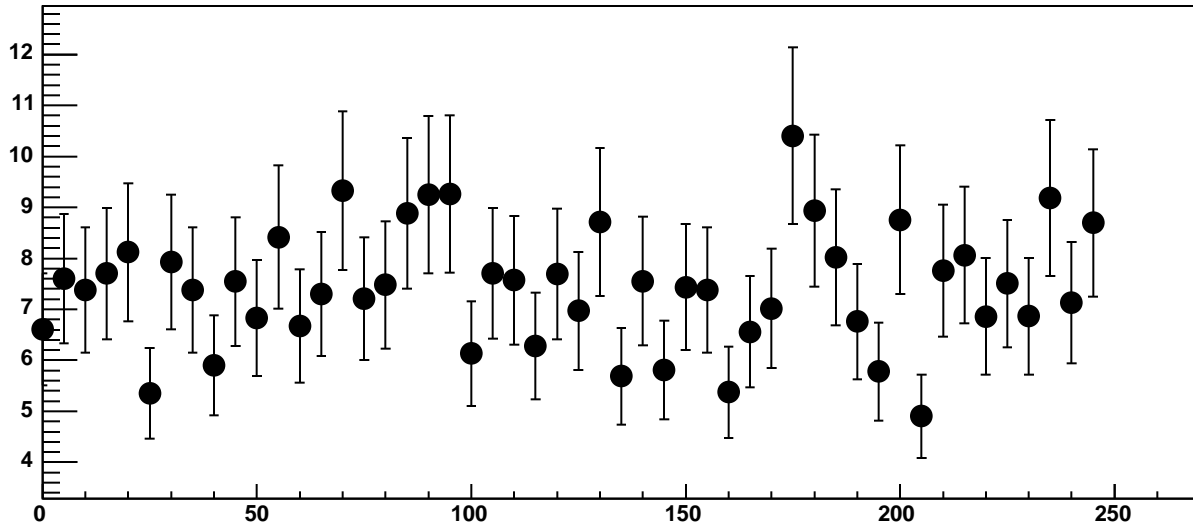
Chip 11, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold



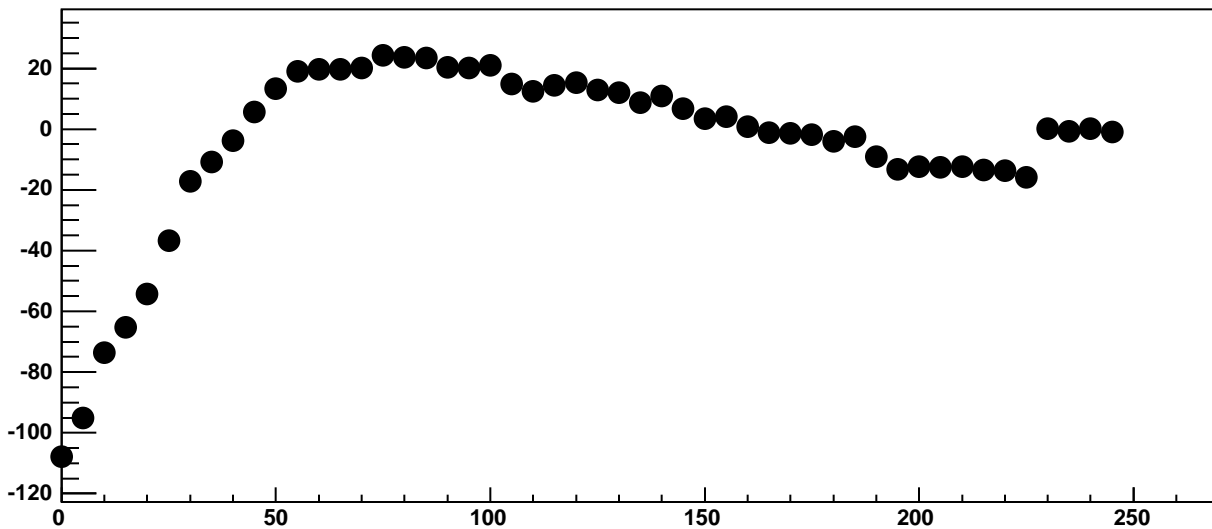
Chip 11, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold



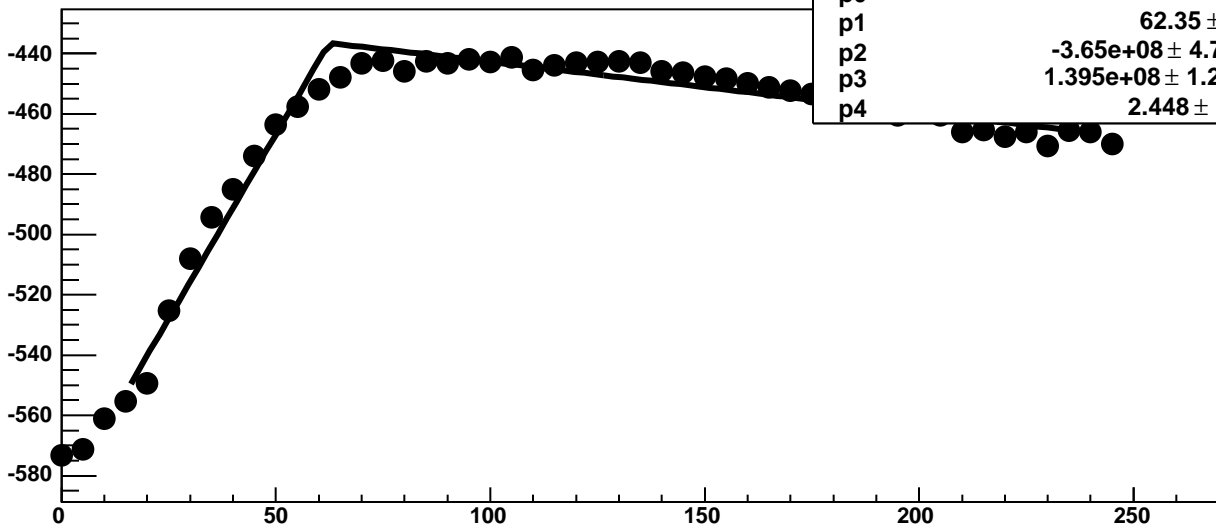
Chip 11, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

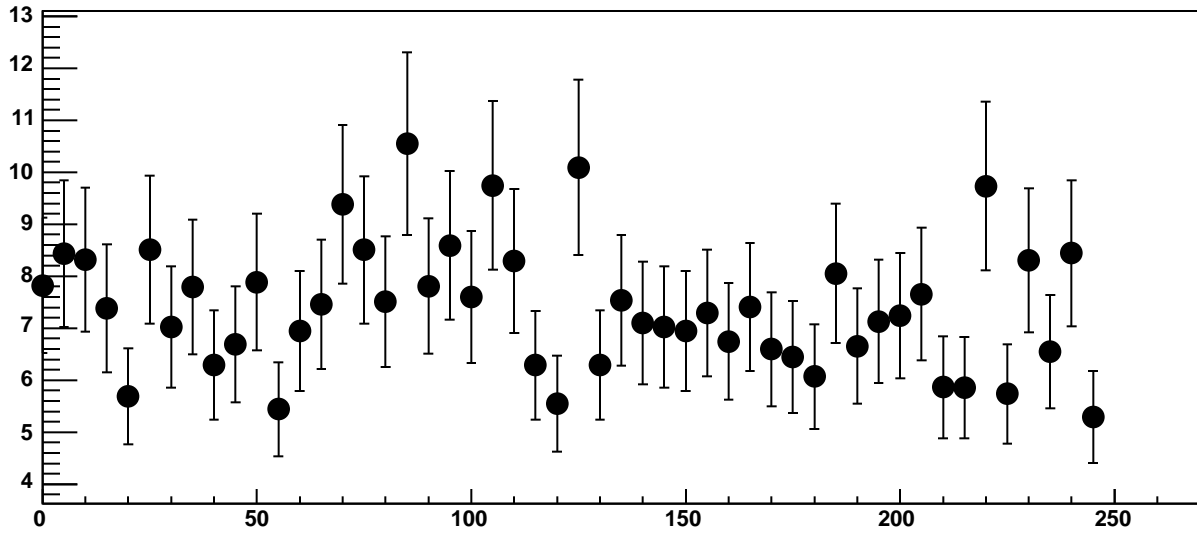


Chip 11, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

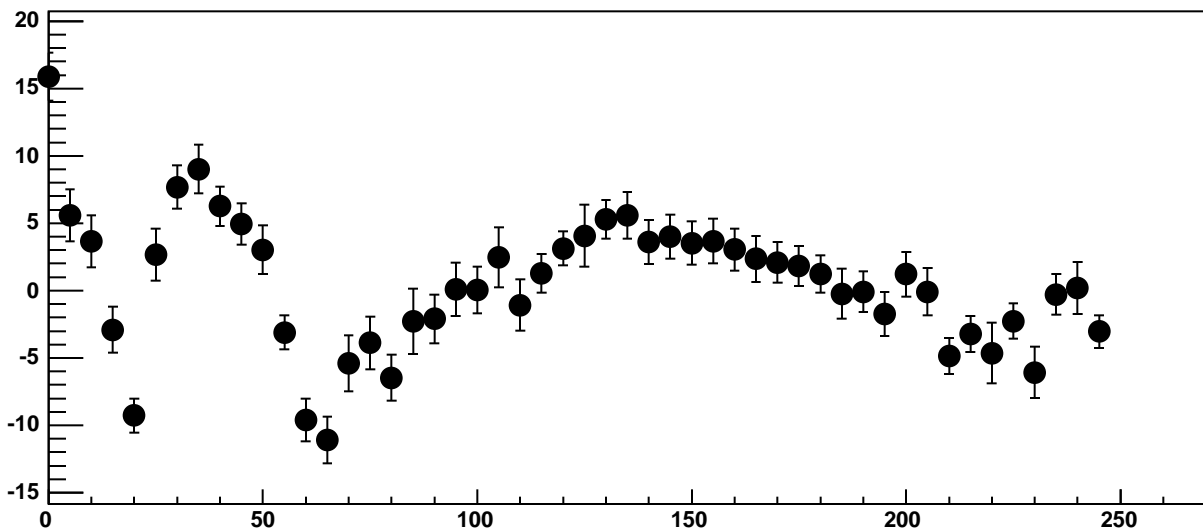


χ^2 / ndf	353.6 / 41
p0	-436.5 ± 0.5631
p1	62.35 ± 0.4265
p2	$-3.65\text{e}+08 \pm 4.706\text{e}+06$
p3	$1.395\text{e}+08 \pm 1.286\text{e}+05$
p4	2.448 ± 0.03332

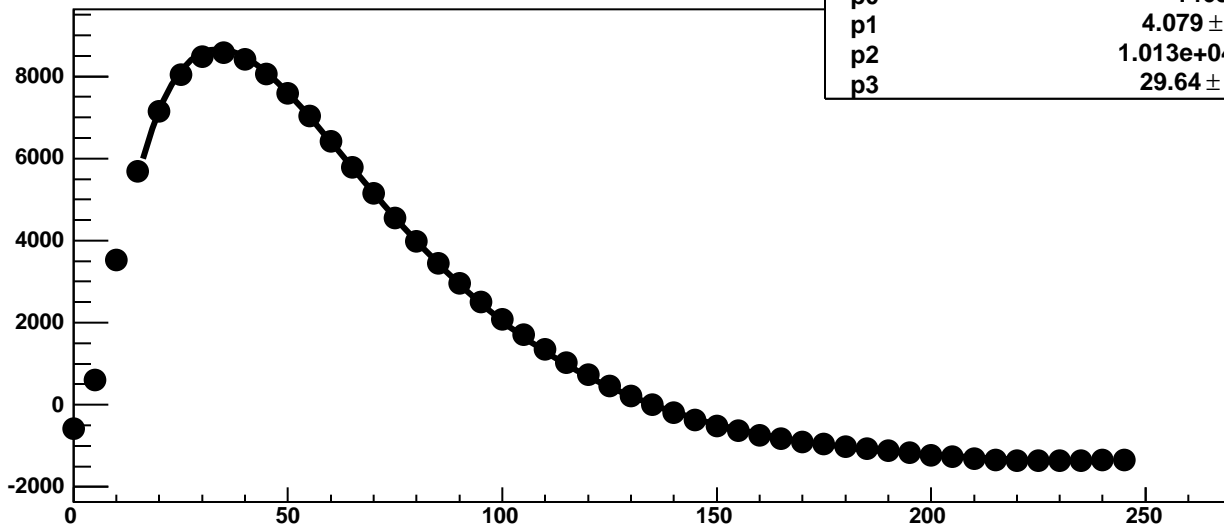
Chip 11, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold

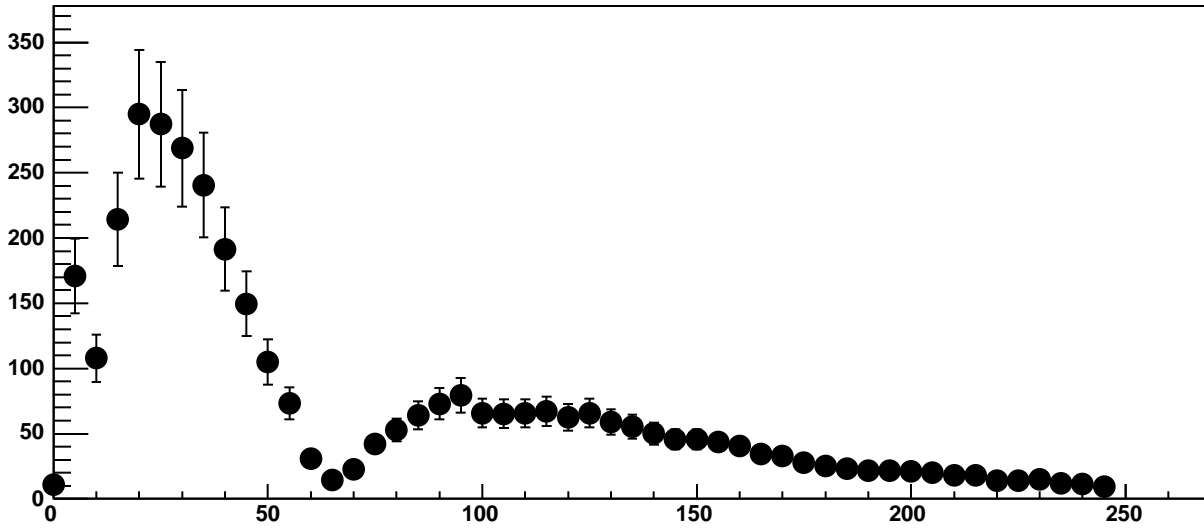


Chip 11, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold

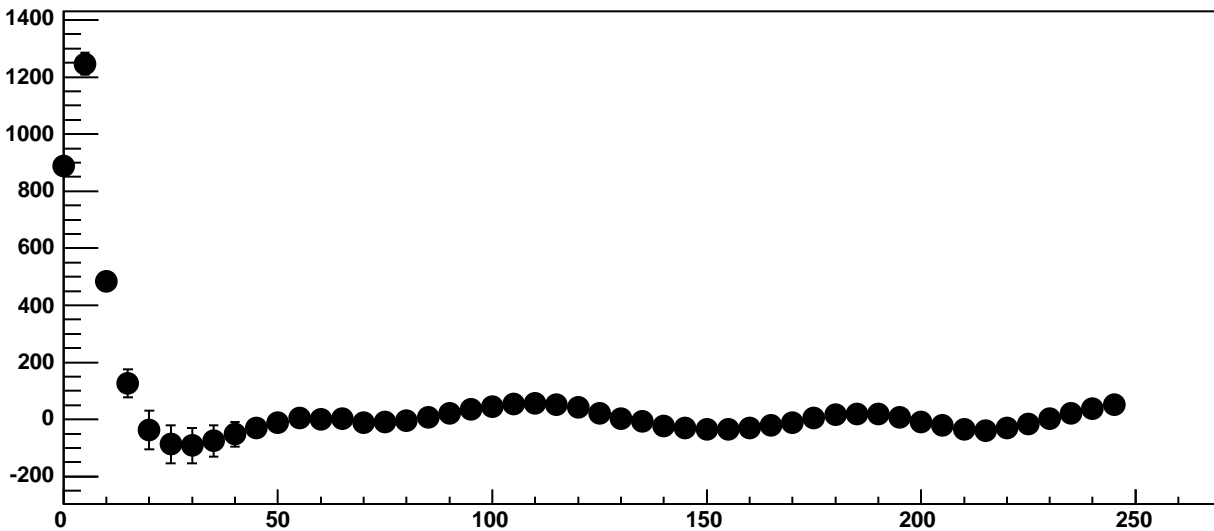


χ^2 / ndf	744.4 / 42
p0	-1463 ± 1.494
p1	4.079 ± 0.09001
p2	1.013e+04 ± 11.6
p3	29.64 ± 0.03503

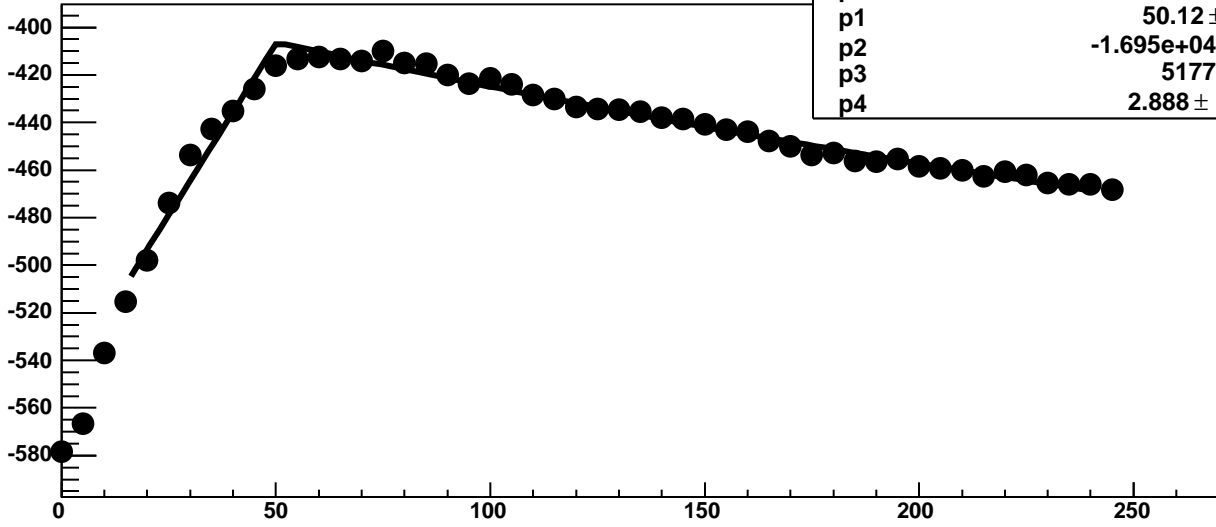
Chip 11, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold

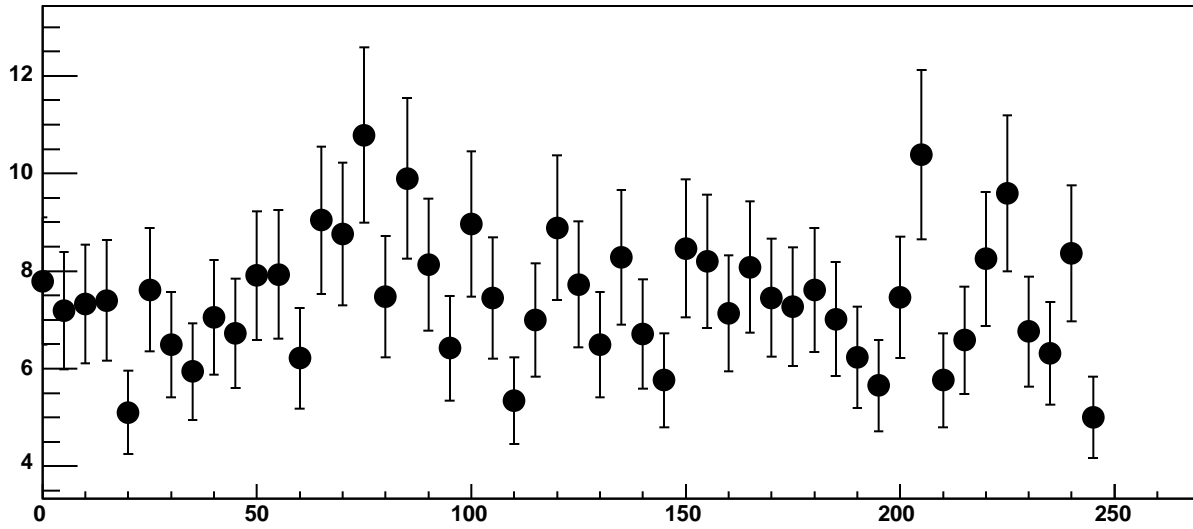


Chip 11, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

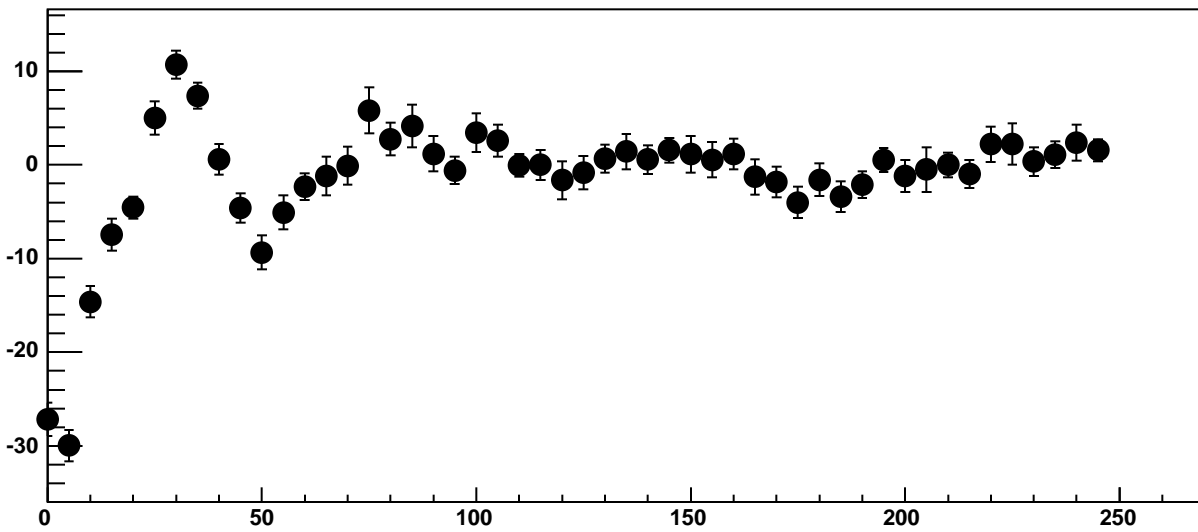


χ^2 / ndf	211.9 / 41
p0	-406.4 ± 0.585
p1	50.12 ± 0.3697
p2	-1.695e+04 ± 2063
p3	5177 ± 638.5
p4	2.888 ± 0.04874

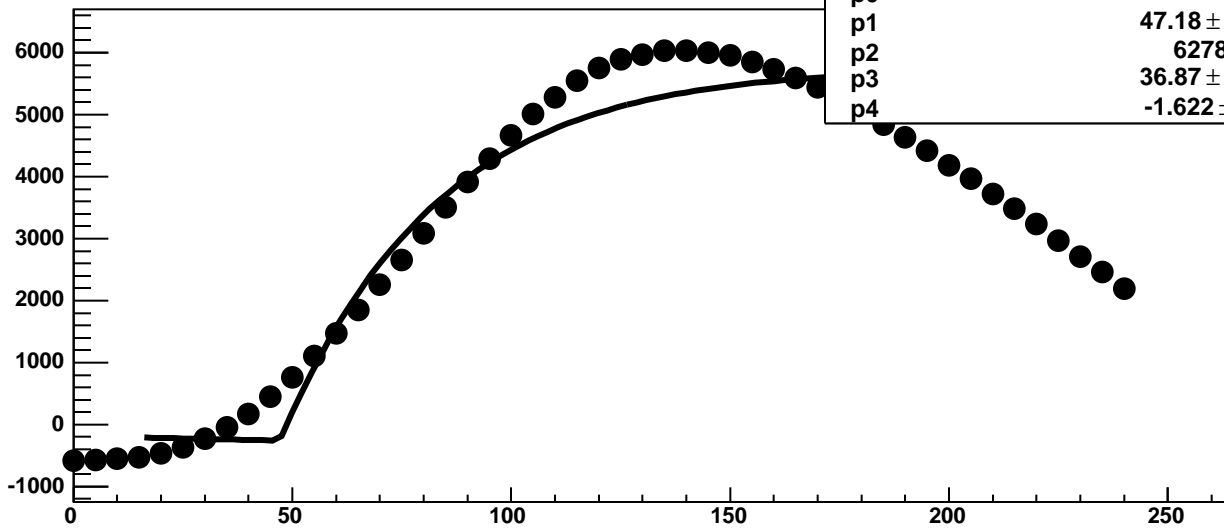
Chip 11, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

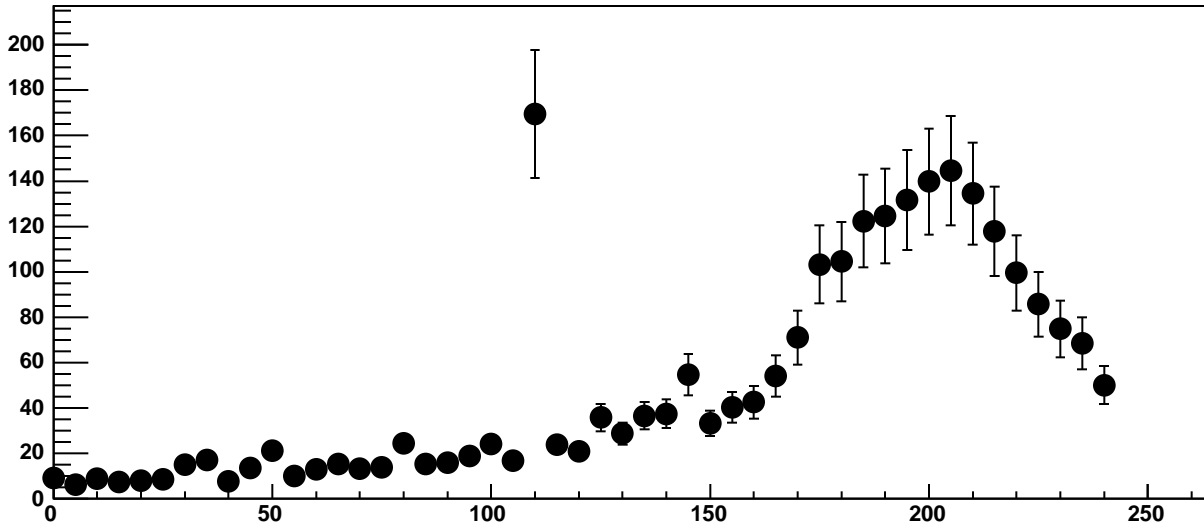


Chip 11, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold

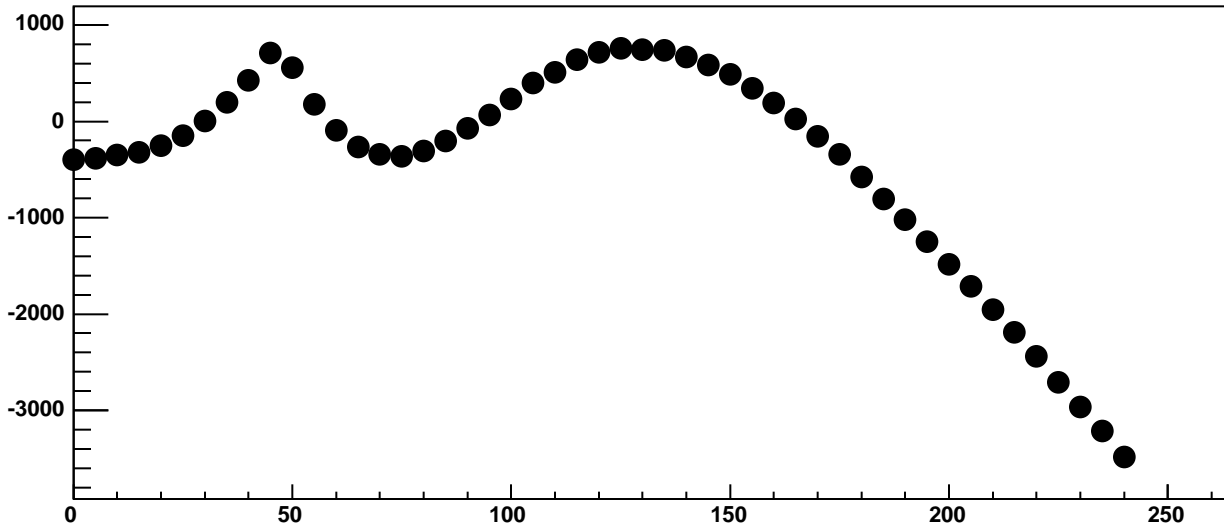


χ^2 / ndf	5.306e+05 / 41
p0	-258.9 ± 1.336
p1	47.18 ± 0.01814
p2	6278 ± 8.438
p3	36.87 ± 0.05917
p4	-1.622 ± 0.0535

Chip 11, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold



Chip 11, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold

χ^2 / ndf

2.666e+07 / 42

p0

-1.348e+04 ± 0.6181

p1

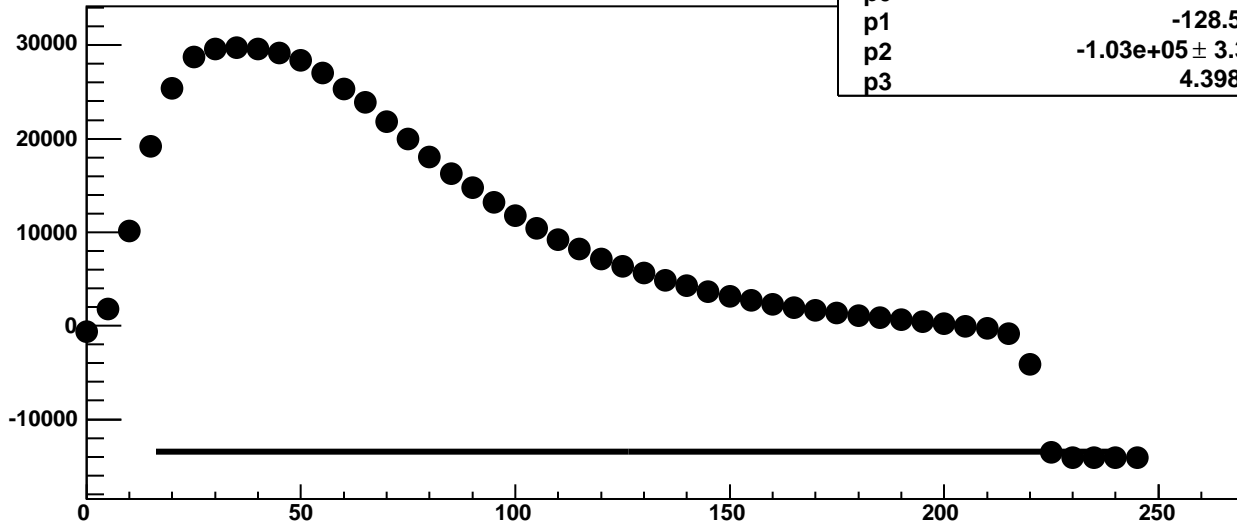
-128.5 ± 1702

p2

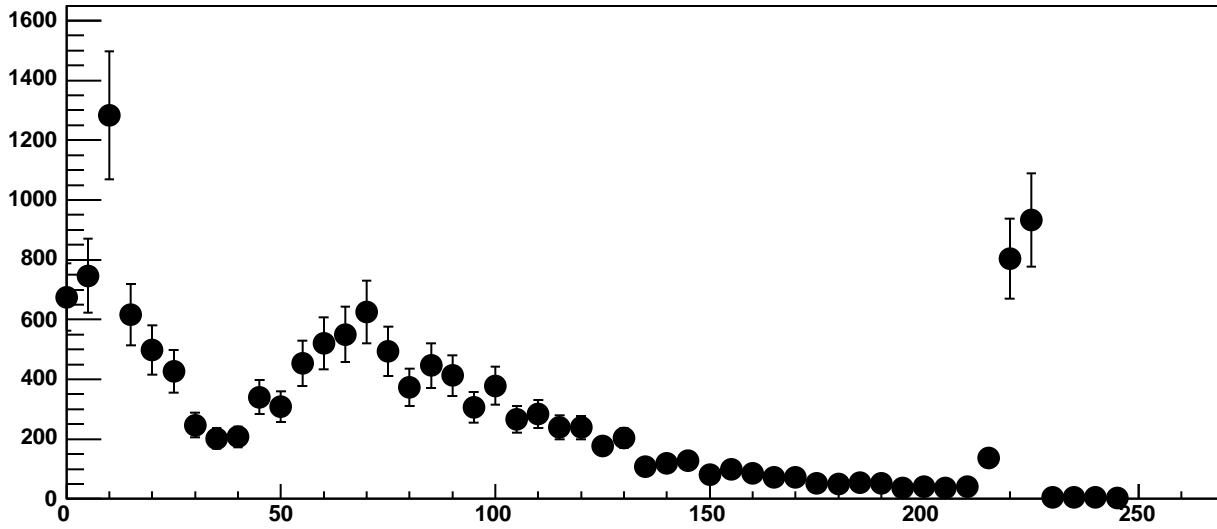
-1.03e+05 ± 3.384e+08

p3

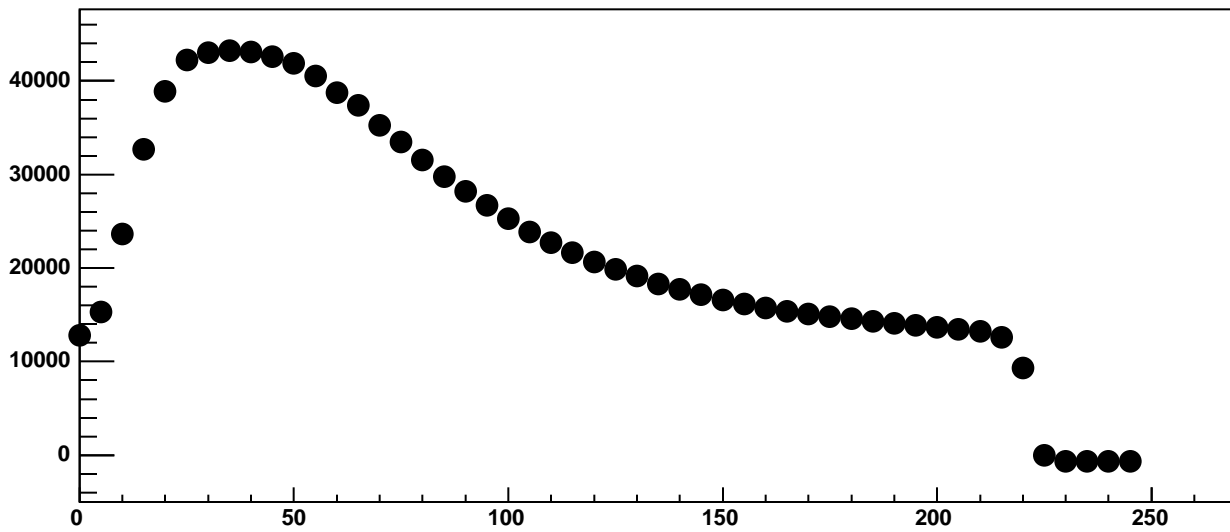
4.398 ± 23.23



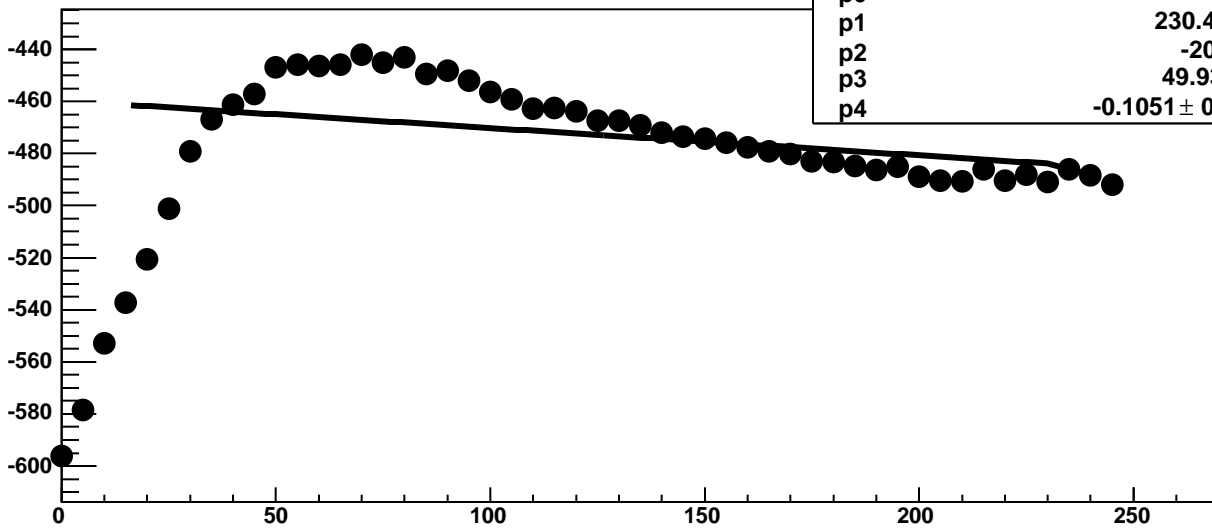
Chip 11, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold

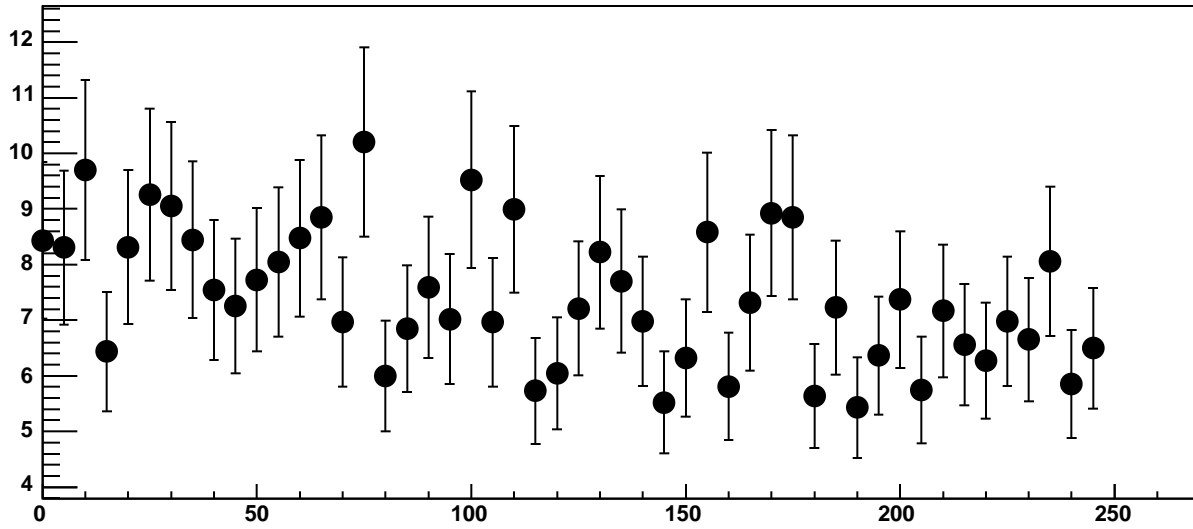


Chip 11, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

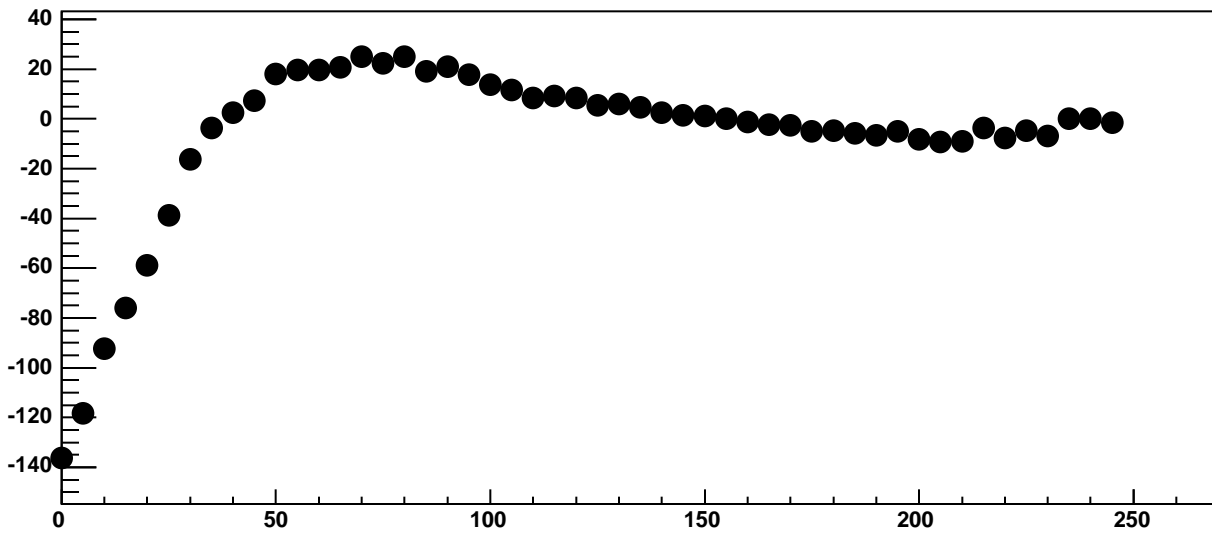


χ^2 / ndf	5996 / 41
p0	-483.9 ± 1.191
p1	230.4 ± 10.04
p2	-20 ± 19.68
p3	49.93 ± 8.86
p4	-0.1051 ± 0.003807

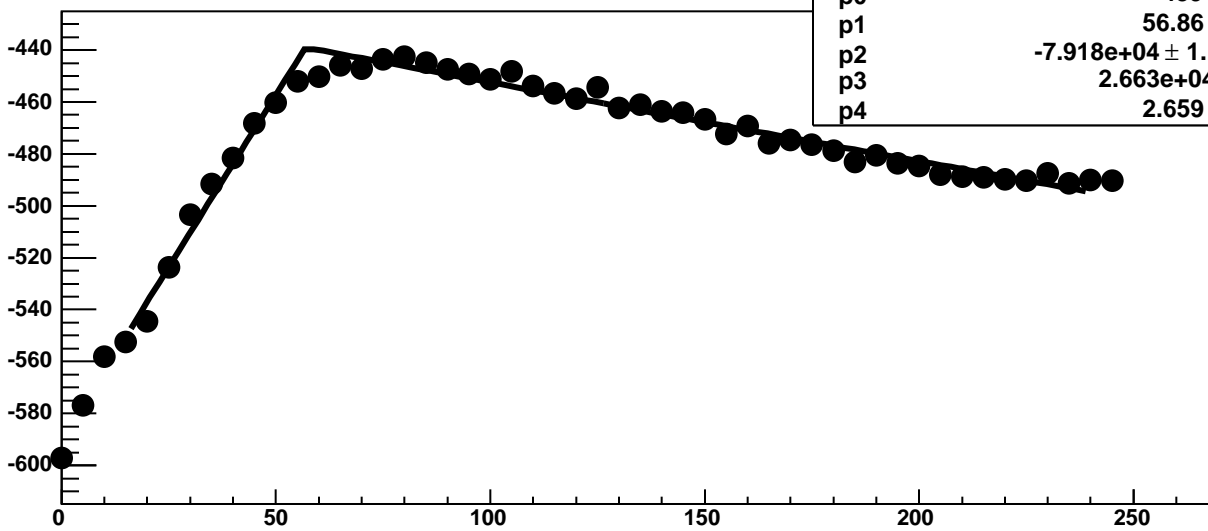
Chip 11, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

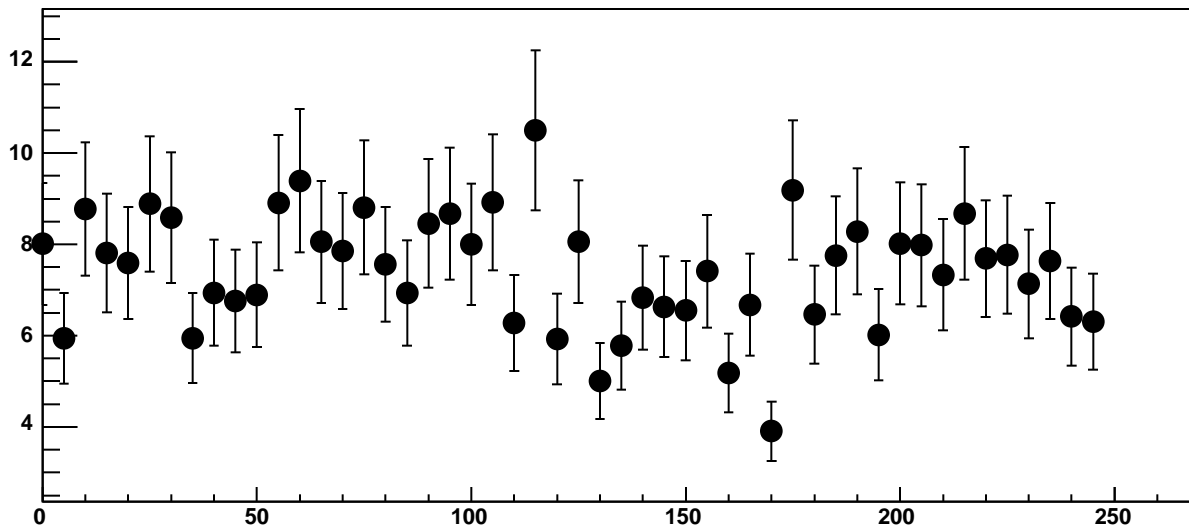


Chip 11, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

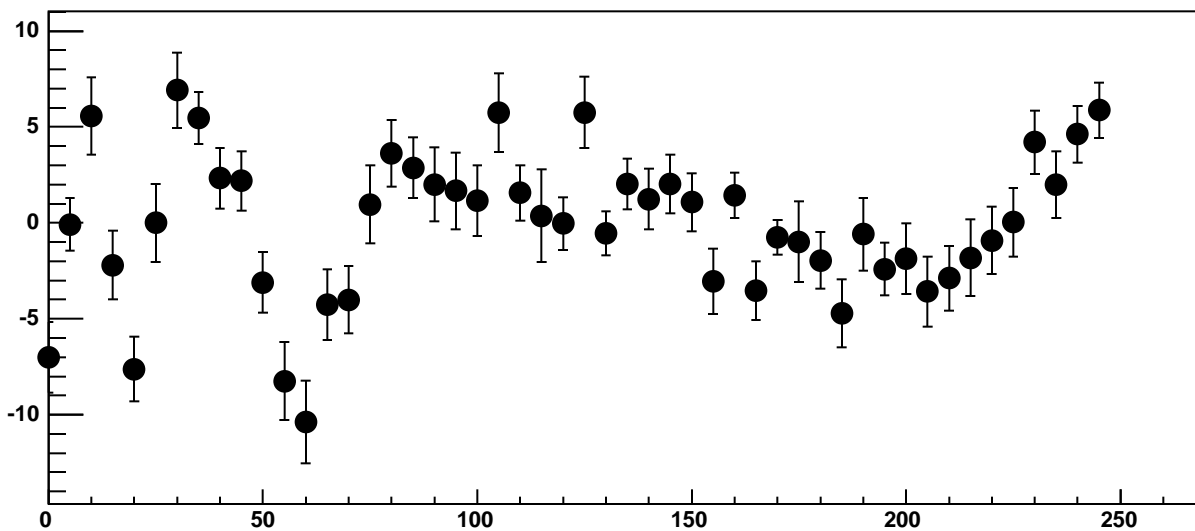


χ^2 / ndf	191.8 / 41
p0	-439 ± 0.5383
p1	56.86 ± 0.4272
p2	$-7.918\text{e}+04 \pm 1.125\text{e}+04$
p3	$2.663\text{e}+04 \pm 3789$
p4	2.659 ± 0.0462

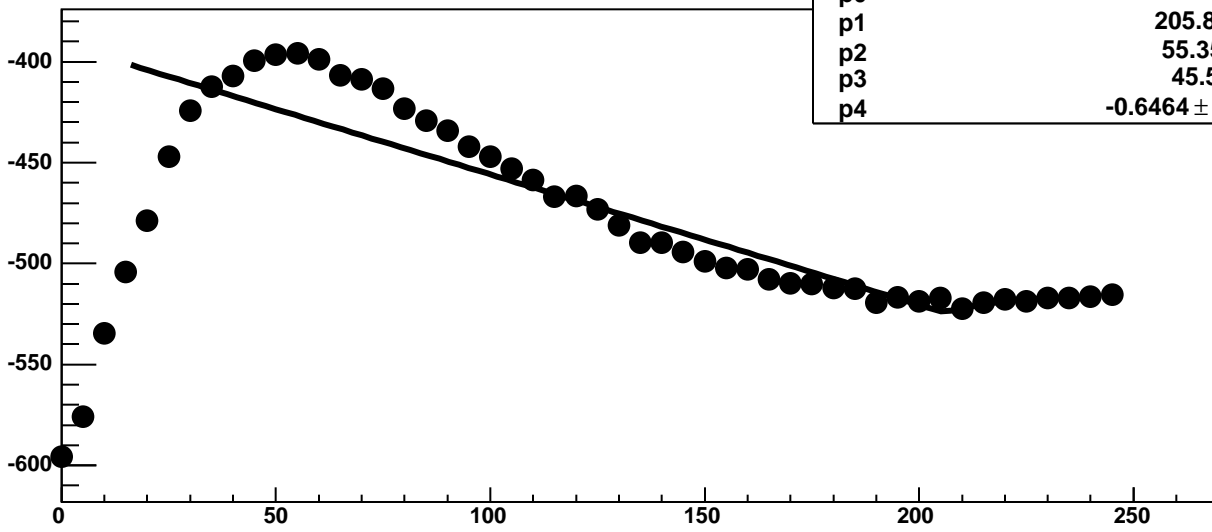
Chip 11, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

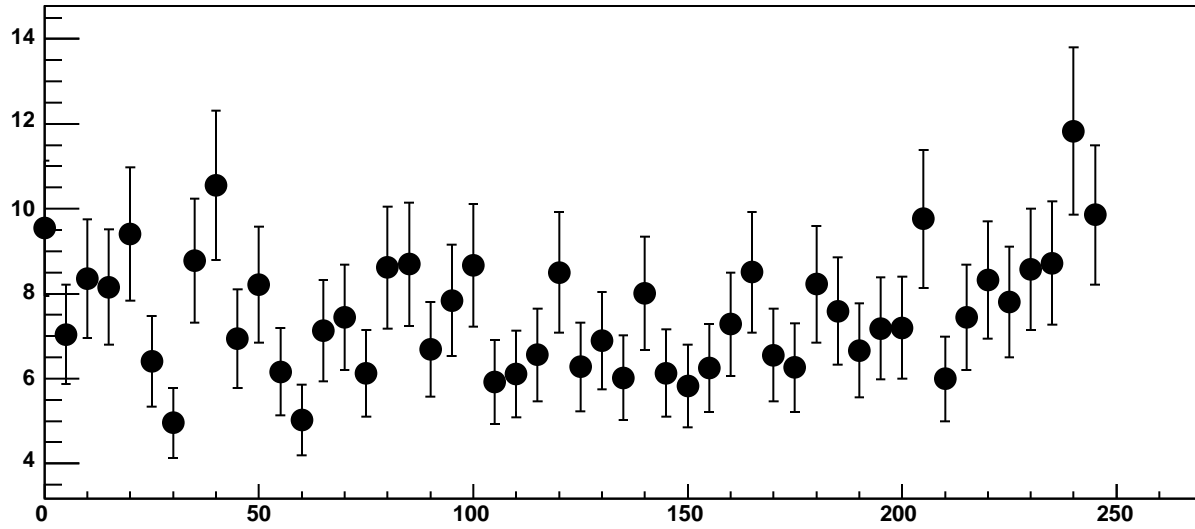


Chip 11, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold

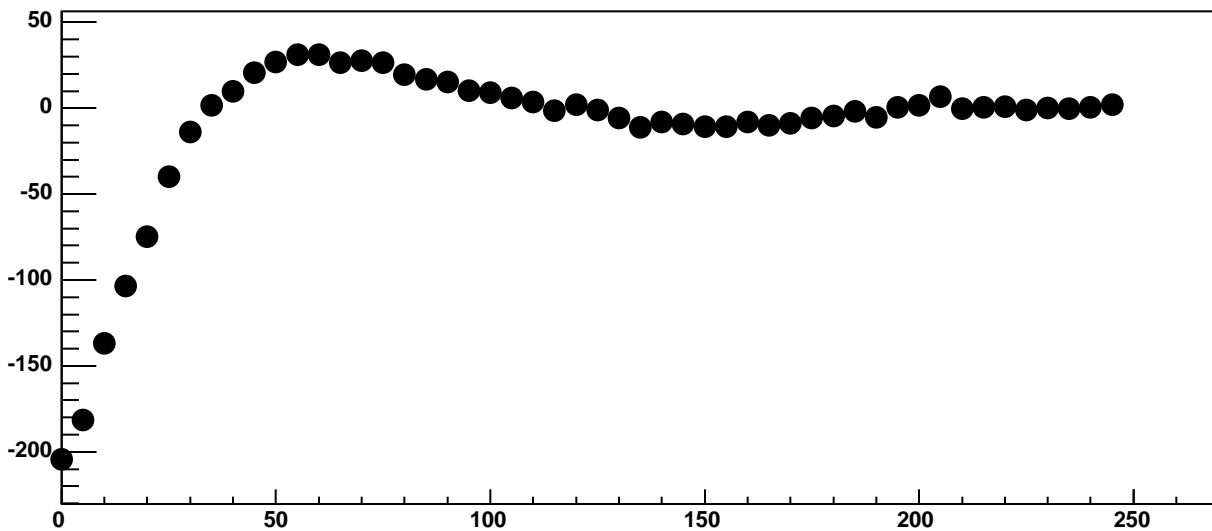


χ^2 / ndf	8362 / 41
p0	-524.2 ± 1.095
p1	205.8 ± 1.699
p2	55.35 ± 24.8
p3	45.5 ± 29.31
p4	-0.6464 ± 0.00468

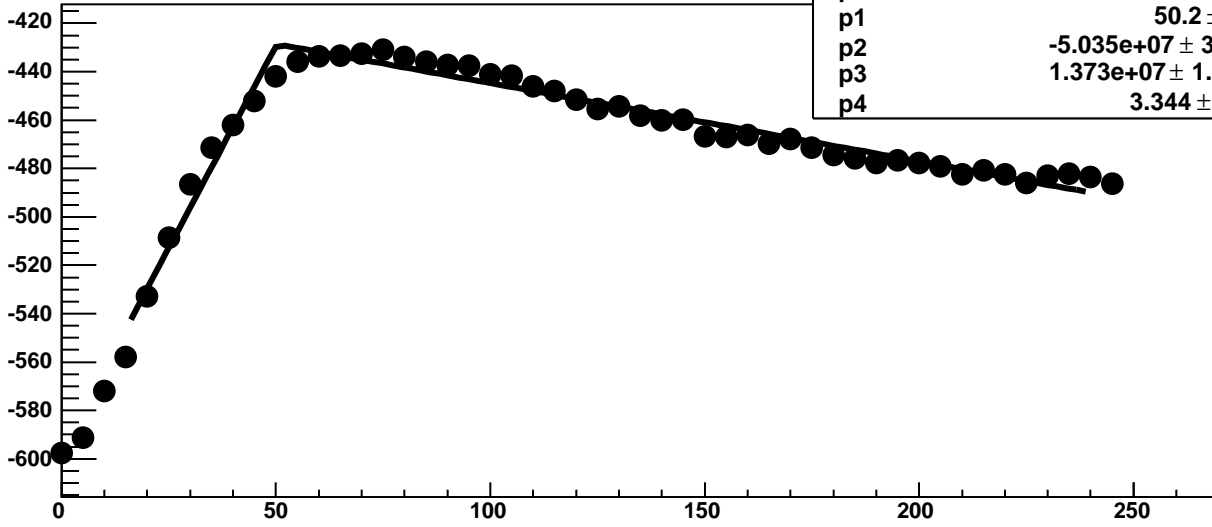
Chip 11, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold

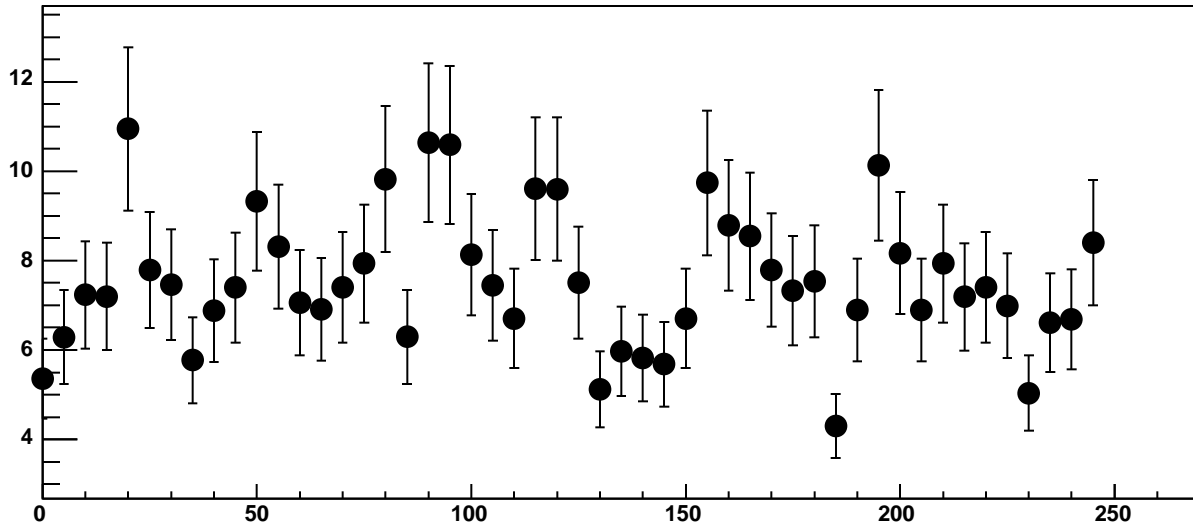


Chip 11, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

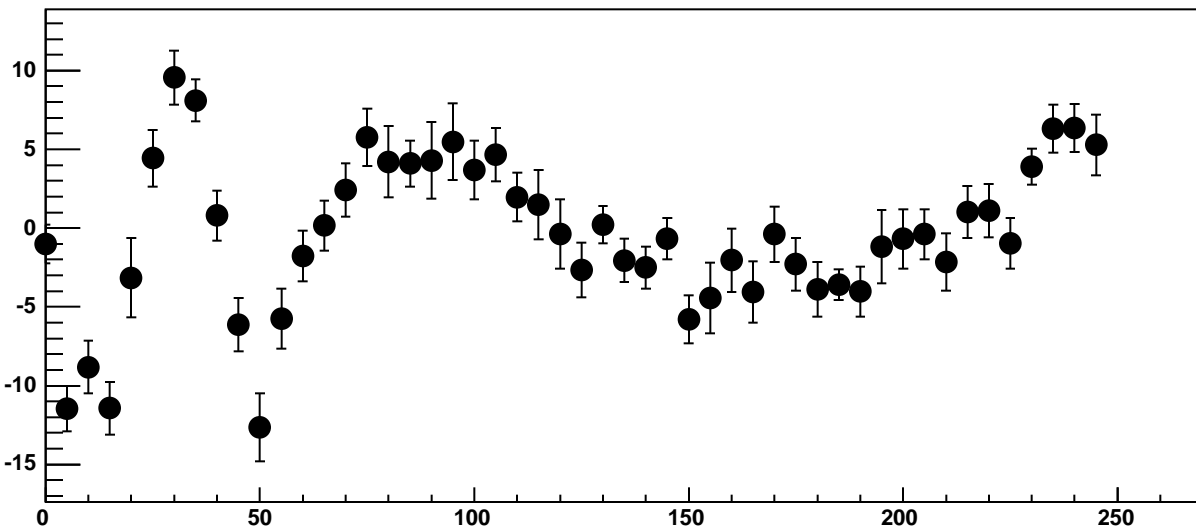


χ^2 / ndf	335.3 / 41
p0	-428.6 ± 0.5132
p1	50.2 ± 0.2605
p2	$-5.035\text{e}+07 \pm 3.97\text{e}+05$
p3	$1.373\text{e}+07 \pm 1.111\text{e}+05$
p4	3.344 ± 0.02641

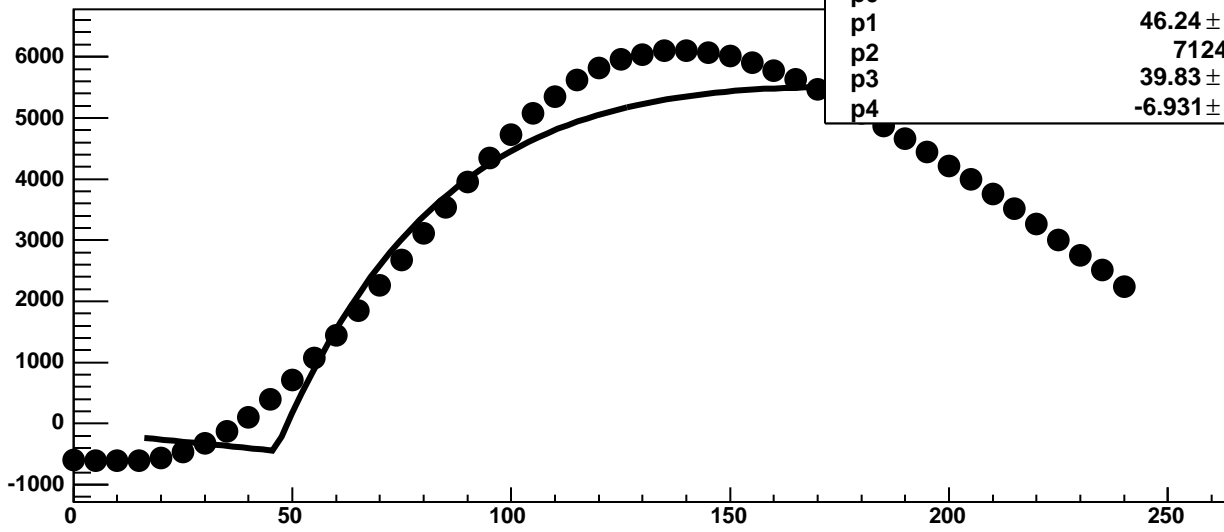
Chip 11, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



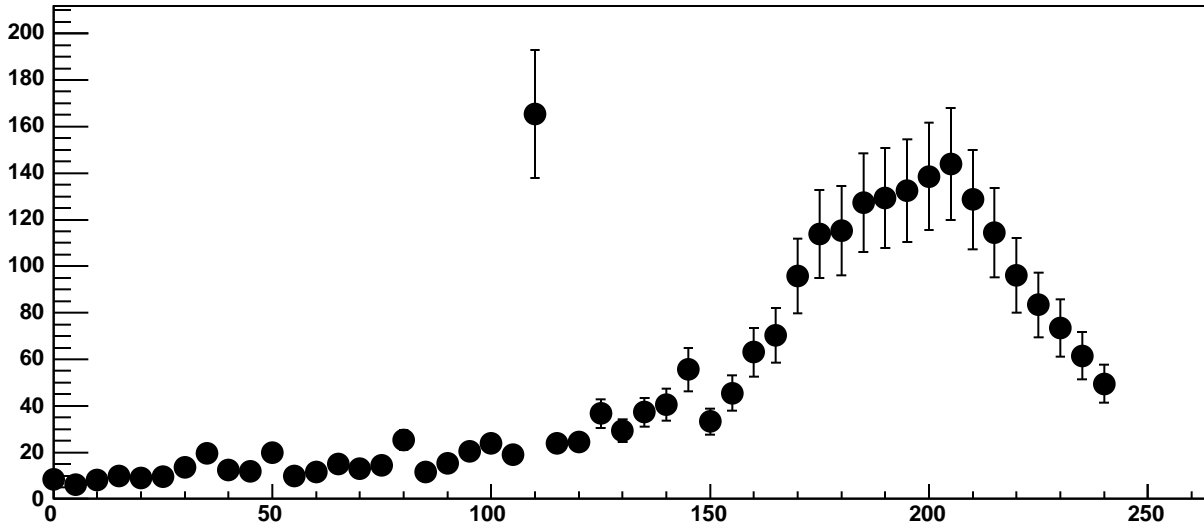
Chip 11, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold



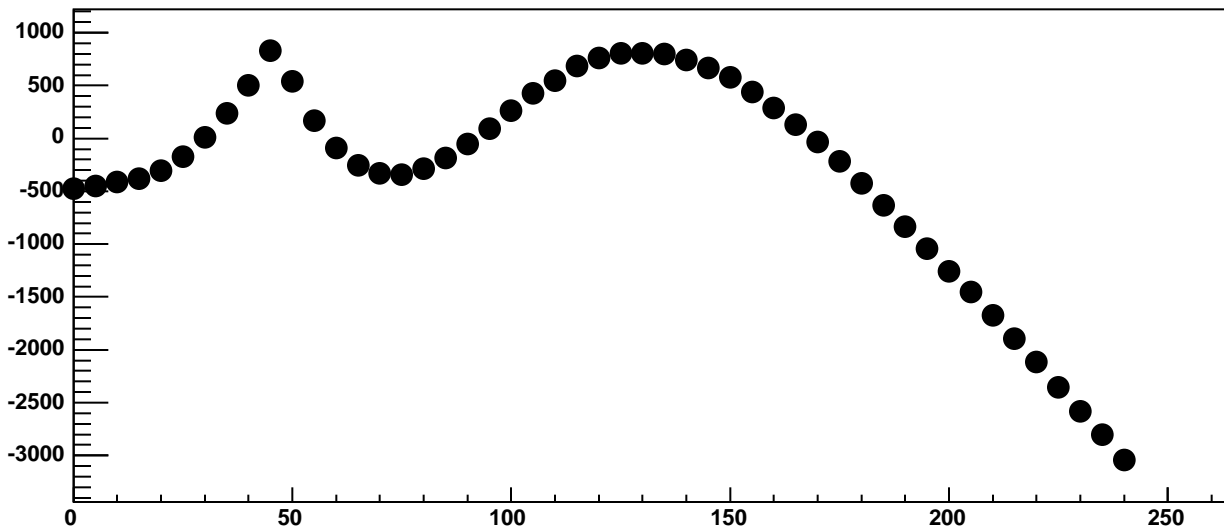
Chip 11, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold



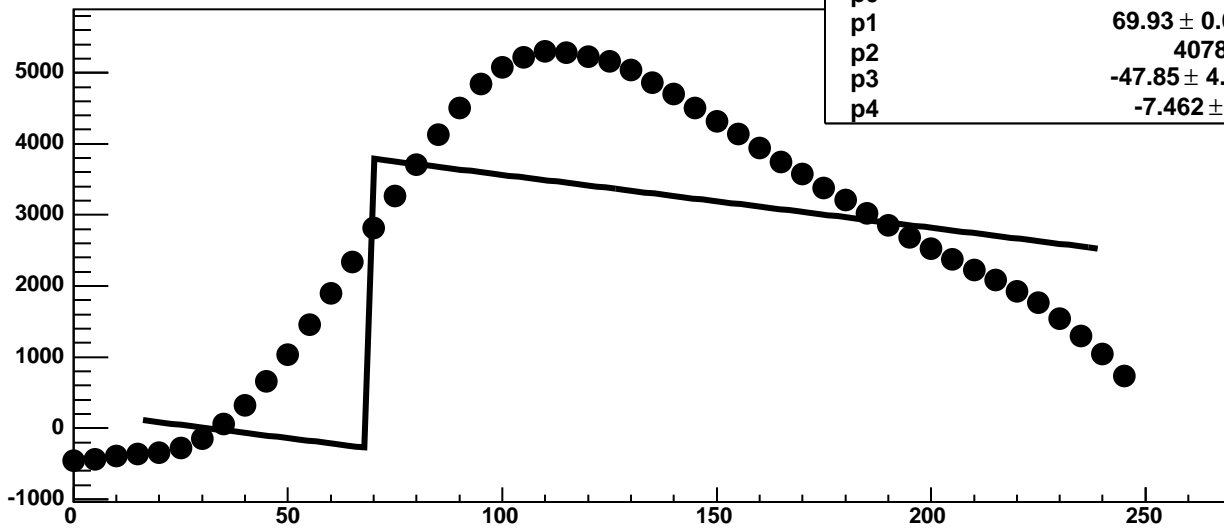
Chip 11, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

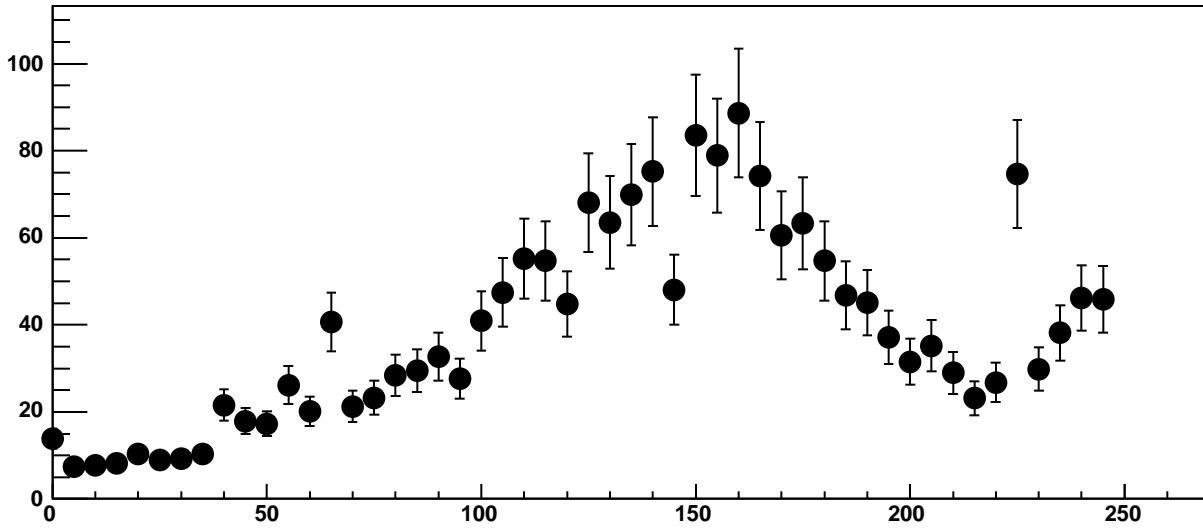


Chip 11, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold

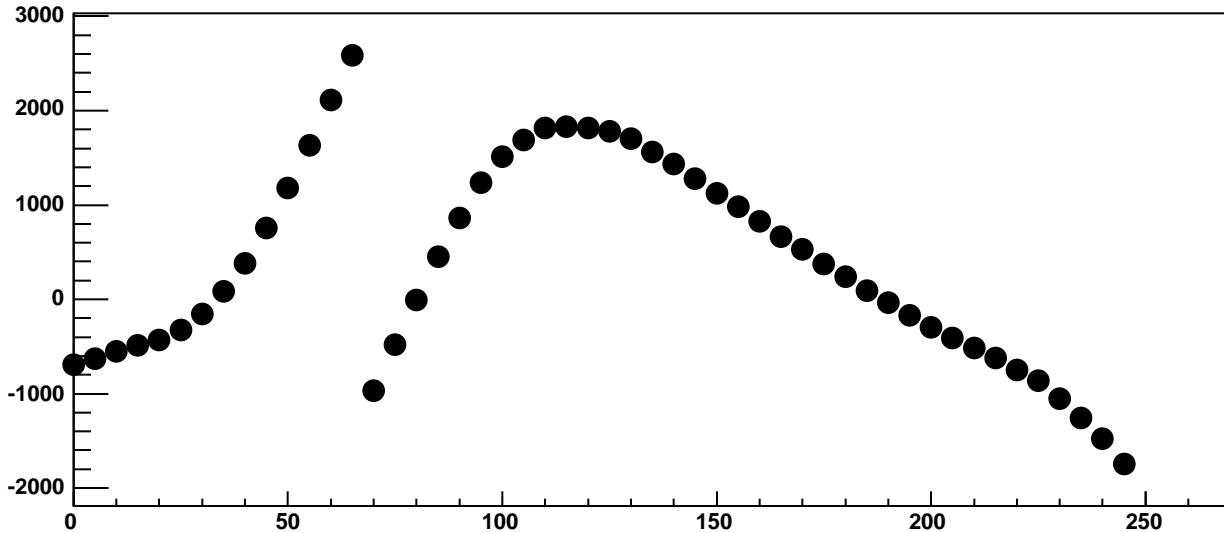


χ^2 / ndf	1.022e+06 / 41
p0	-288.9 ± 1.48
p1	69.93 ± 0.0001792
p2	4078 ± 3.439
p3	-47.85 ± 4.888e-07
p4	-7.462 ± 0.02531

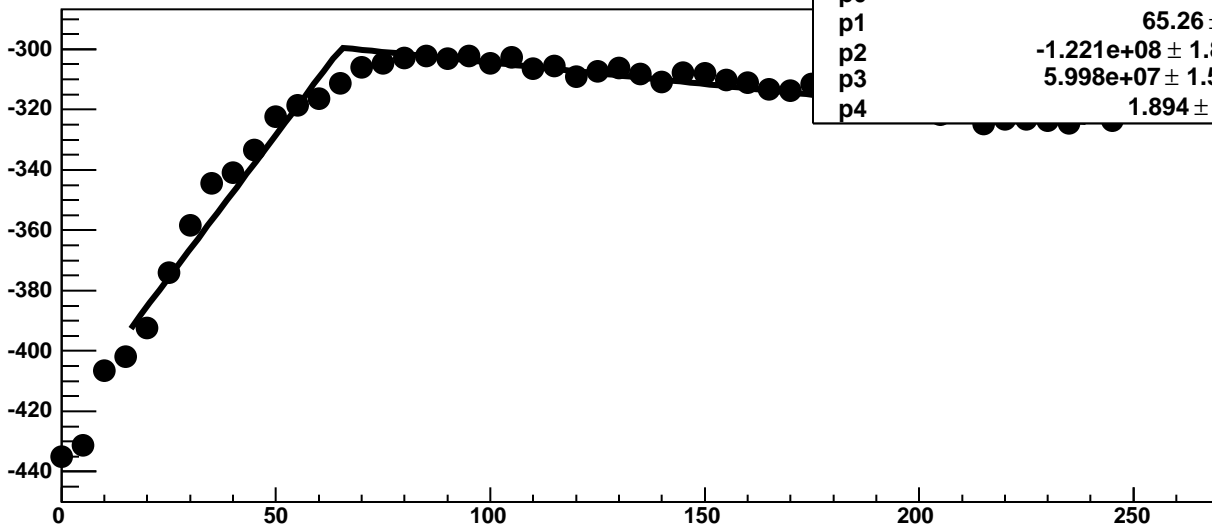
Chip 11, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold

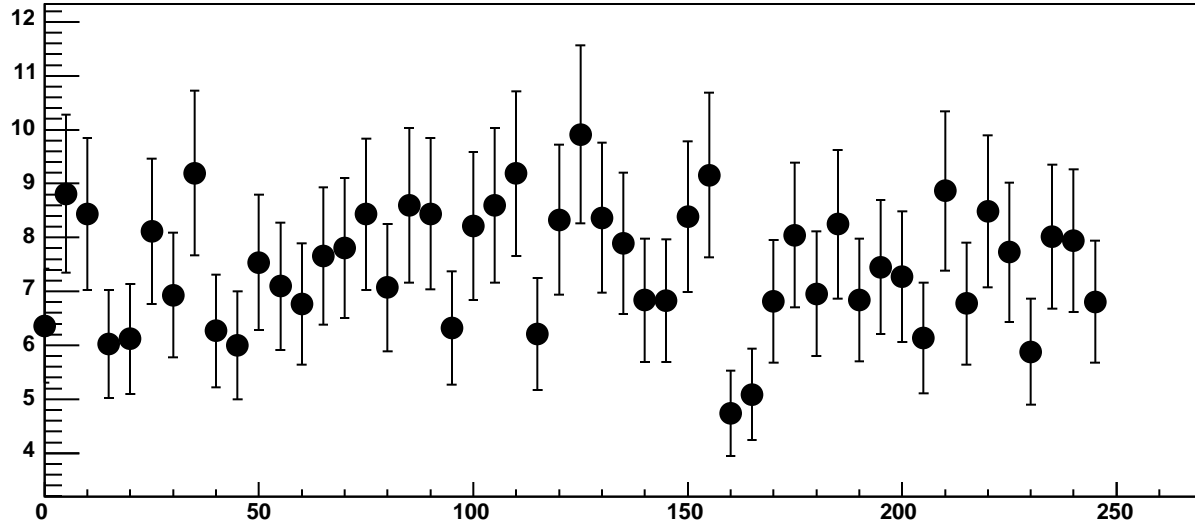


Chip 11, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

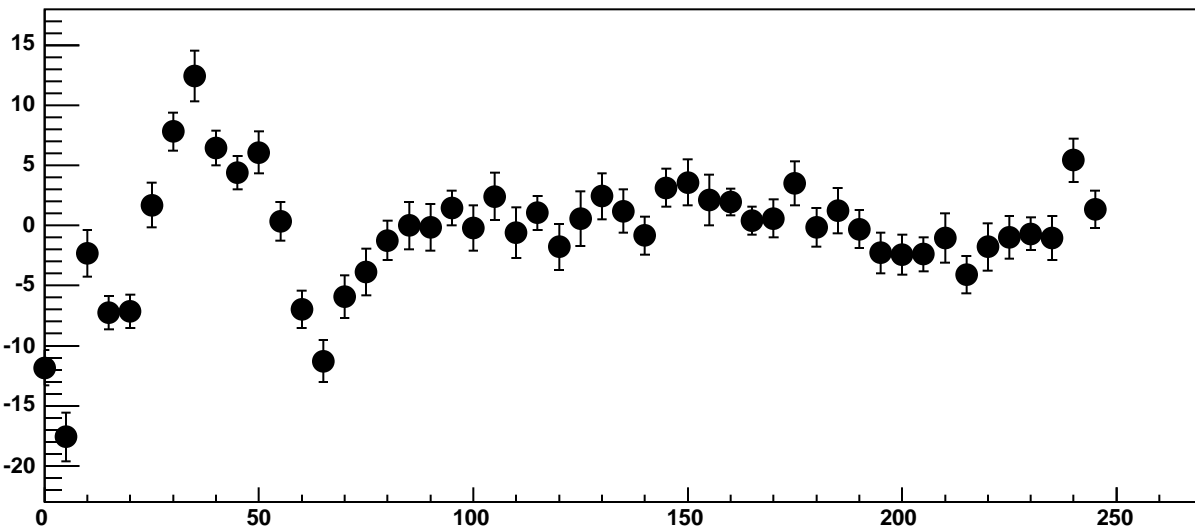


χ^2 / ndf	279.7 / 41
p0	-299.5 ± 0.5704
p1	65.26 ± 0.5382
p2	$-1.221\text{e}+08 \pm 1.837\text{e}+06$
p3	$5.998\text{e}+07 \pm 1.577\text{e}+05$
p4	1.894 ± 0.02963

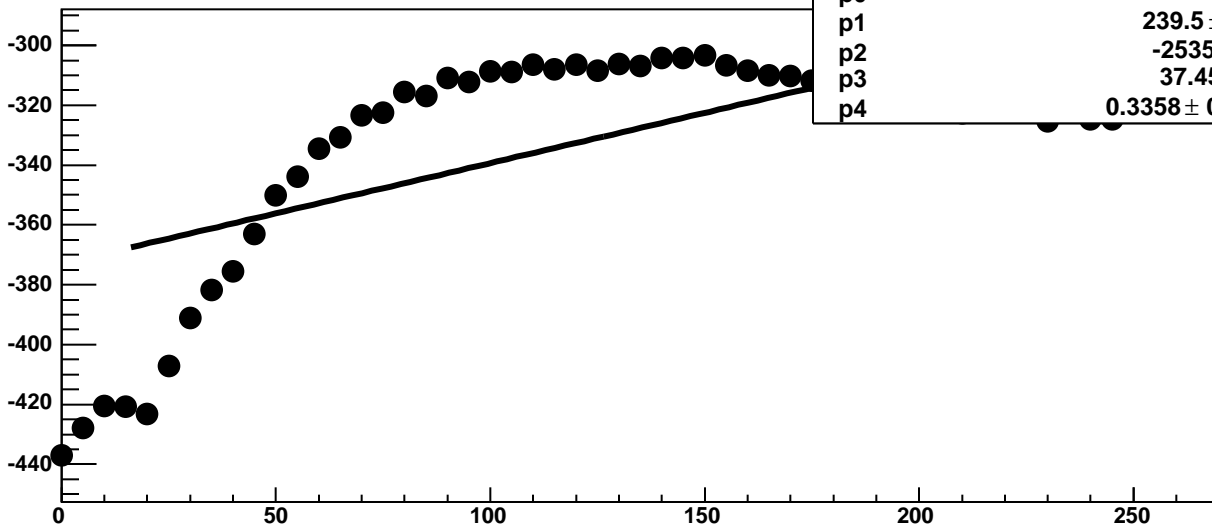
Chip 11, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold

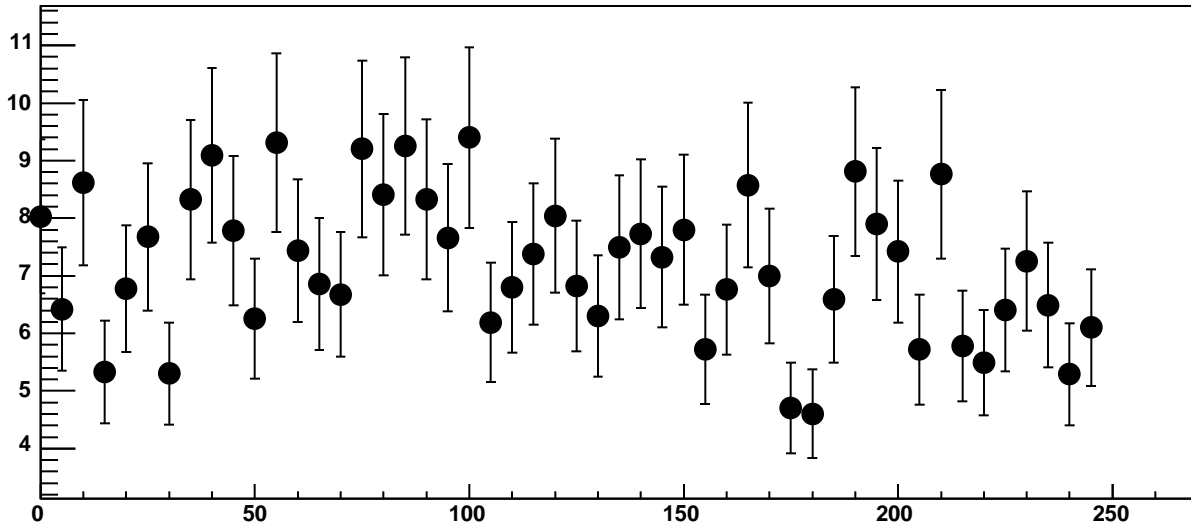


Chip 11, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

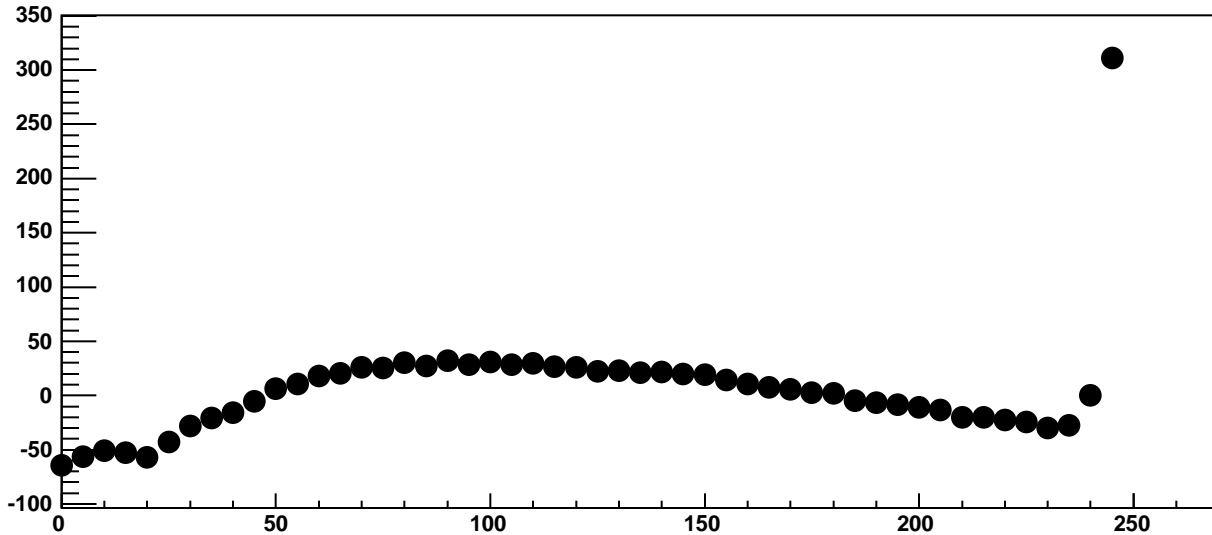


χ^2 / ndf	1.074e+04 / 41
p0	-292.5 ± 0.4471
p1	239.5 ± 0.0199
p2	-2535 ± 8.008
p3	37.45 ± 0.111
p4	0.3358 ± 0.003501

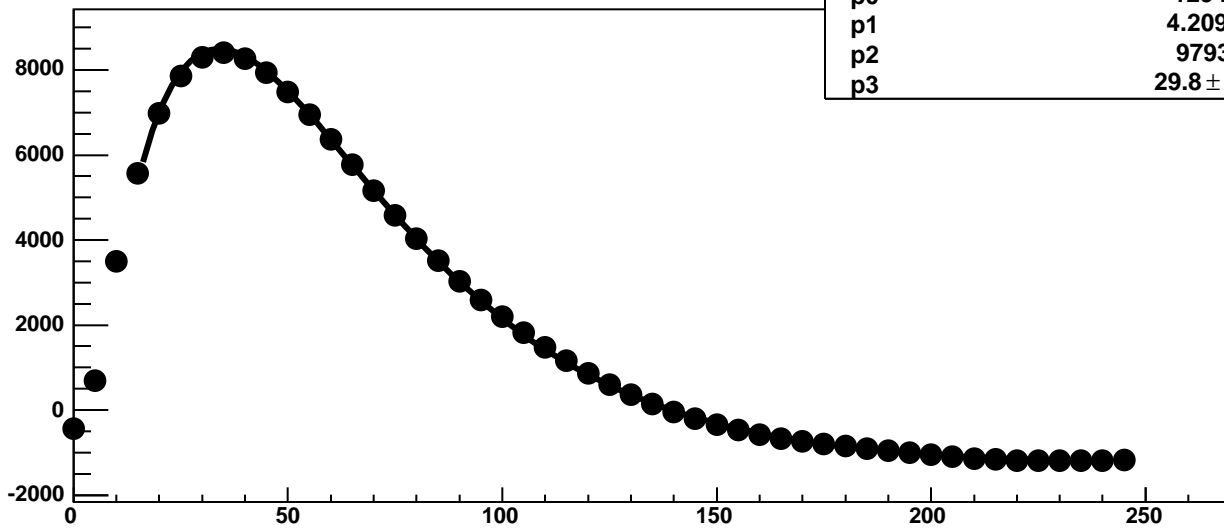
Chip 11, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

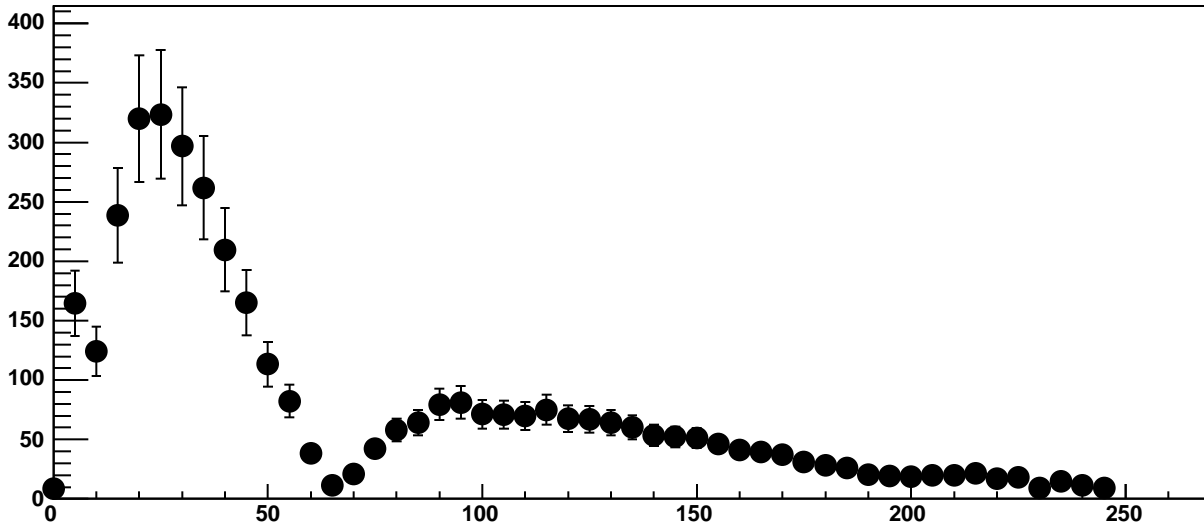


Chip 11, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold

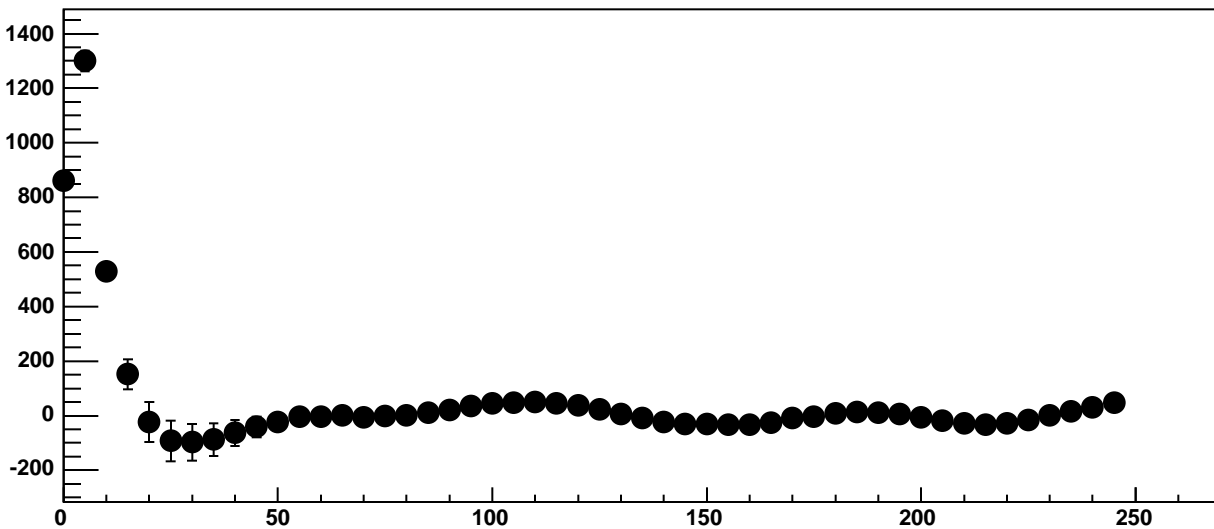


χ^2 / ndf	451.4 / 42
p0	-1294 ± 1.515
p1	4.209 ± 0.102
p2	9793 ± 12.61
p3	29.8 ± 0.03838

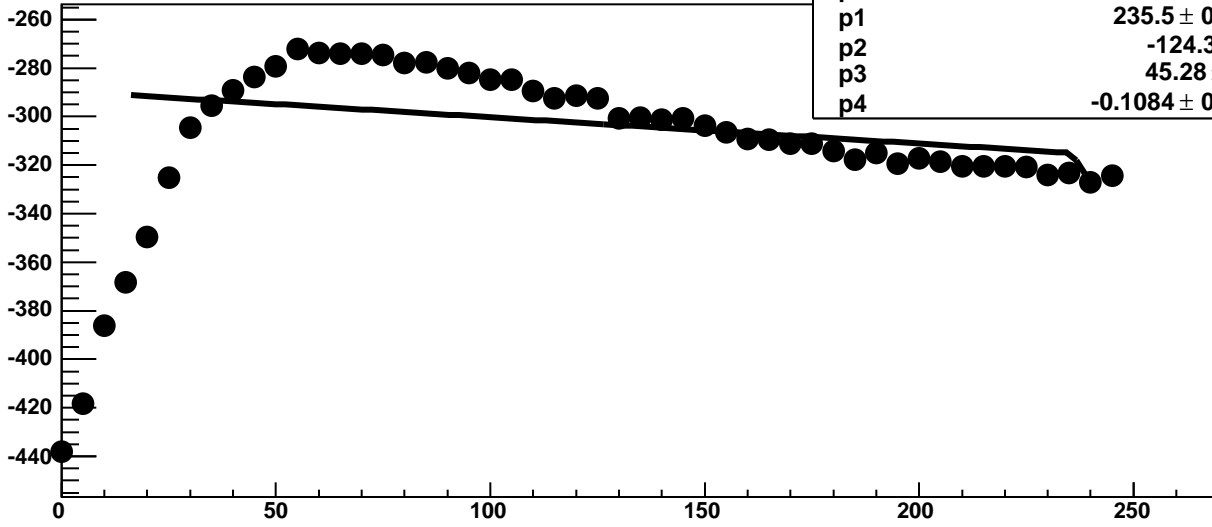
Chip 11, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold

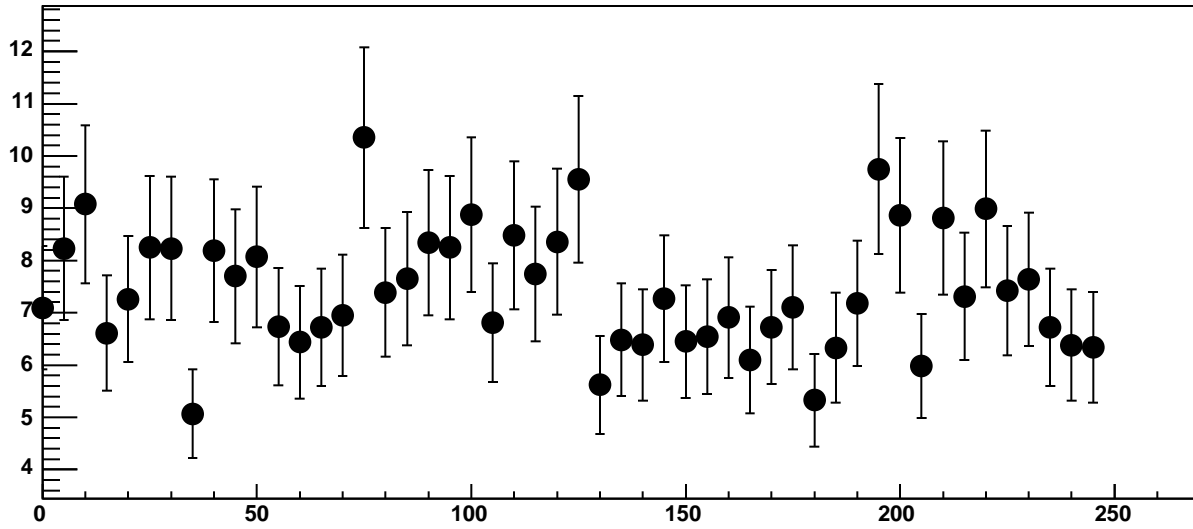


Chip 11, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

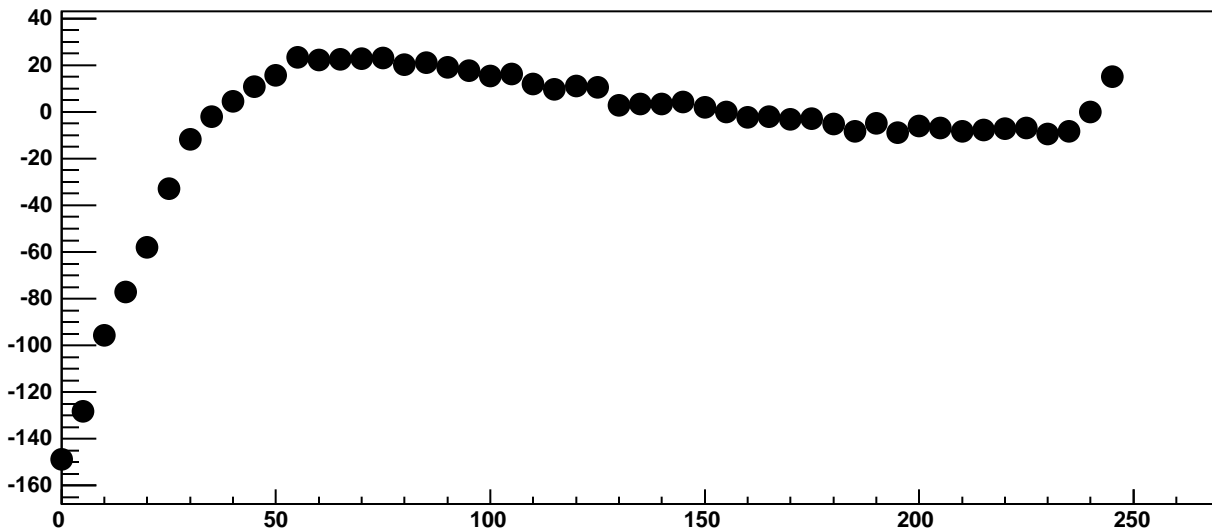


χ^2 / ndf	6258 / 41
p0	-315 ± 0.4835
p1	235.5 ± 0.004345
p2	-124.3 ± 16.54
p3	45.28 ± 0.1021
p4	-0.1084 ± 0.003788

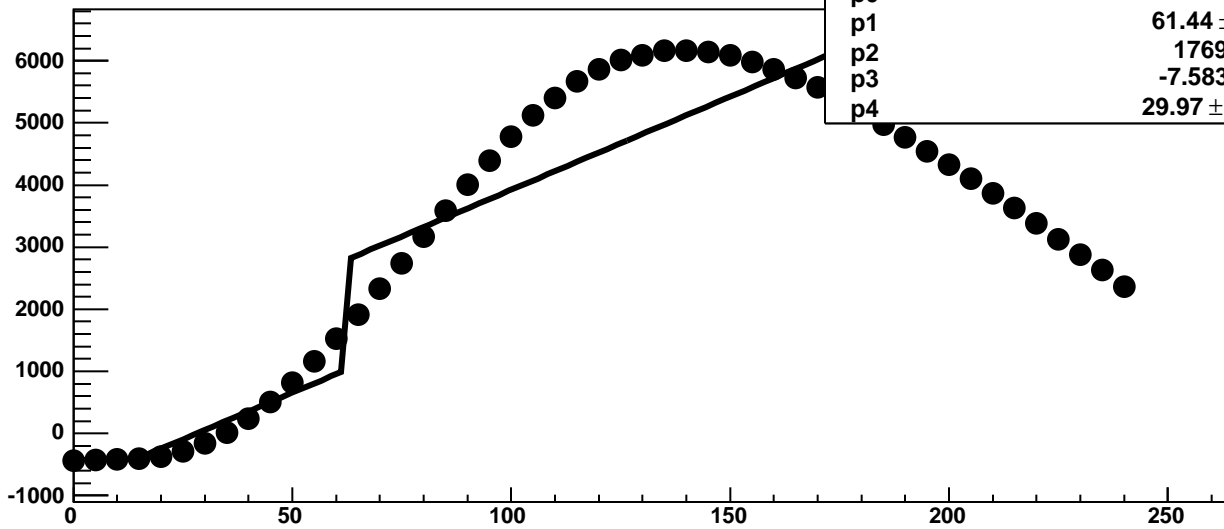
Chip 11, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

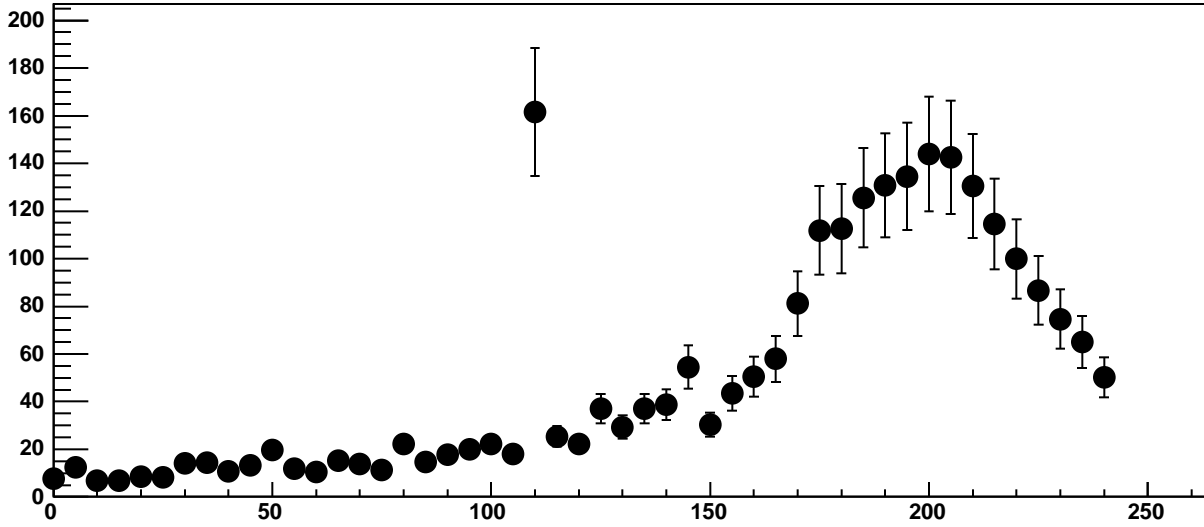


Chip 11, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold

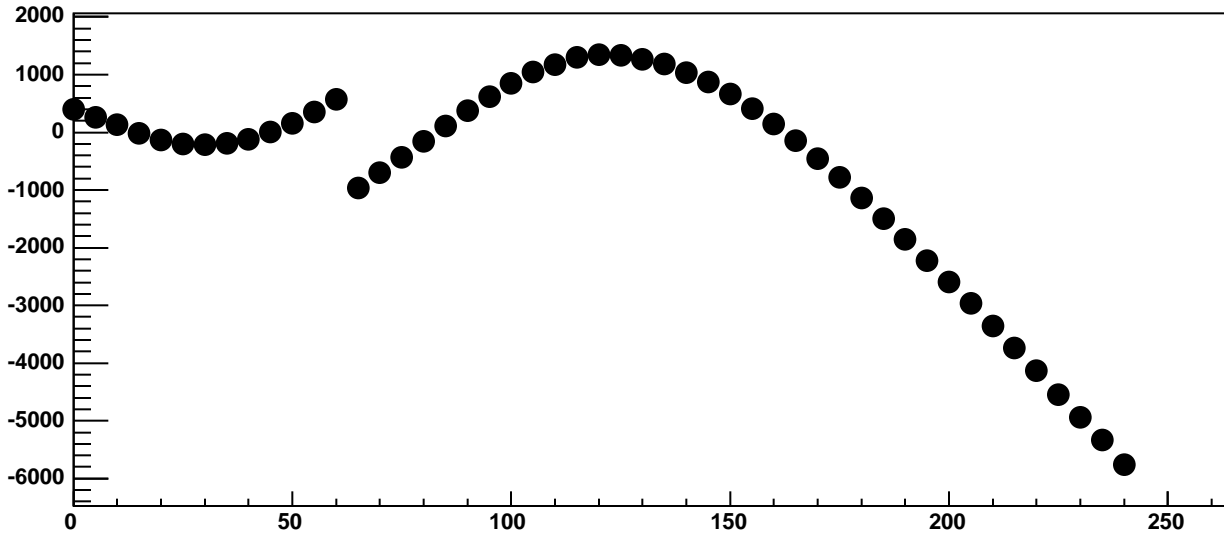


χ^2 / ndf	1.214e+06 / 41
p0	998.7 ± 1.333
p1	61.44 ± 0.1095
p2	1769 ± 3.454
p3	-7.583 ± 2.155
p4	29.97 ± 0.01661

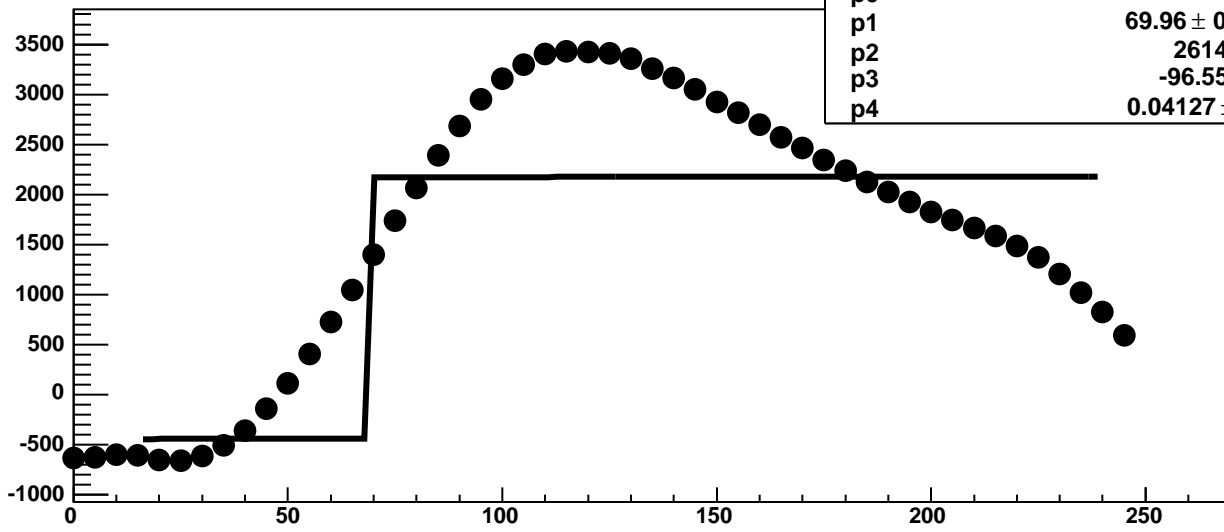
Chip 11, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

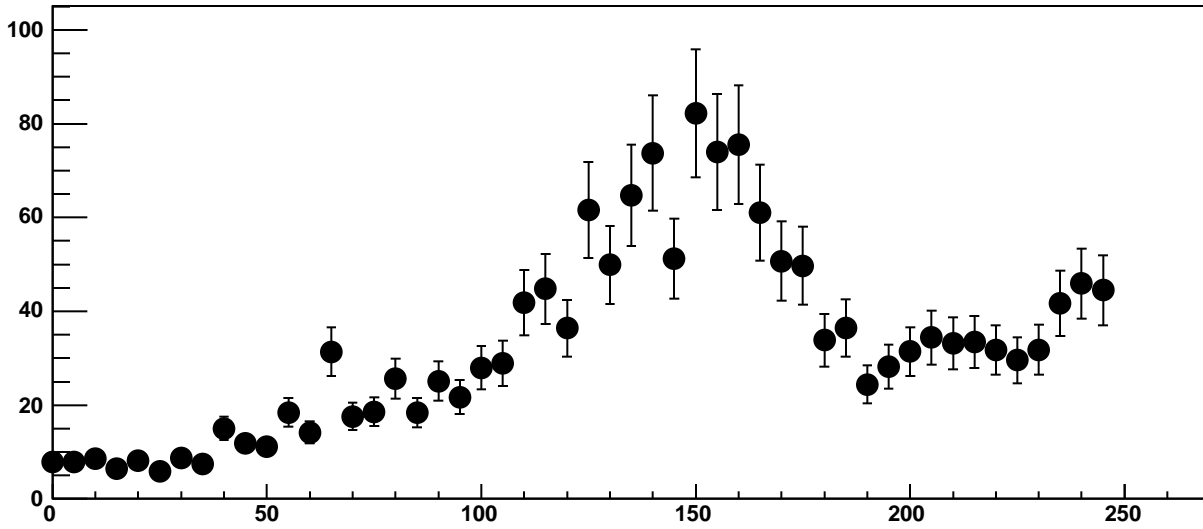


Chip 11, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

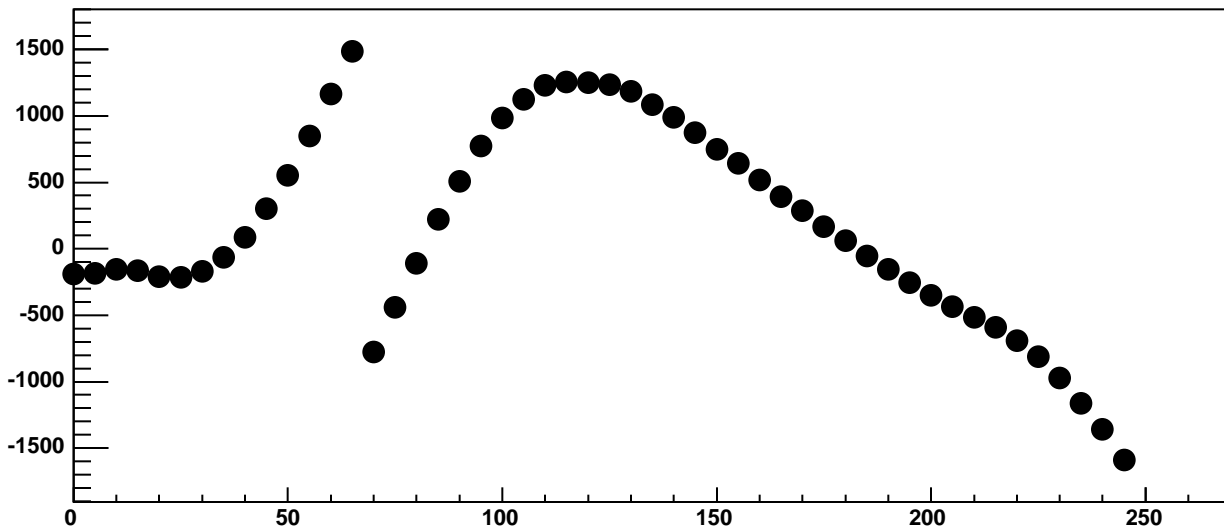


χ^2 / ndf	6.487e+05 / 41
p0	-441.2 ± 6.742
p1	69.96 ± 0.003957
p2	2614 ± 8.945
p3	-96.55 ± 4.147
p4	0.04127 ± 0.1213

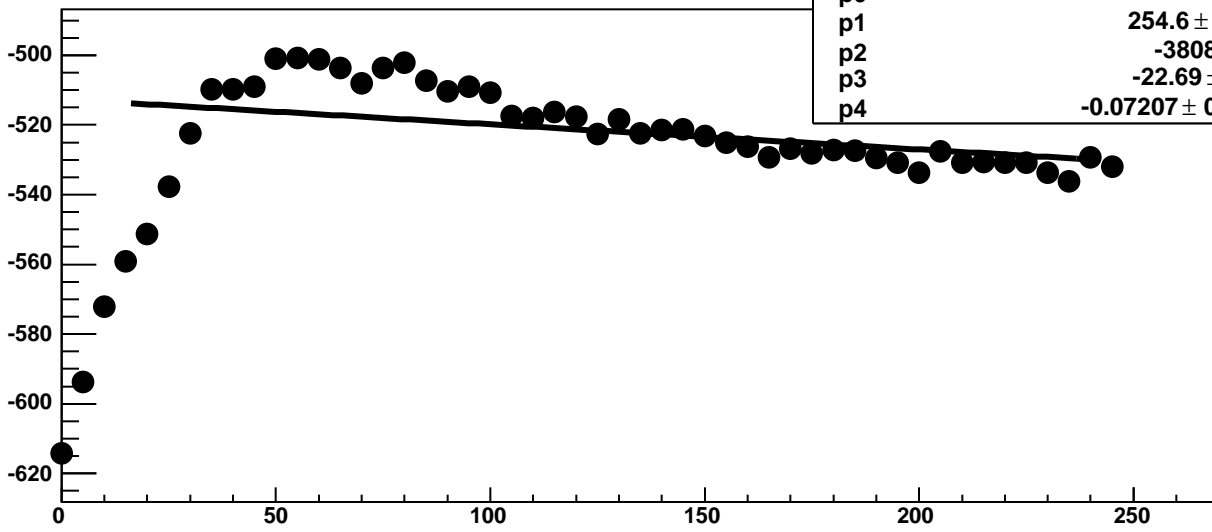
Chip 11, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



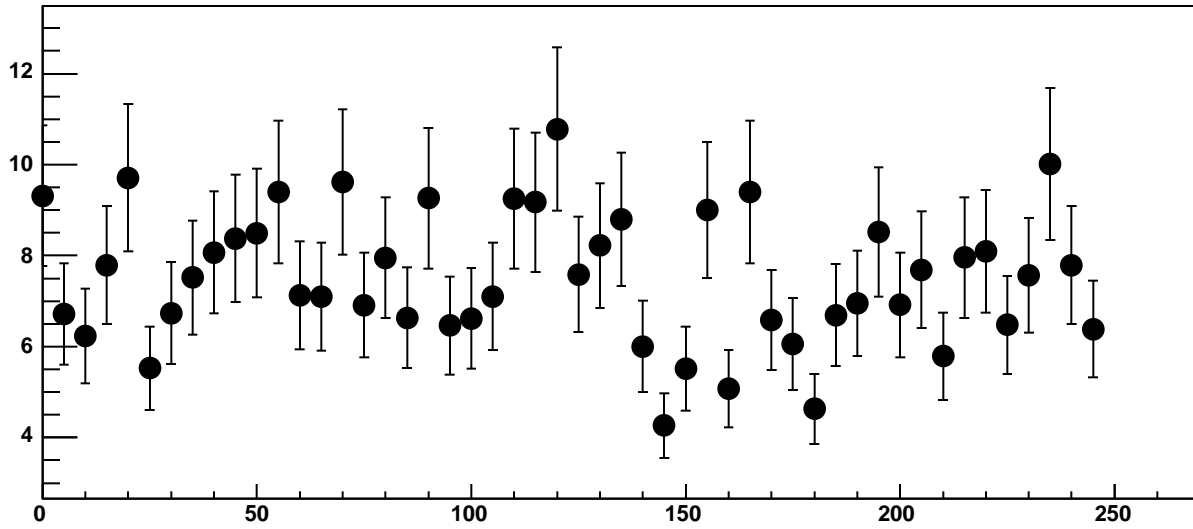
Chip 11, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold



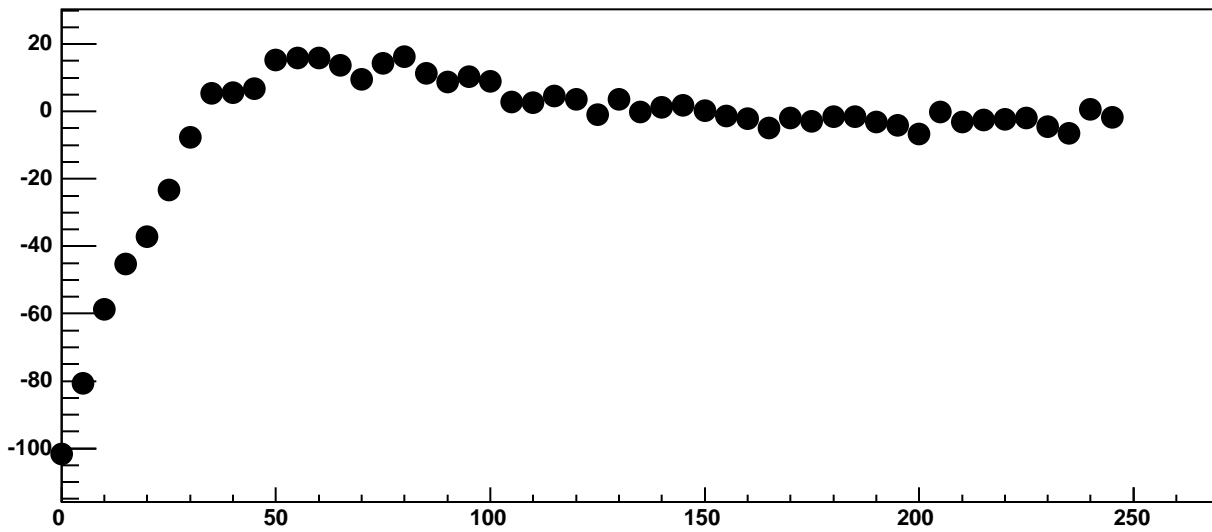
Chip 11, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold



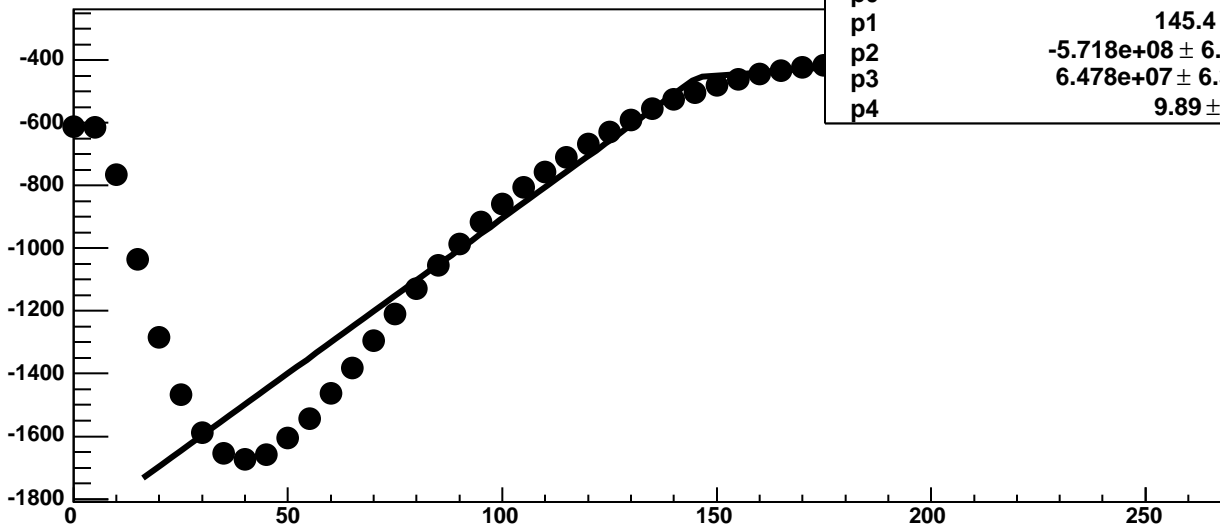
Chip 11, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold

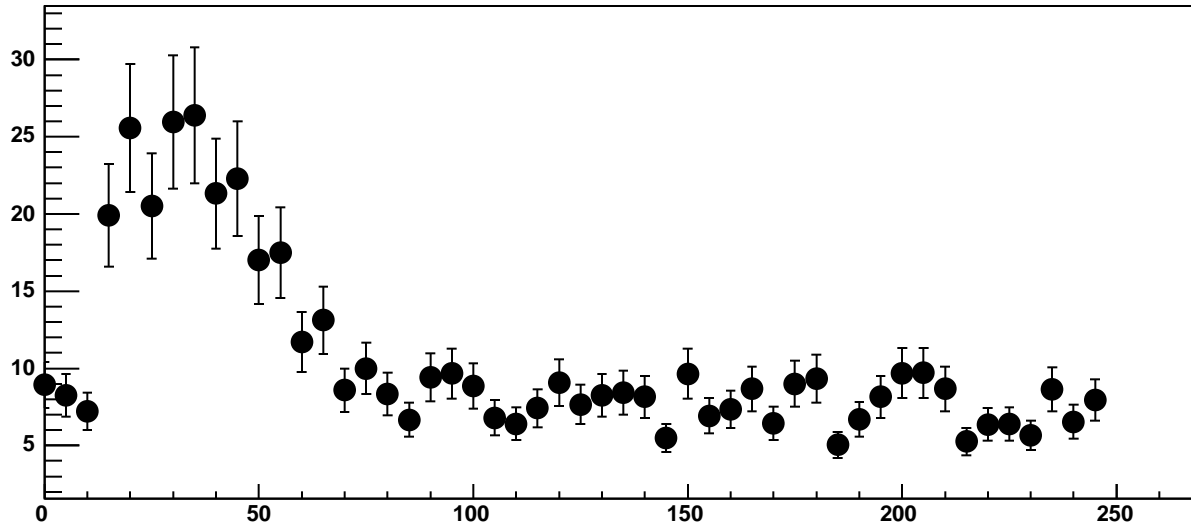


Chip 11, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

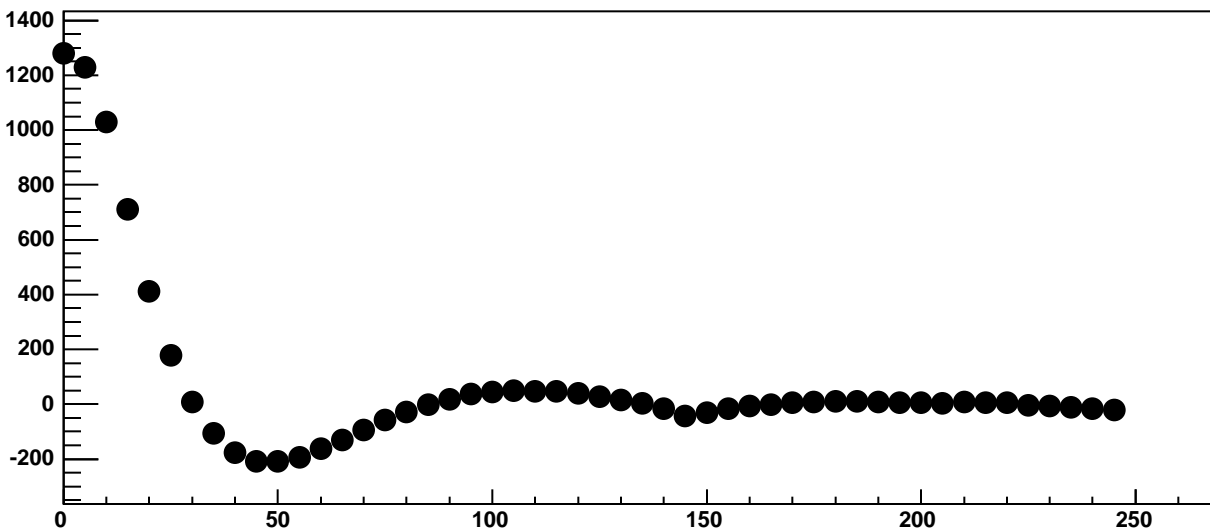


χ^2 / ndf	5.432e+04 / 41
p0	-455 ± 0.9746
p1	145.4 ± 0.1318
p2	-5.718e+08 ± 6.131e+06
p3	6.478e+07 ± 6.353e+05
p4	9.89 ± 0.01392

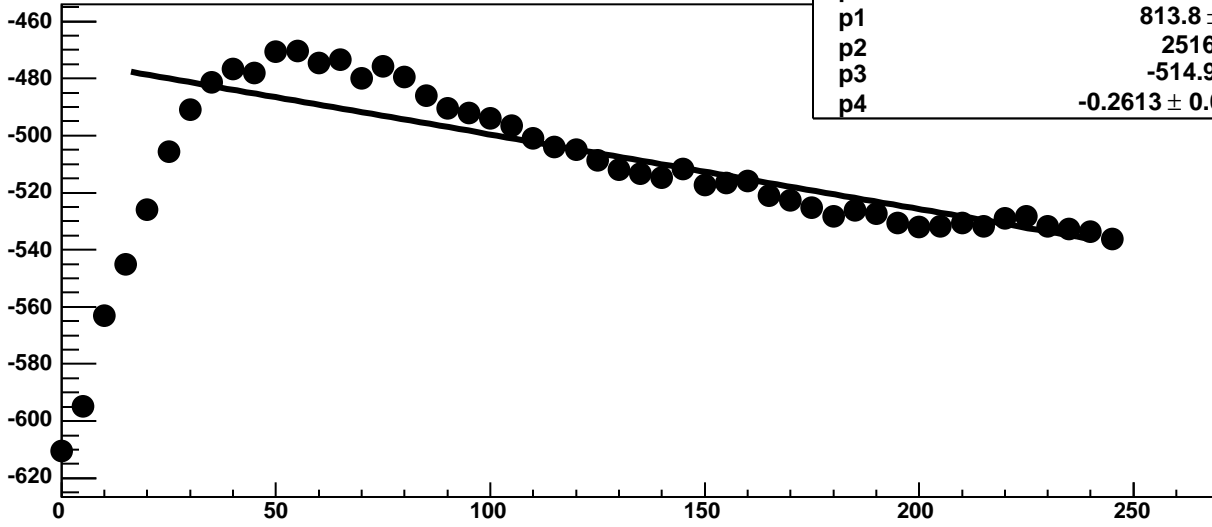
Chip 11, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

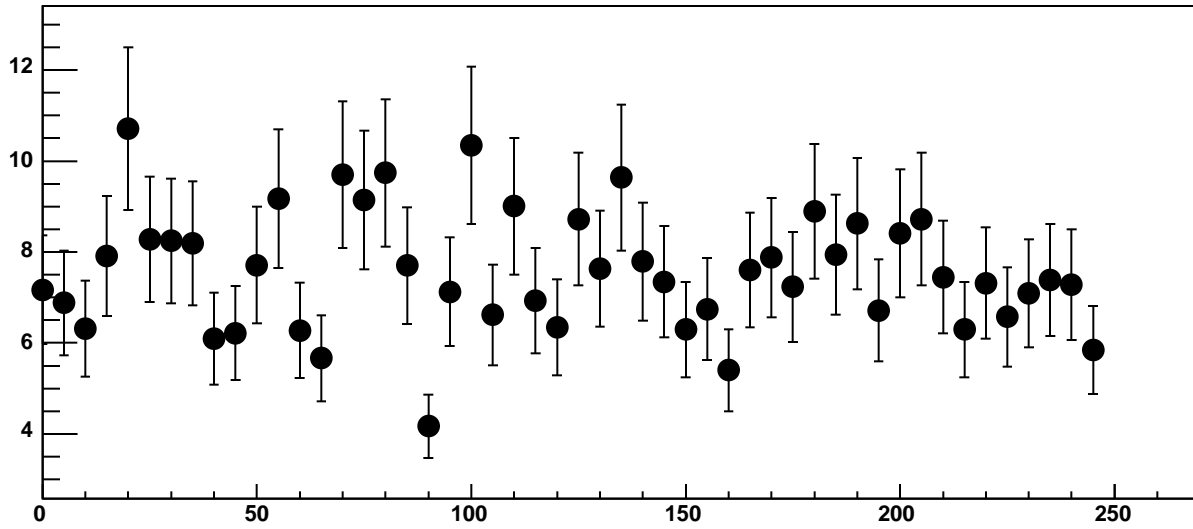


Chip 11, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

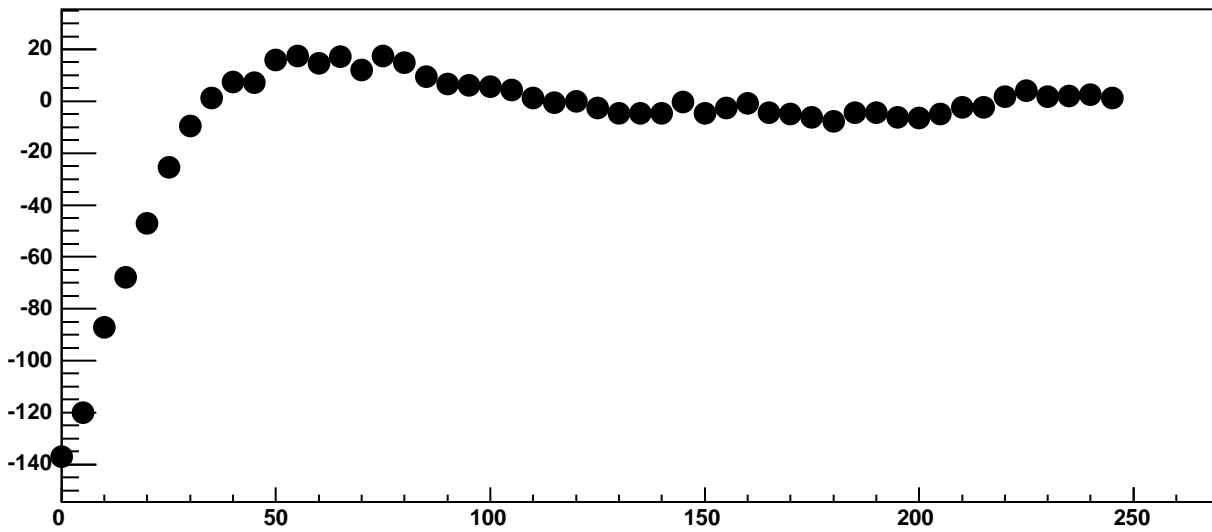


χ^2 / ndf	2828 / 41
p0	-686.1 ± 0.2463
p1	813.8 ± 0.9426
p2	2516 ± 84.85
p3	-514.9 ± 1.414
p4	-0.2613 ± 0.0003578

Chip 11, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold

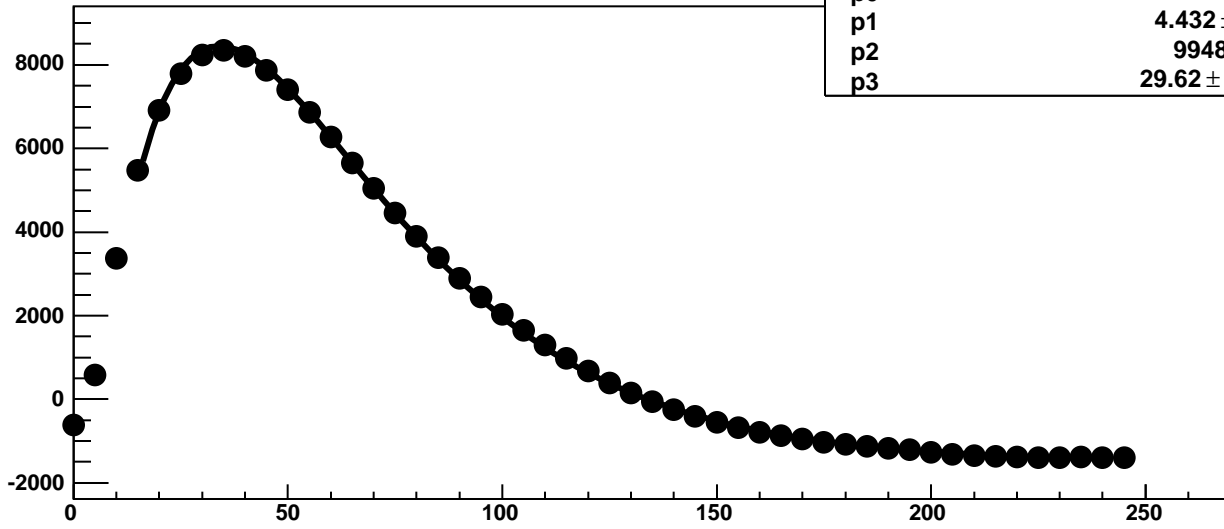


Chip 11, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

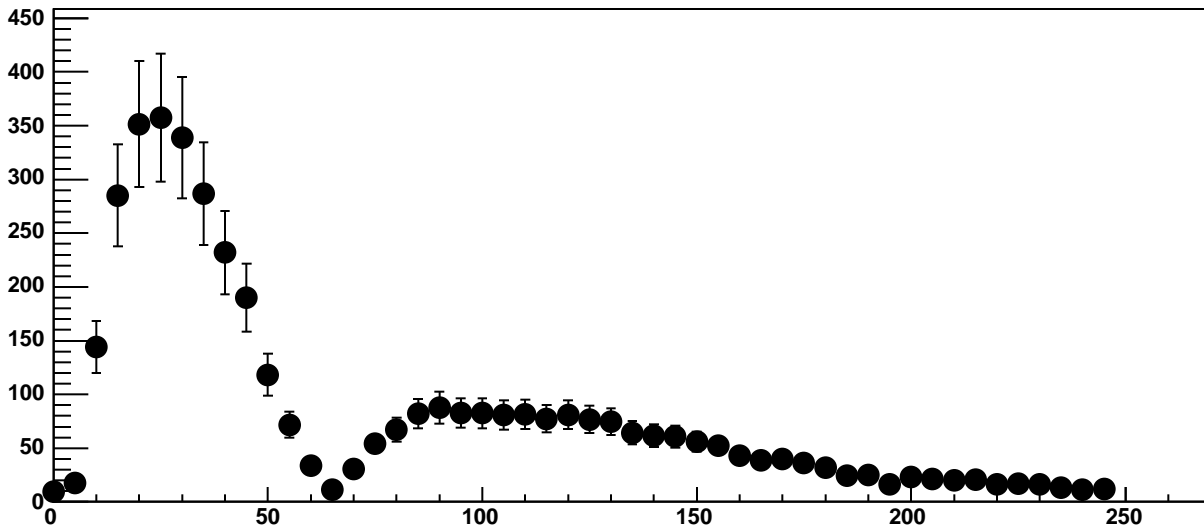


Chip 11, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold

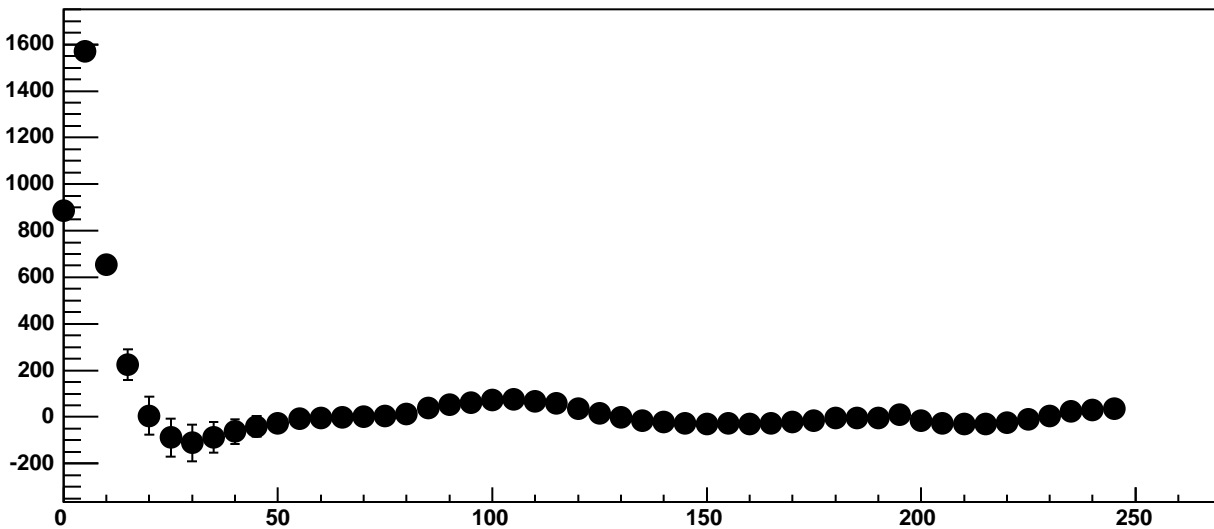
χ^2 / ndf	504.5 / 42
p0	-1500 ± 1.653
p1	4.432 ± 0.1125
p2	9948 ± 14.03
p3	29.62 ± 0.04147



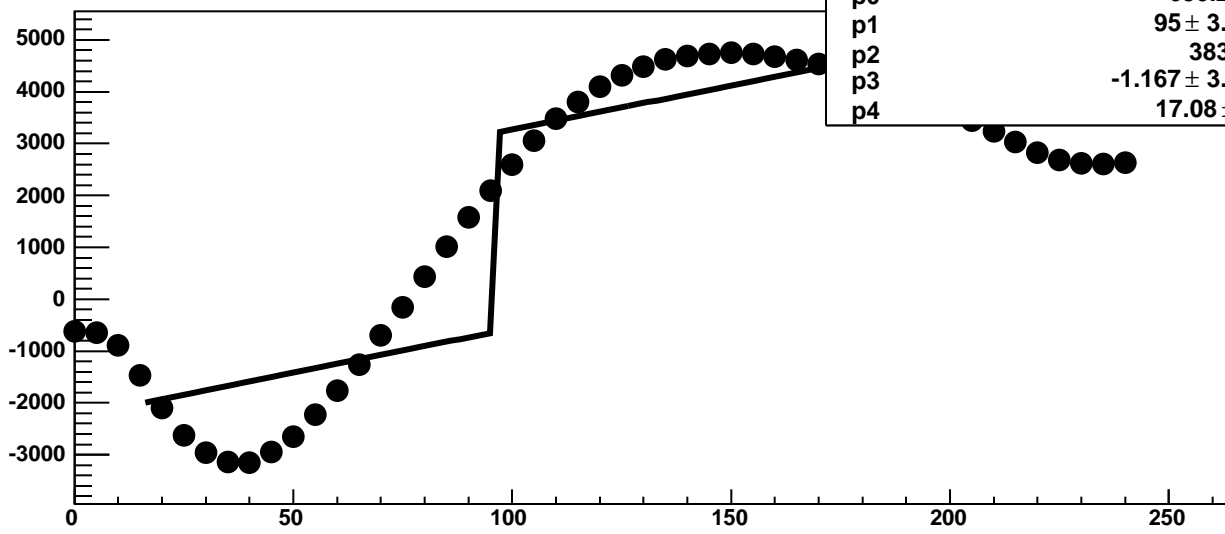
Chip 11, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold

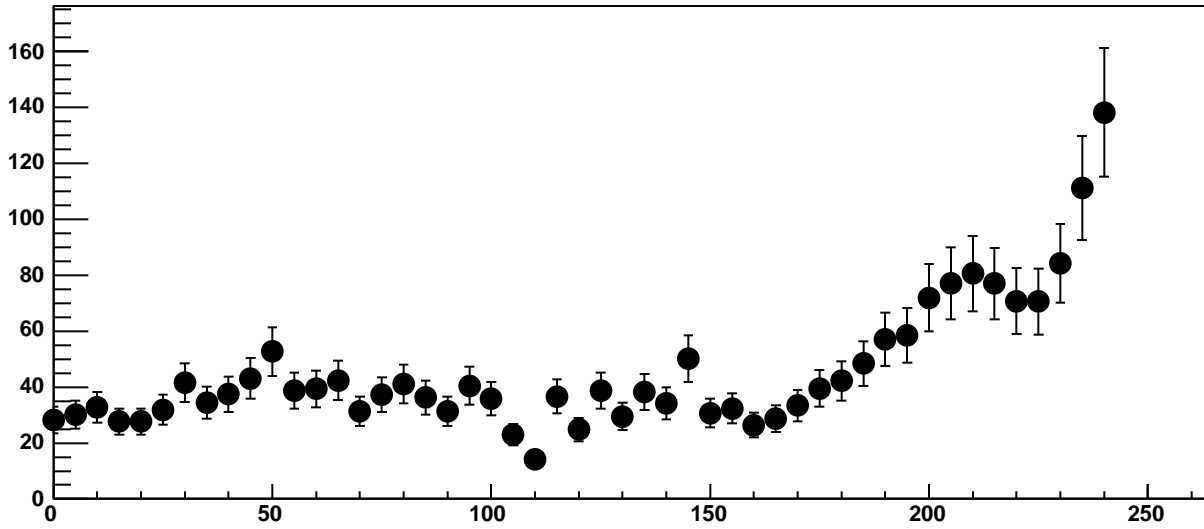


Chip 11, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

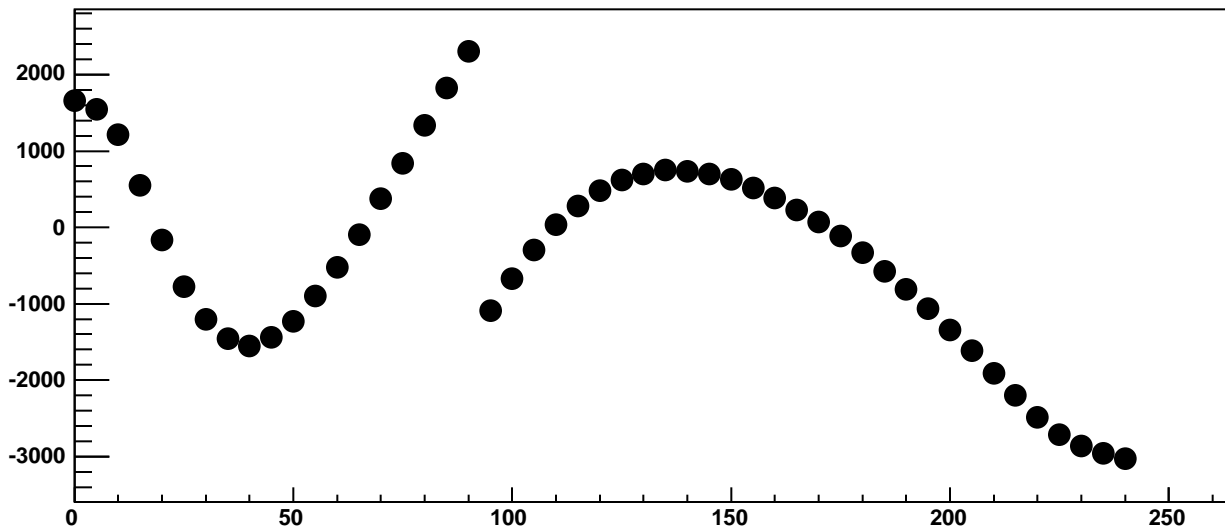


χ^2 / ndf	5.694e+05 / 41
p0	-650.2 ± 6.667
p1	95 ± 3.449e-08
p2	3834 ± 7.9
p3	-1.167 ± 3.785e-05
p4	17.08 ± 0.1267

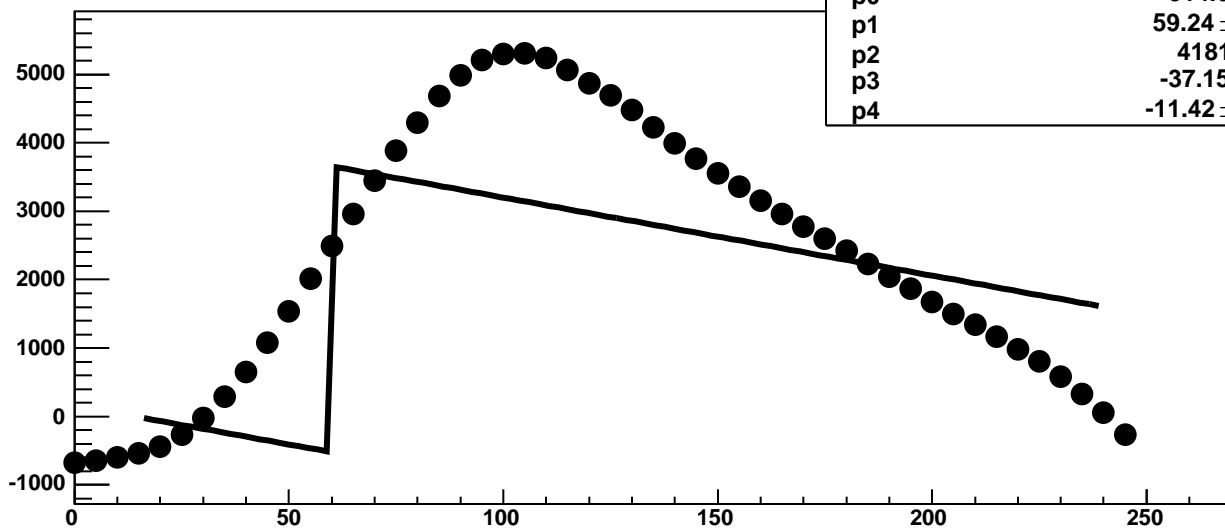
Chip 11, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold

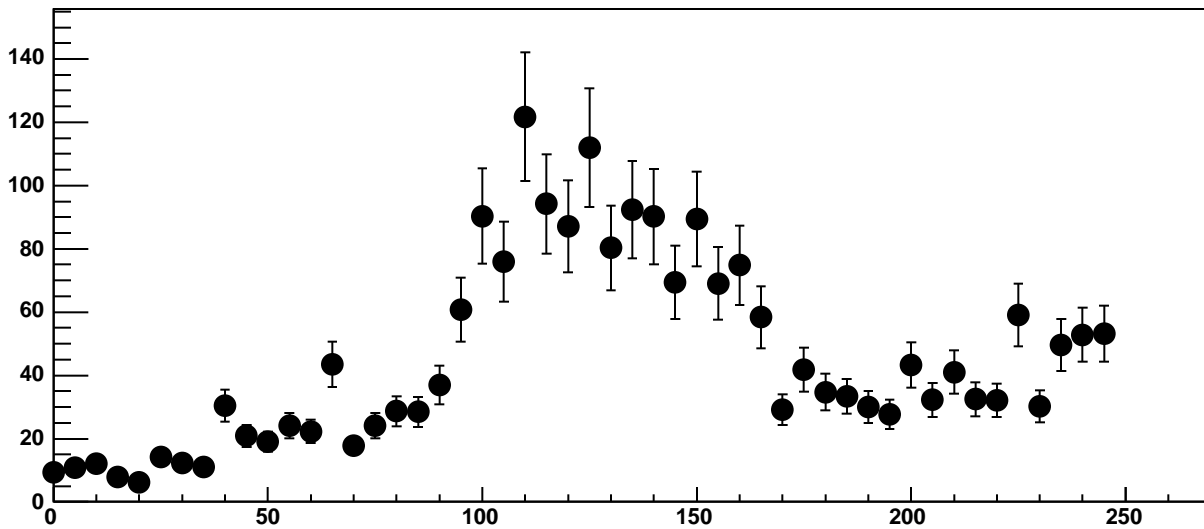


Chip 11, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold

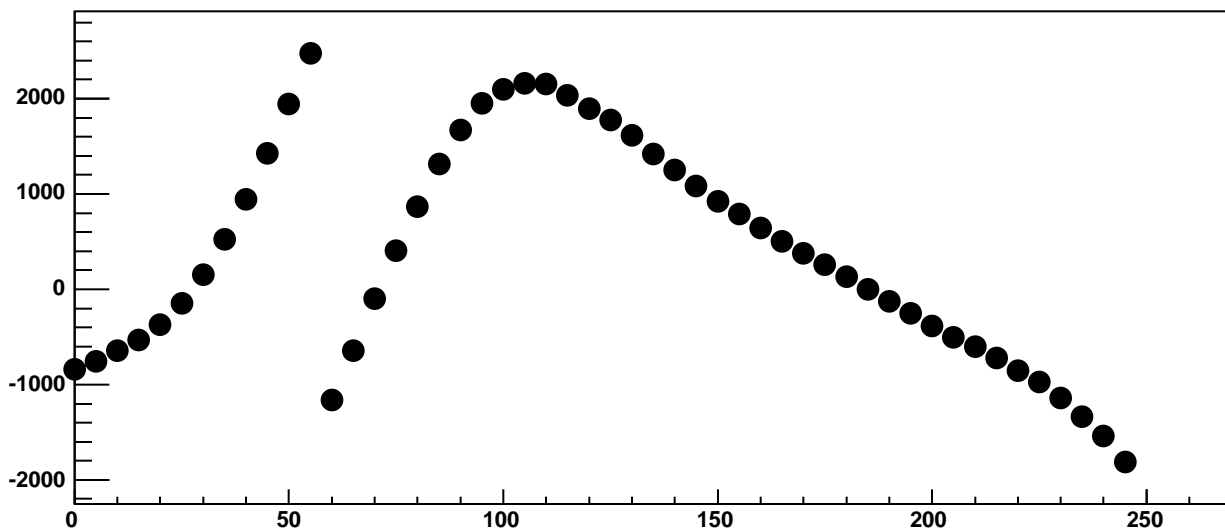


χ^2 / ndf	1.07e+06 / 41
p0	-514.6 ± 9.106
p1	59.24 ± 0.5786
p2	4181 ± 3.825
p3	-37.15 ± 1.683
p4	-11.42 ± 0.0786

Chip 11, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold

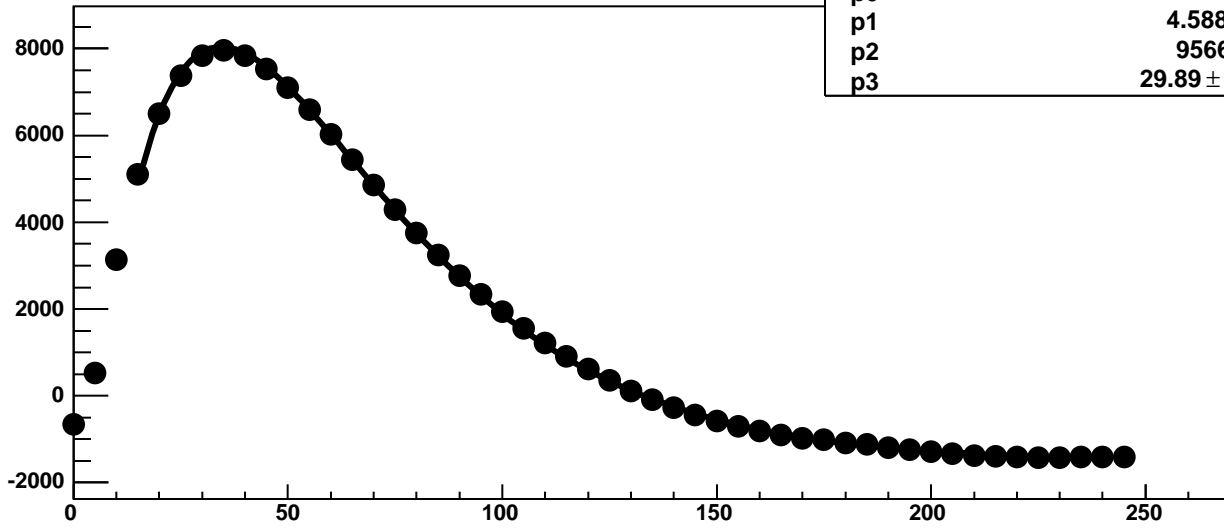


Chip 11, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold

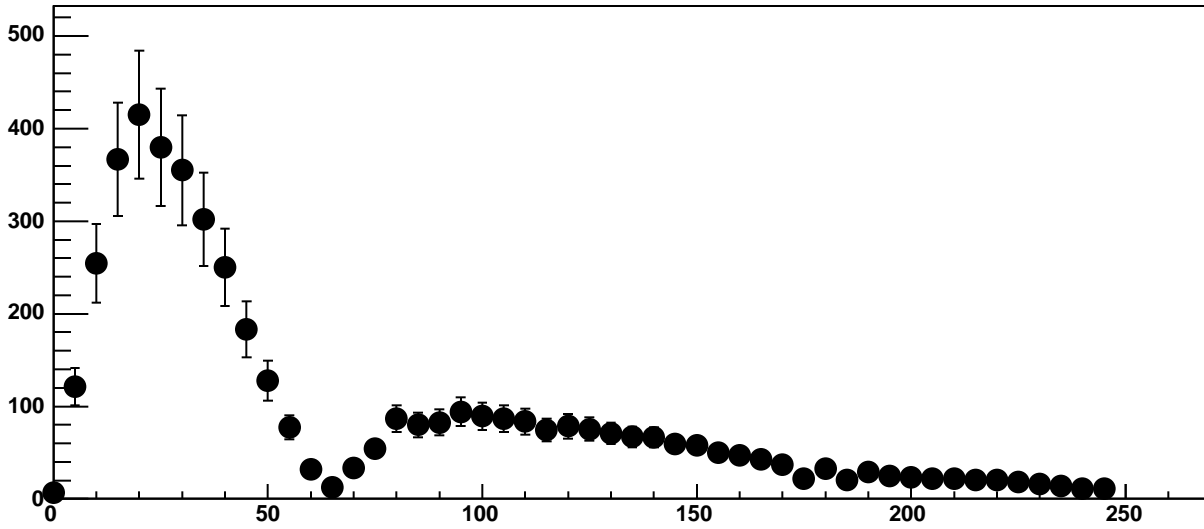


Chip 11, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold

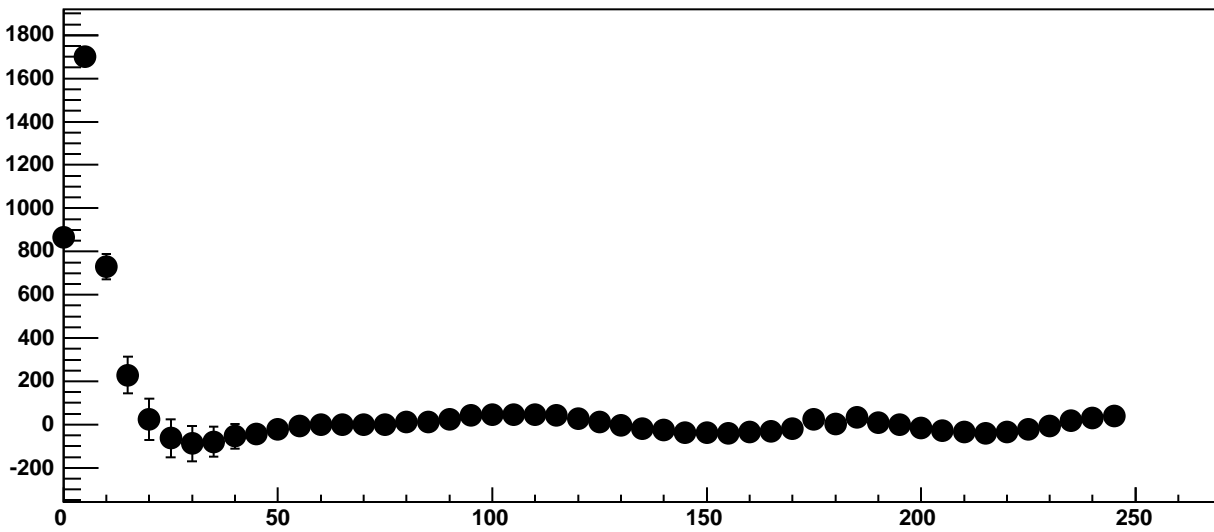
χ^2 / ndf	591.8 / 42
p0	-1522 ± 1.761
p1	4.588 ± 0.137
p2	9566 ± 15.81
p3	29.89 ± 0.04509



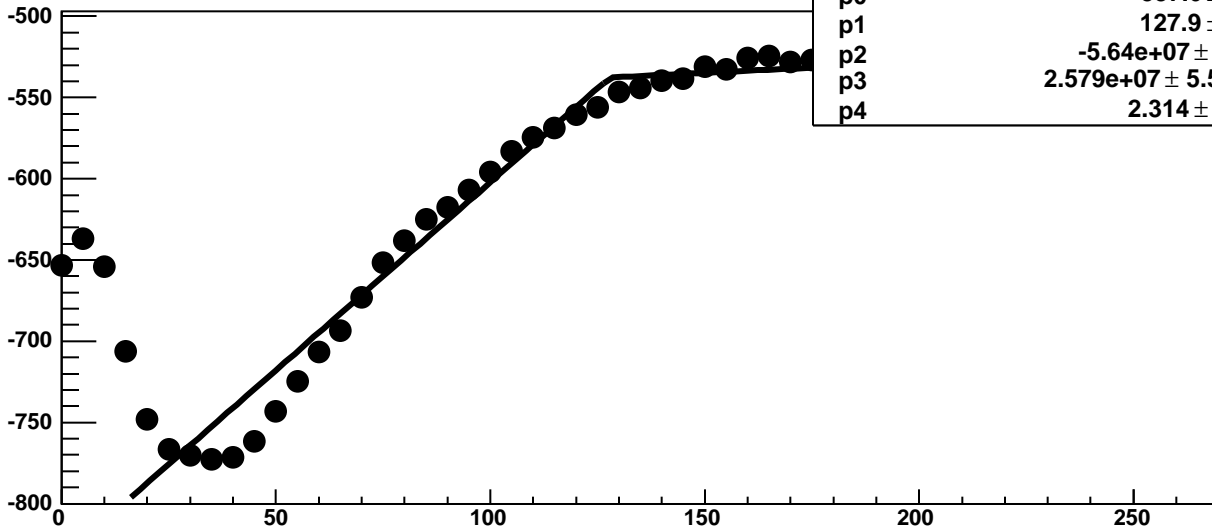
Chip 11, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold

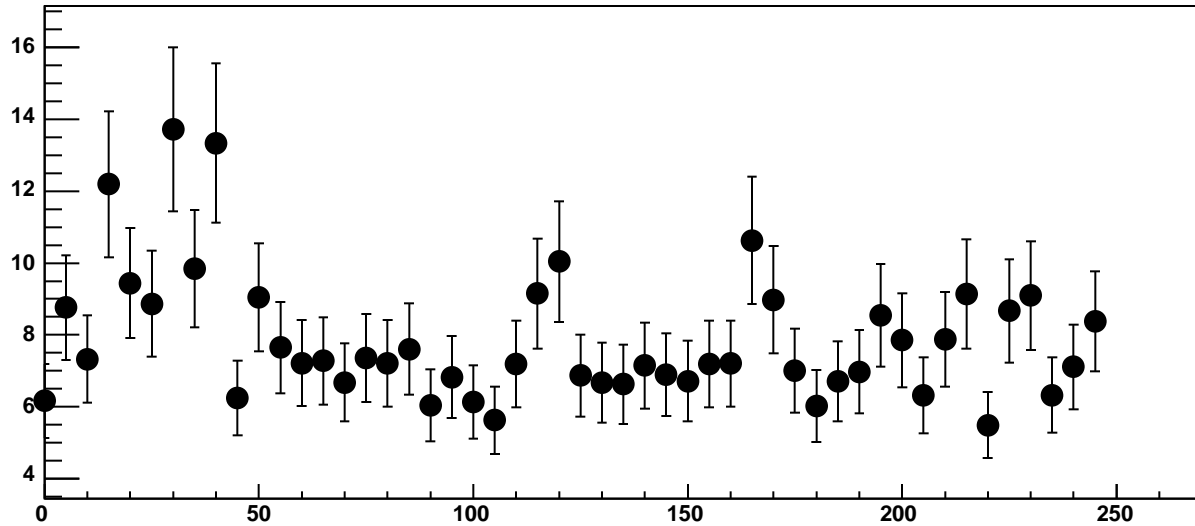


Chip 11, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

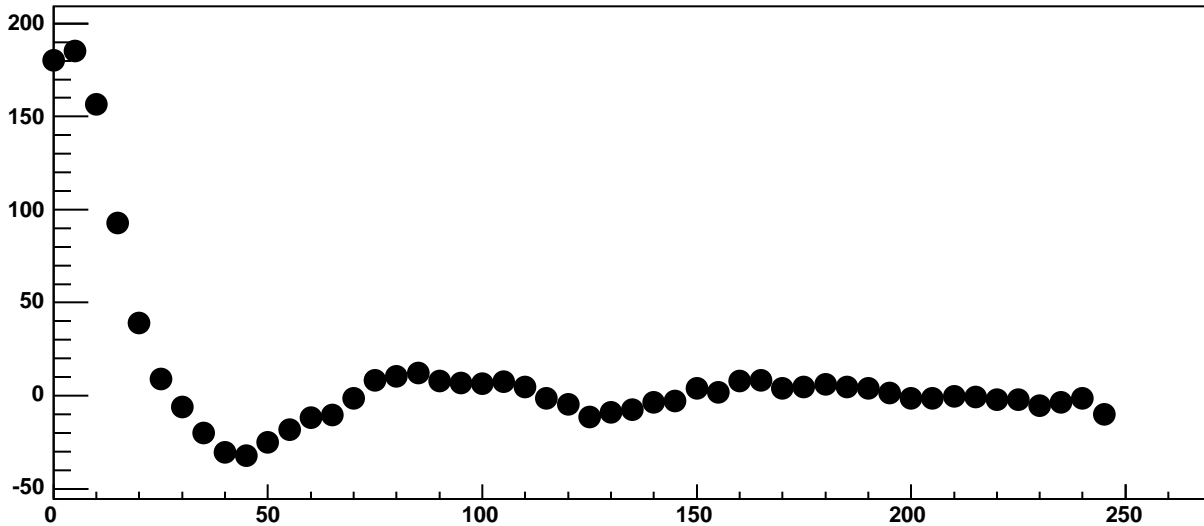


χ^2 / ndf	2955 / 41
p0	-537.6 ± 0.7075
p1	127.9 ± 0.4485
p2	$-5.64e+07 \pm 1.3e+06$
p3	$2.579e+07 \pm 5.544e+05$
p4	2.314 ± 0.01235

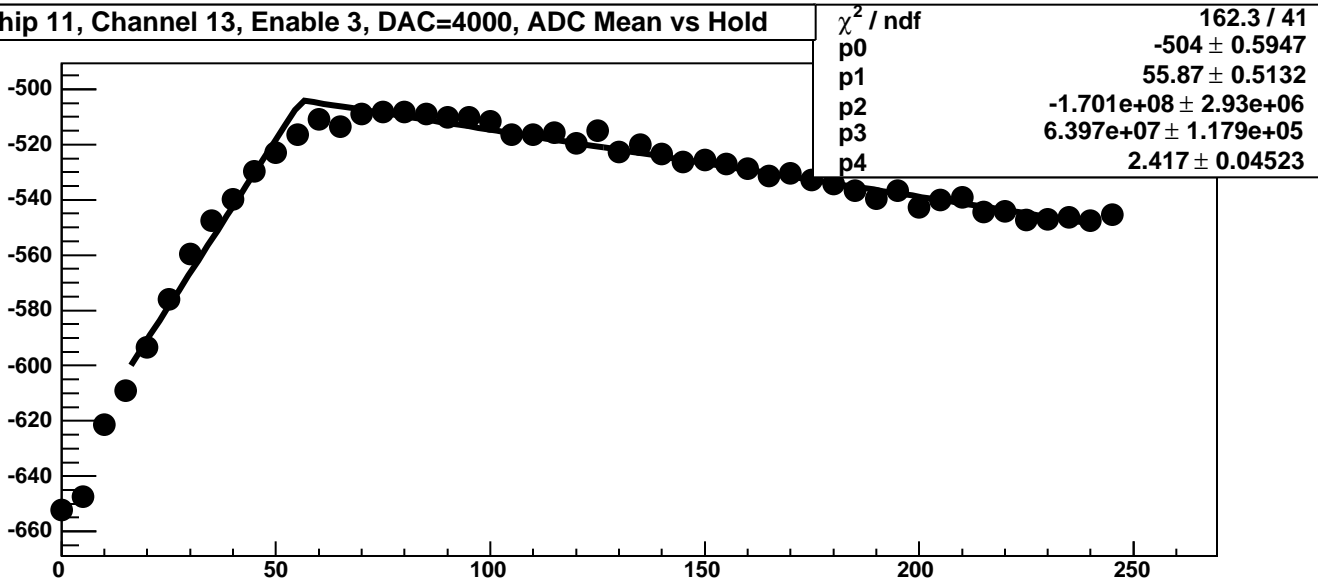
Chip 11, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



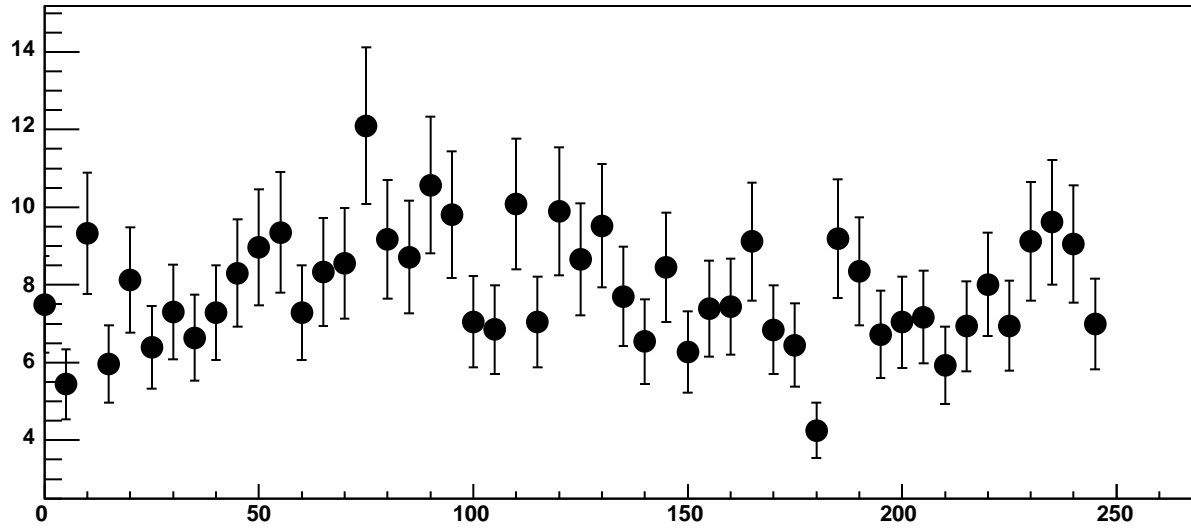
Chip 11, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



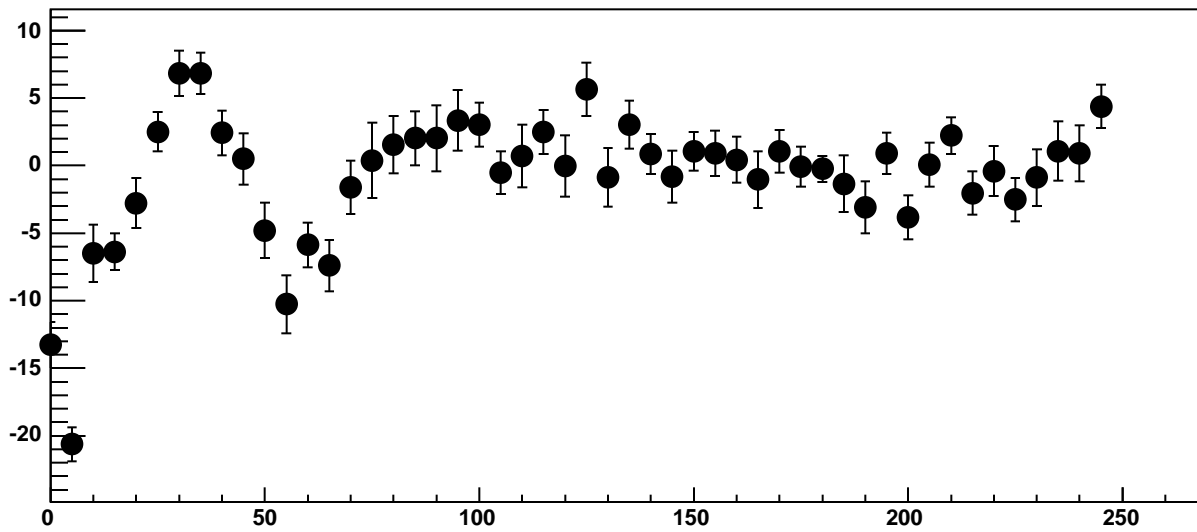
Chip 11, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold



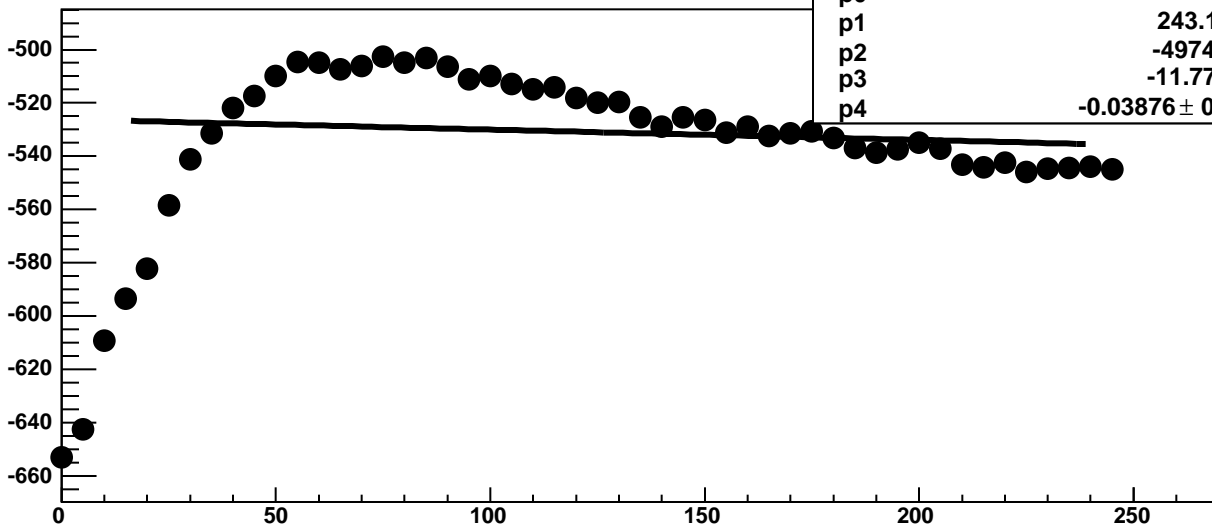
Chip 11, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold



Chip 11, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold



χ^2 / ndf

6026 / 41

p0

-535.6 ± 0.2443

p1

243.1 ± 6.303

p2

-4974 ± 84.85

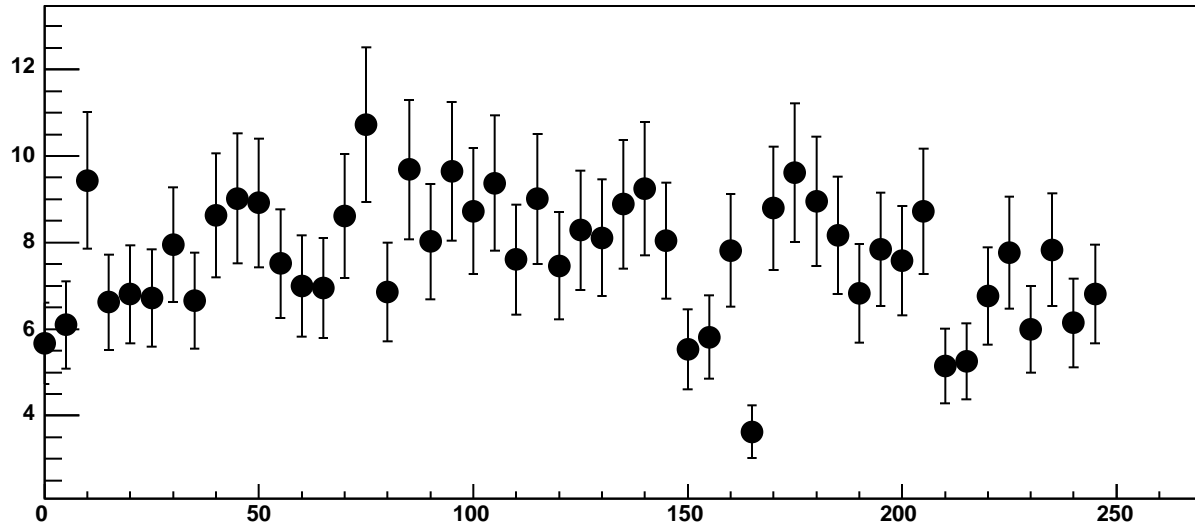
p3

-11.77 ± 1.414

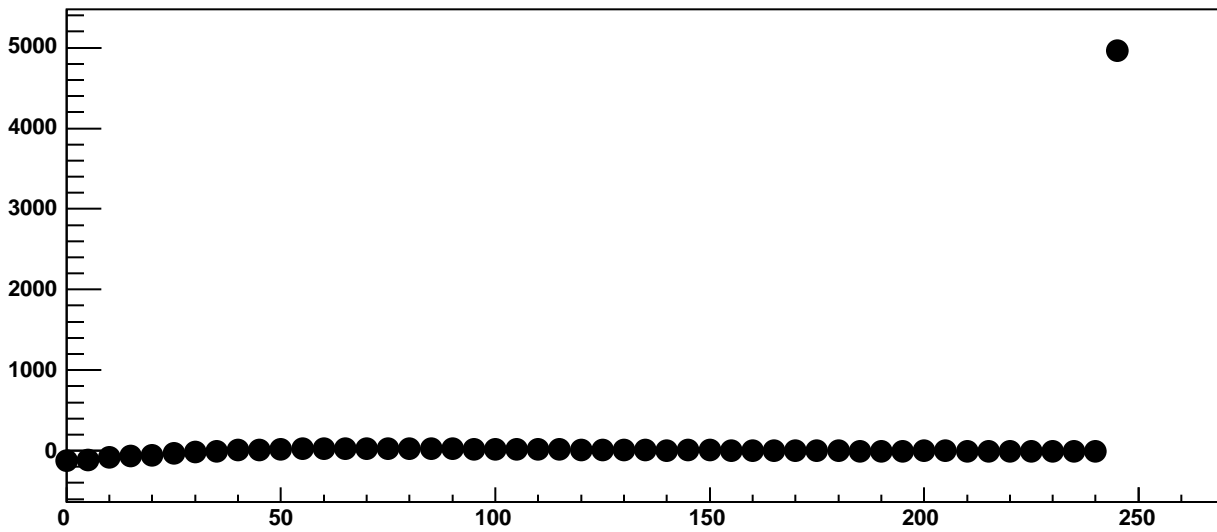
p4

-0.03876 ± 0.001925

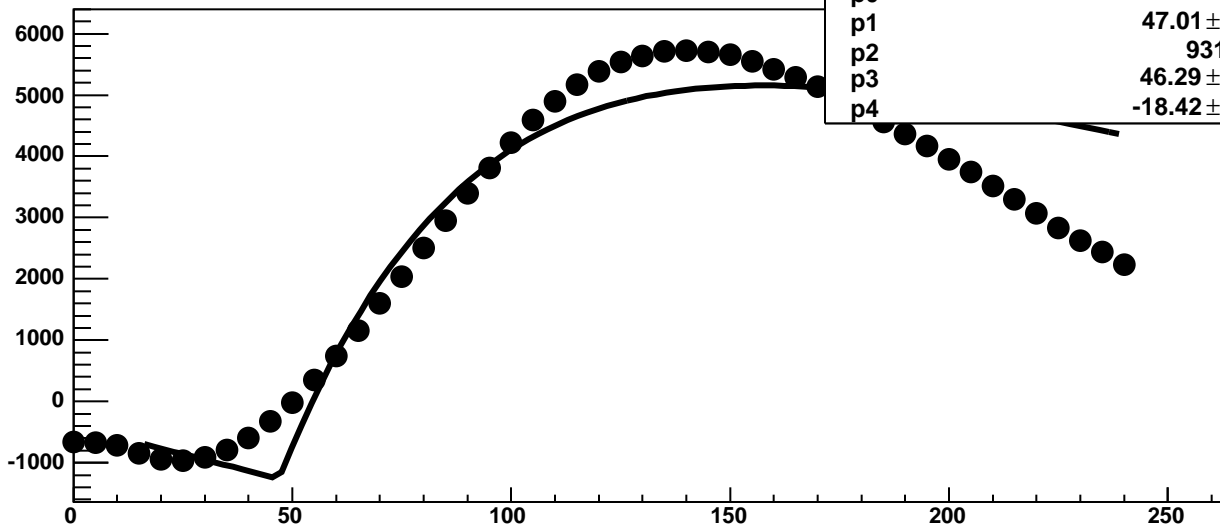
Chip 11, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold

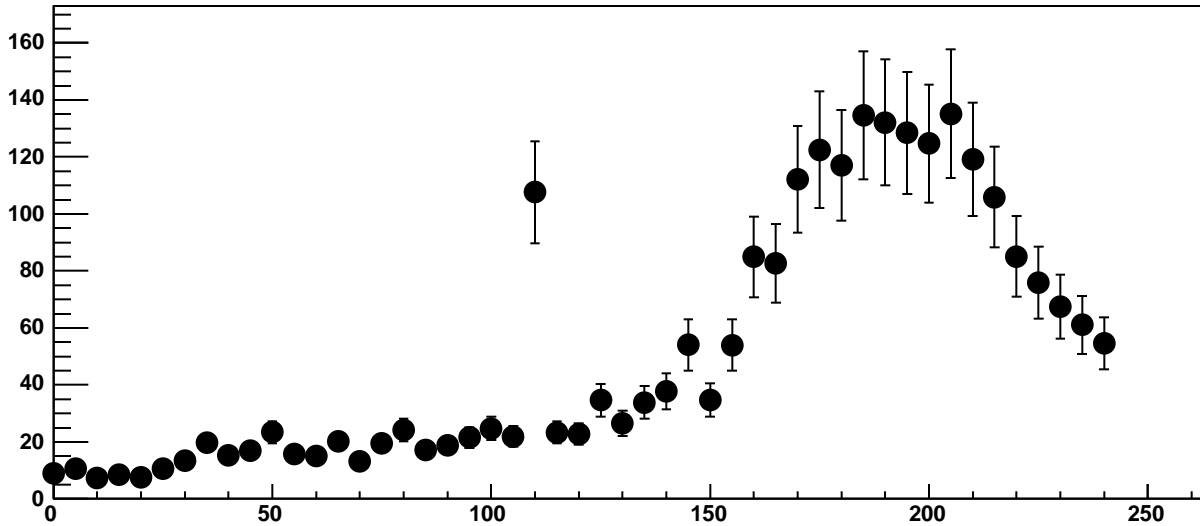


Chip 11, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

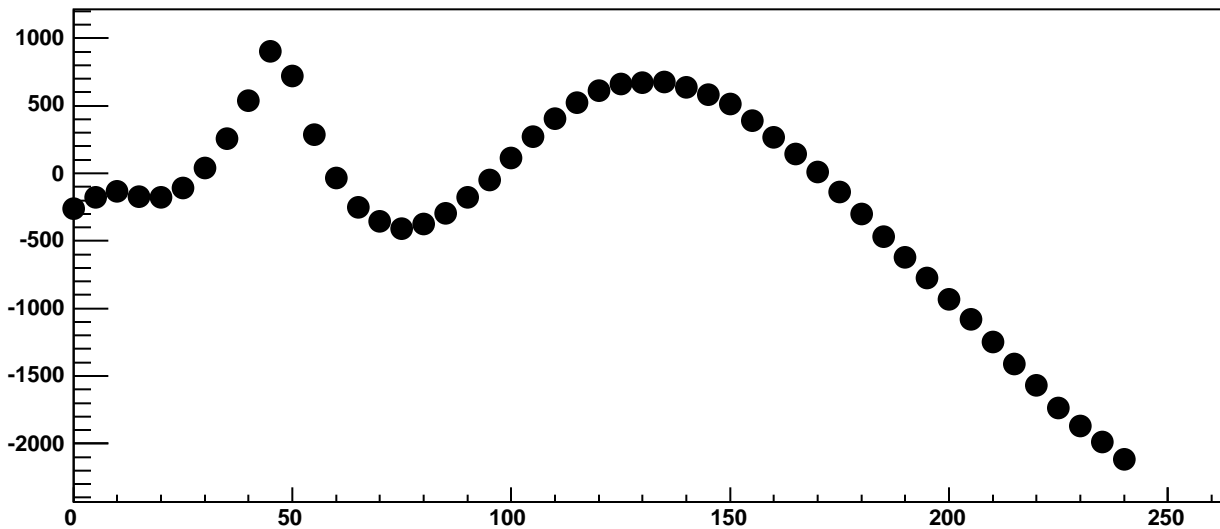


χ^2 / ndf	3.167e+05 / 41
p0	-1265 ± 1.771
p1	47.01 ± 0.01944
p2	9311 ± 13.9
p3	46.29 ± 0.07408
p4	-18.42 ± 0.07375

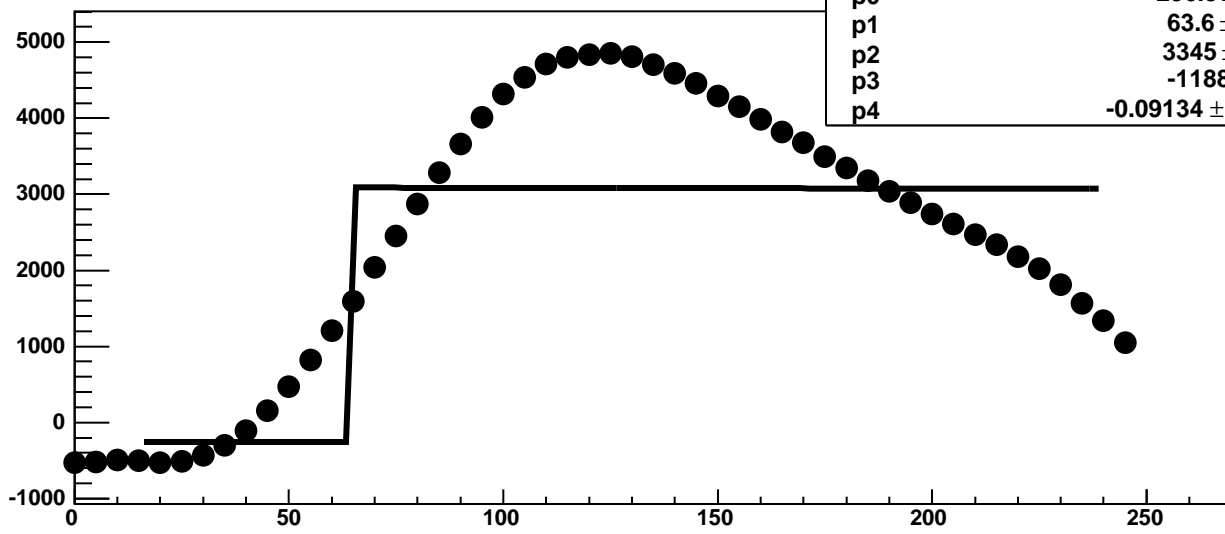
Chip 11, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold

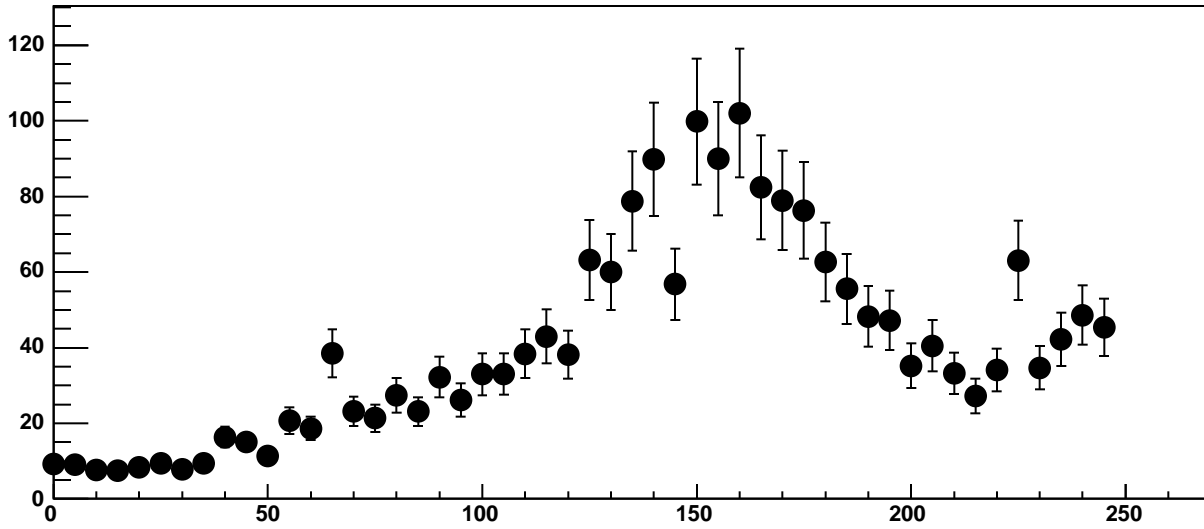


Chip 11, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

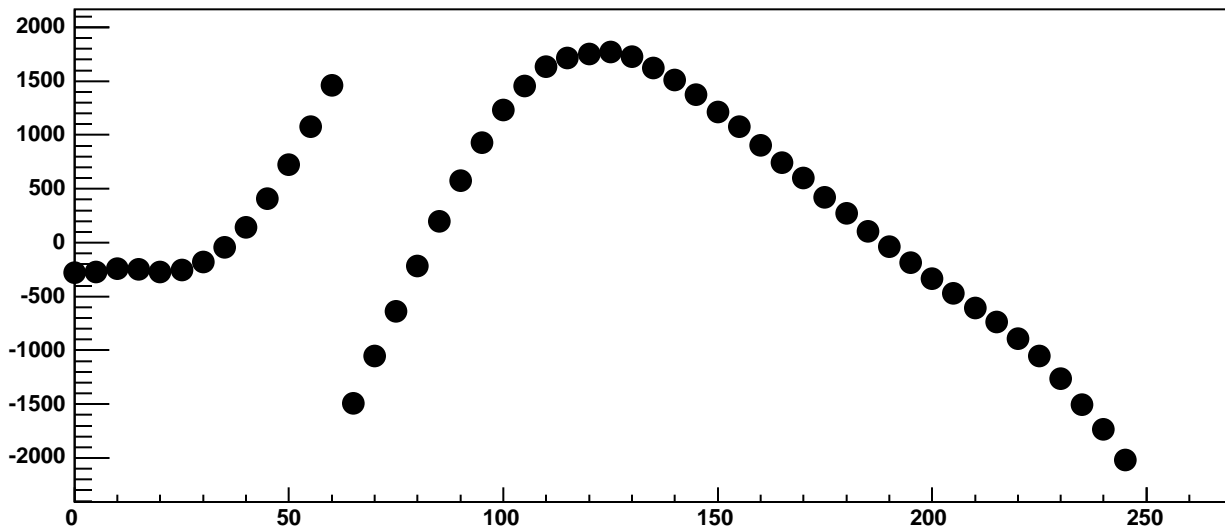


χ^2 / ndf	7.946e+05 / 41
p0	-256.8 ± 0.1322
p1	63.6 ± 0.3556
p2	3345 ± 0.9423
p3	-1188 ± 1.103
p4	-0.09134 ± 0.01108

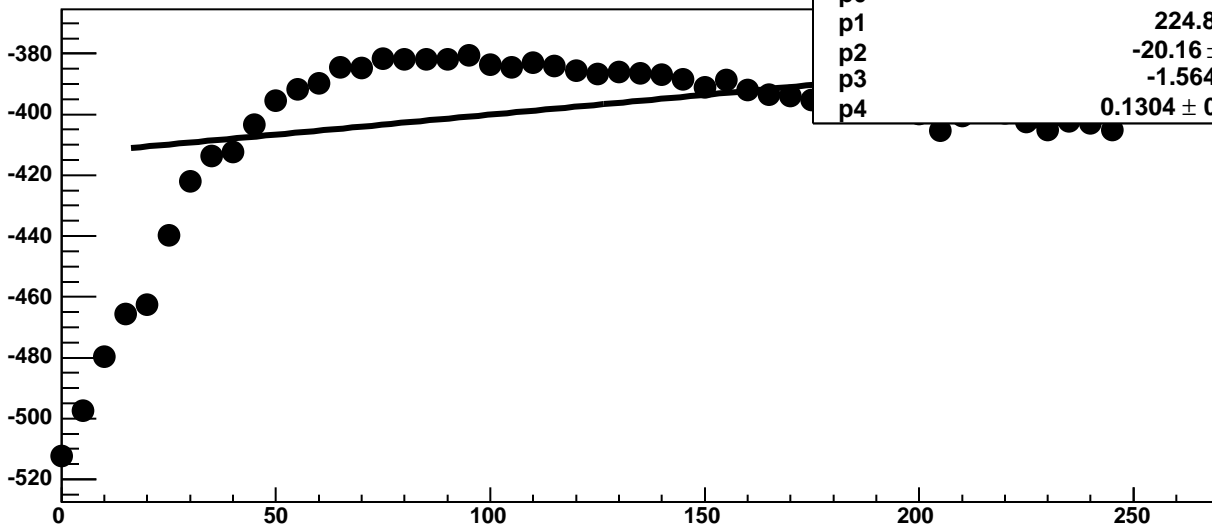
Chip 11, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

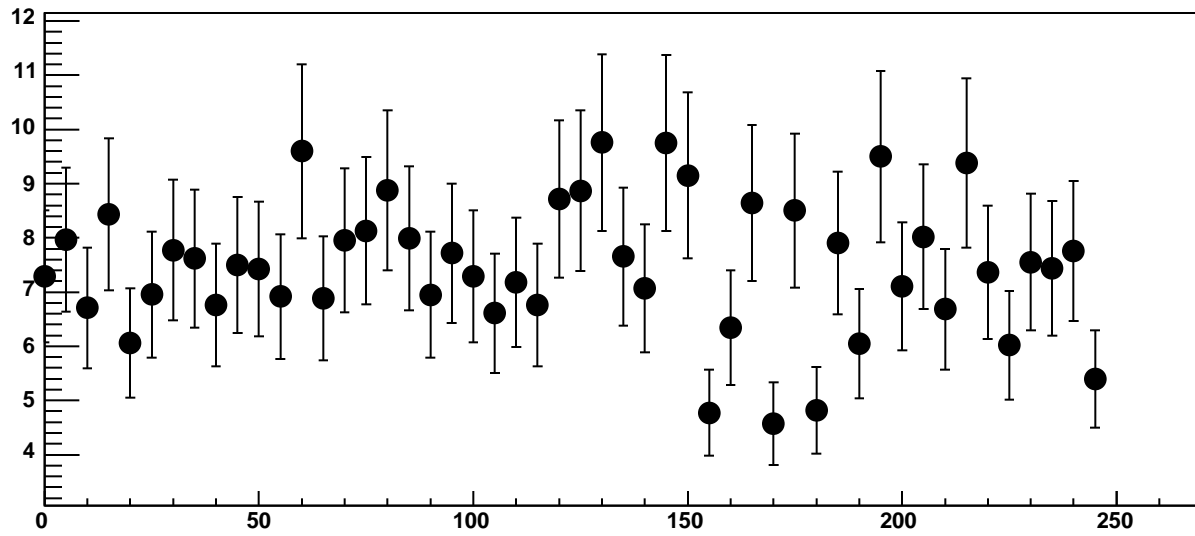


Chip 11, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold

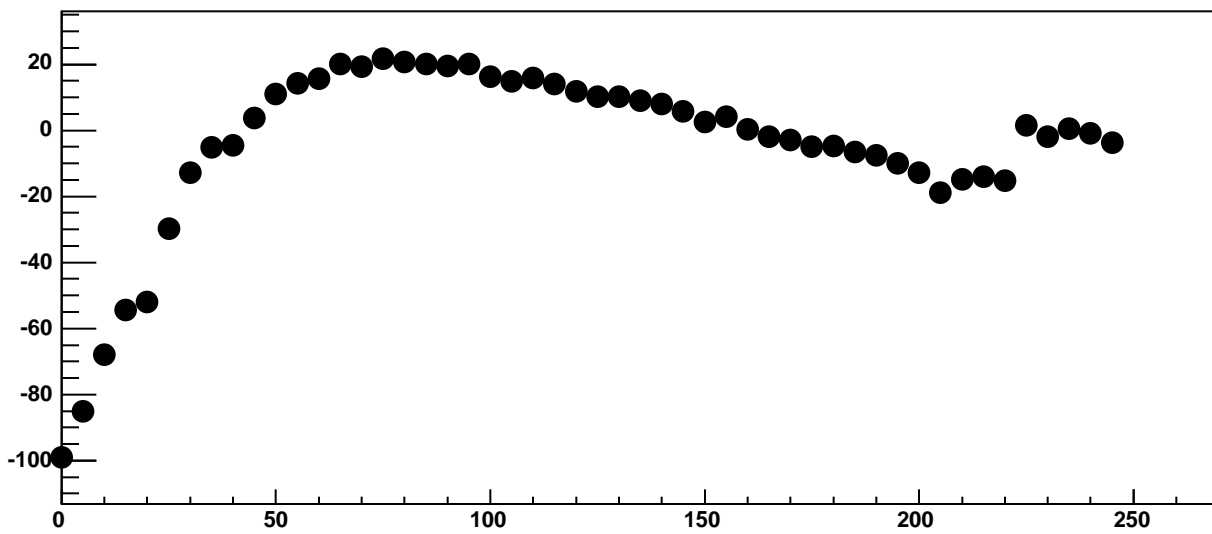


χ^2 / ndf	4693 / 41
p0	-383.8 ± 0.613
p1	224.8 ± 17.53
p2	-20.16 ± 0.9884
p3	-1.564 ± 8.693
p4	0.1304 ± 0.002722

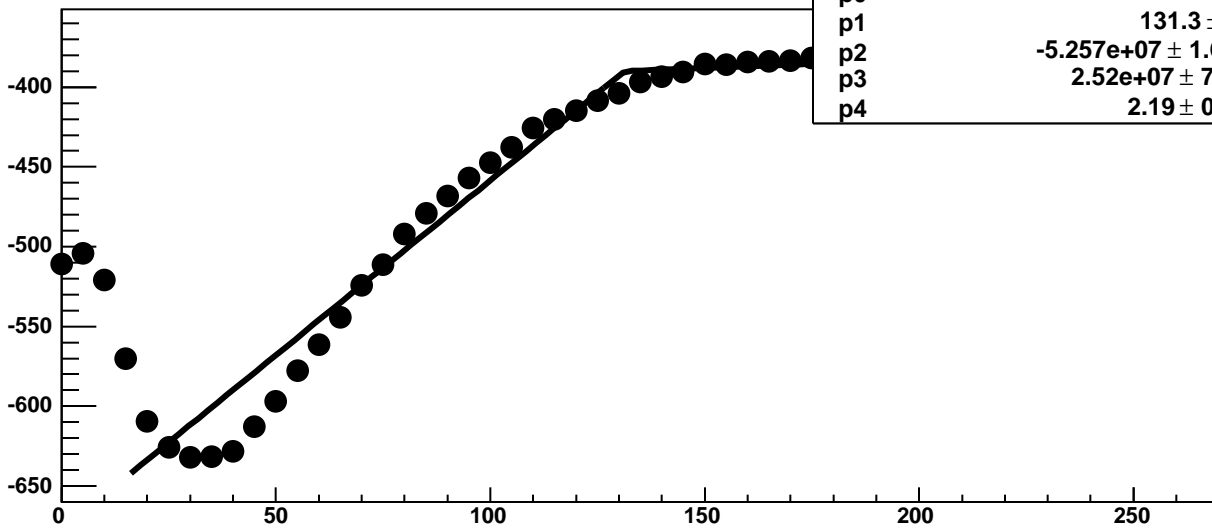
Chip 11, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold

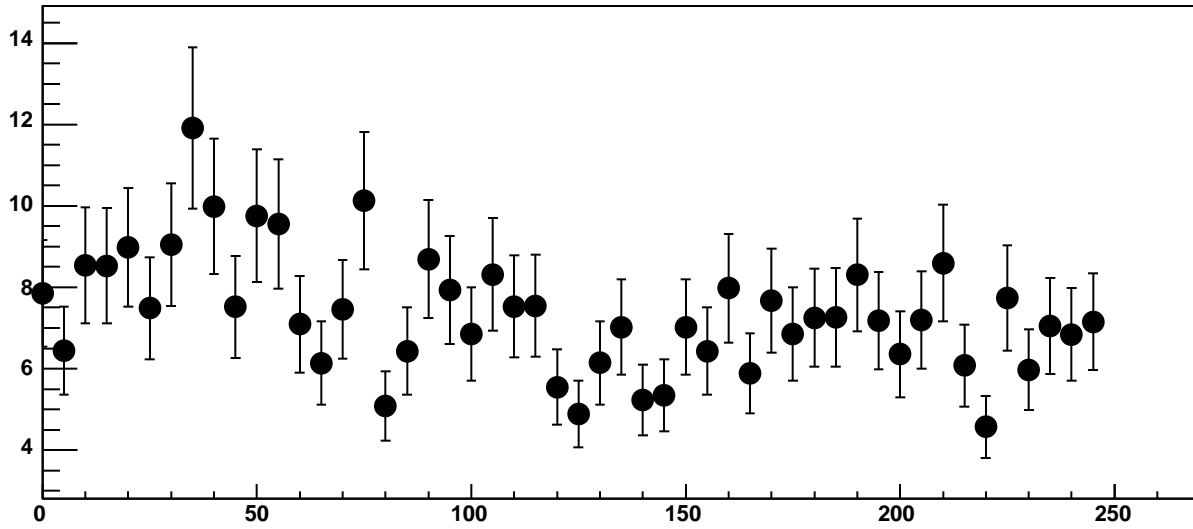


Chip 11, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

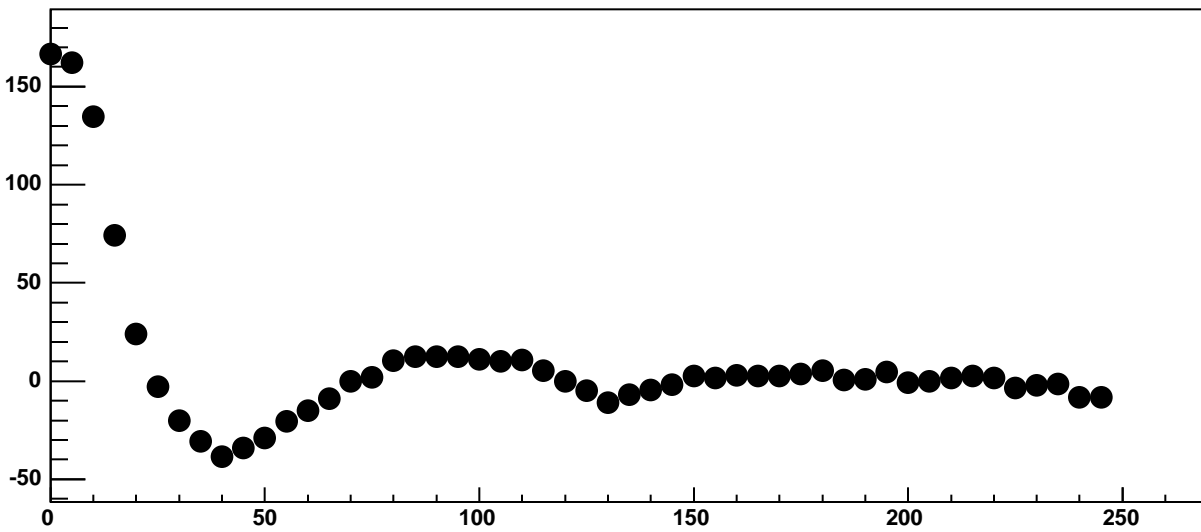


χ^2 / ndf	3416 / 41
p0	-389.9 ± 0.6719
p1	131.3 ± 0.4194
p2	$-5.257\text{e}+07 \pm 1.629\text{e}+06$
p3	$2.52\text{e}+07 \pm 7.36\text{e}+05$
p4	2.19 ± 0.009848

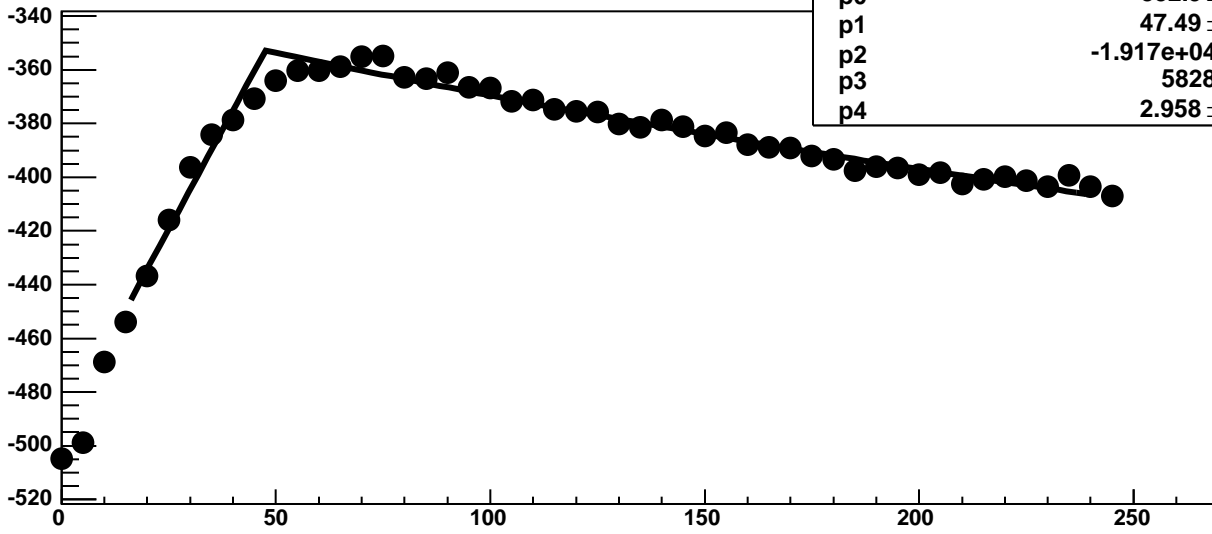
Chip 11, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

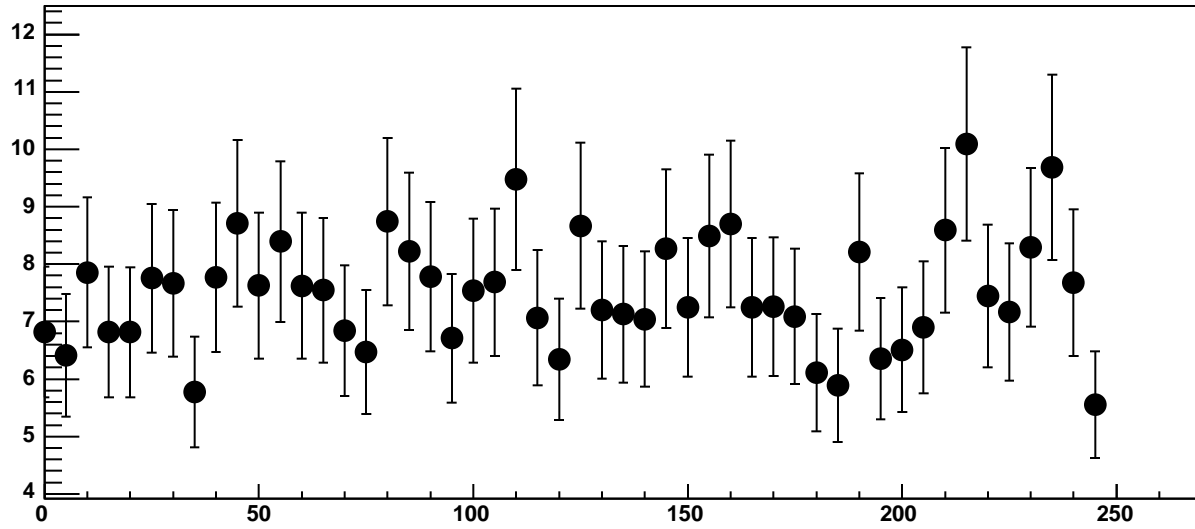


Chip 11, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

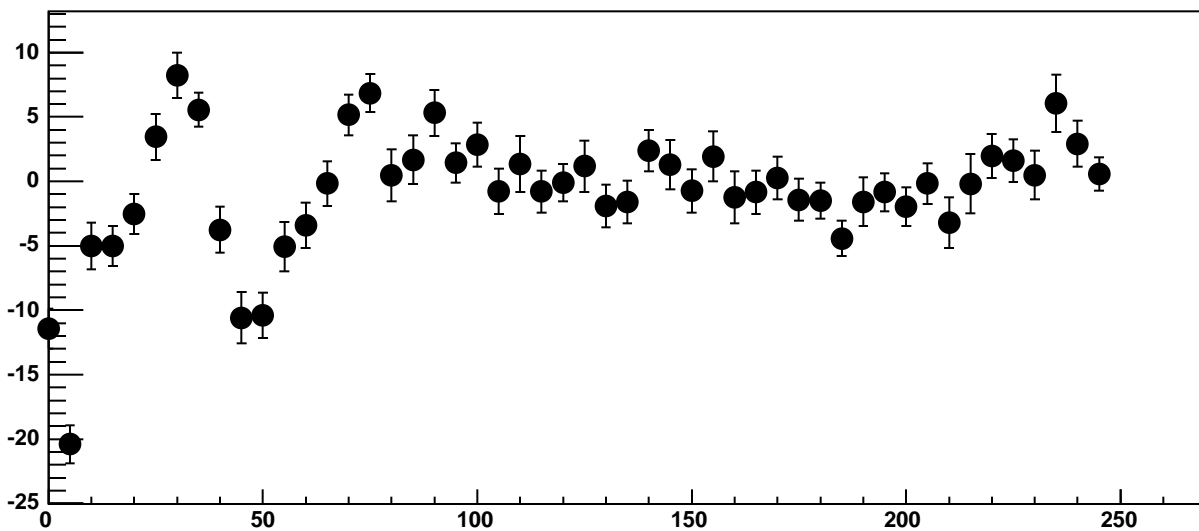


χ^2 / ndf	218.9 / 41
p0	-352.9 ± 0.7675
p1	47.49 ± 0.4785
p2	$-1.917\text{e}+04 \pm 6825$
p3	5828 ± 2099
p4	2.958 ± 0.0644

Chip 11, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold

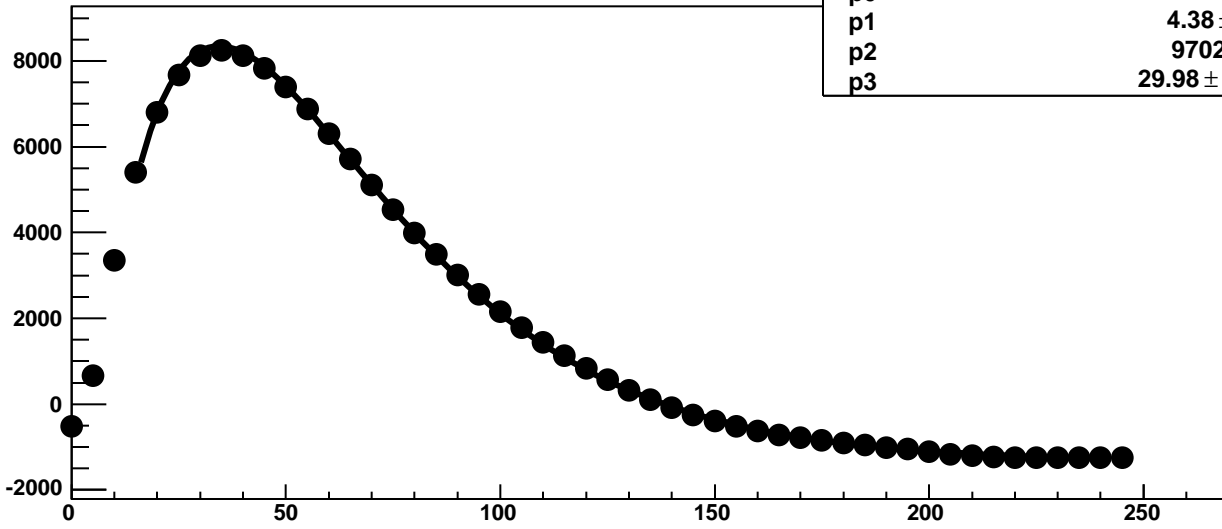


Chip 11, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

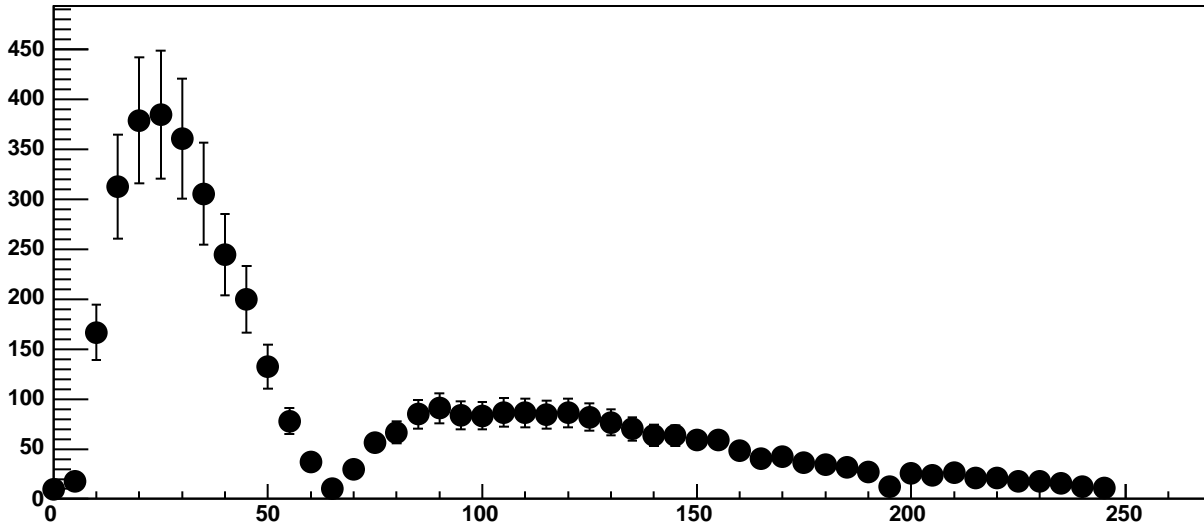


Chip 11, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold

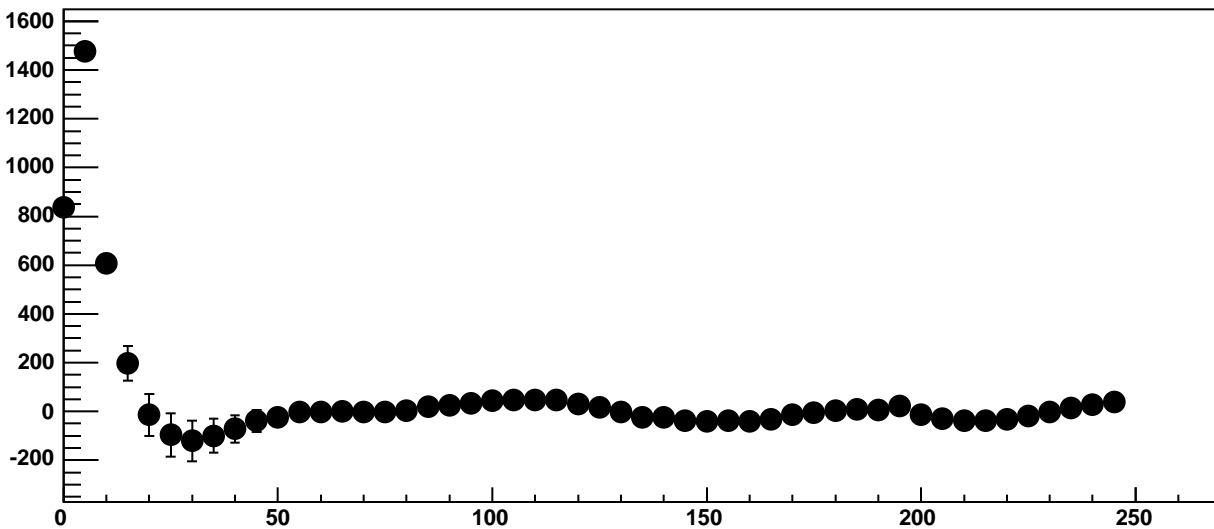
χ^2 / ndf	488.3 / 42
p0	-1351 ± 1.846
p1	4.38 ± 0.1257
p2	9702 ± 14.82
p3	29.98 ± 0.04563



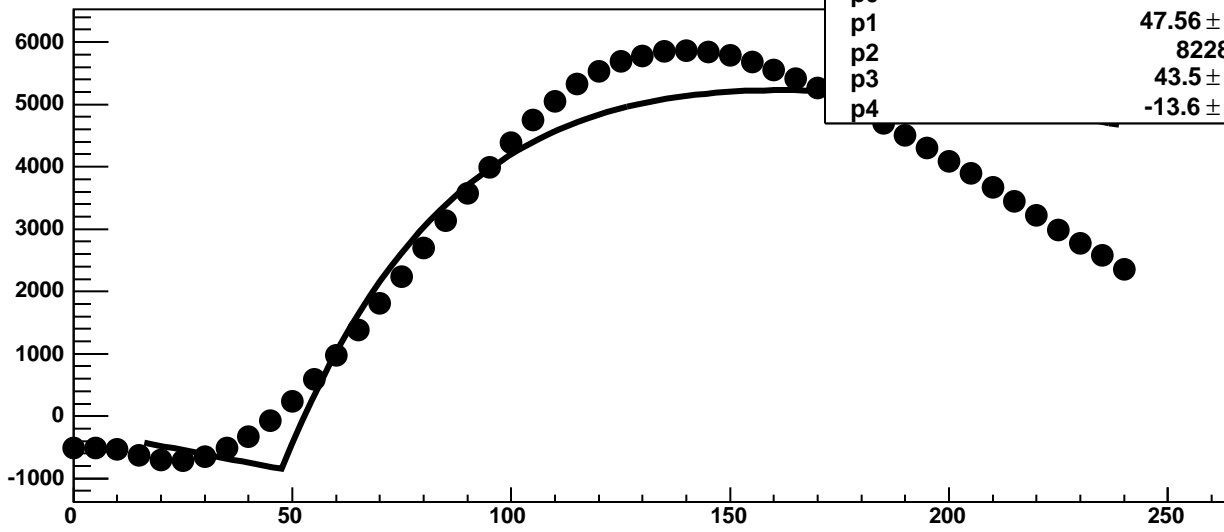
Chip 11, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold

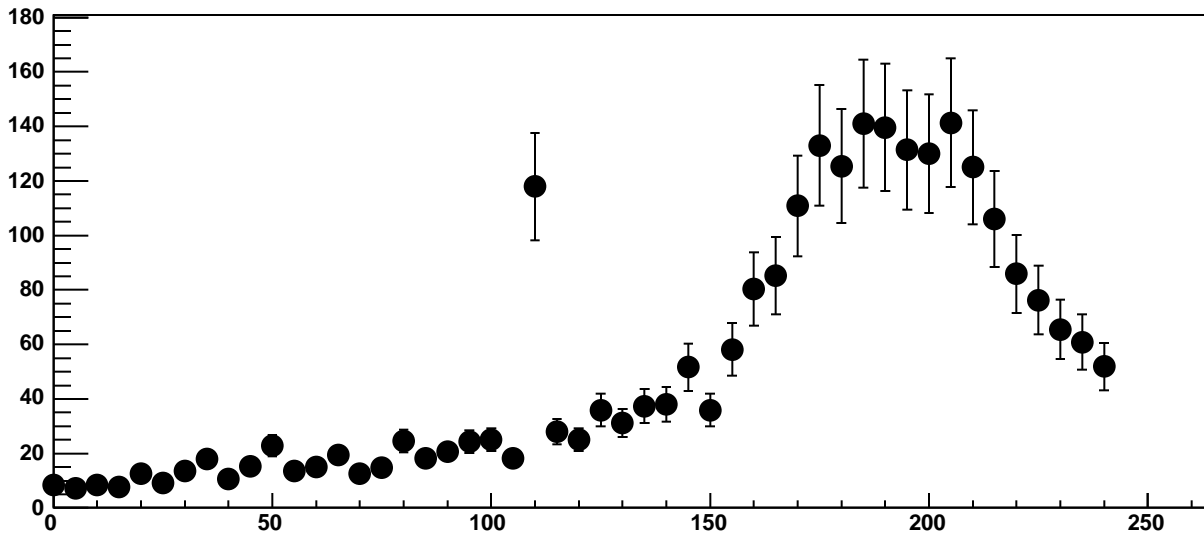


Chip 11, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

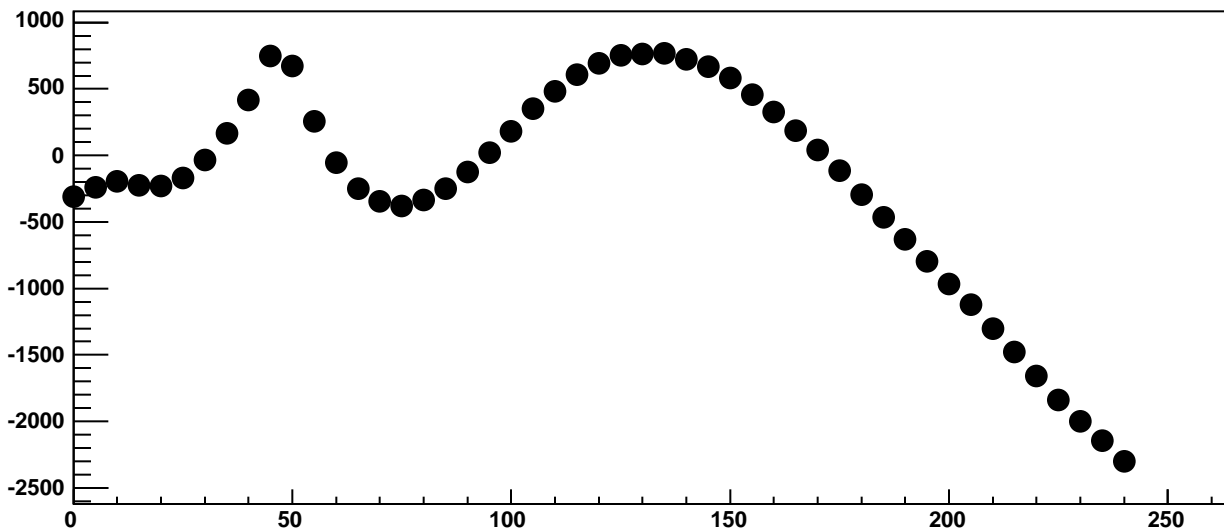


χ^2 / ndf	3.498e+05 / 41
p0	-851 ± 1.586
p1	47.56 ± 0.01866
p2	8228 ± 11.92
p3	43.5 ± 0.06944
p4	-13.6 ± 0.06588

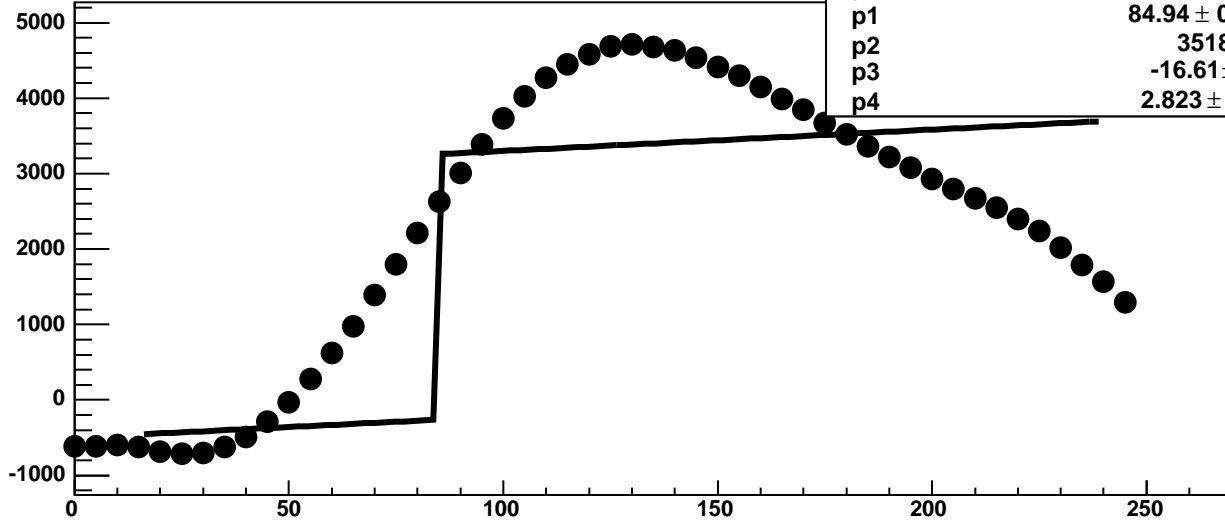
Chip 11, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

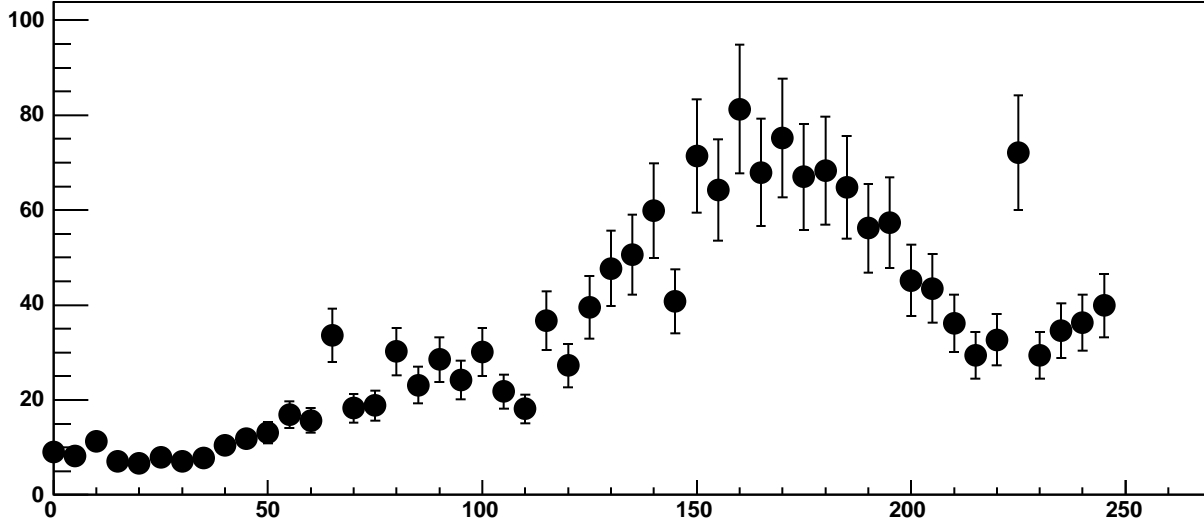


Chip 11, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

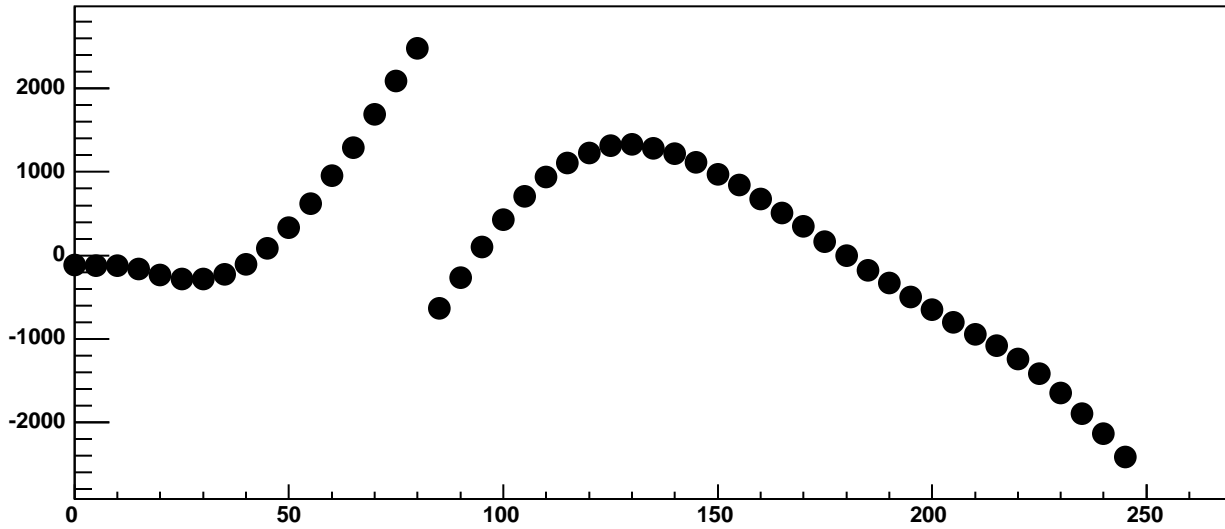


χ^2 / ndf	1.266e+06 / 41
p0	-260.5 ± 6.91
p1	84.94 ± 0.003692
p2	3518 ± 5.681
p3	-16.61 ± 0.6758
p4	2.823 ± 0.08297

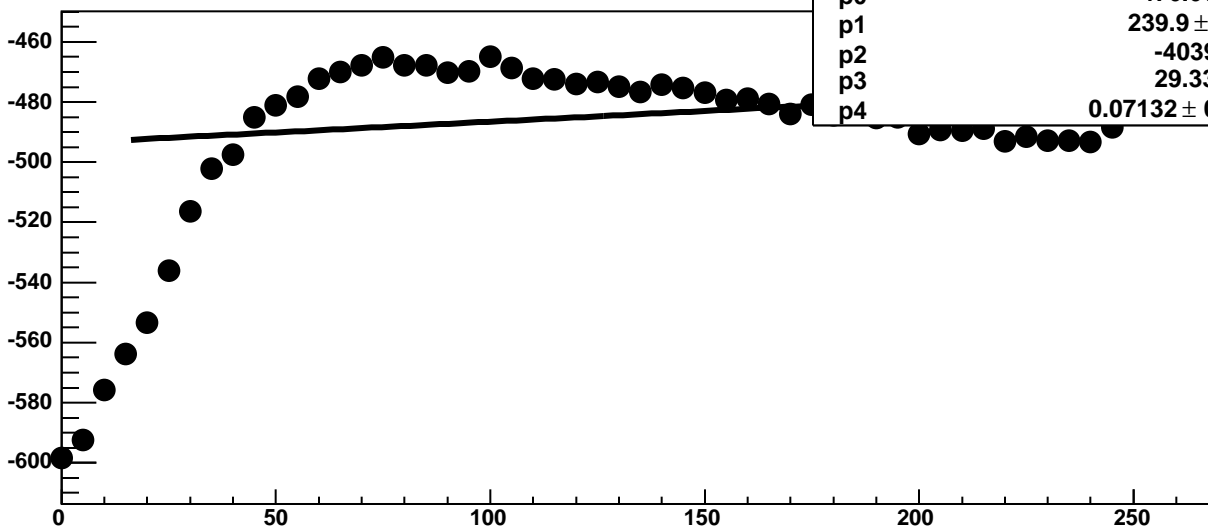
Chip 11, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold

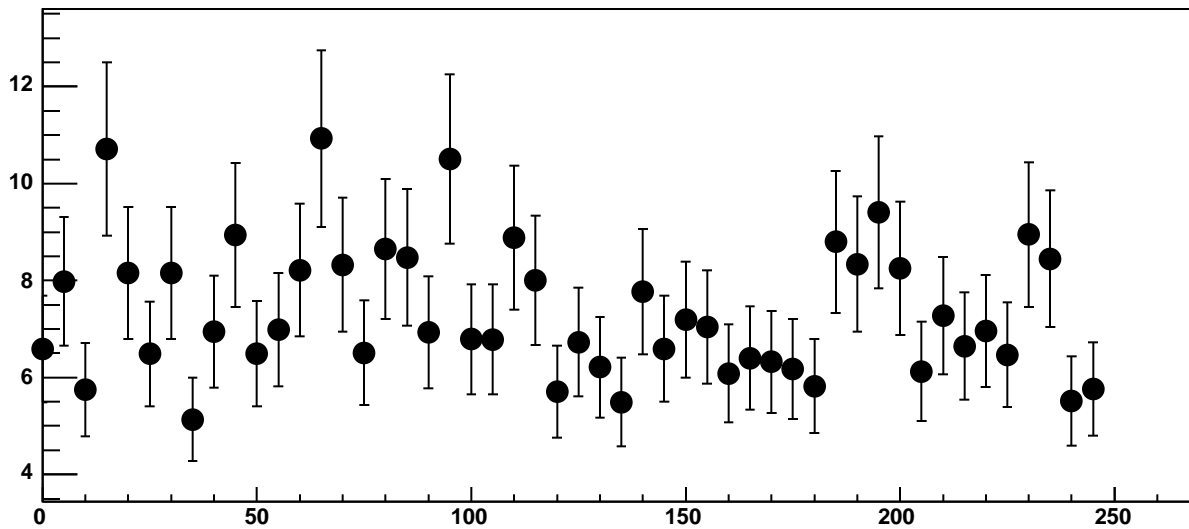


Chip 11, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

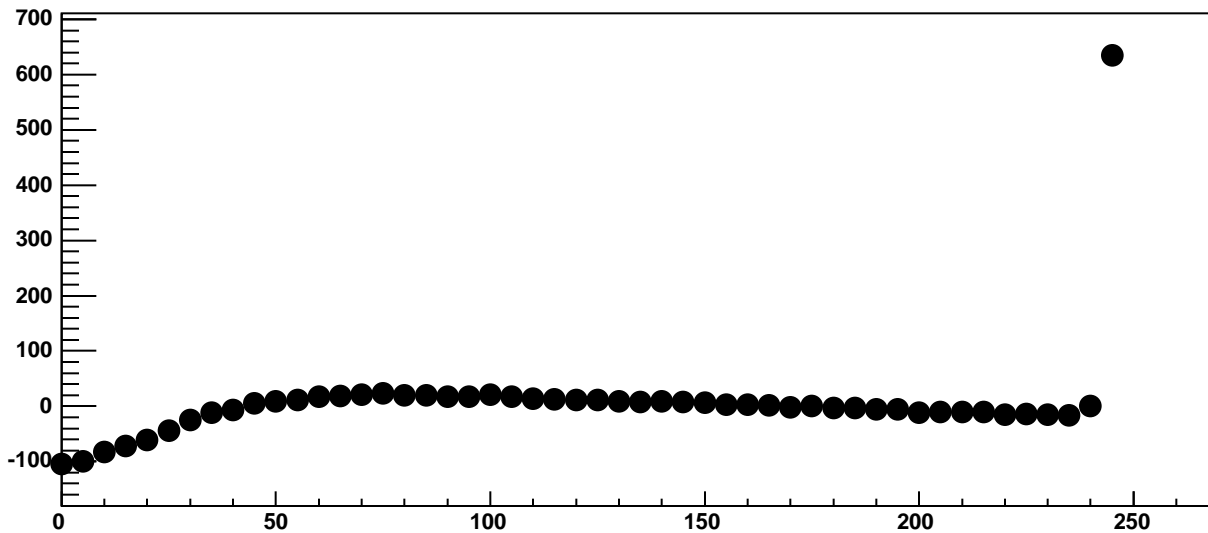


χ^2 / ndf	5245 / 41
p0	-476.6 ± 0.4995
p1	239.9 ± 0.01089
p2	-4039 ± 84.85
p3	29.33 ± 1.439
p4	0.07132 ± 0.003872

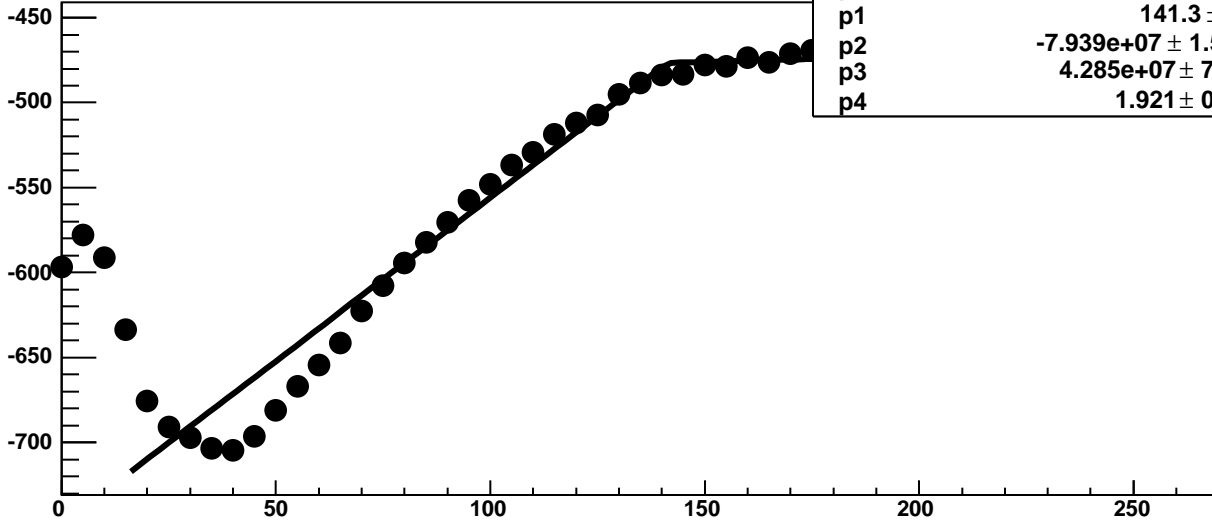
Chip 11, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

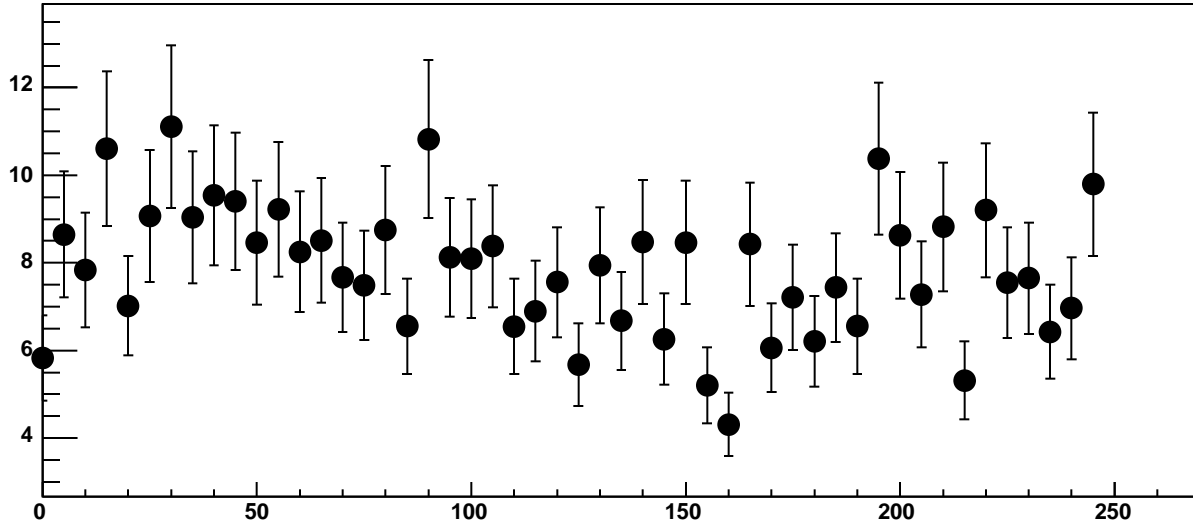


Chip 11, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold

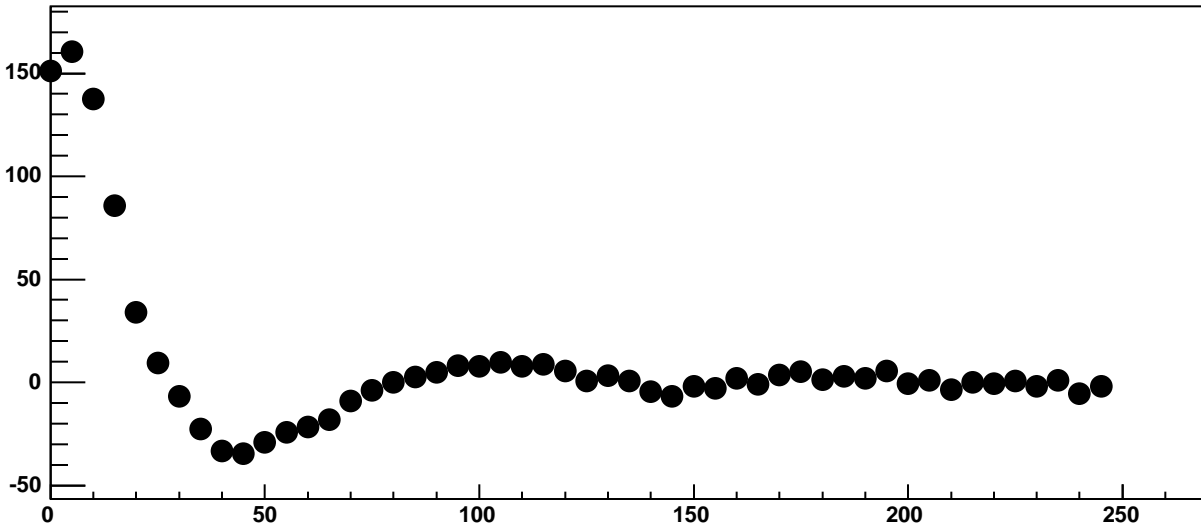


χ^2 / ndf	3177 / 41
p0	-476.6 ± 0.6712
p1	141.3 ± 0.4975
p2	-7.939e+07 ± 1.551e+06
p3	4.285e+07 ± 7.58e+05
p4	1.921 ± 0.009636

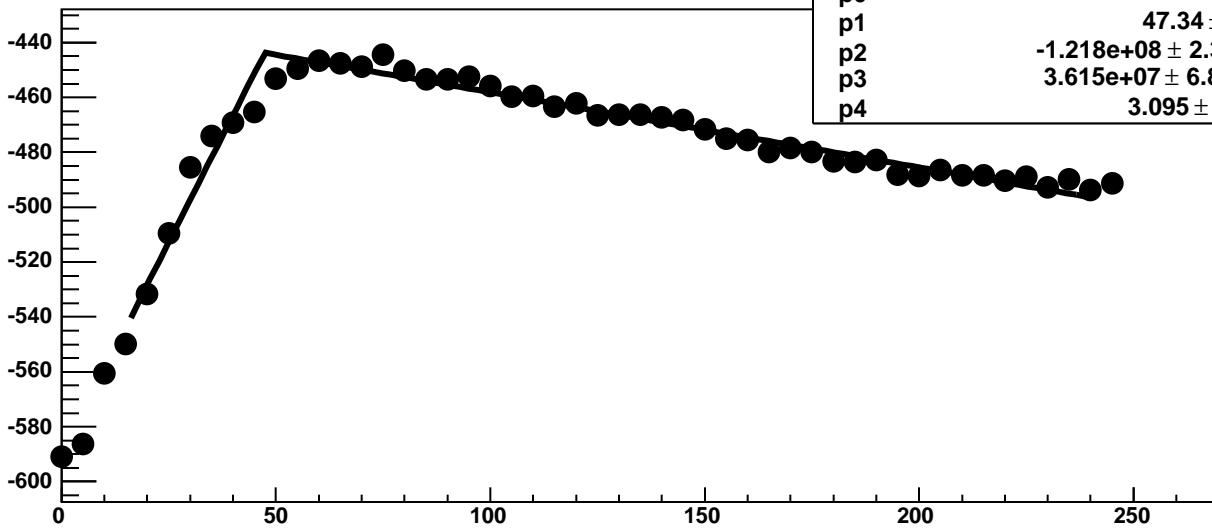
Chip 11, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold

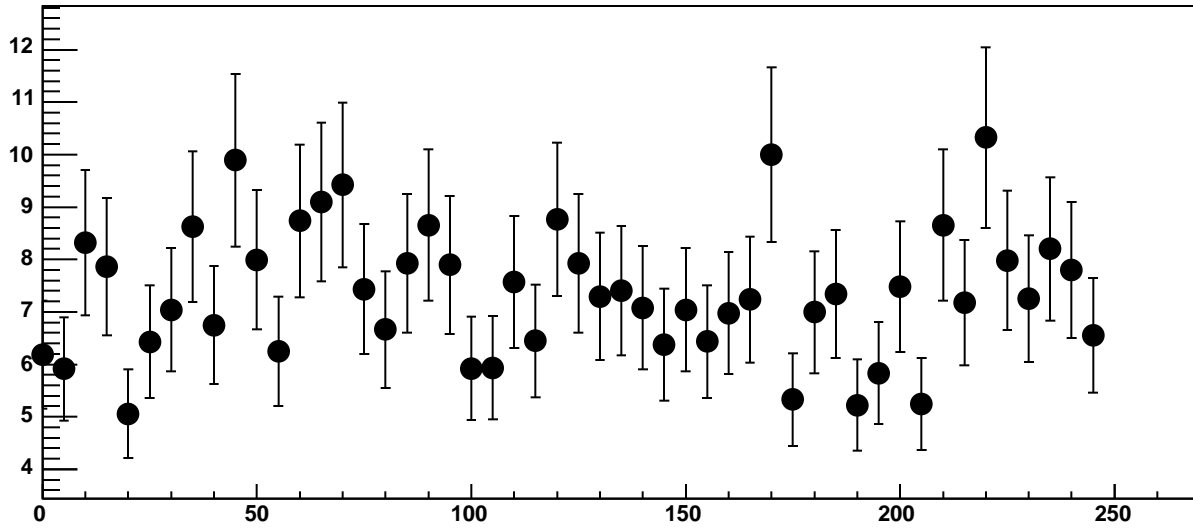


Chip 11, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

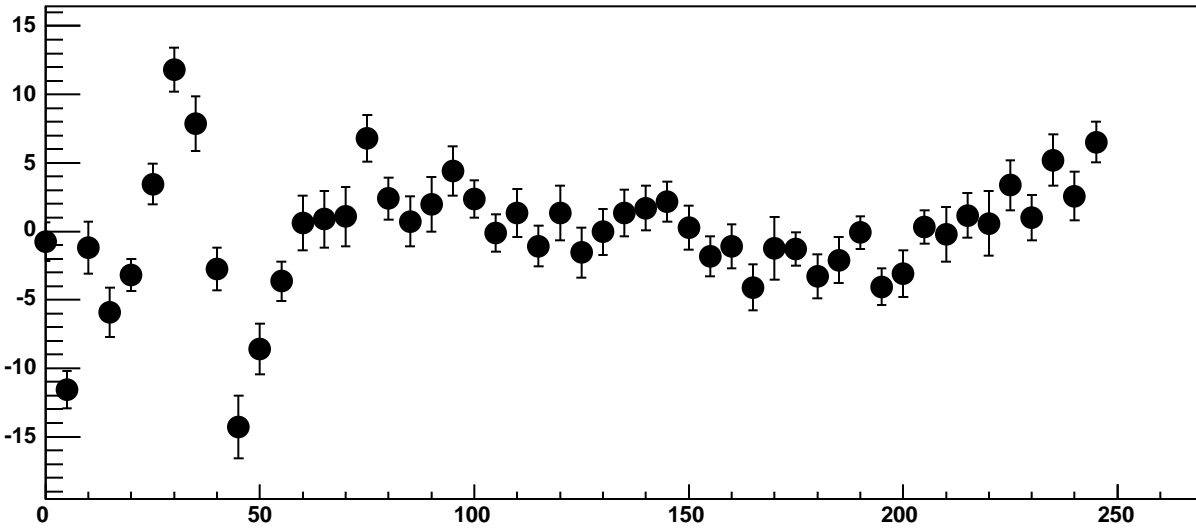


χ^2 / ndf	241.2 / 41
p0	-443.7 ± 0.5182
p1	47.34 ± 0.4463
p2	$-1.218\text{e}+08 \pm 2.379\text{e}+06$
p3	$3.615\text{e}+07 \pm 6.826\text{e}+04$
p4	3.095 ± 0.06512

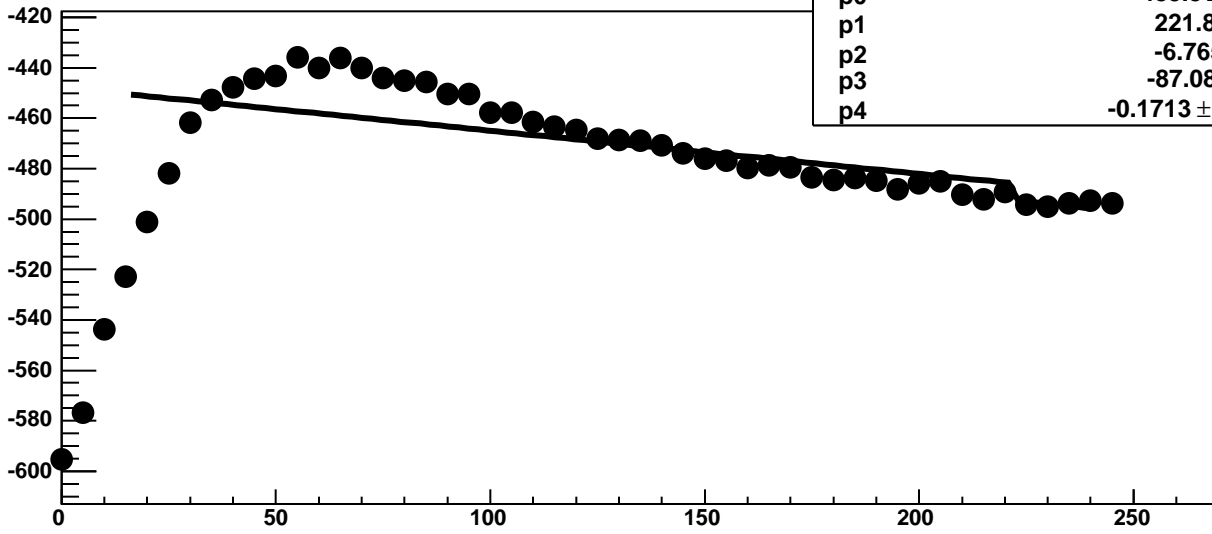
Chip 11, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

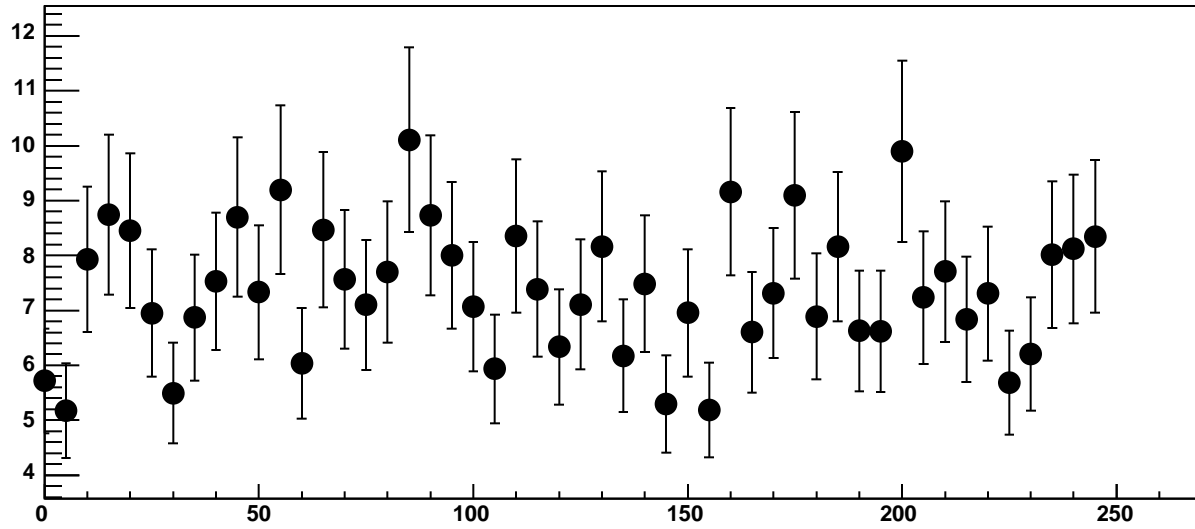


Chip 11, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

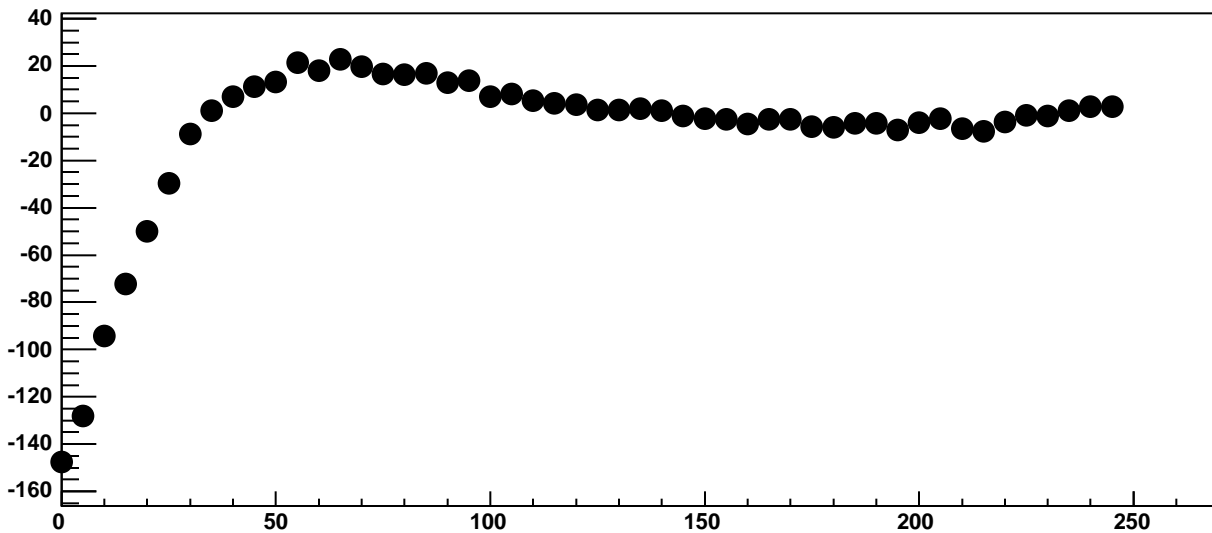


χ^2 / ndf	3552 / 41
p0	-485.8 ± 0.8774
p1	221.8 ± 19.64
p2	-6.765 ± 2.05
p3	-87.08 ± 201.7
p4	-0.1713 ± 0.01831

Chip 11, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold

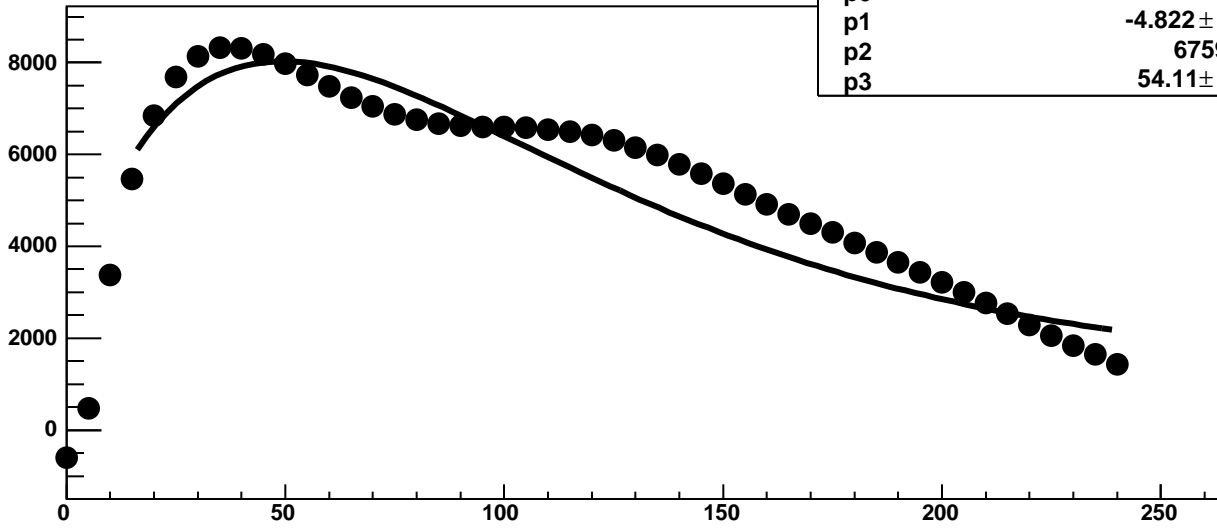


Chip 11, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

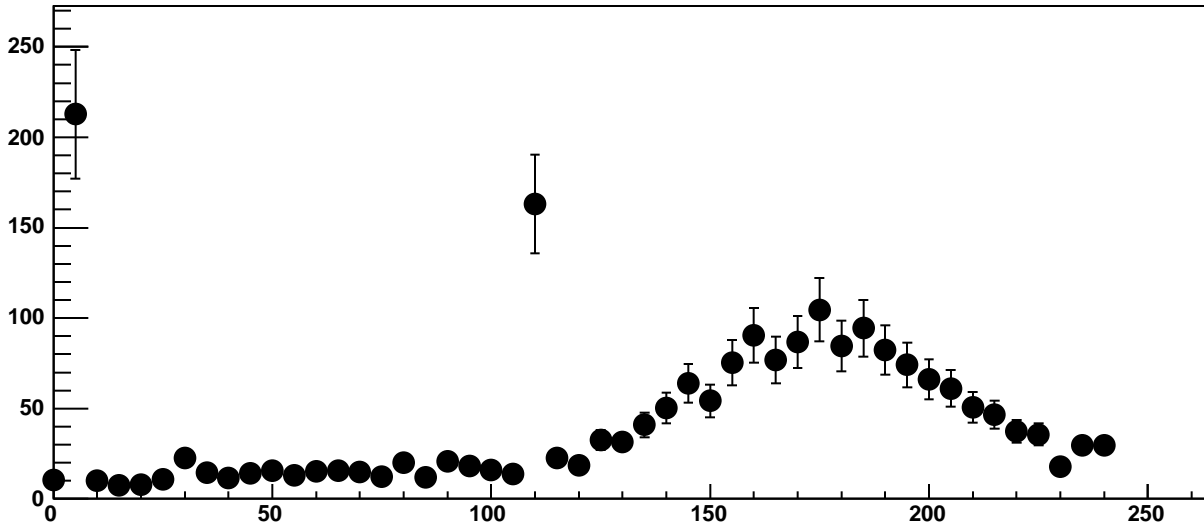


Chip 11, Channel 15, Enable 5!, DAC=4000, ADC Mean vs Hold

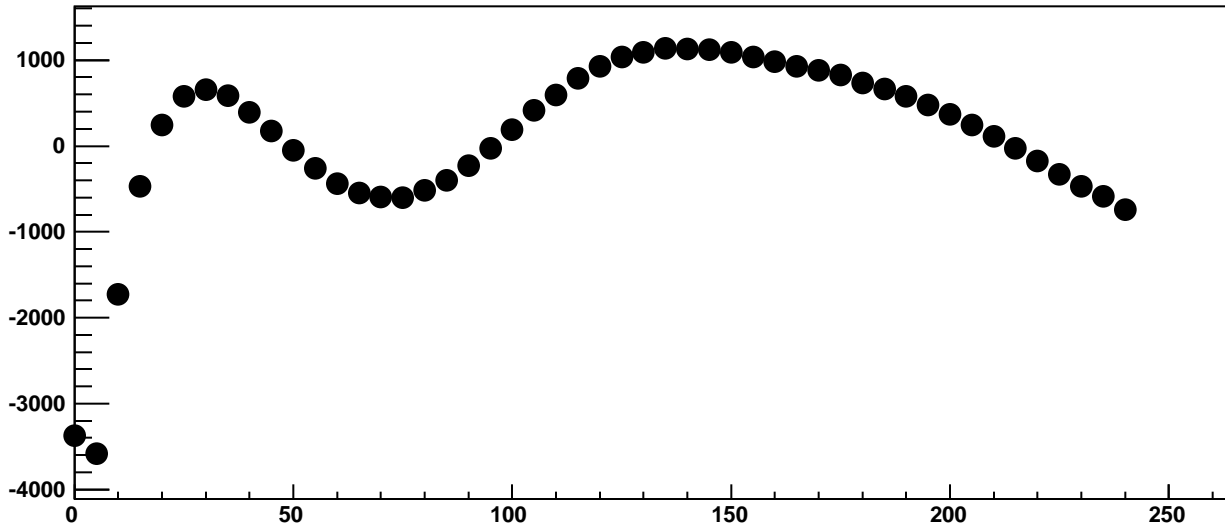
χ^2 / ndf	5.969e+05 / 42
p0	1271 ± 5.551
p1	-4.822 ± 0.03812
p2	6759 ± 5.17
p3	54.11 ± 0.05014



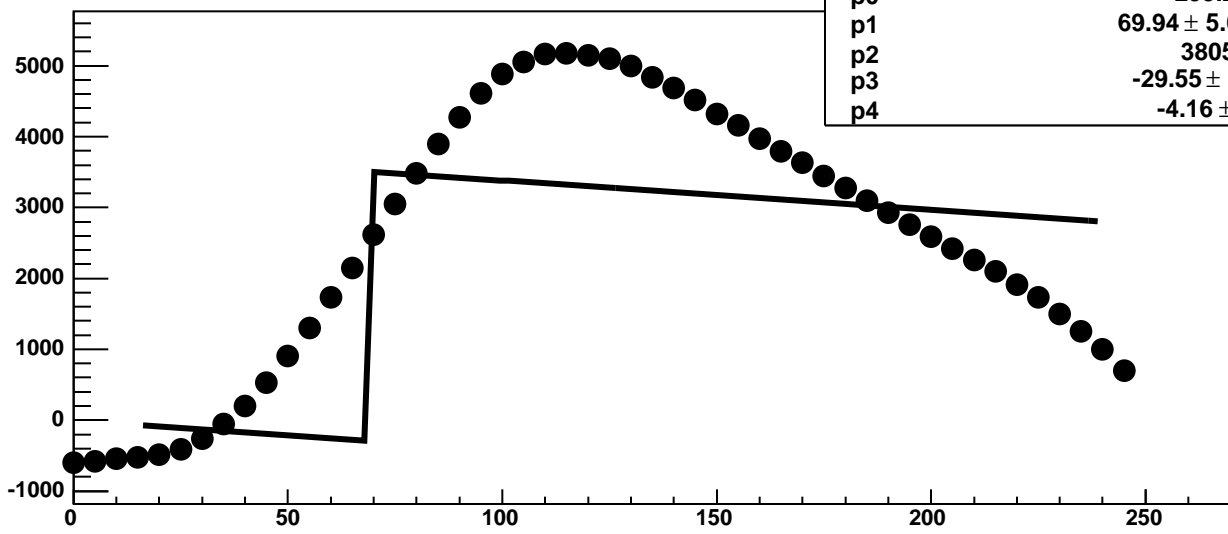
Chip 11, Channel 15, Enable 5!, DAC=4000, ADC Noise vs Hold



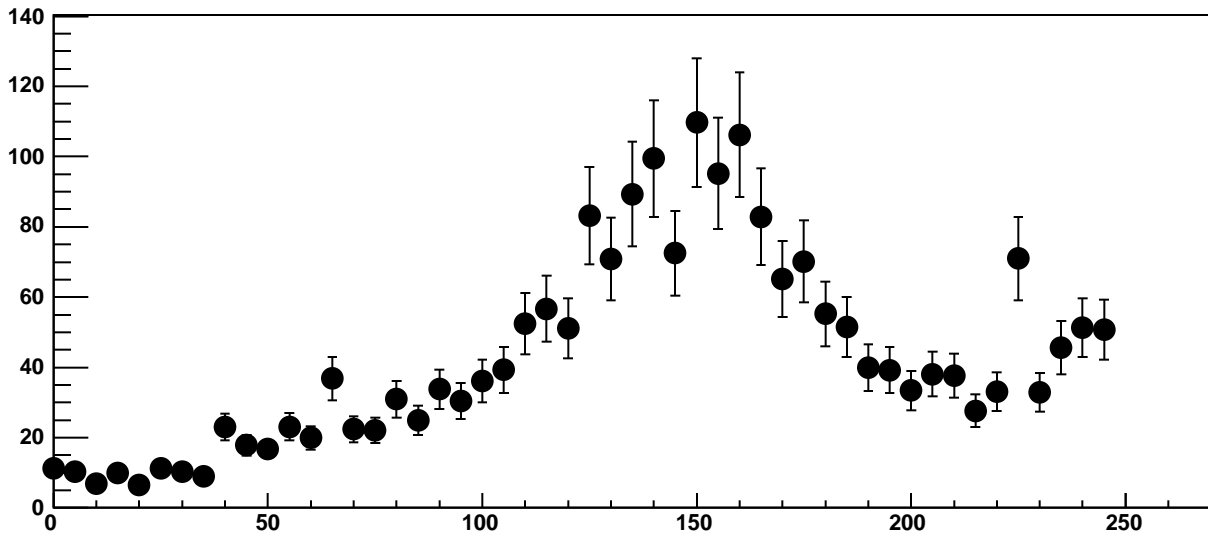
Chip 11, Channel 15, Enable 5!, DAC=4000, ADC Residuals vs Hold



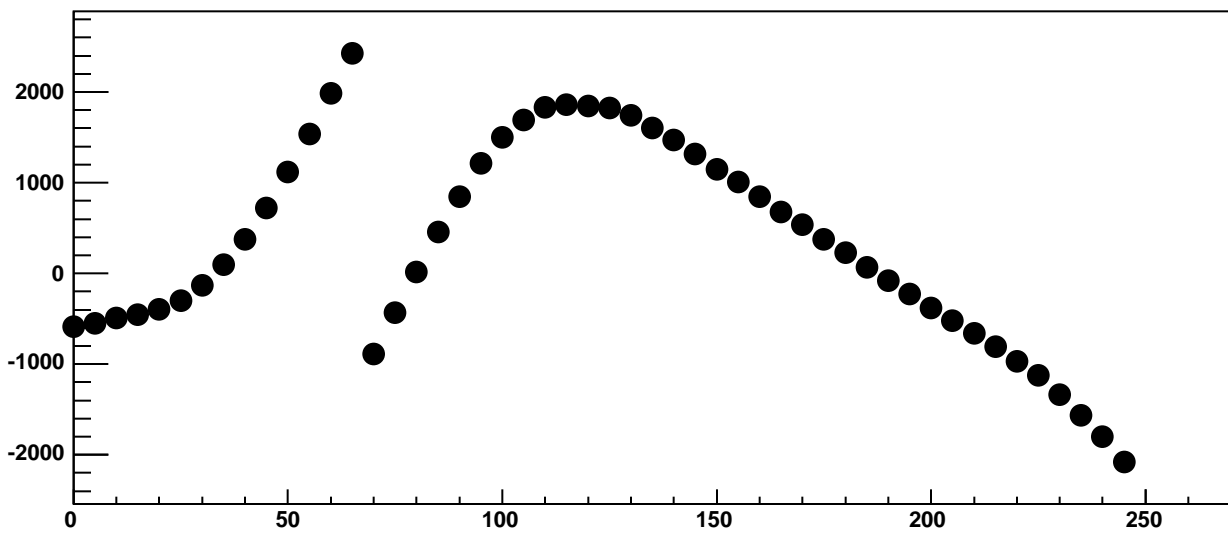
Chip 11, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

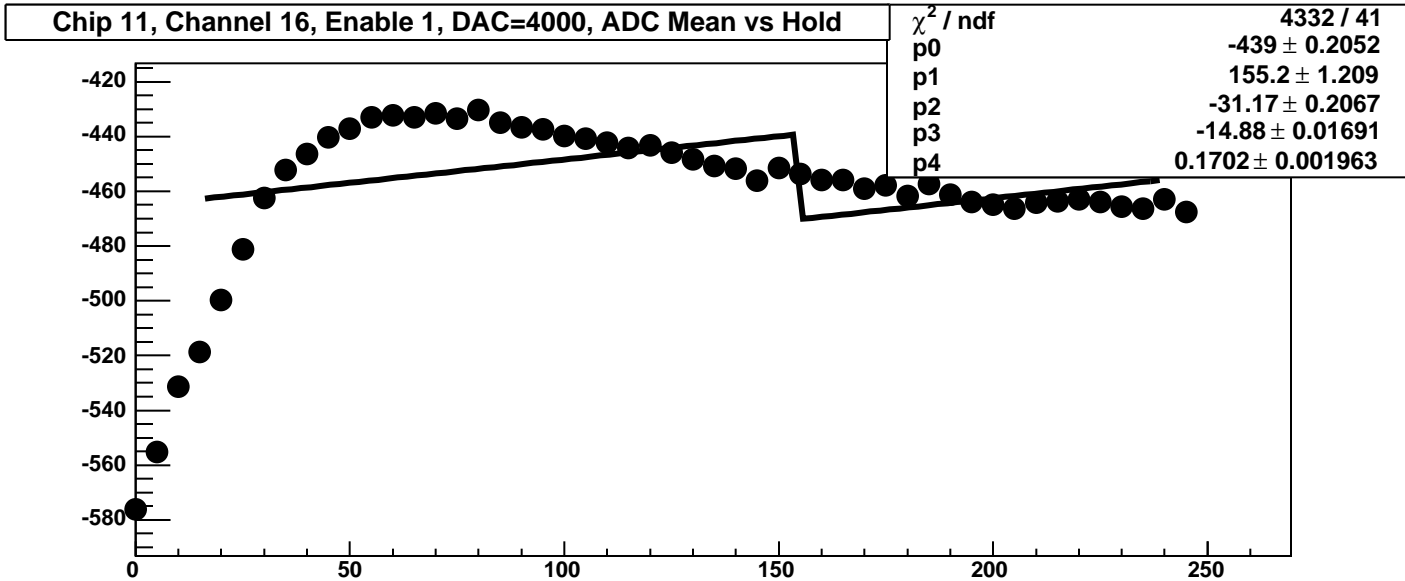


Chip 11, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold

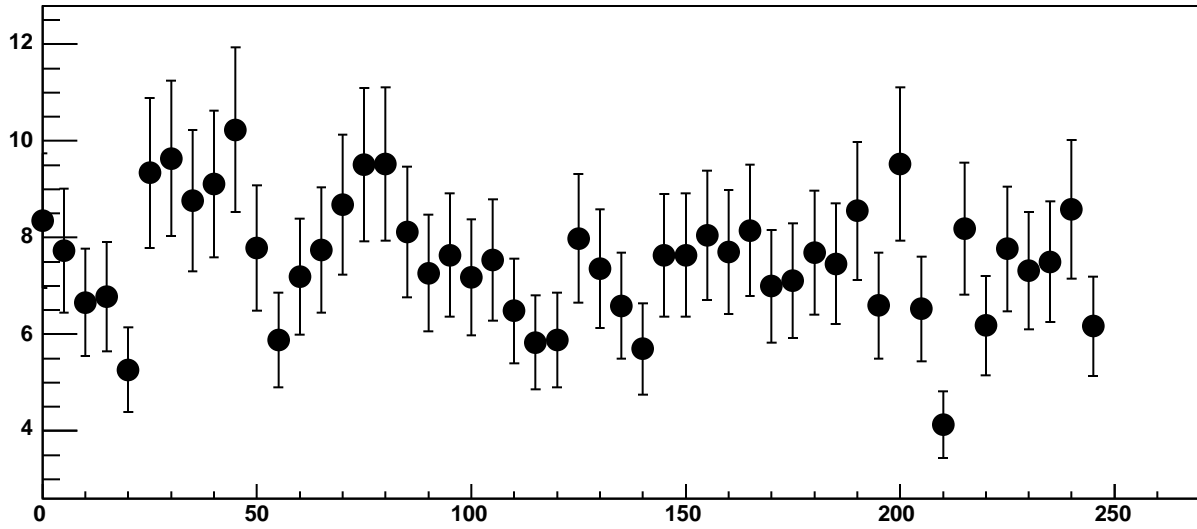


Chip 11, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

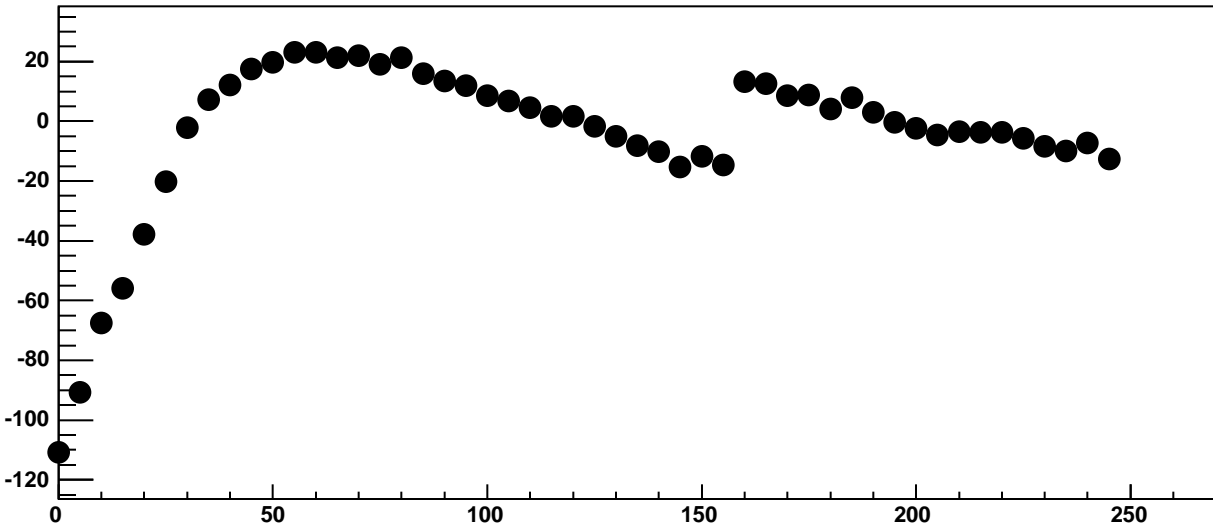




Chip 11, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold

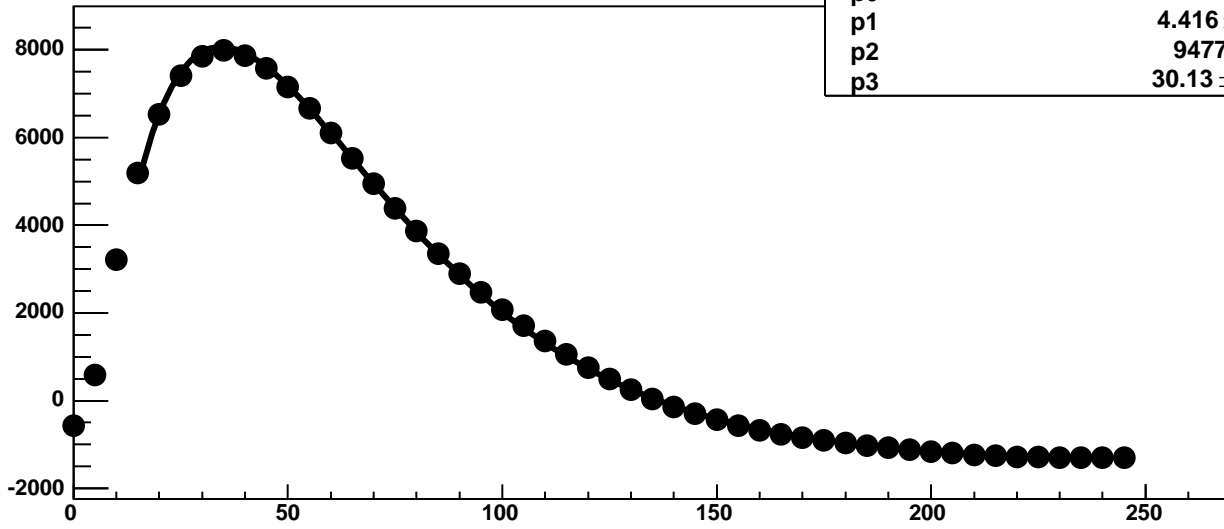


Chip 11, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold

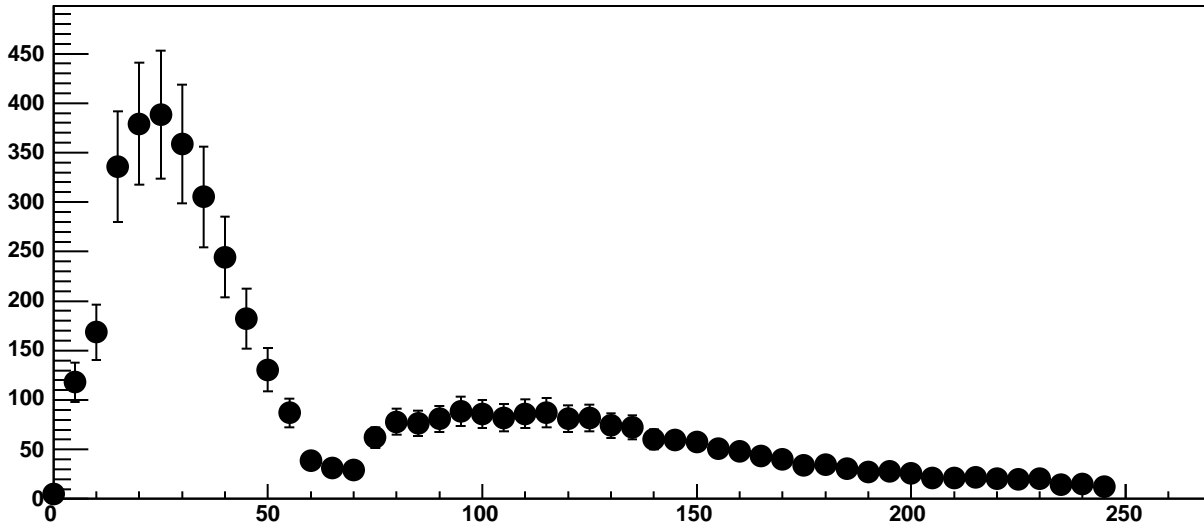


Chip 11, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold

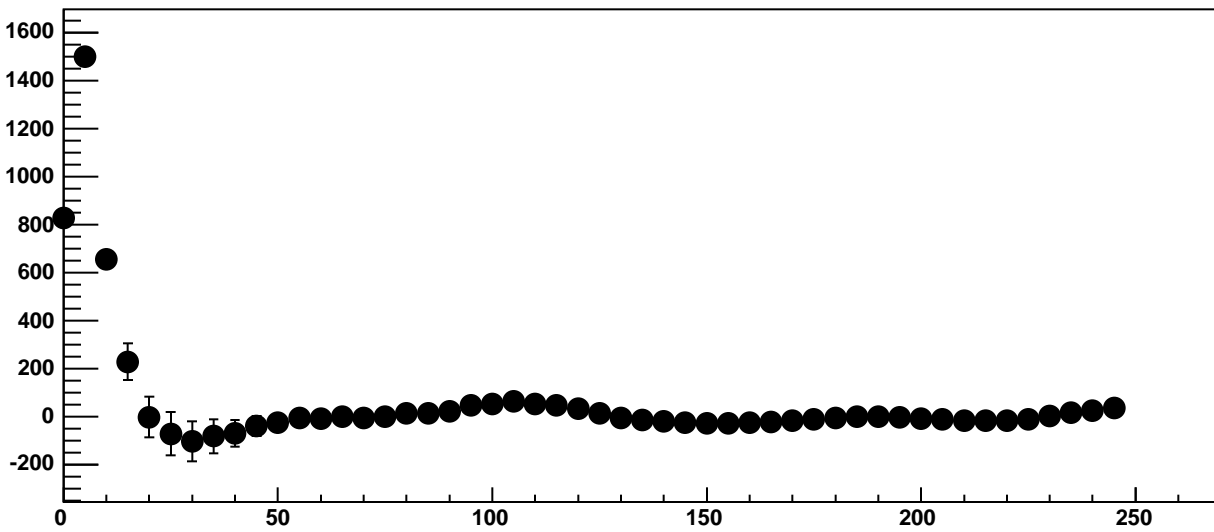
χ^2 / ndf	213.9 / 42
p0	-1406 ± 1.977
p1	4.416 ± 0.1351
p2	9477 ± 15.46
p3	30.13 ± 0.0486



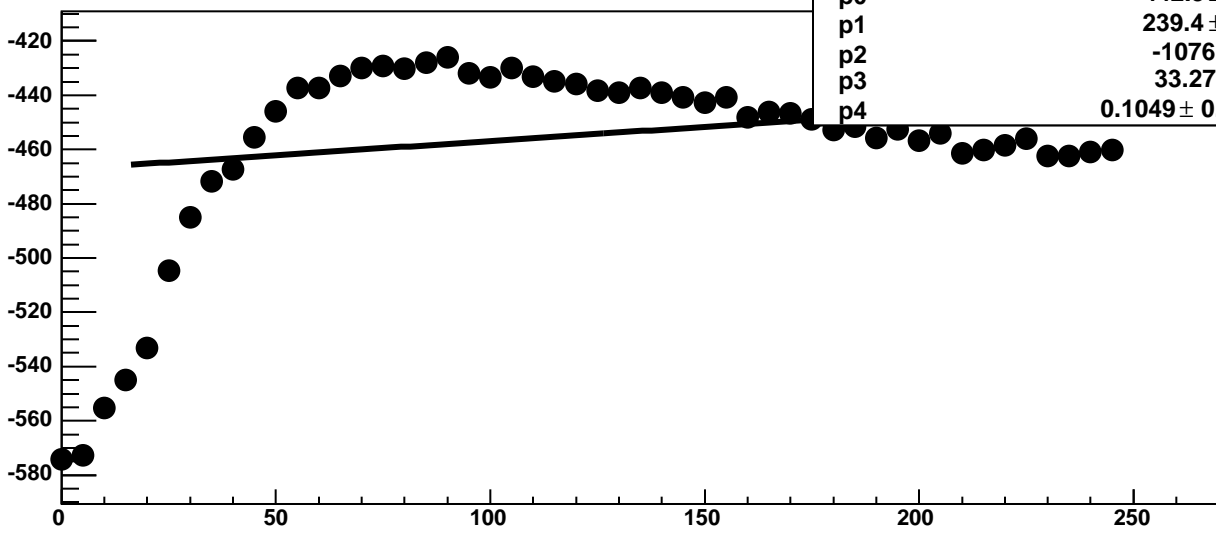
Chip 11, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold

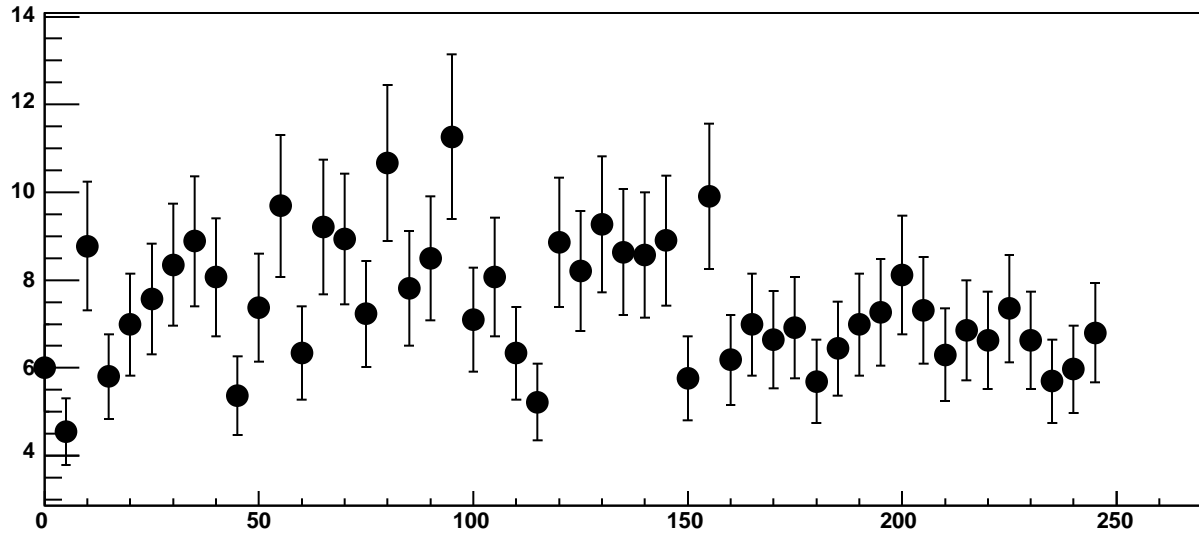


Chip 11, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

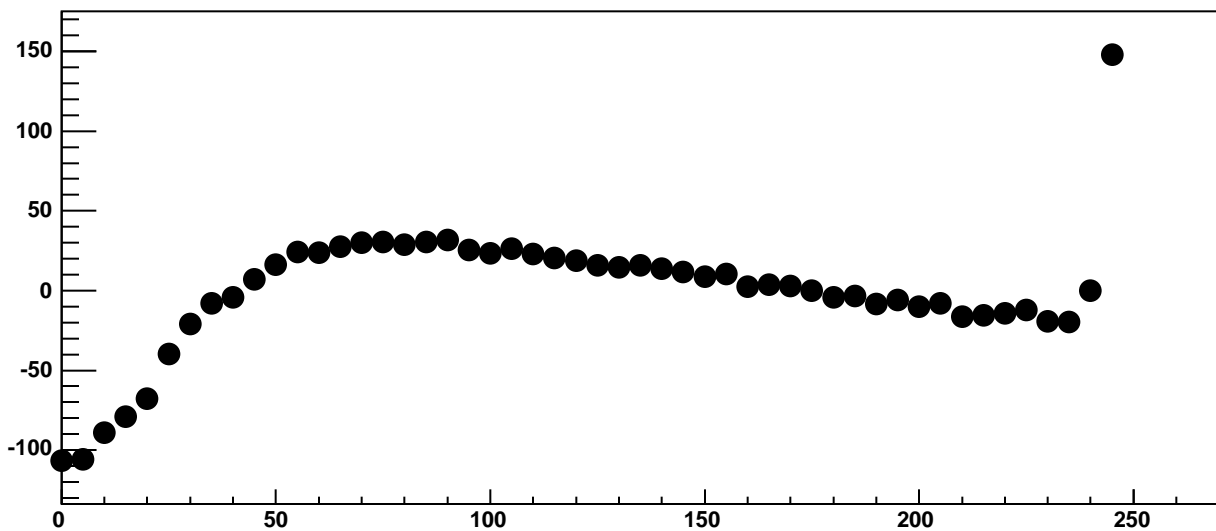


χ^2 / ndf	1.024e+04 / 41
p0	-442.3 ± 0.4712
p1	239.4 ± 0.3947
p2	-1076 ± 718.7
p3	33.27 ± 18.38
p4	0.1049 ± 0.003665

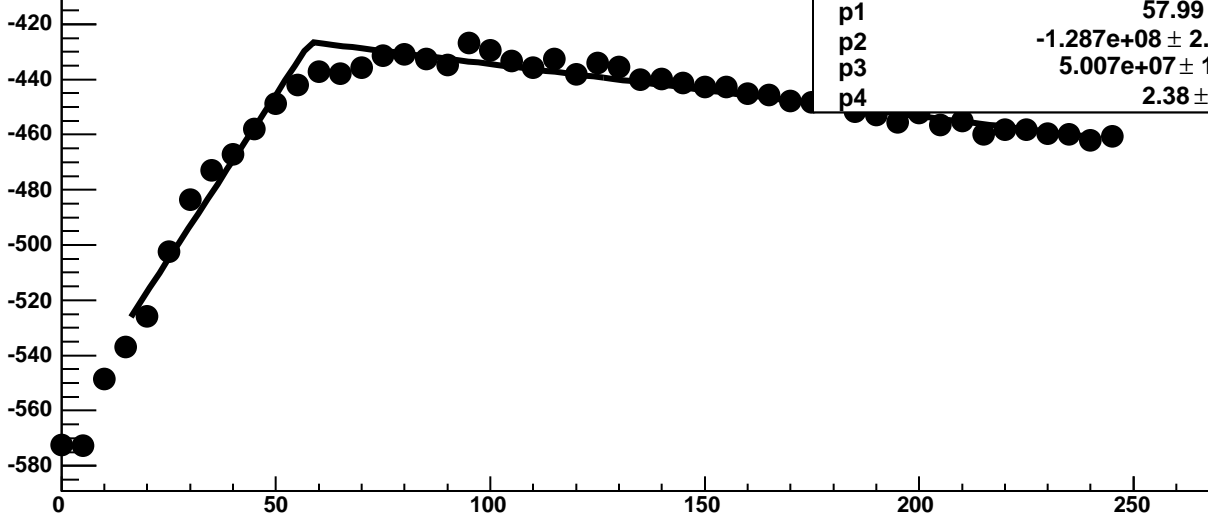
Chip 11, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

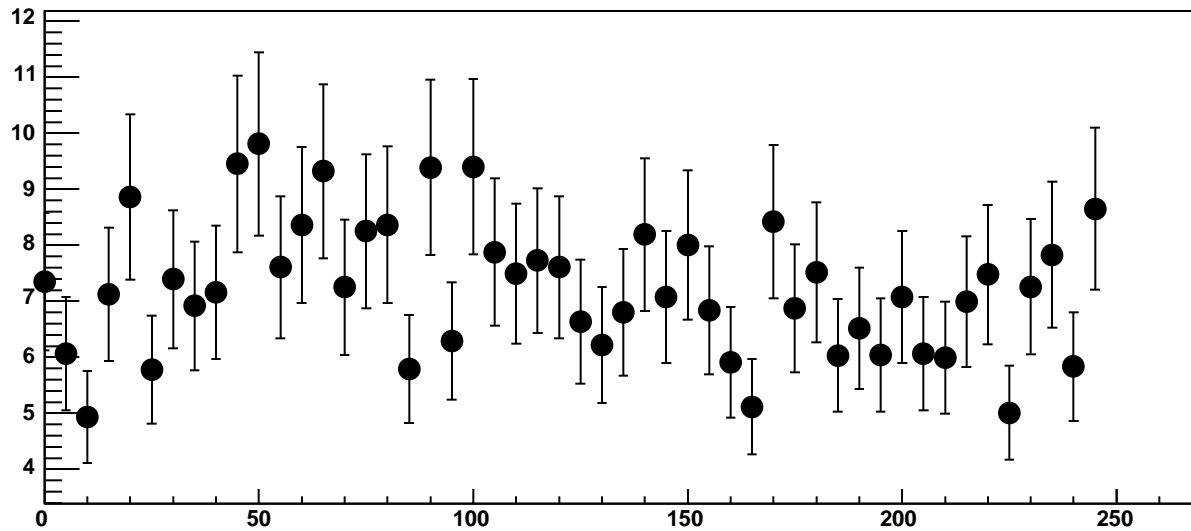


Chip 11, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

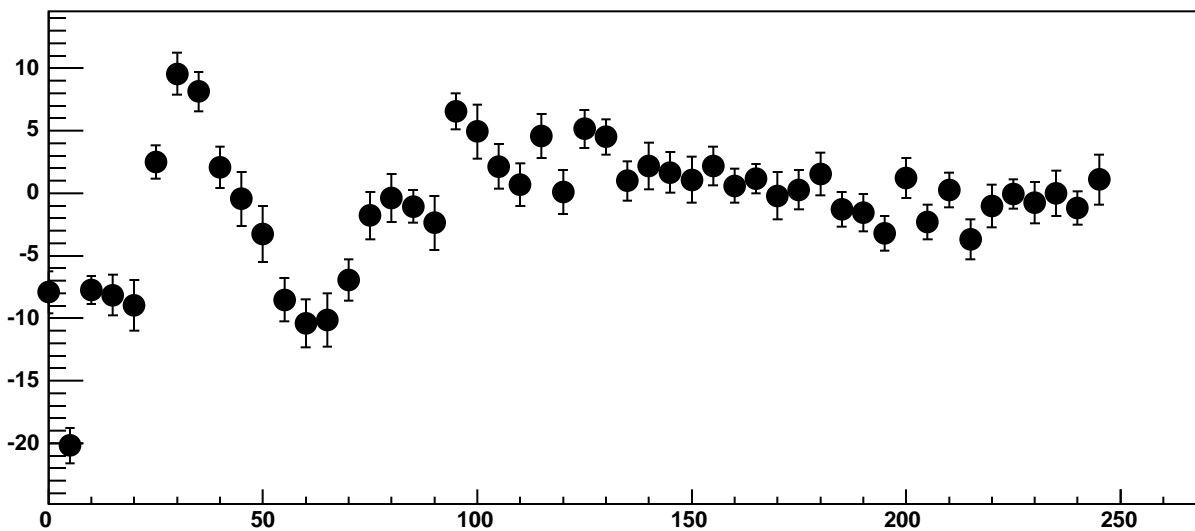


χ^2 / ndf	285.7 / 41
p0	-426.5 ± 0.5308
p1	57.99 ± 0.5433
p2	$-1.287\text{e}+08 \pm 2.345\text{e}+06$
p3	$5.007\text{e}+07 \pm 1.18\text{e}+05$
p4	2.38 ± 0.04622

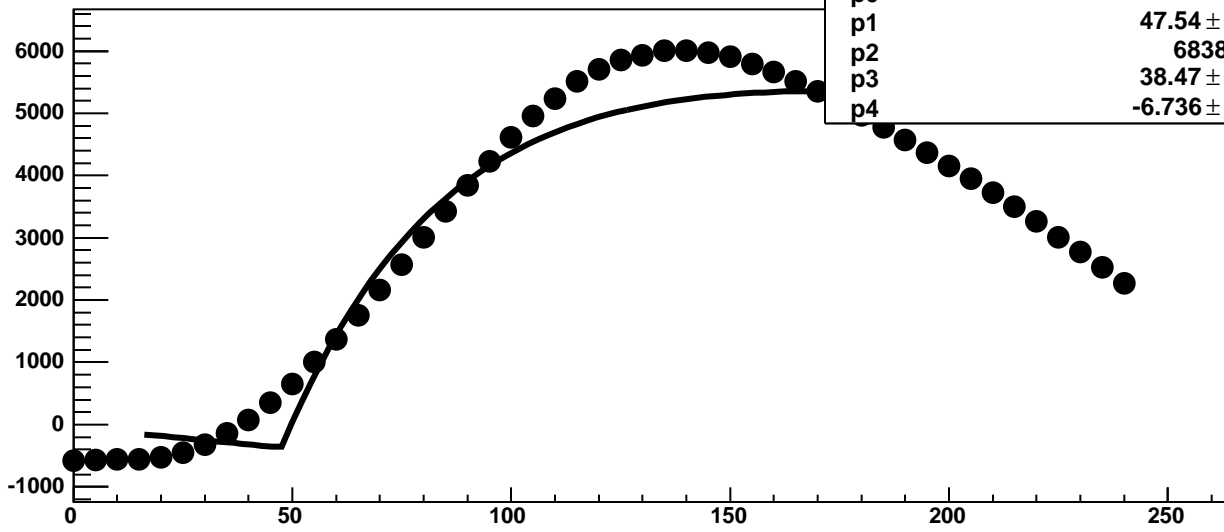
Chip 11, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

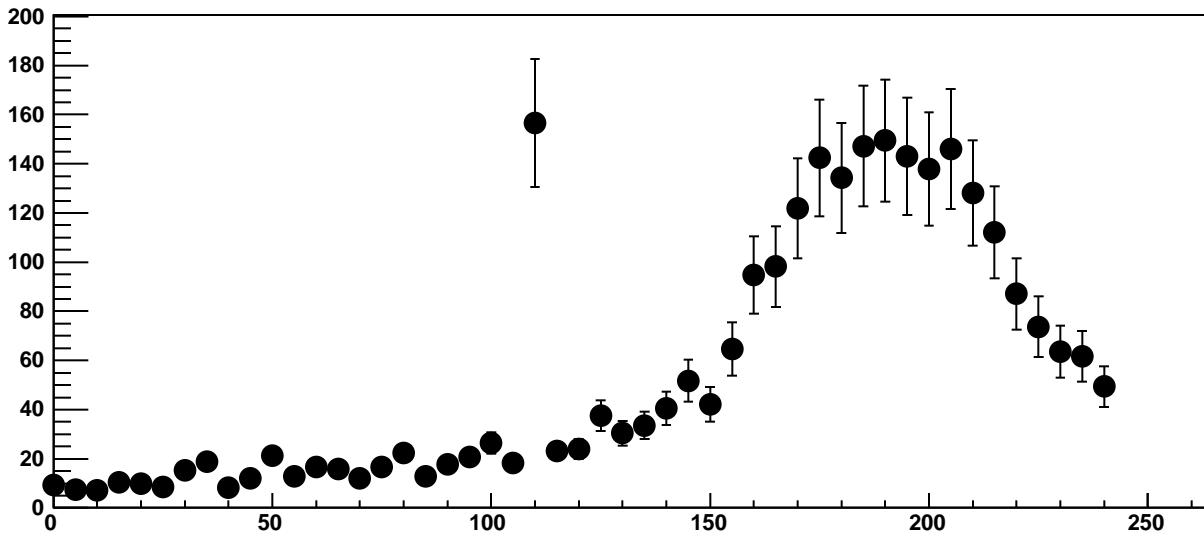


Chip 11, Channel 16, Enable 5, DAC=4000, ADC Mean vs Hold

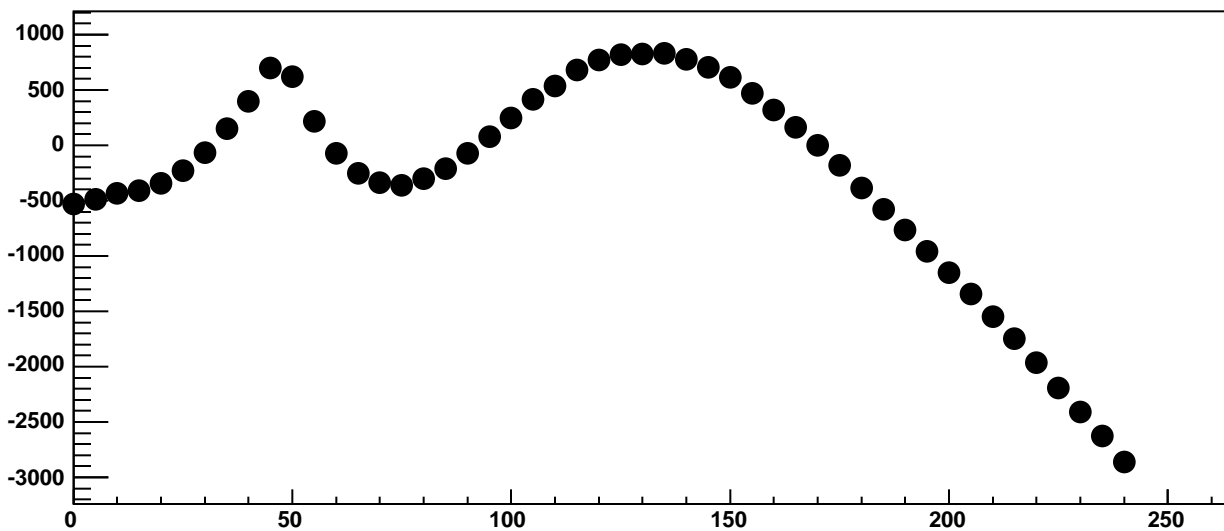


χ^2 / ndf	4.963e+05 / 41
p0	-370.1 ± 1.373
p1	47.54 ± 0.01876
p2	6838 ± 9.607
p3	38.47 ± 0.06225
p4	-6.736 ± 0.05867

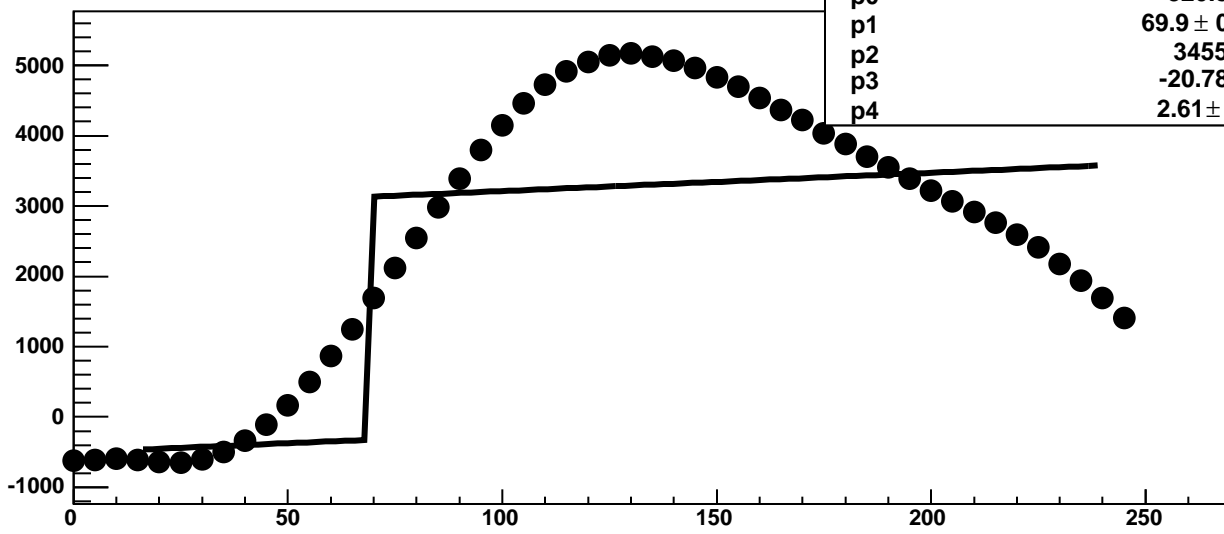
Chip 11, Channel 16, Enable 5, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 16, Enable 5, DAC=4000, ADC Residuals vs Hold

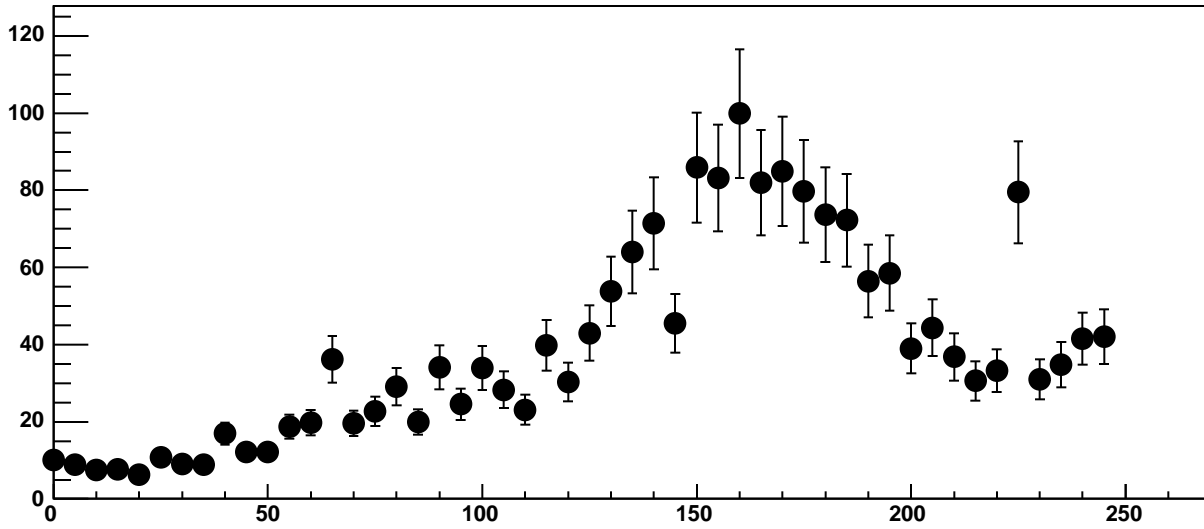


Chip 11, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

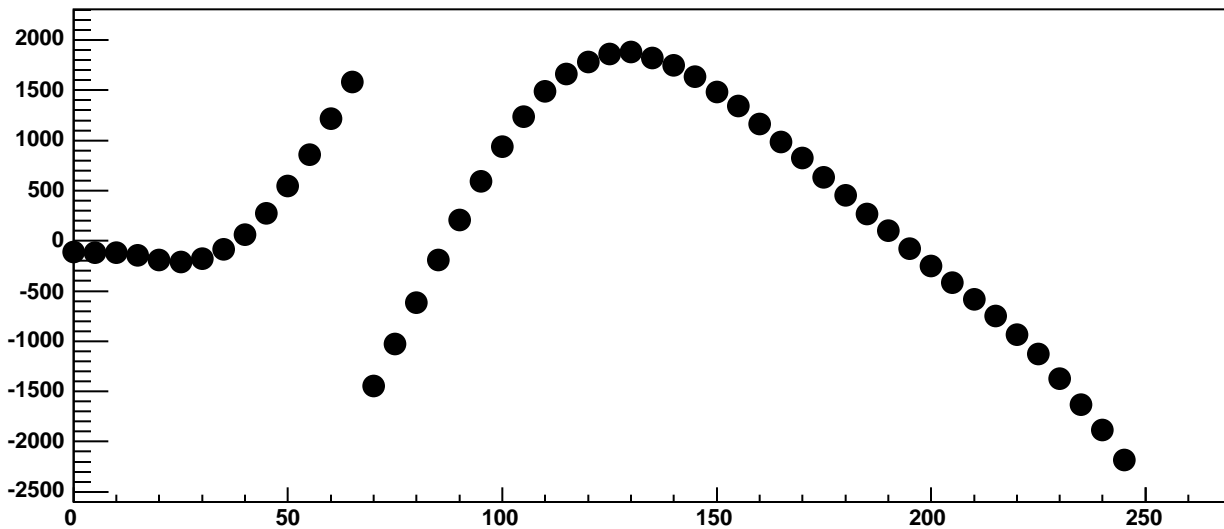


χ^2 / ndf	9.162e+05 / 41
p0	-320.8 ± 4.504
p1	69.9 ± 0.004872
p2	3455 ± 13.14
p3	-20.78 ± 1.226
p4	2.61 ± 0.01548

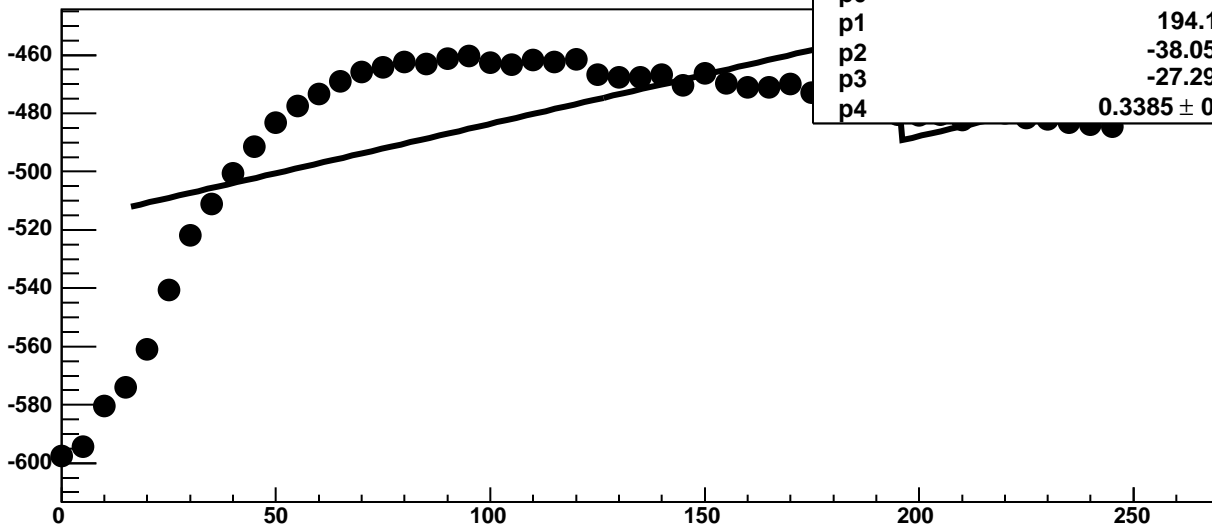
Chip 11, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

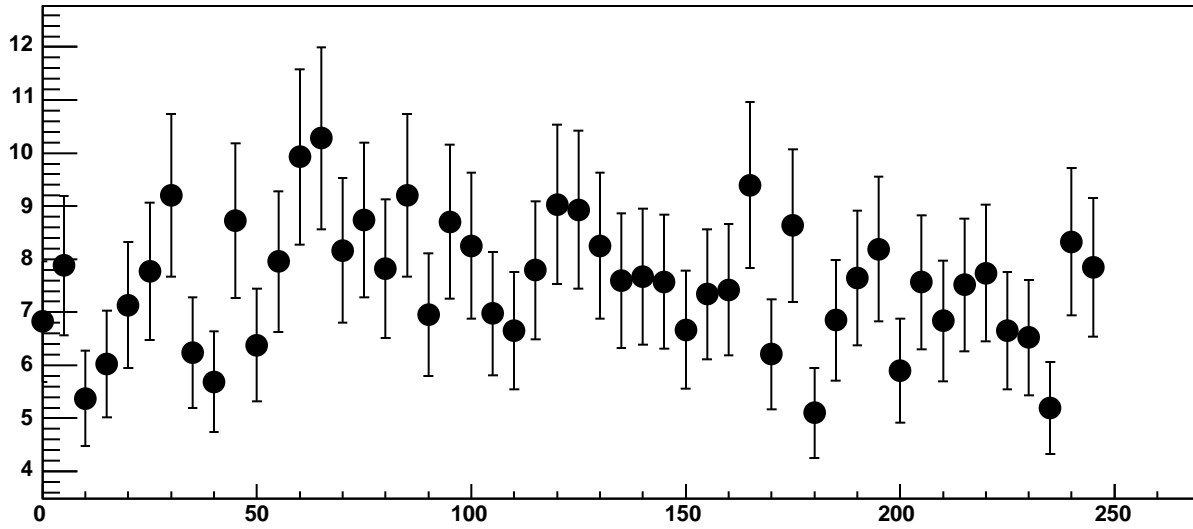


Chip 11, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

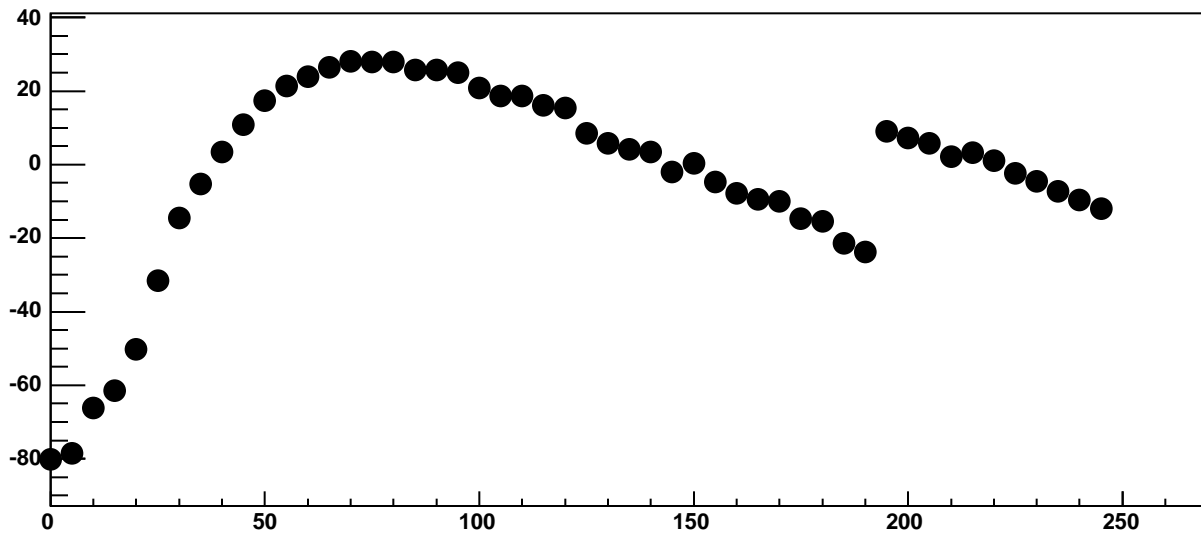


χ^2 / ndf	6498 / 41
p0	-451.8 ± 1.746
p1	194.1 ± 8.612
p2	-38.05 ± 2.533
p3	-27.29 ± 29.89
p4	0.3385 ± 0.003593

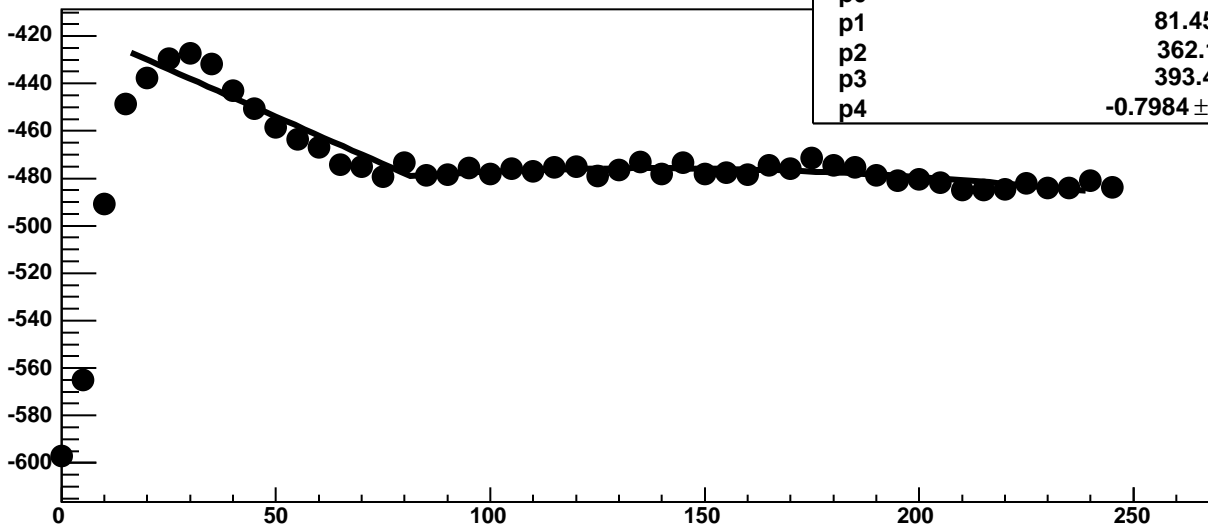
Chip 11, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



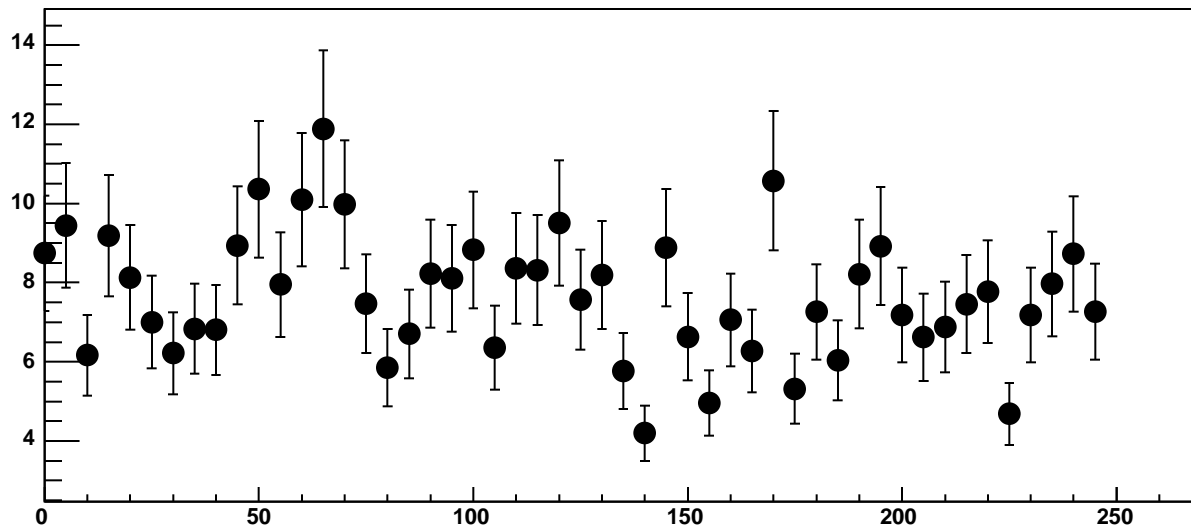
Chip 11, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold



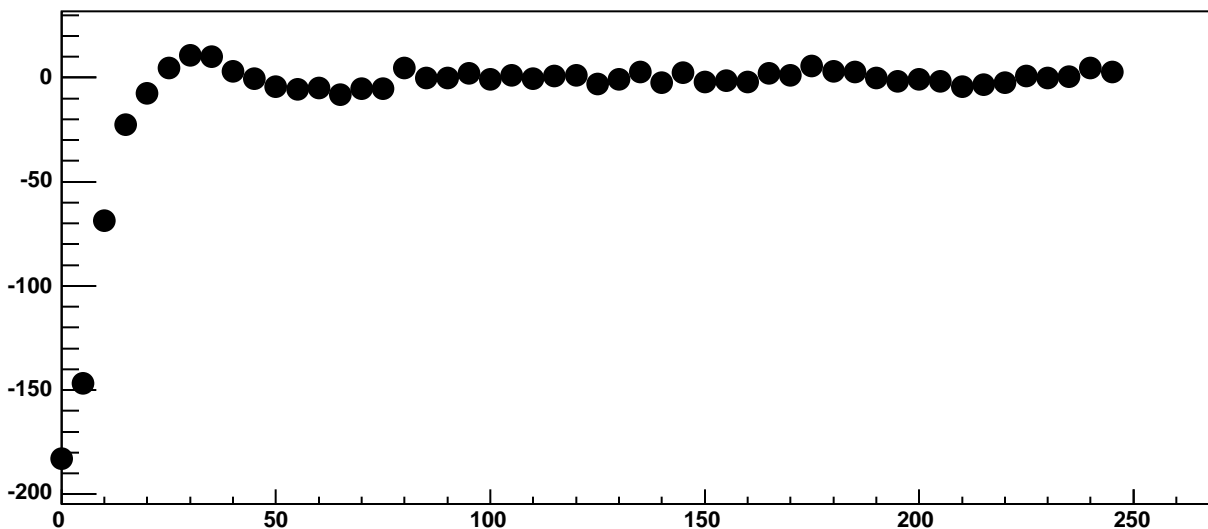
Chip 11, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold



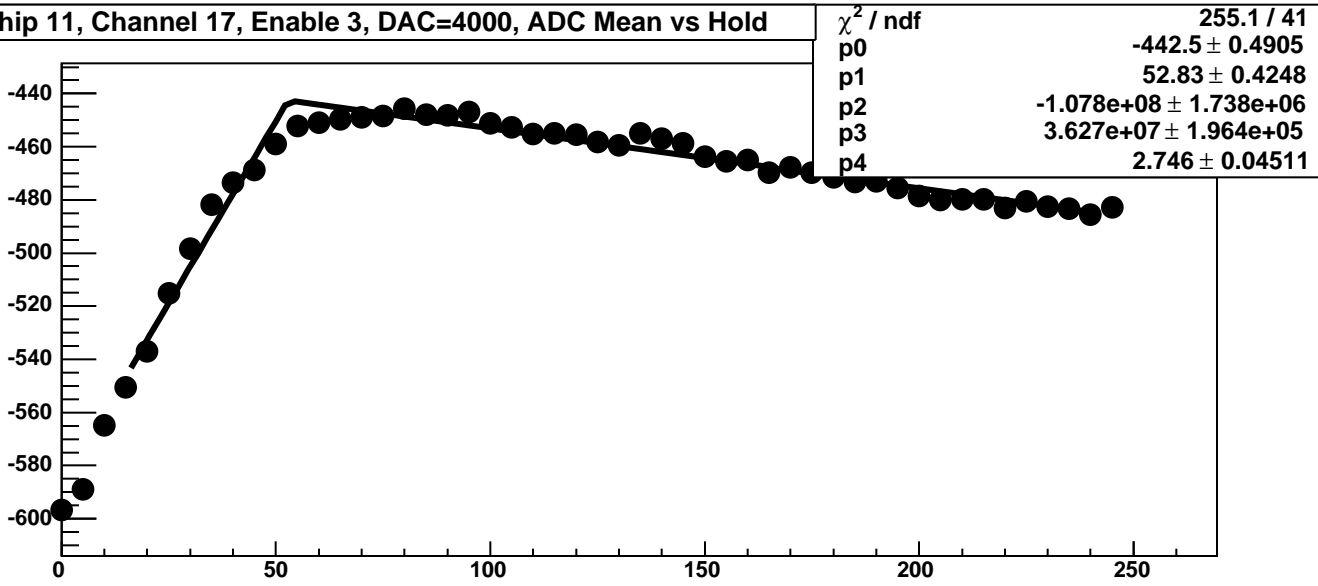
Chip 11, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold



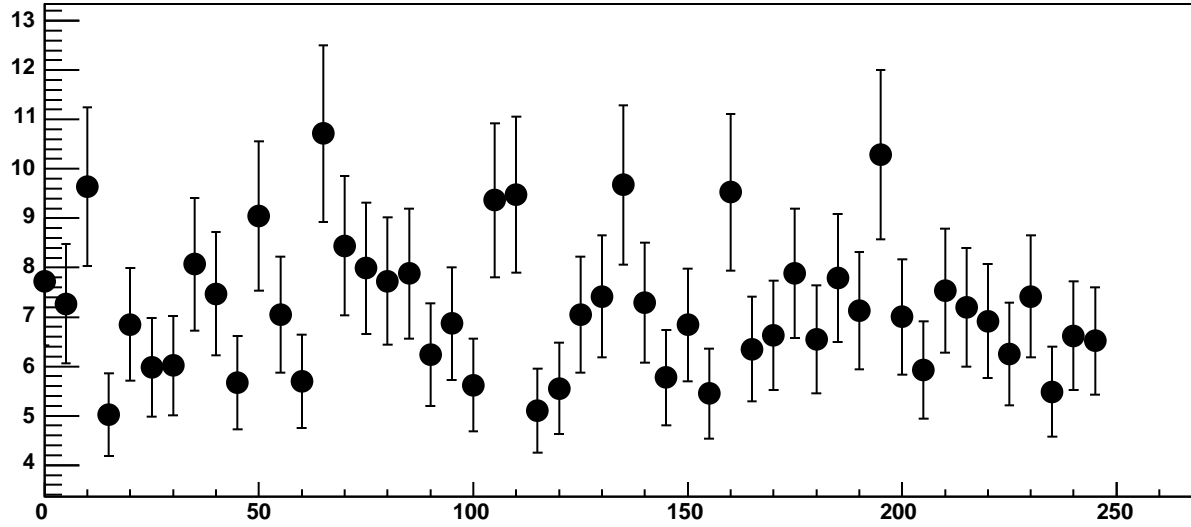
Chip 11, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold



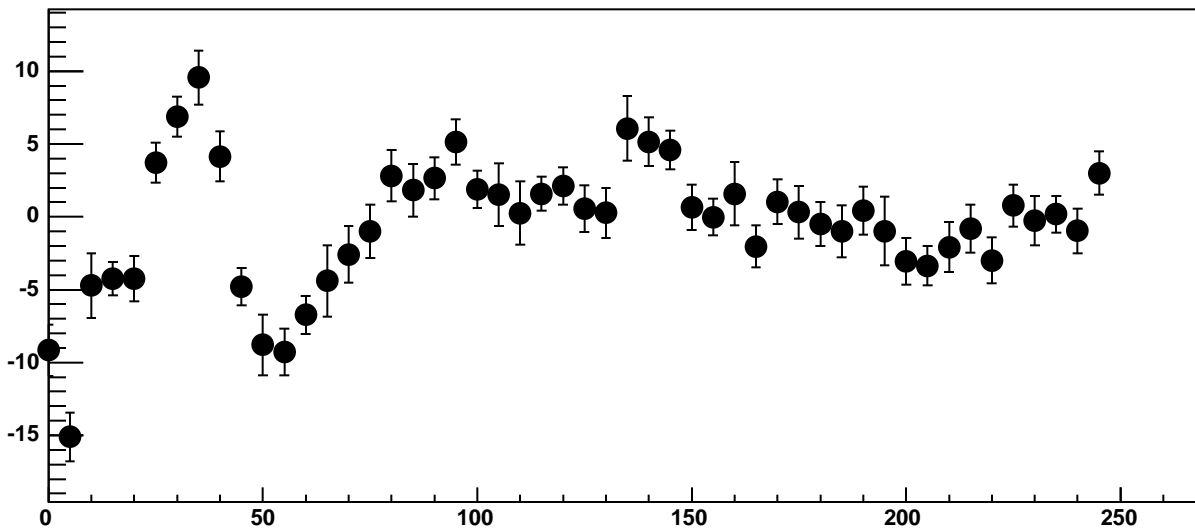
Chip 11, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



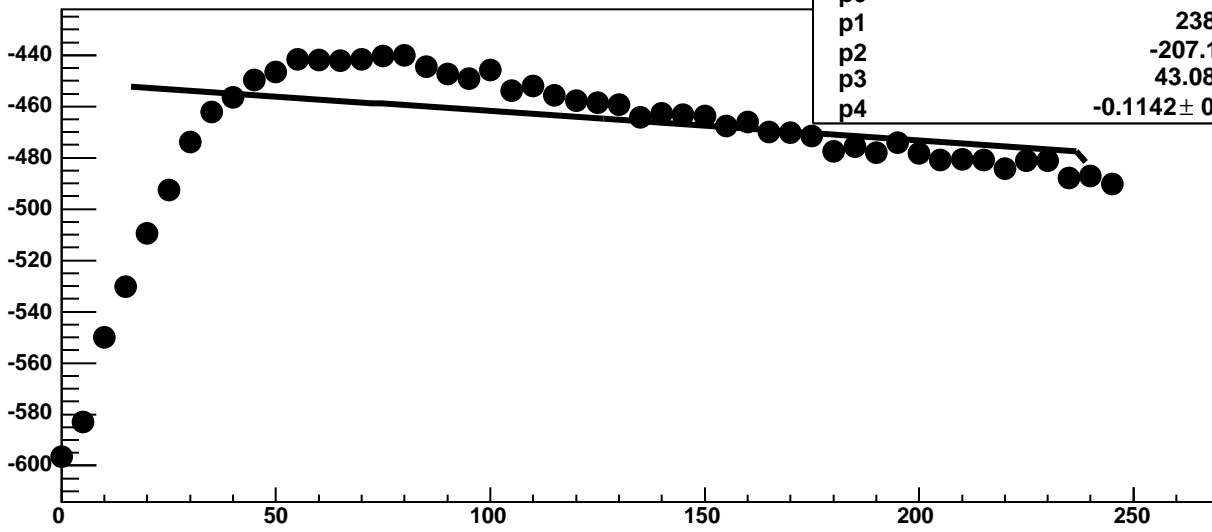
Chip 11, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold



Chip 11, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



χ^2 / ndf

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p0

-477.6 ± 0.5288

p1

238 ± 2.273

p2

-207.1 ± 230.2

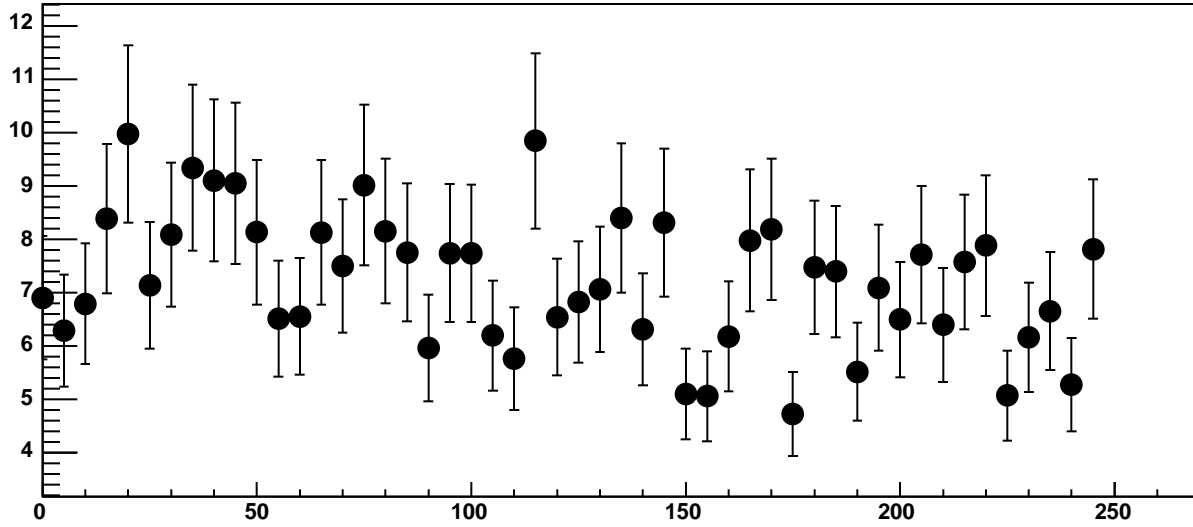
p3

43.08 ± 40.33

p4

-0.1142 ± 0.003814

Chip 11, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

