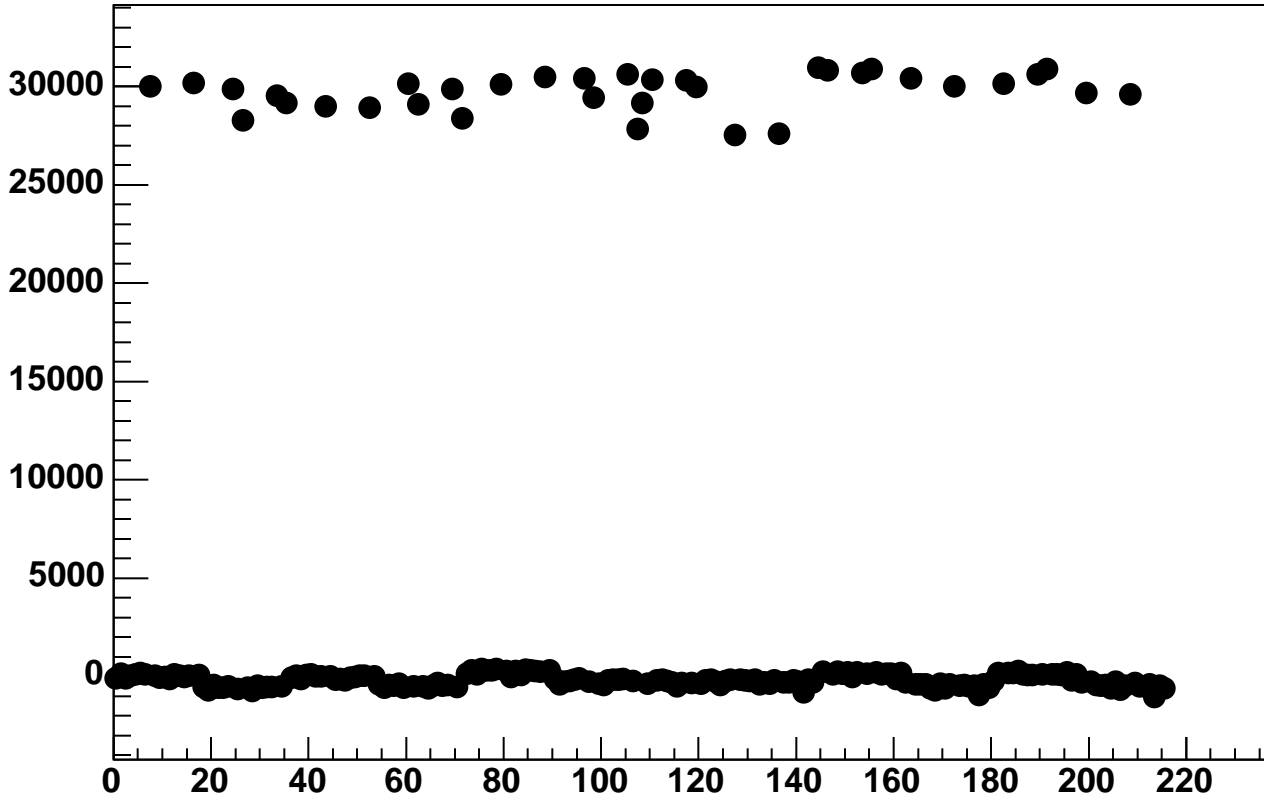
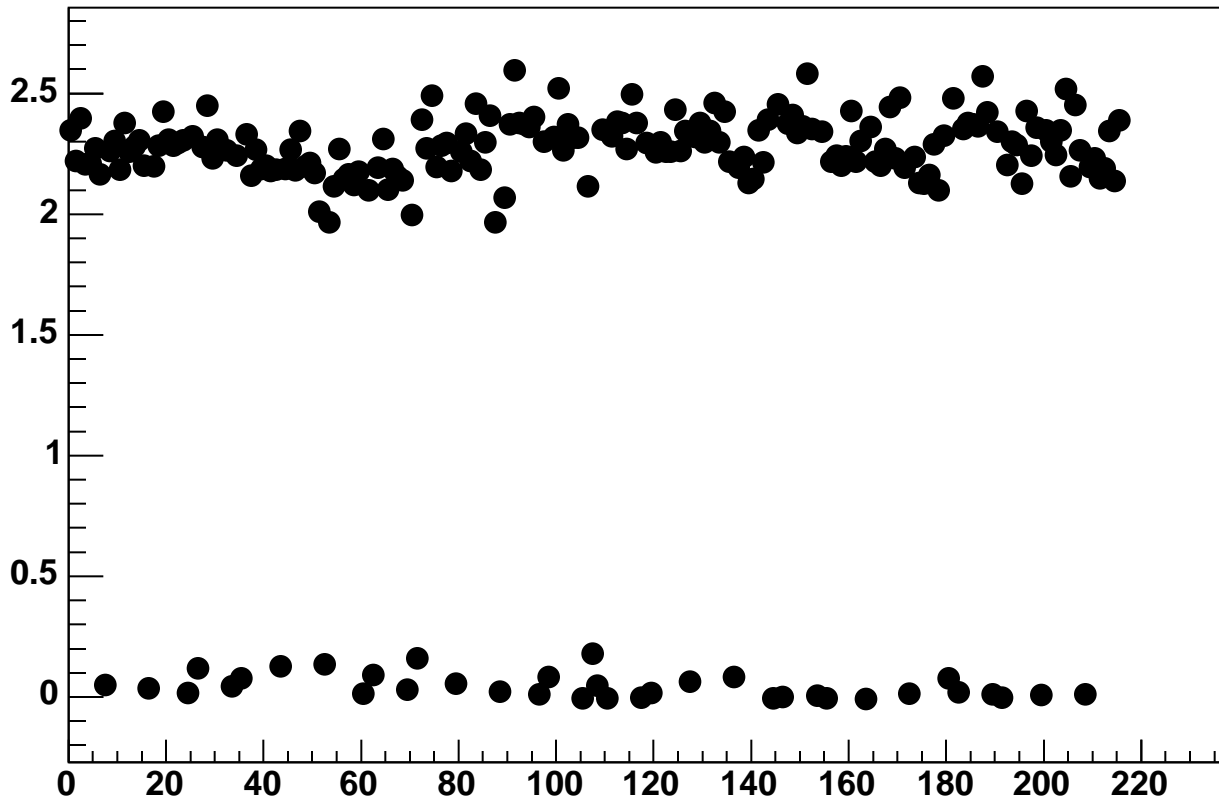


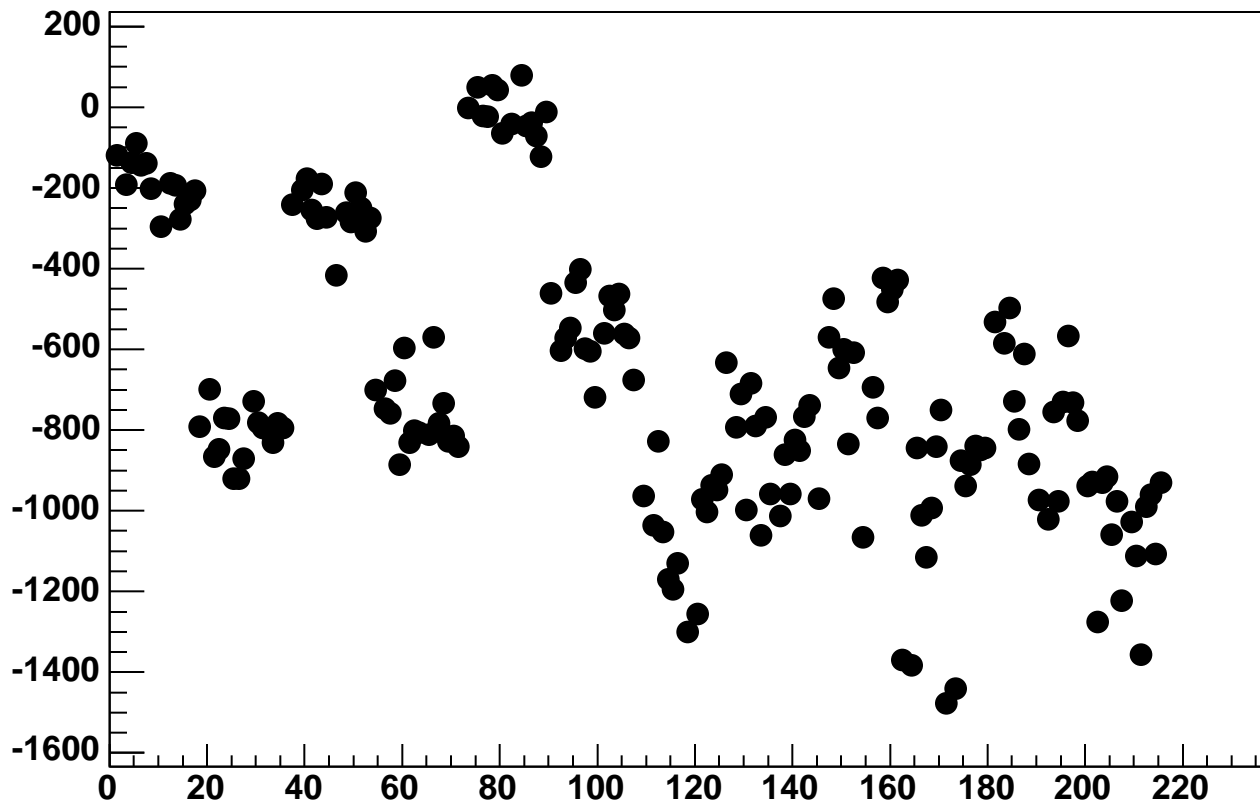
Channel Enabled, Hold=35, Fit Intercept vs $18 \cdot \text{Chip} + \text{Chan}$



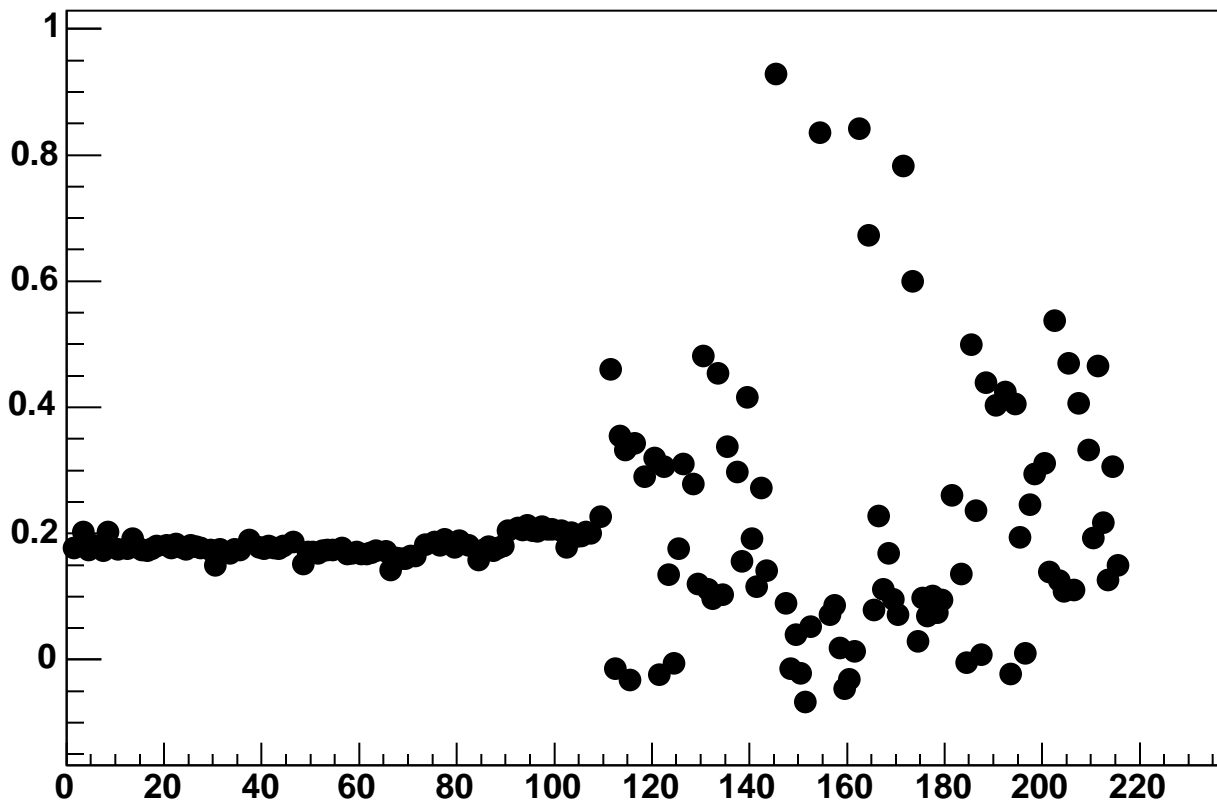
Channel Enabled, Hold=35, Fit Slope vs $18 \cdot \text{Chip} + \text{Chan}$



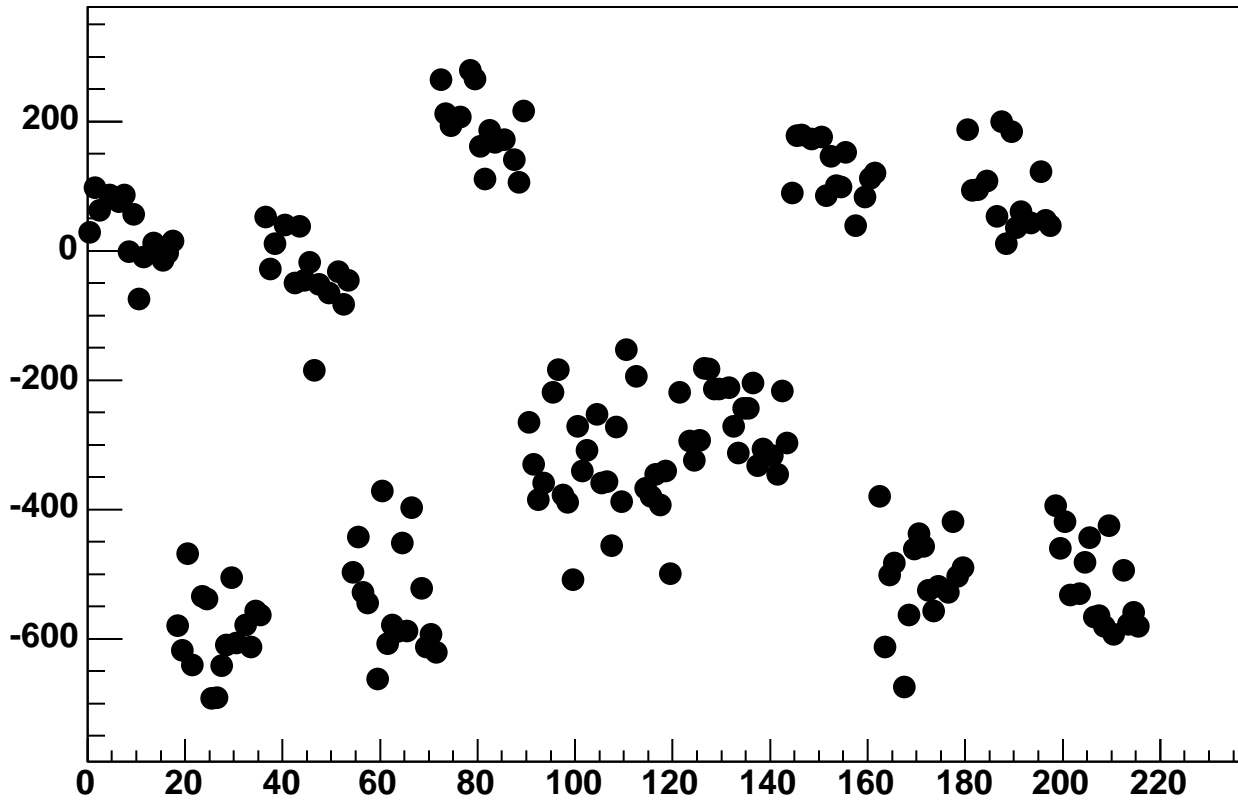
Disabled 0, Hold=35, Fit Intercept vs $18 \cdot \text{Chip} + \text{Chan}$



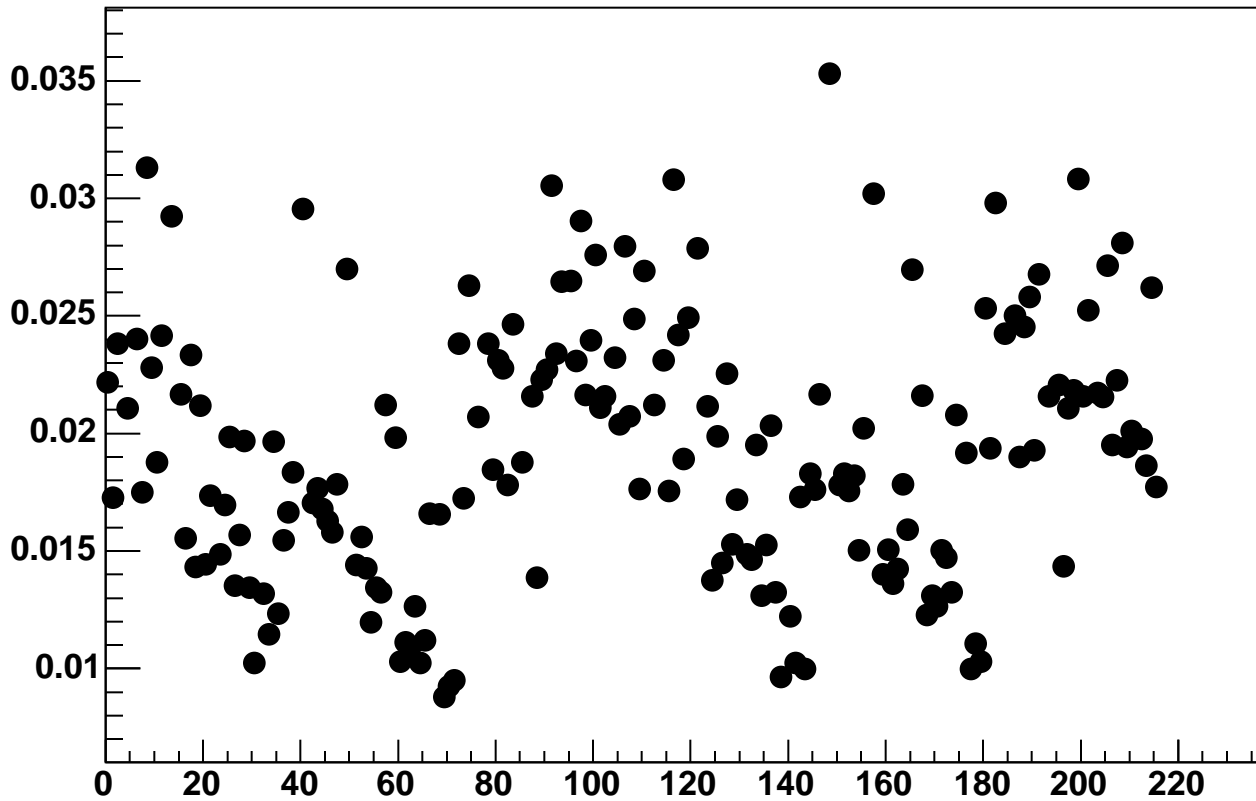
Disabled 0, Hold=35, Fit Slope vs $18 \cdot \text{Chip} + \text{Chan}$



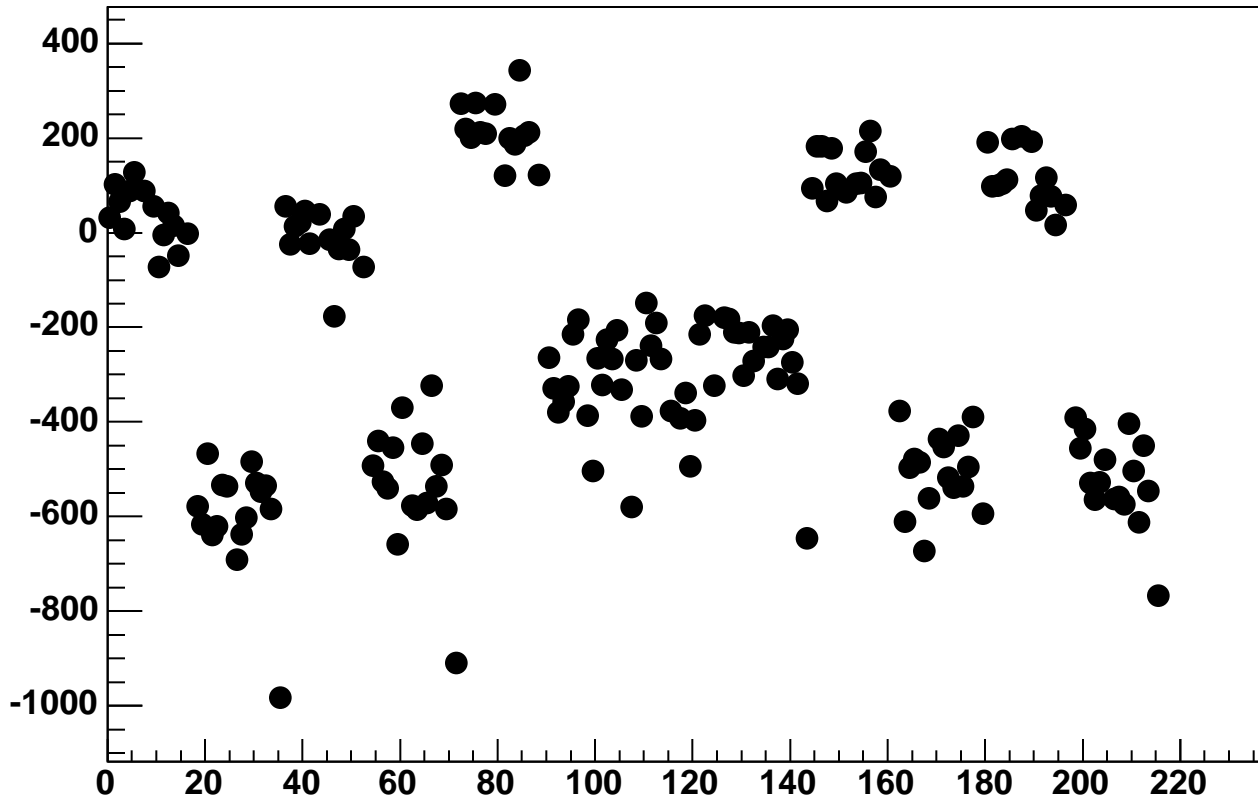
Disabled 1, Hold=35, Fit Intercept vs $18 \cdot \text{Chip} + \text{Chan}$



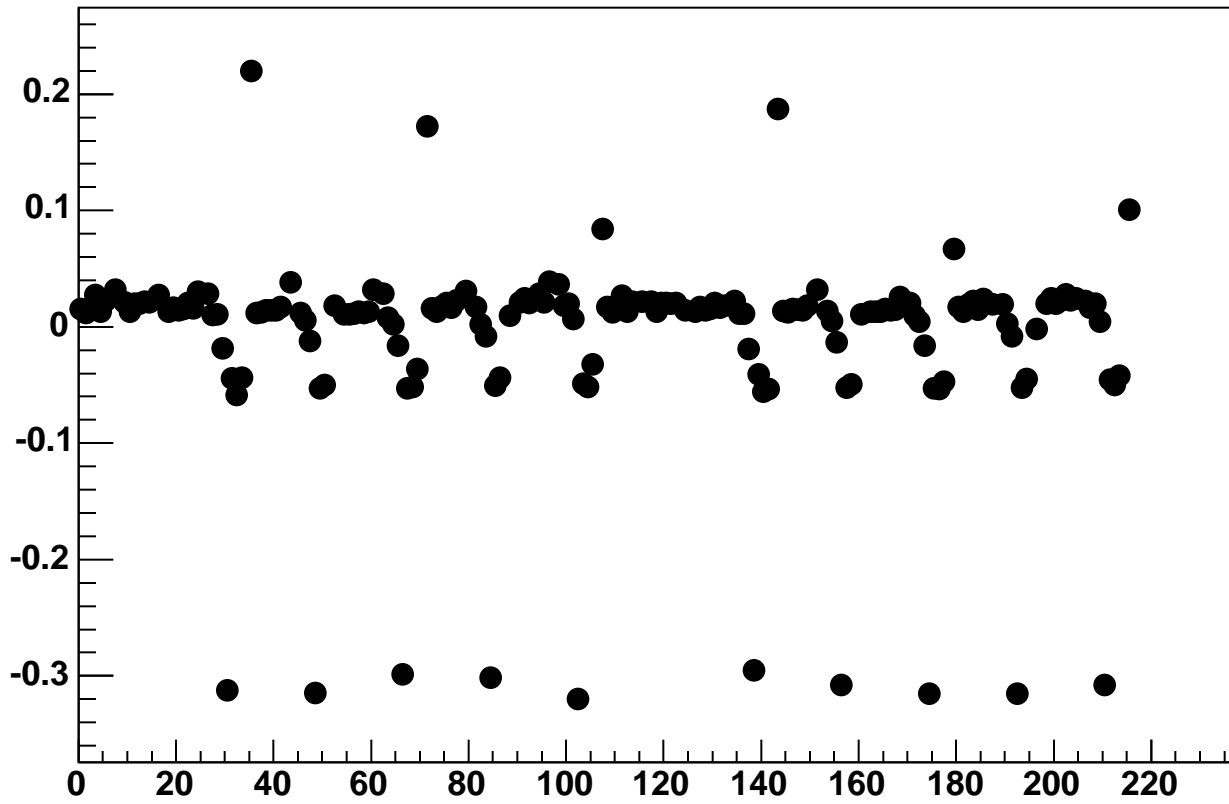
Disabled 1, Hold=35, Fit Slope vs $18 \cdot \text{Chip} + \text{Chan}$



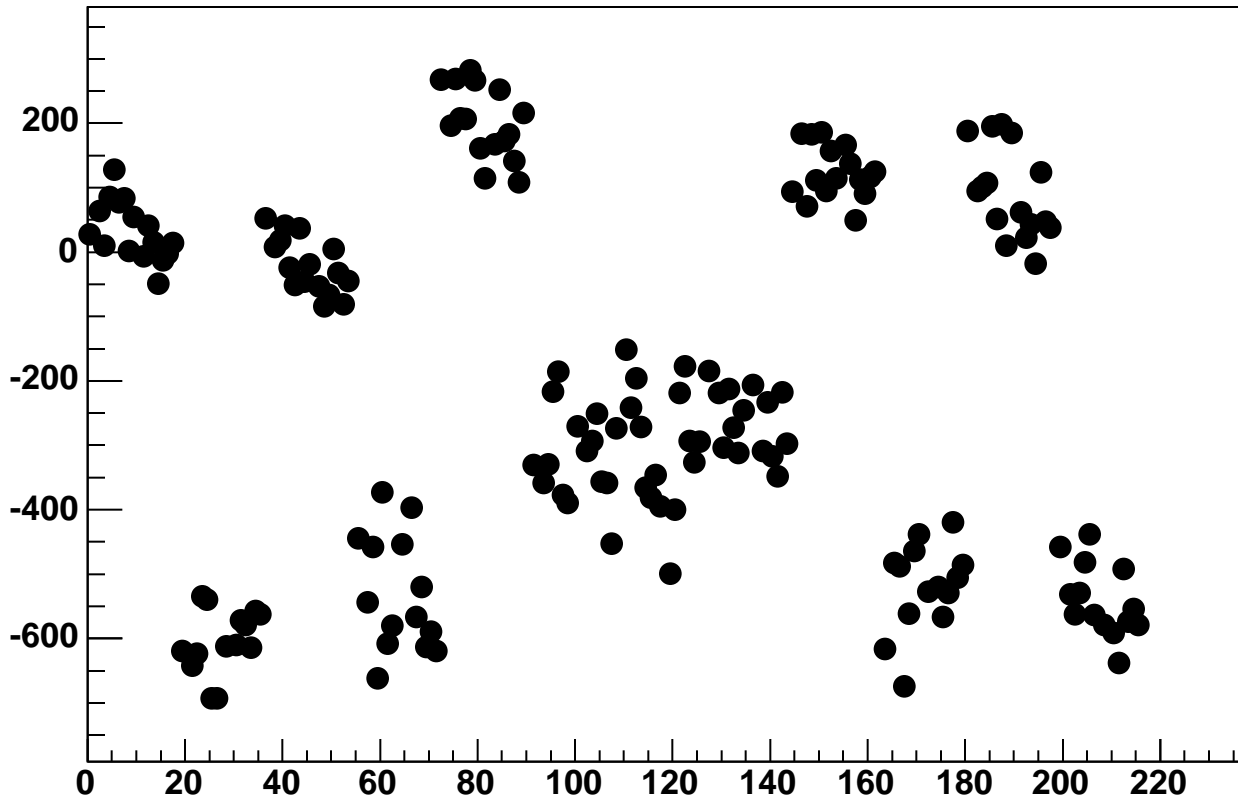
Disabled 2, Hold=35, Fit Intercept vs 18*Chip+Chan



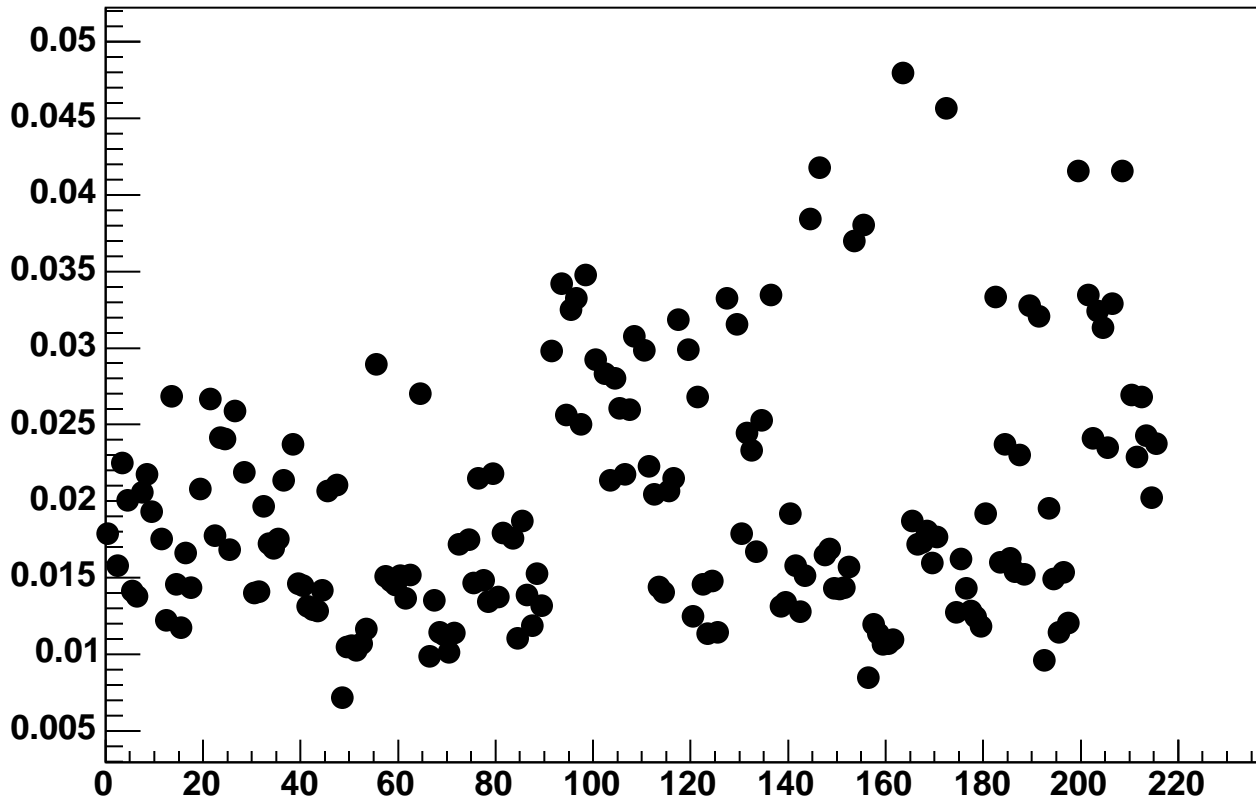
Disabled 2, Hold=35, Fit Slope vs 18*Chip+Chan



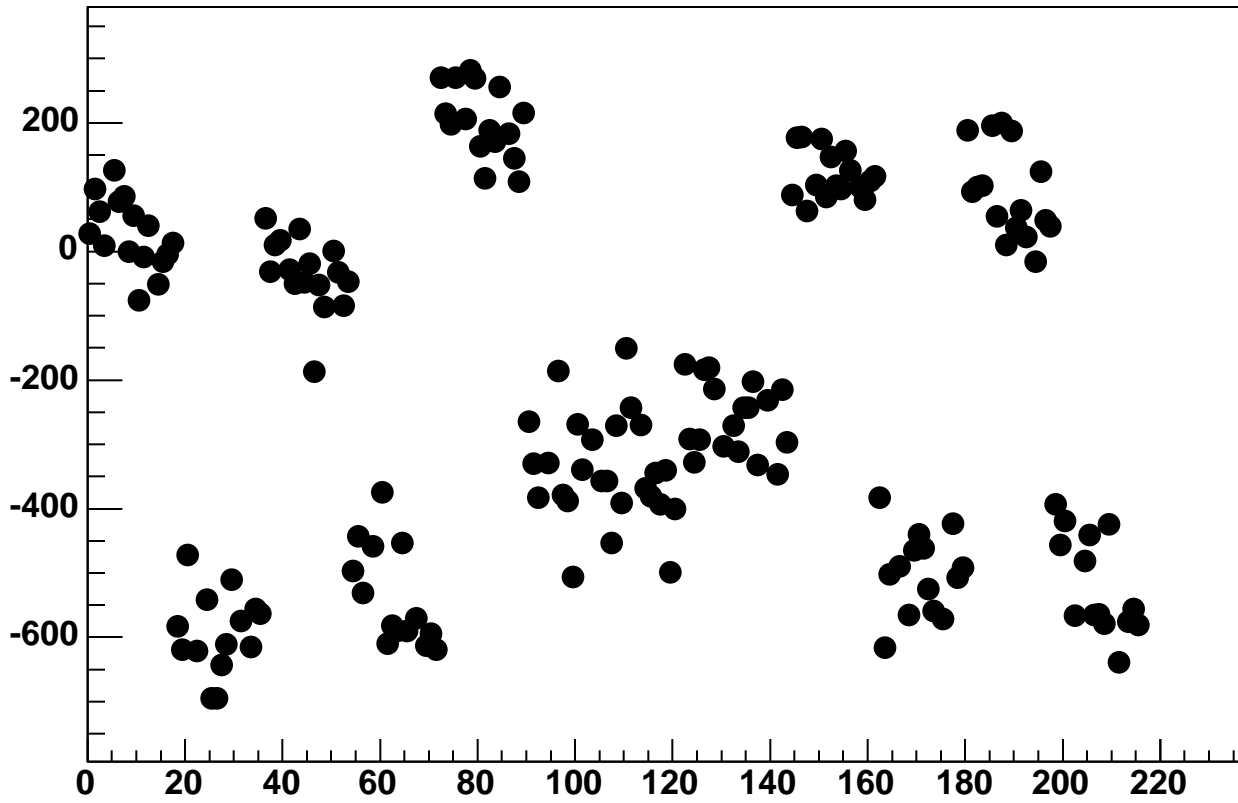
Disabled 3, Hold=35, Fit Intercept vs 18*Chip+Chan



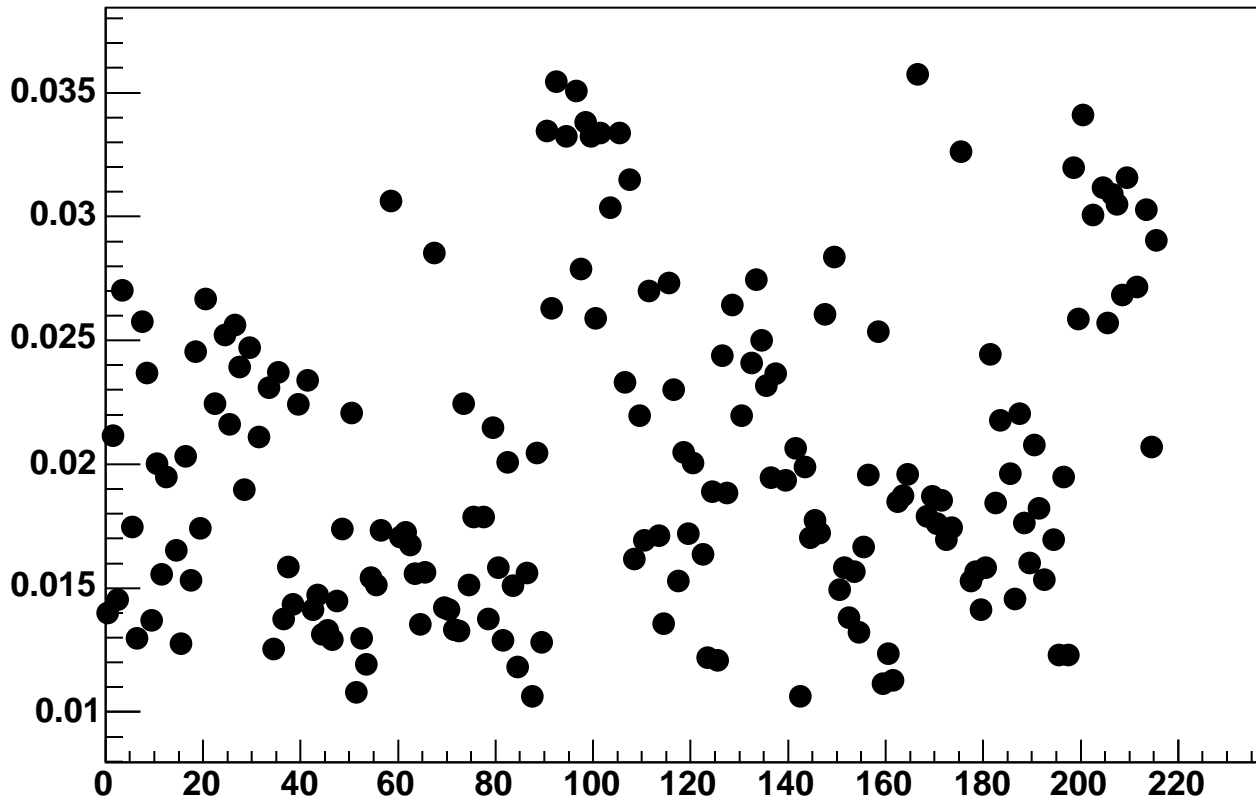
Disabled 3, Hold=35, Fit Slope vs 18*Chip+Chan



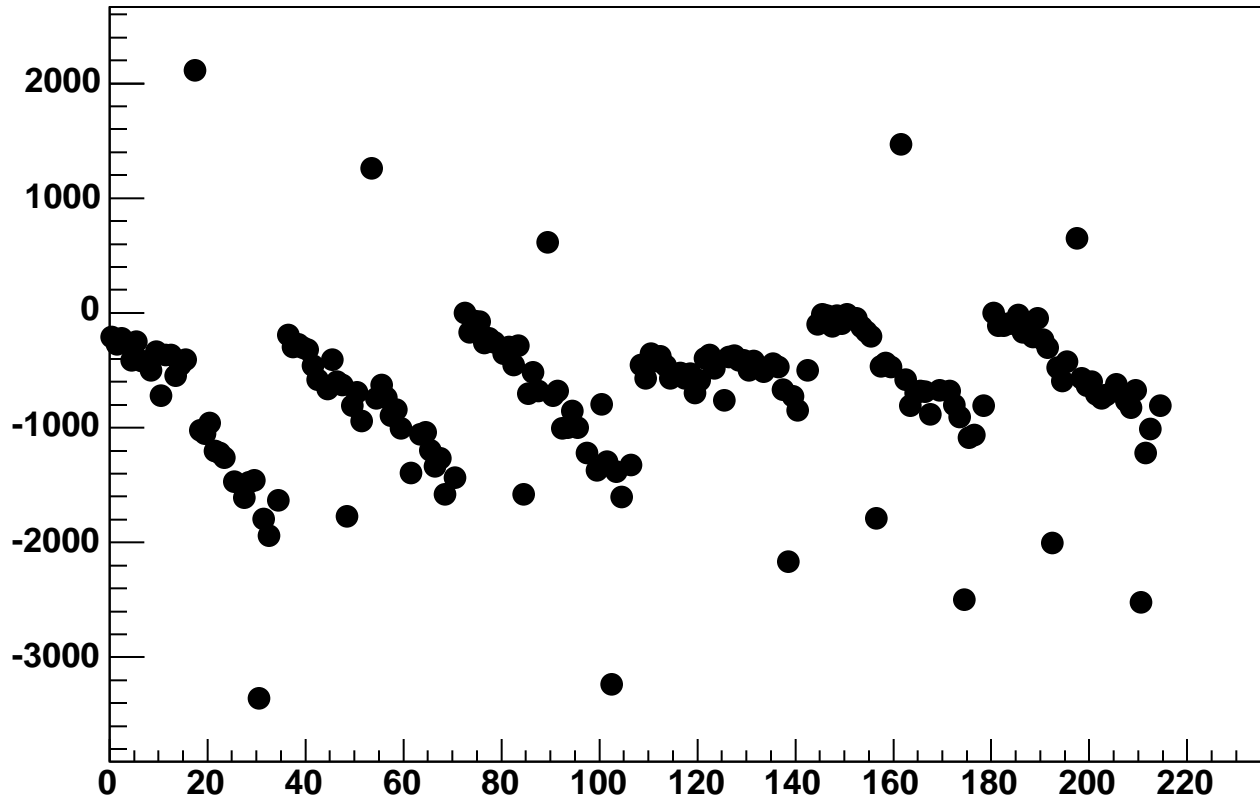
Disabled 4, Hold=35, Fit Intercept vs $18 \cdot \text{Chip} + \text{Chan}$



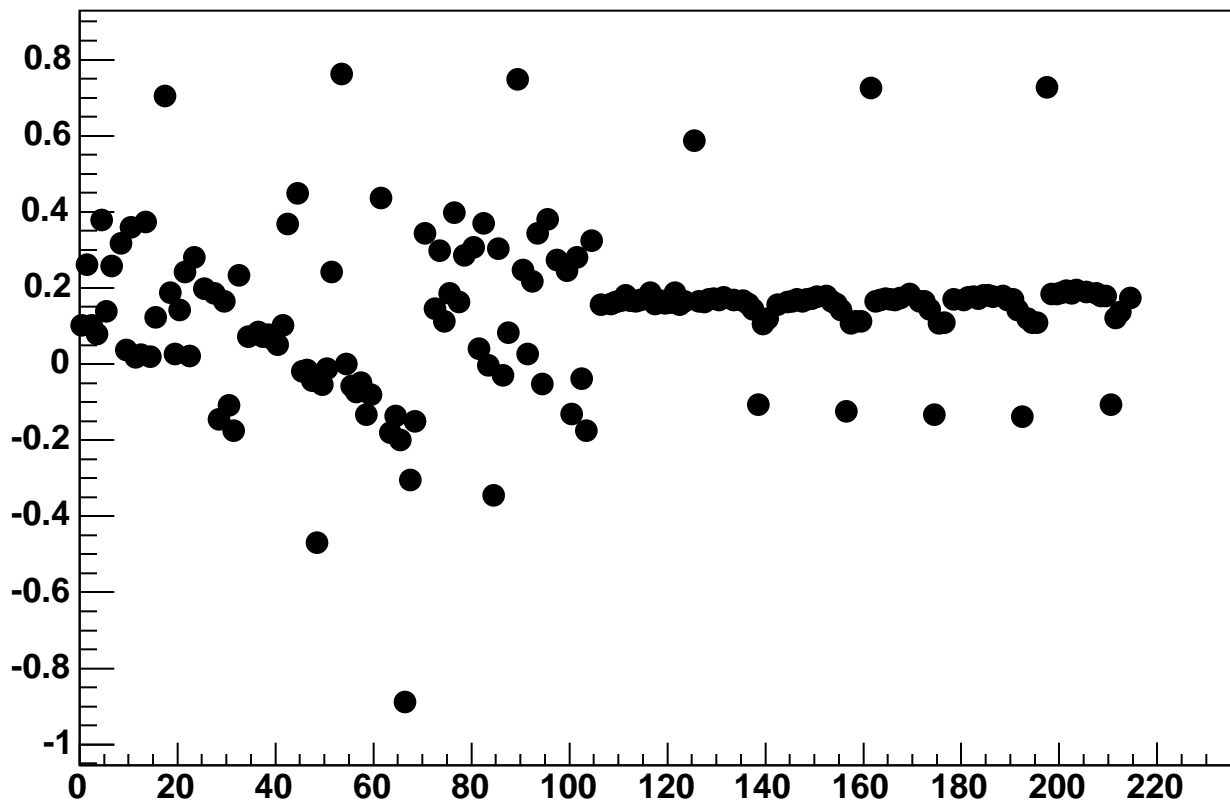
Disabled 4, Hold=35, Fit Slope vs $18 \cdot \text{Chip} + \text{Chan}$



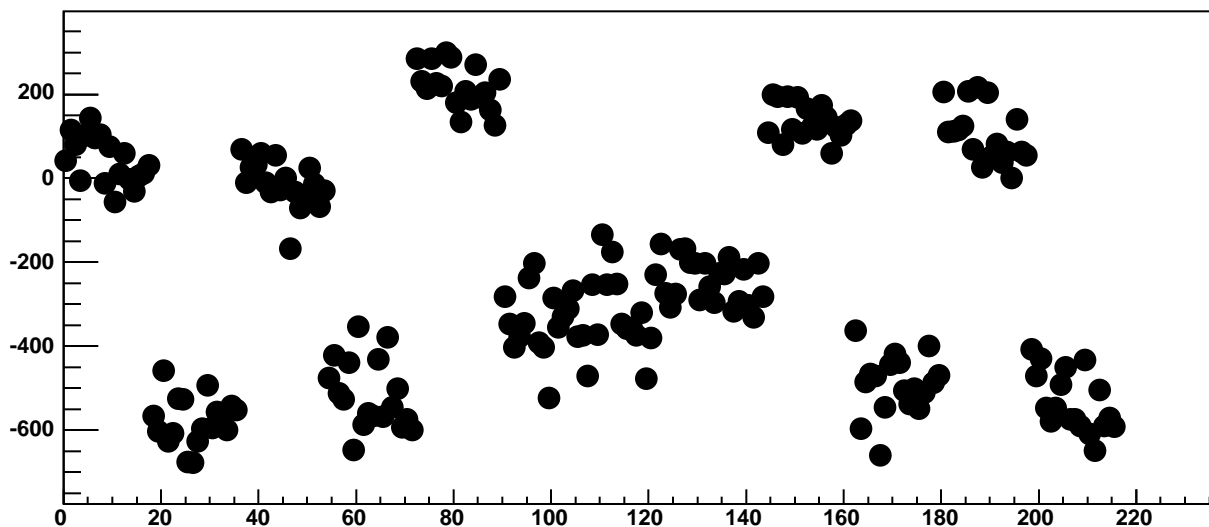
Disabled 5, Hold=35, Fit Intercept vs $18 \cdot \text{Chip} + \text{Chan}$



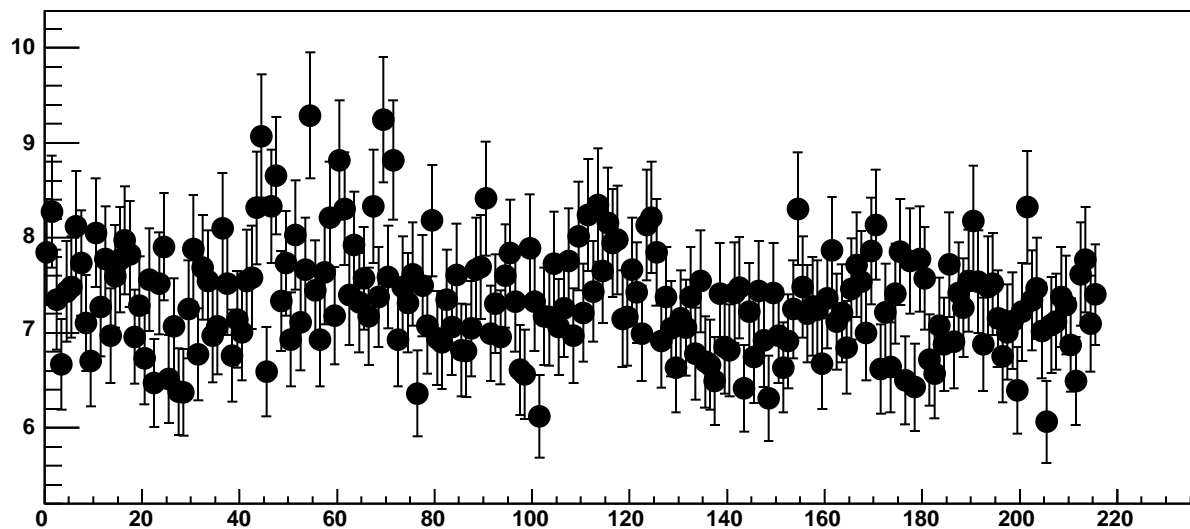
Disabled 5, Hold=35, Fit Slope vs $18 \cdot \text{Chip} + \text{Chan}$



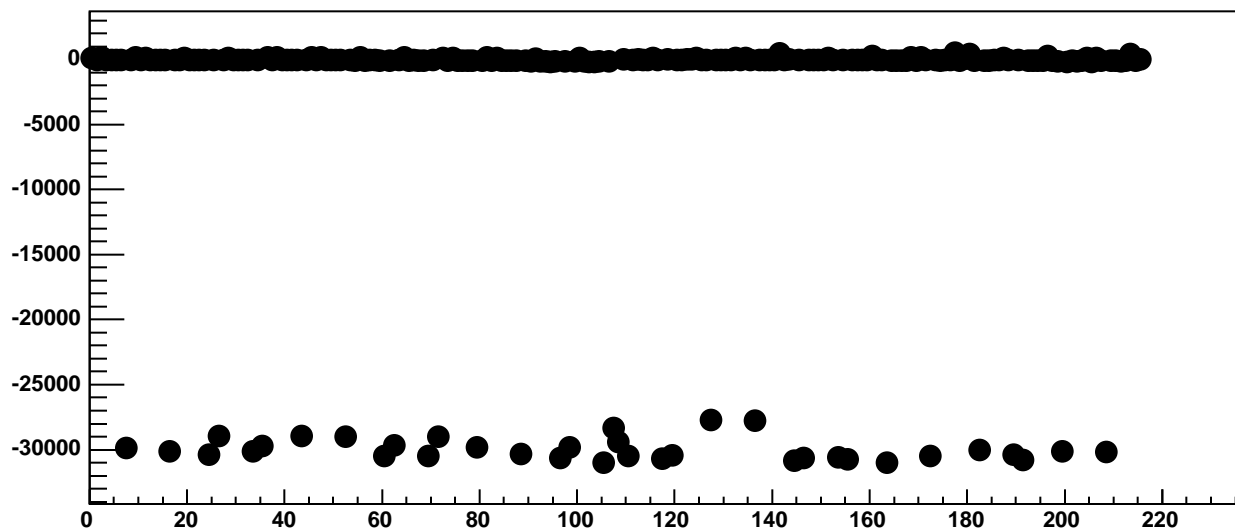
Enables off, ADC Mean vs 18*Chip+Chan



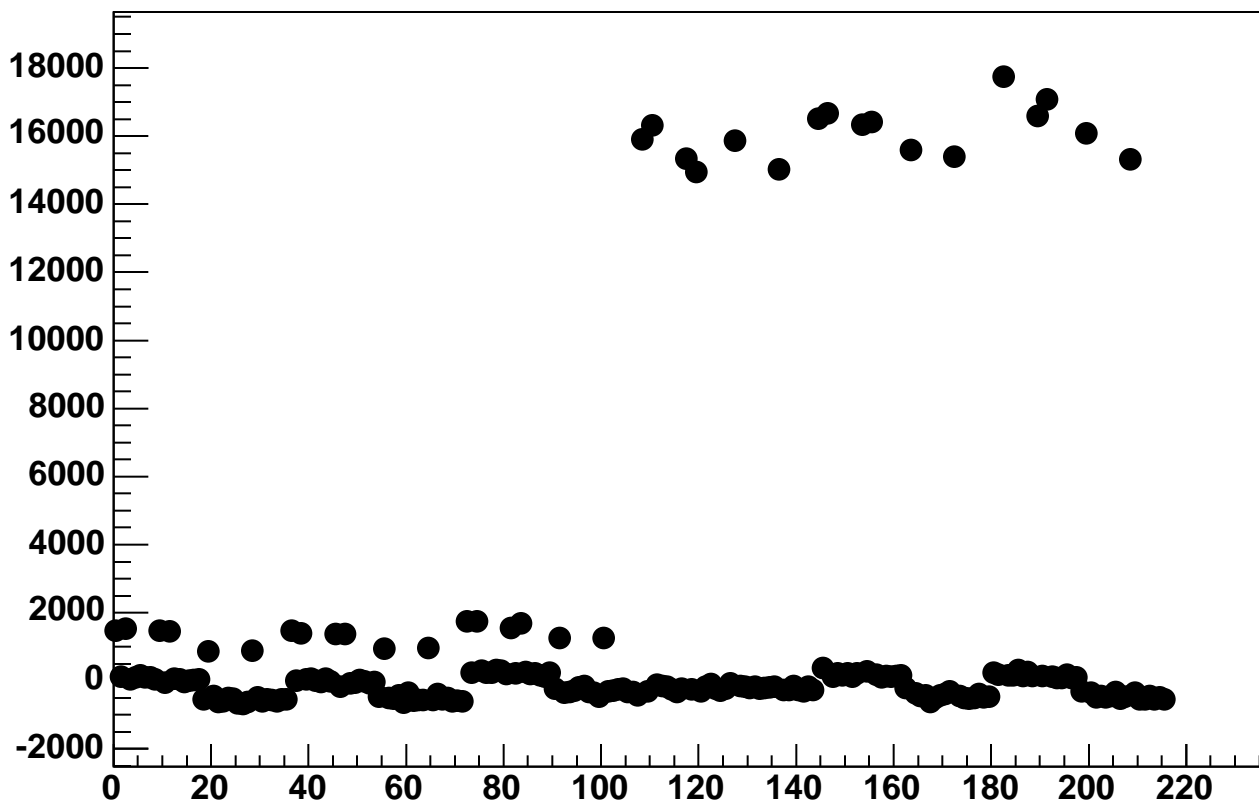
Enables off, ADC Noise vs 18*Chip+Chan



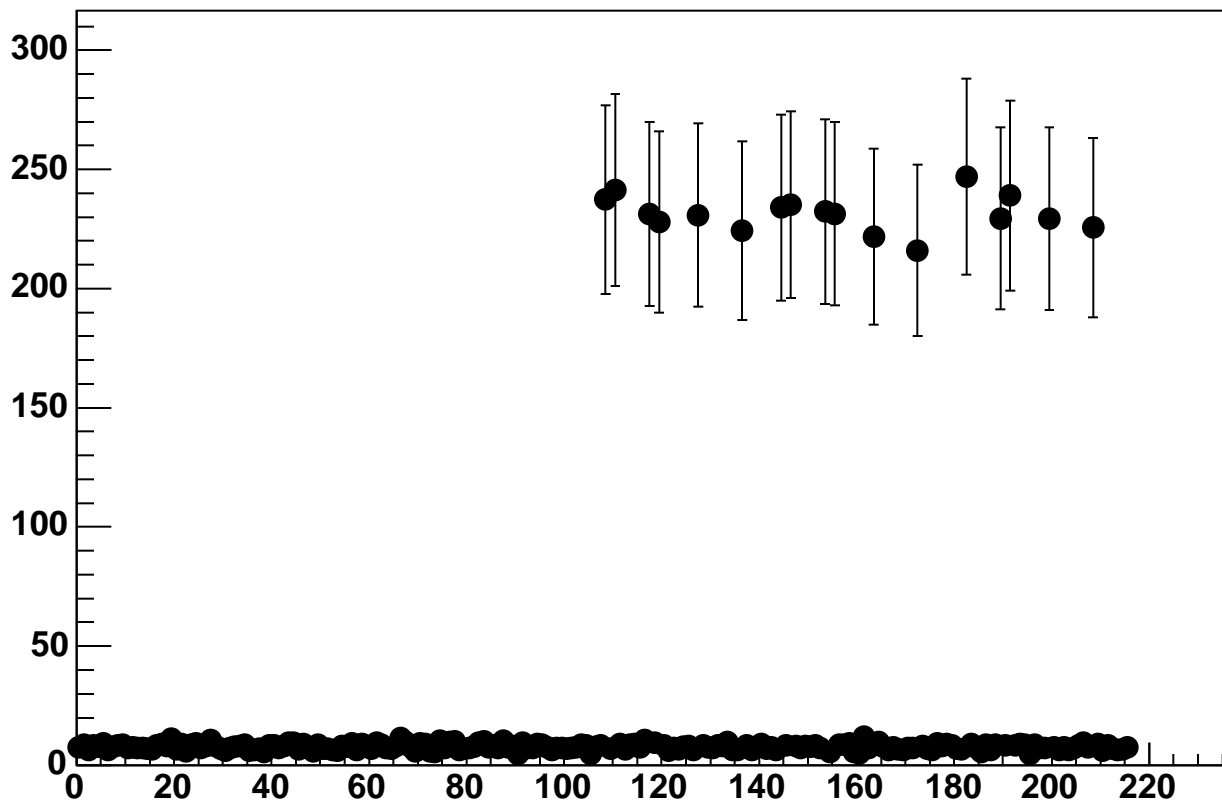
Enables off, ADC Residuals vs 18*Chip+Chan



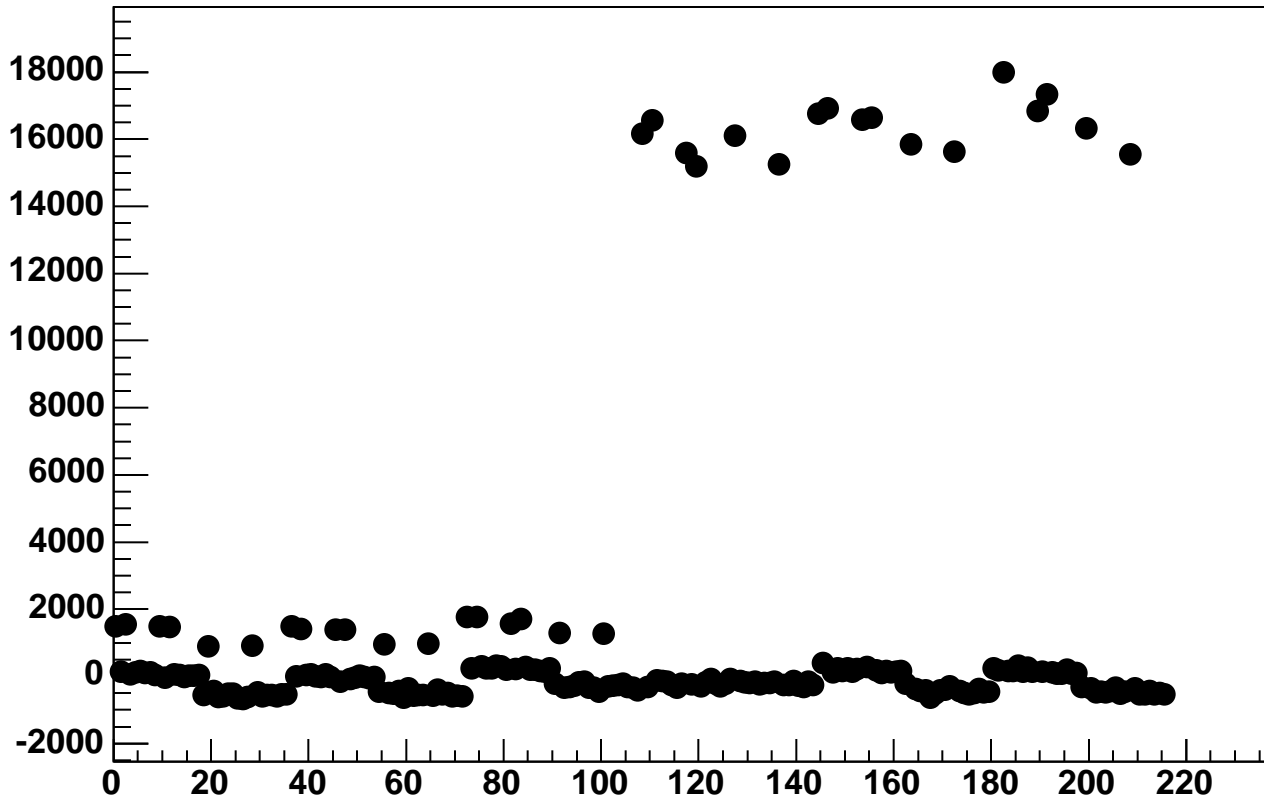
Enable 0, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



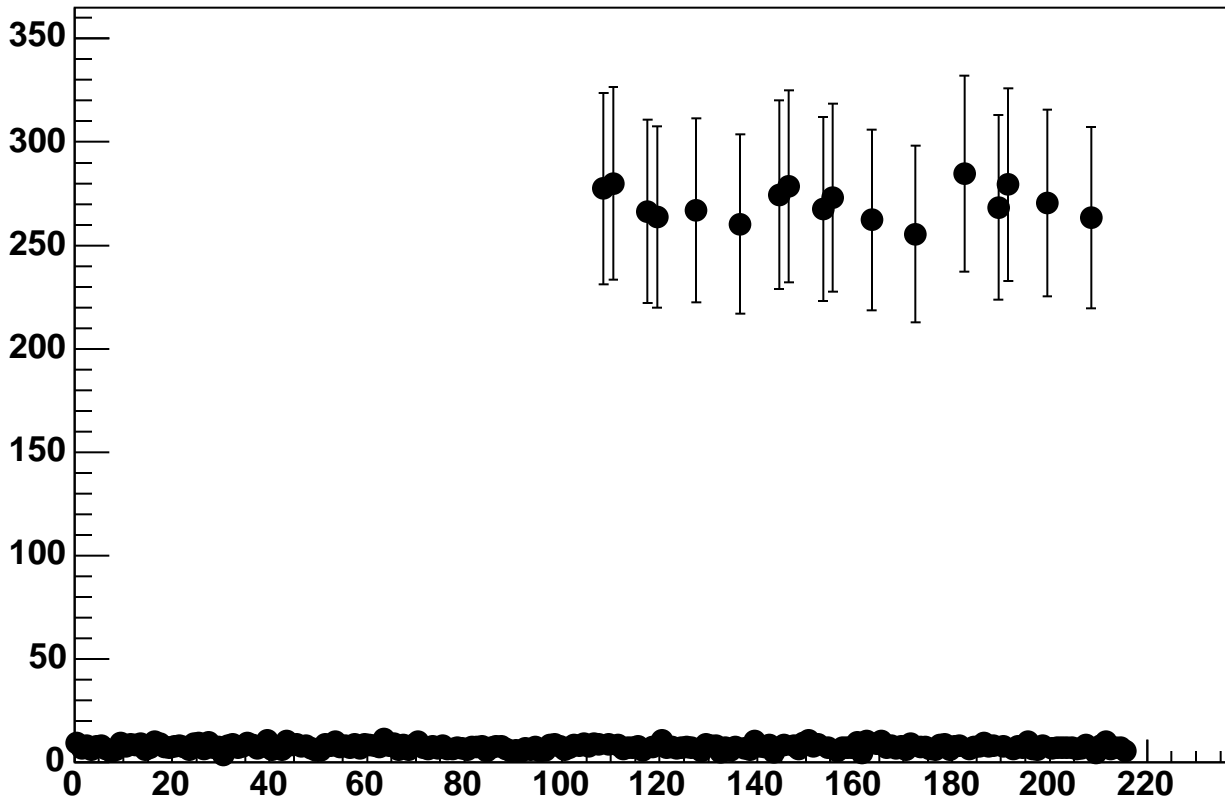
Enable 0, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



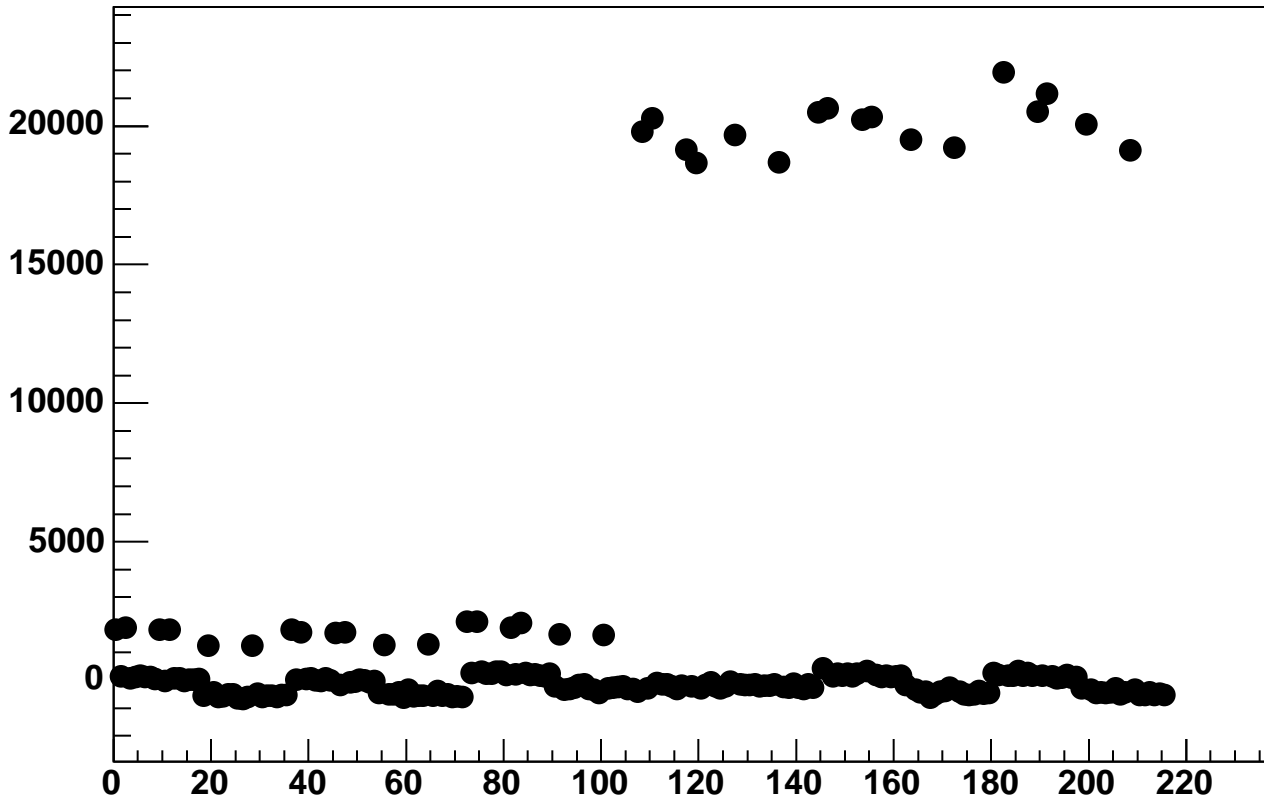
Enable 0, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



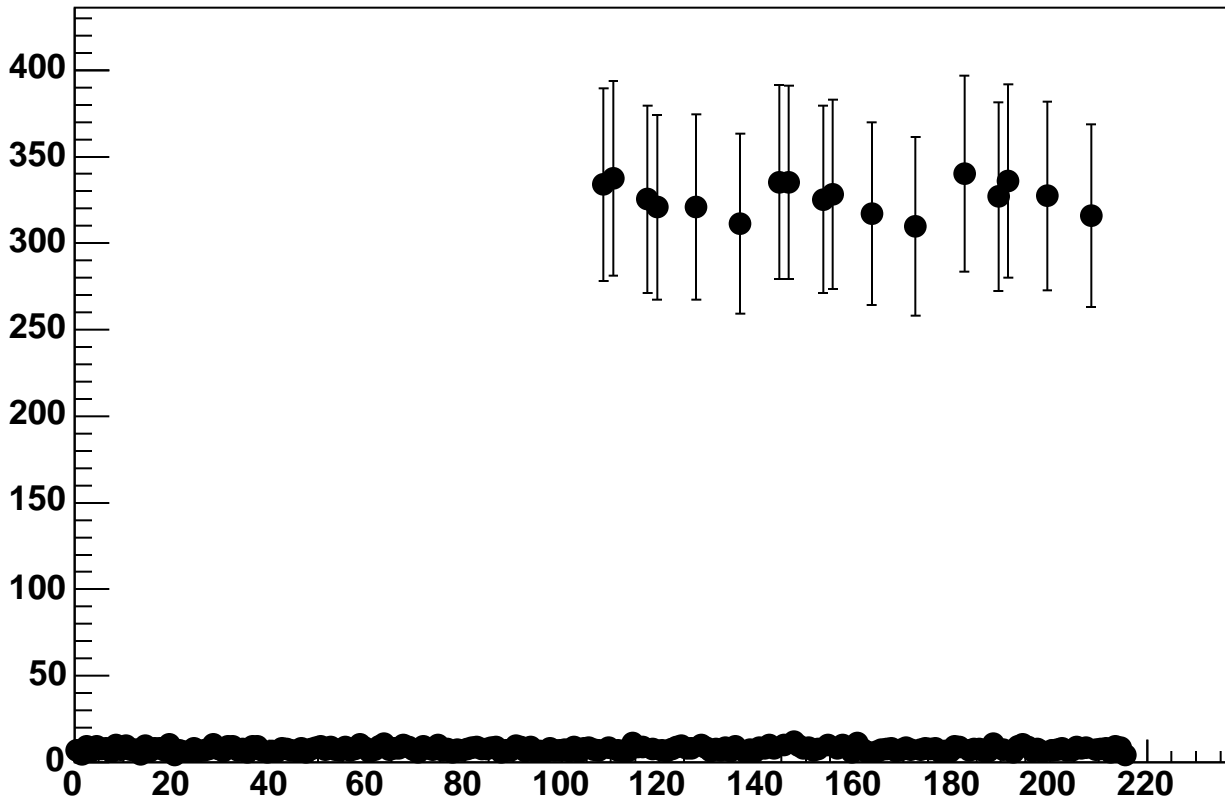
Enable 0, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



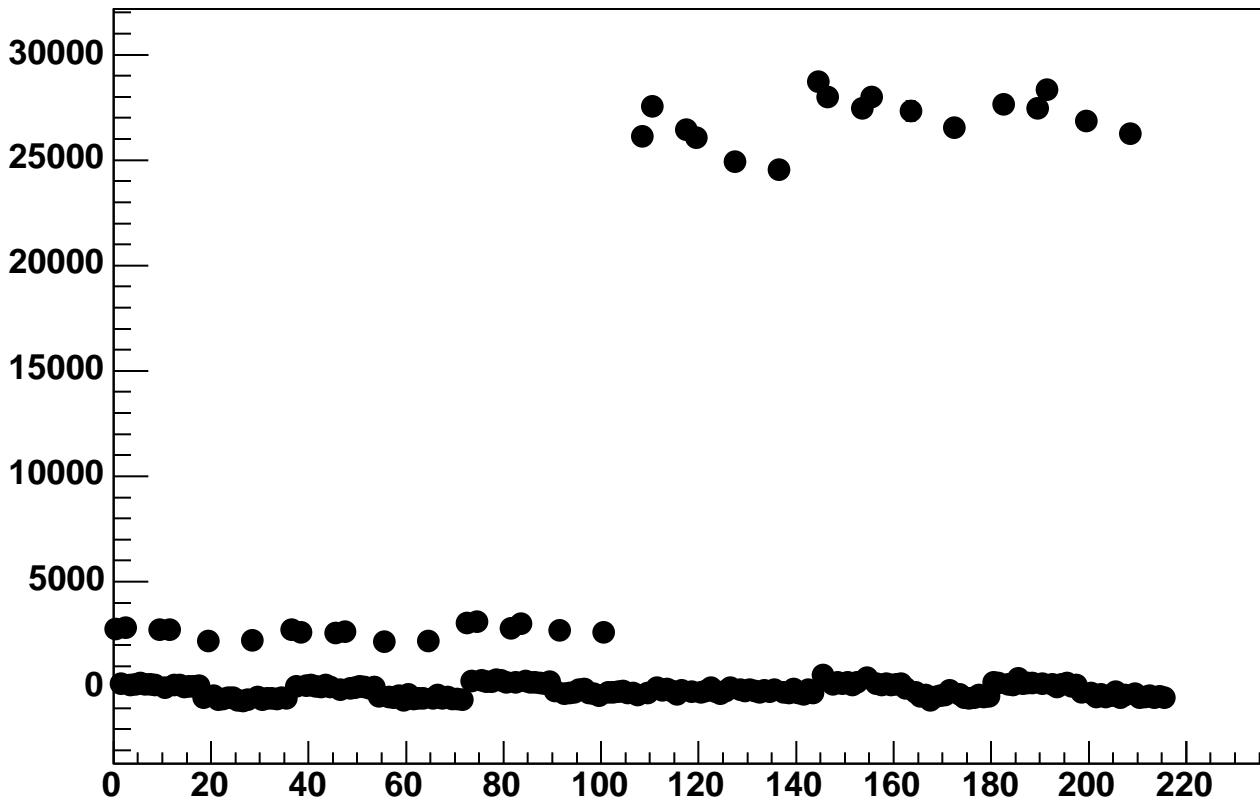
Enable 0, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



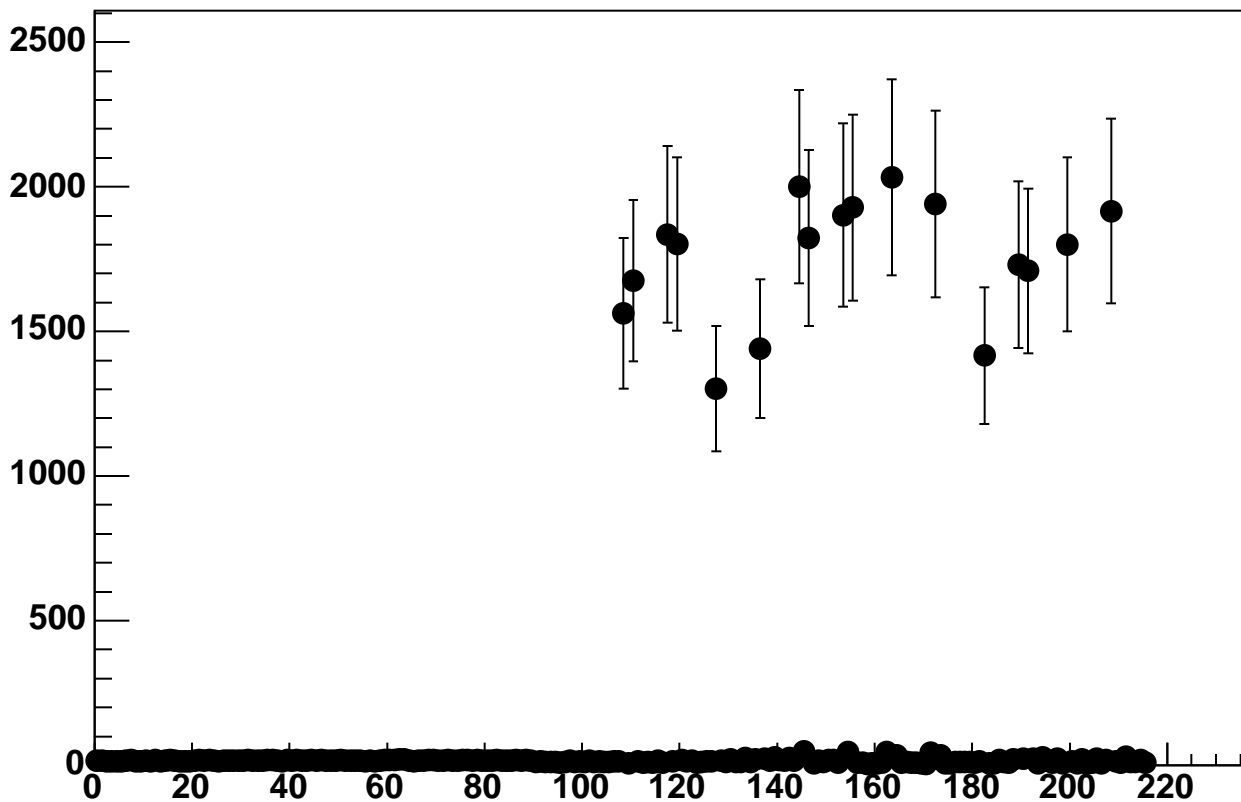
Enable 0, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



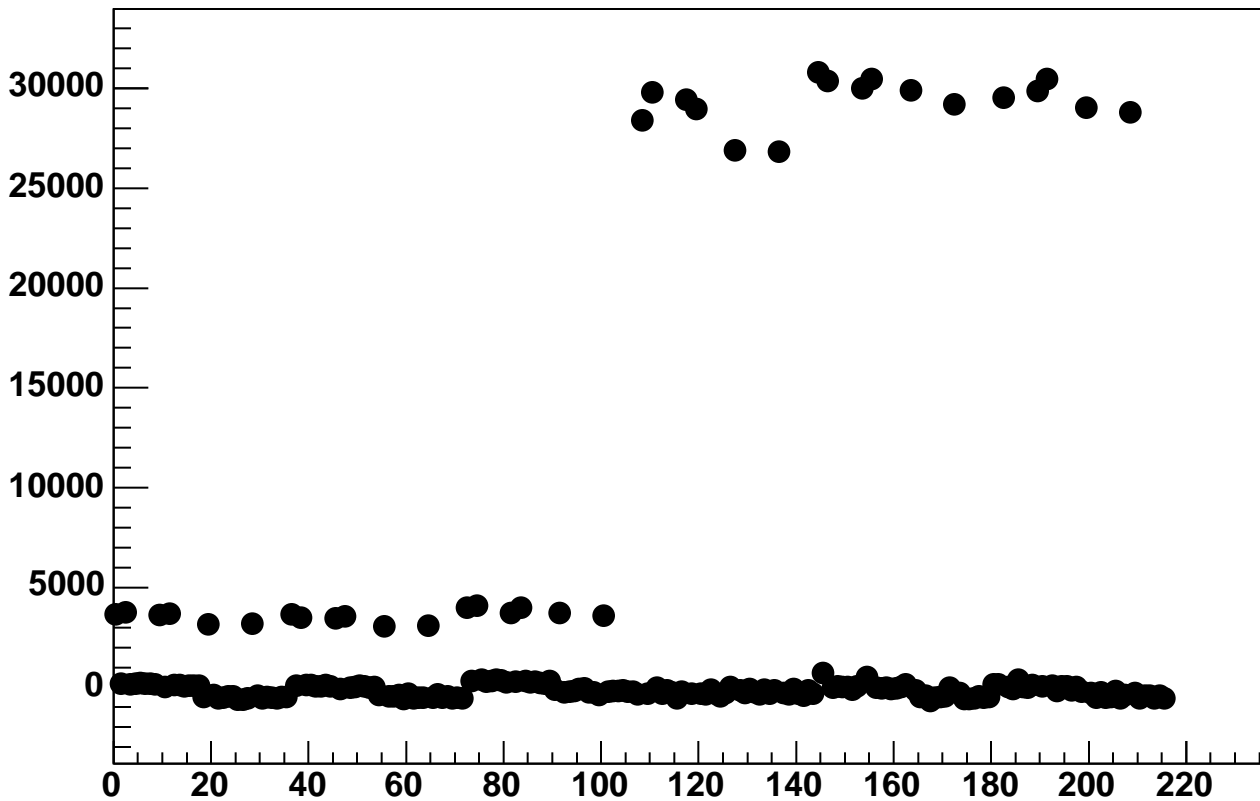
Enable 0, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



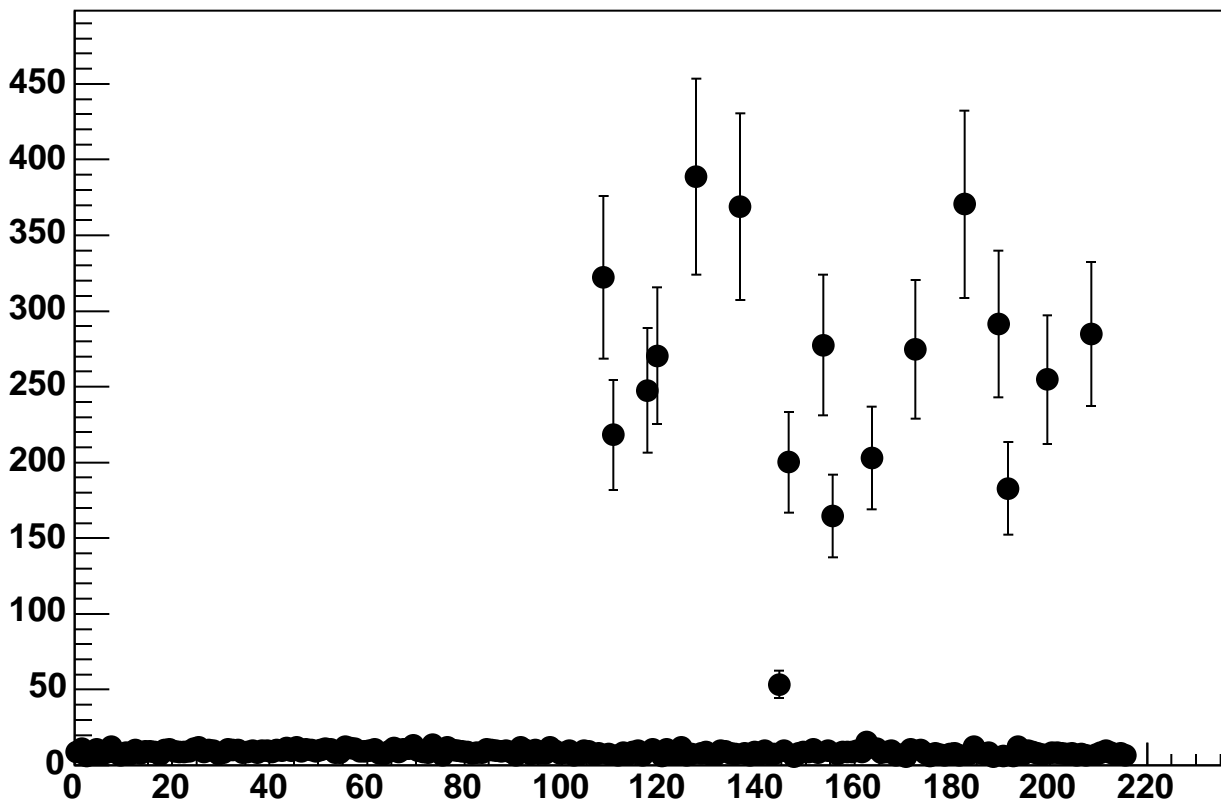
Enable 0, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



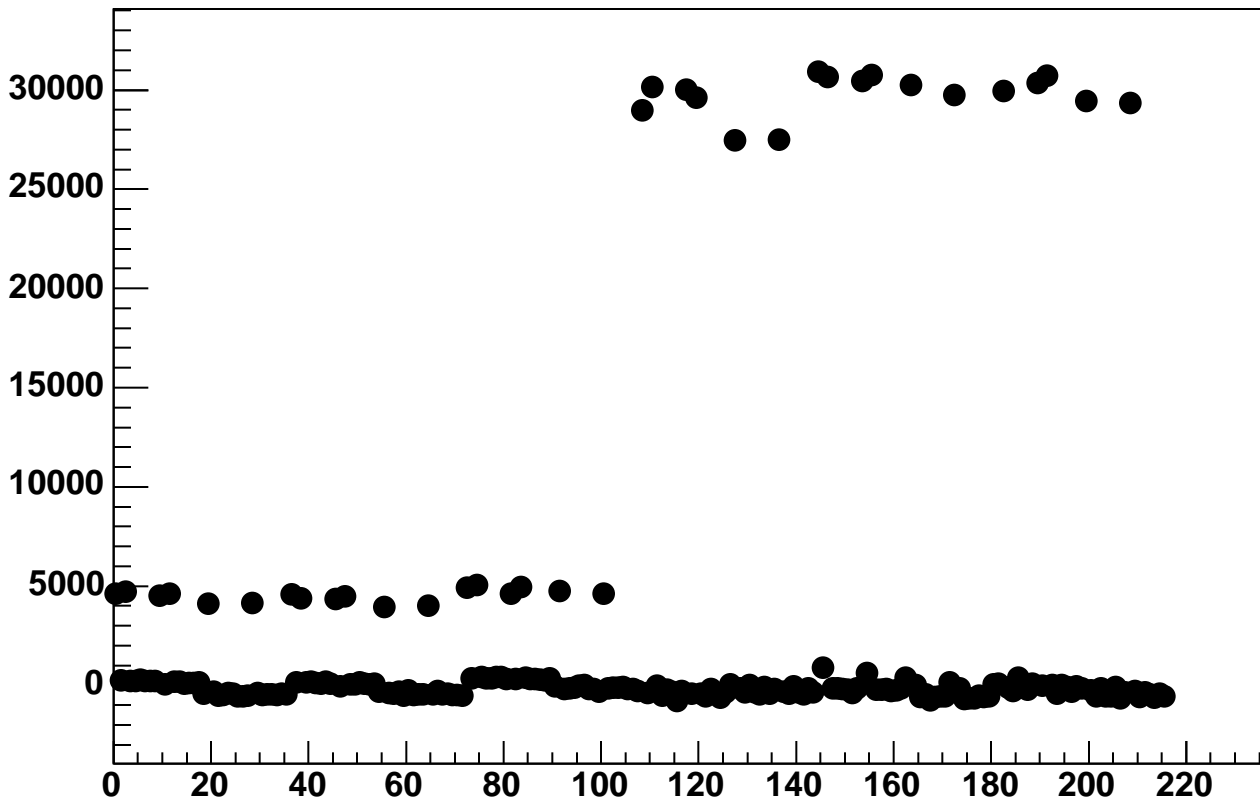
Enable 0, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



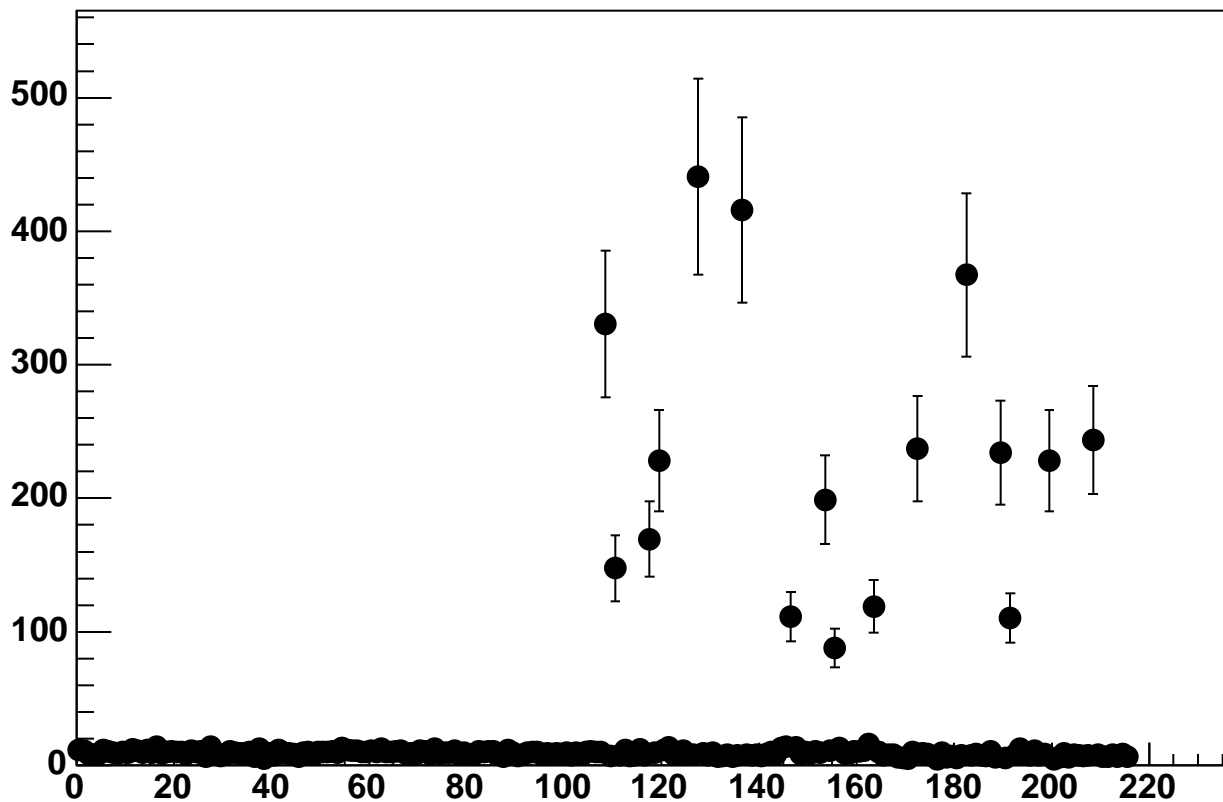
Enable 0, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



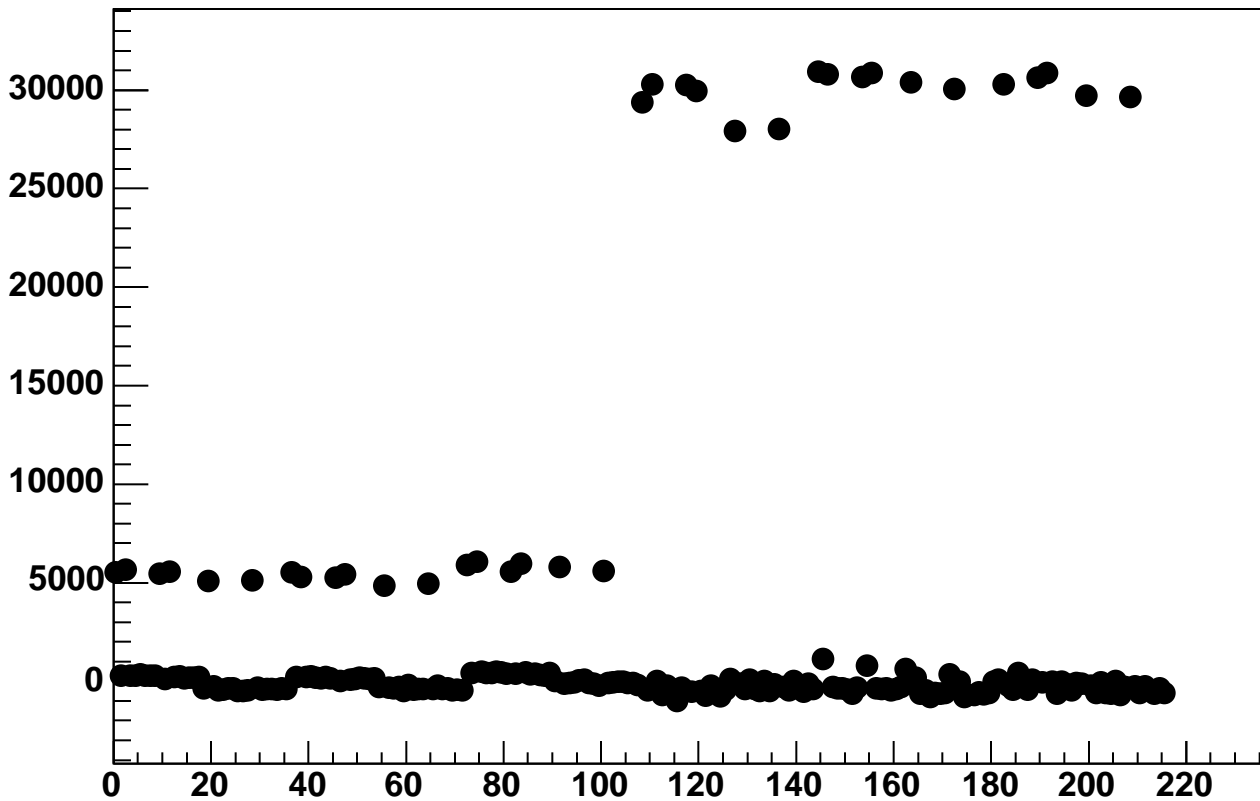
Enable 0, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



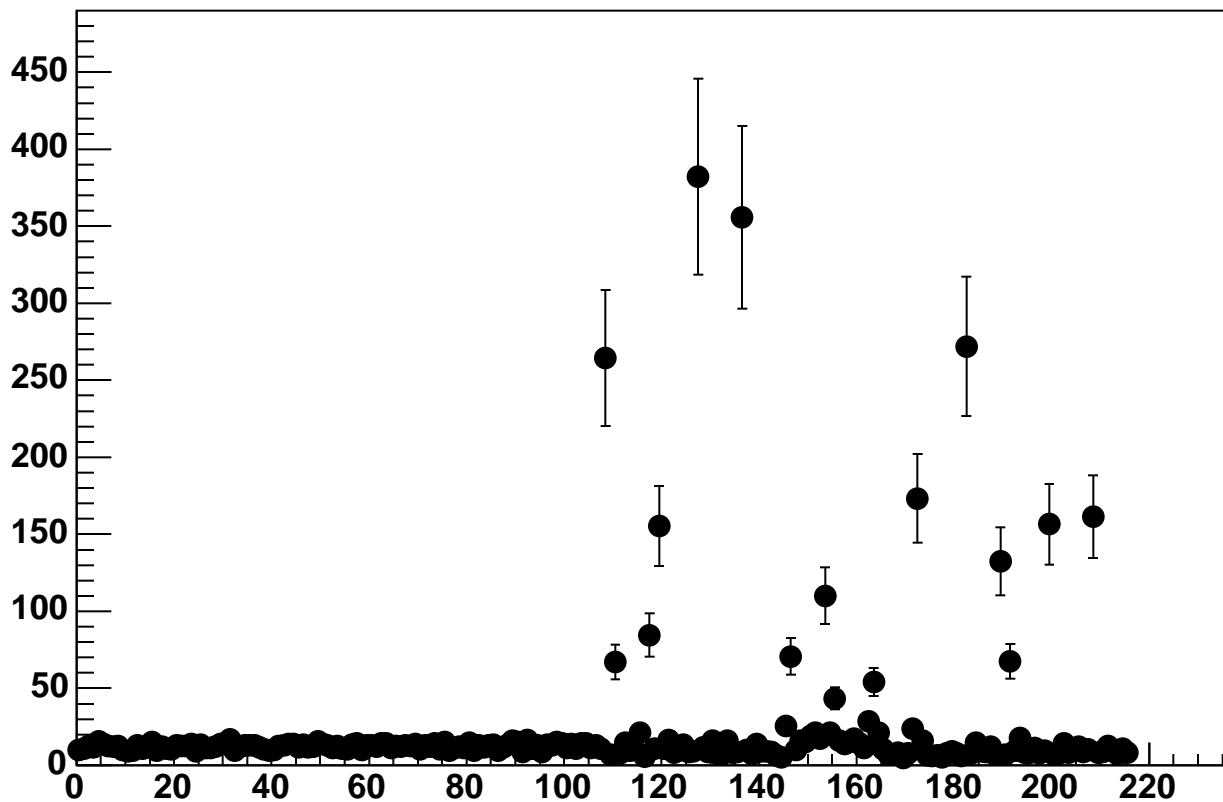
Enable 0, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



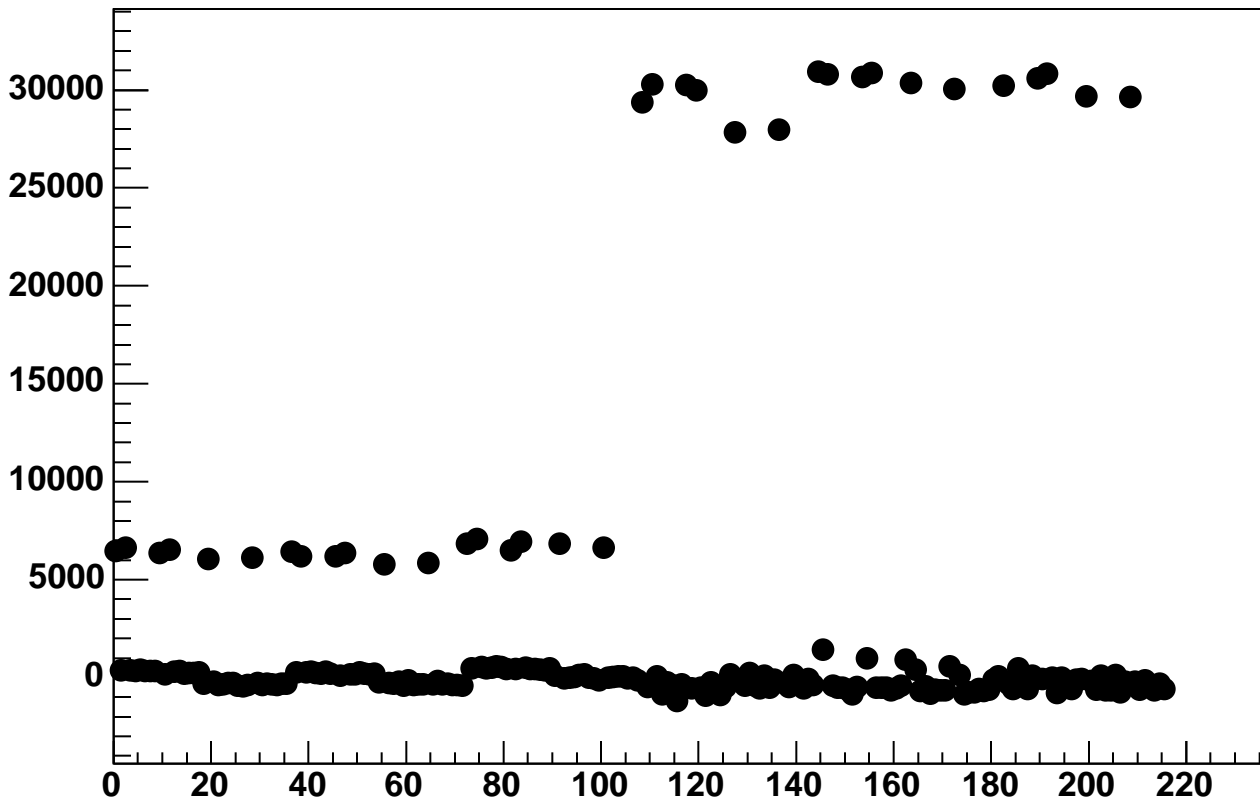
Enable 0, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



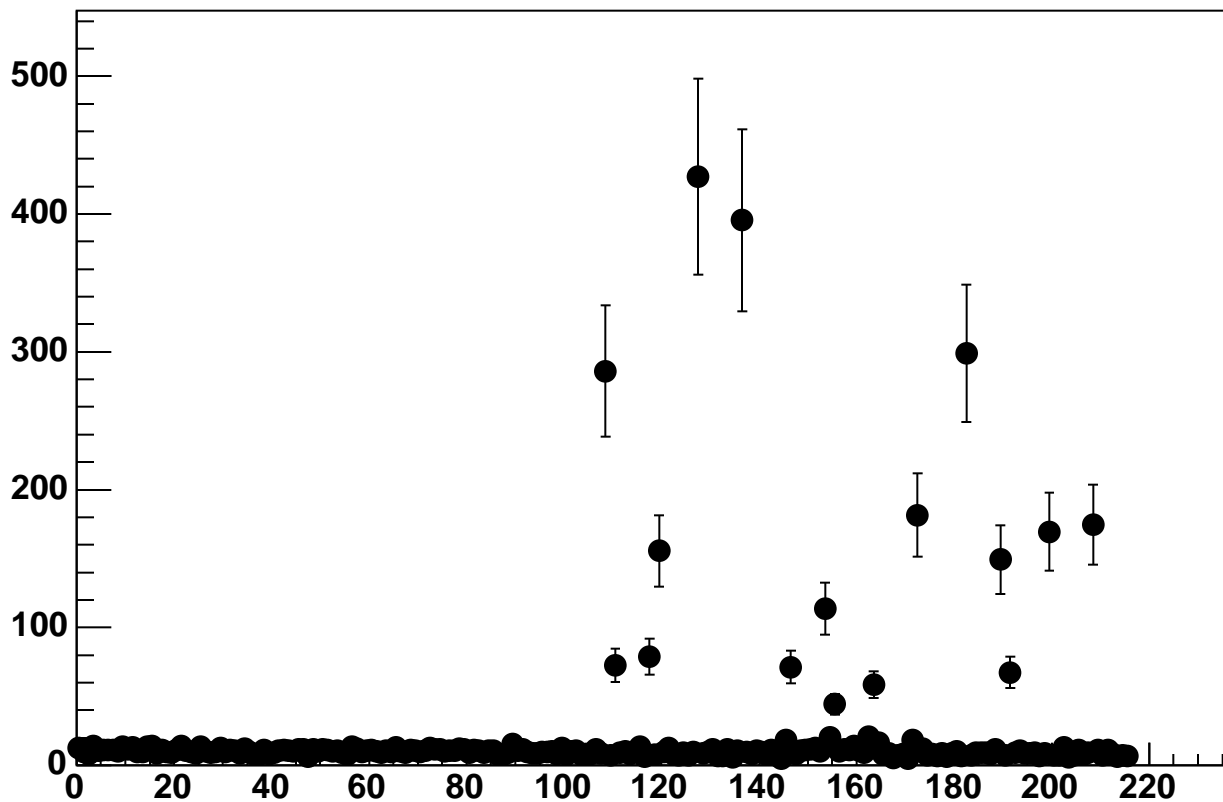
Enable 0, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



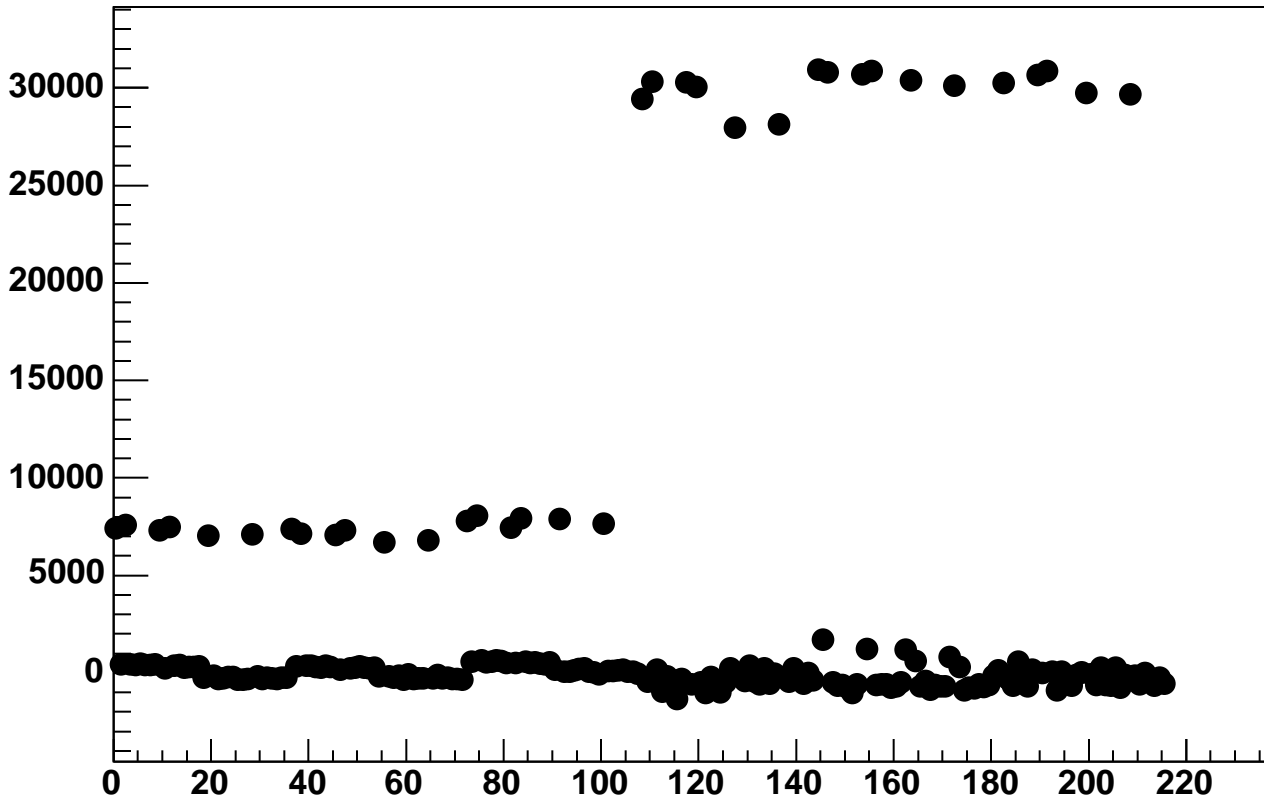
Enable 0, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



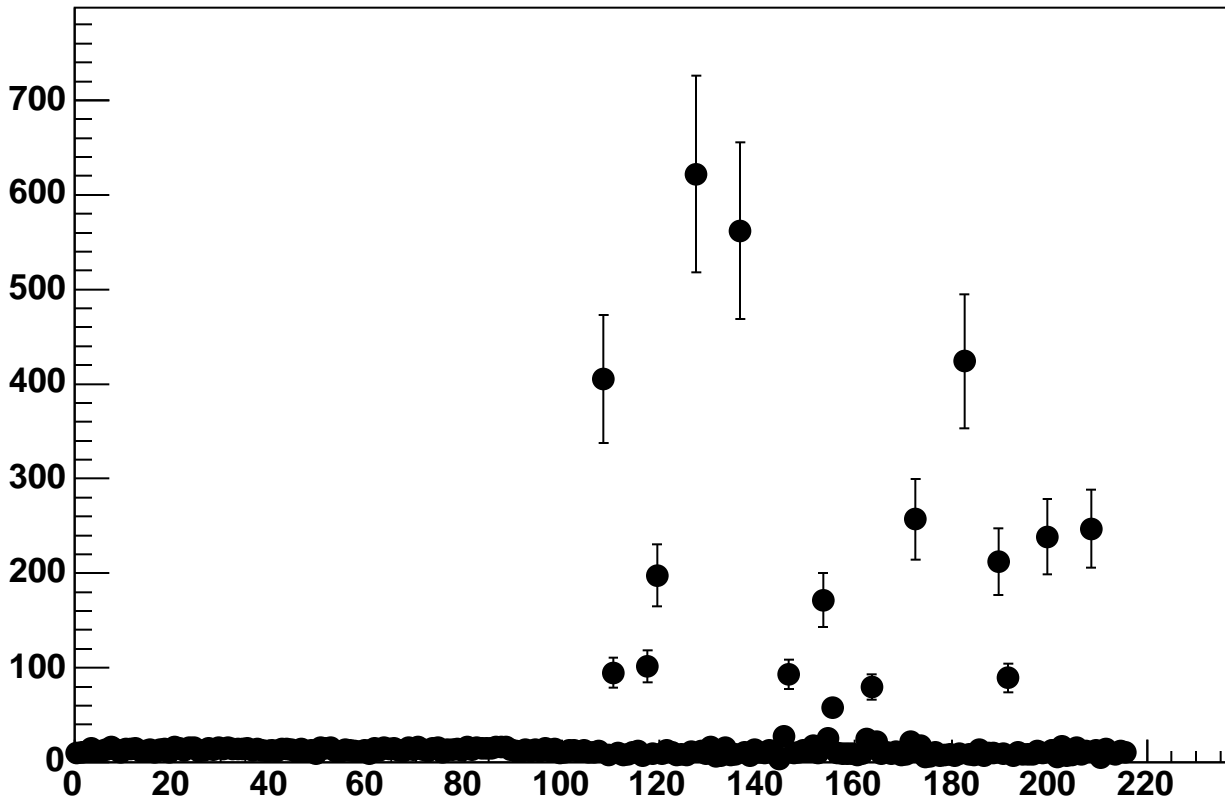
Enable 0, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



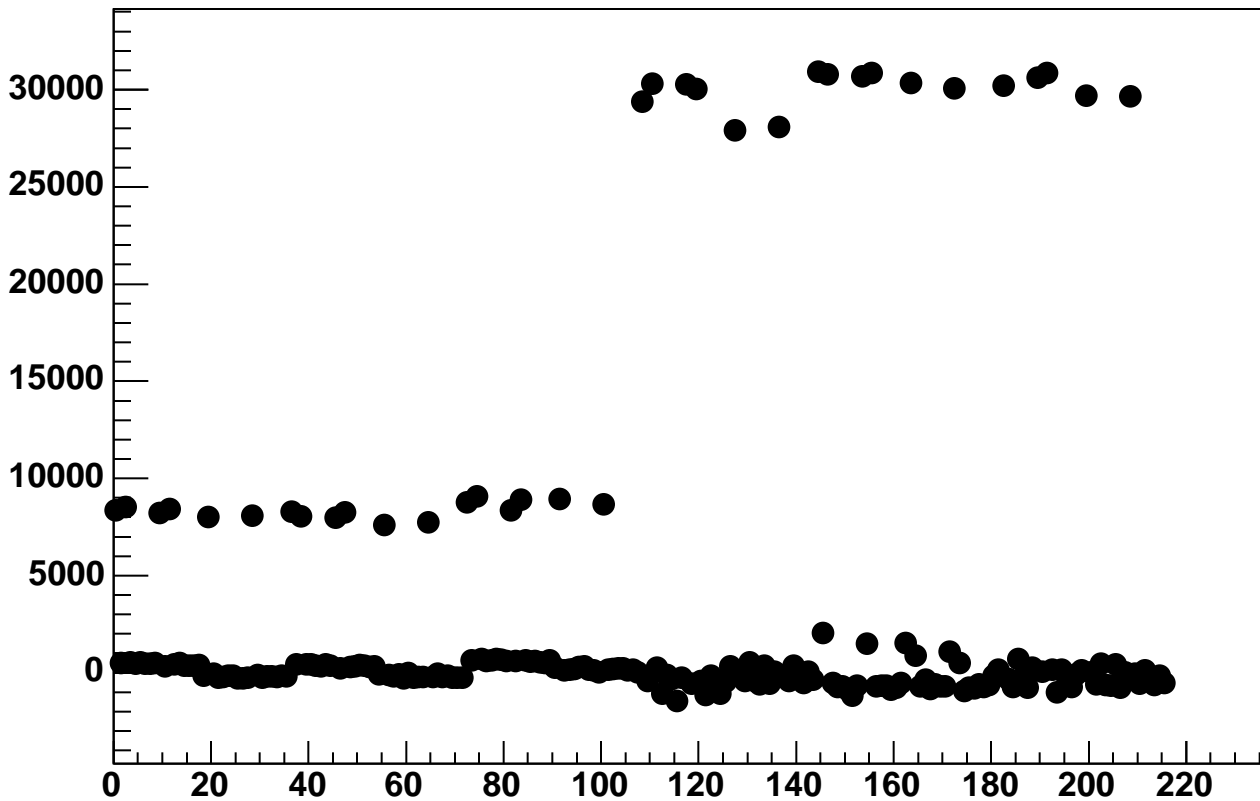
Enable 0, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



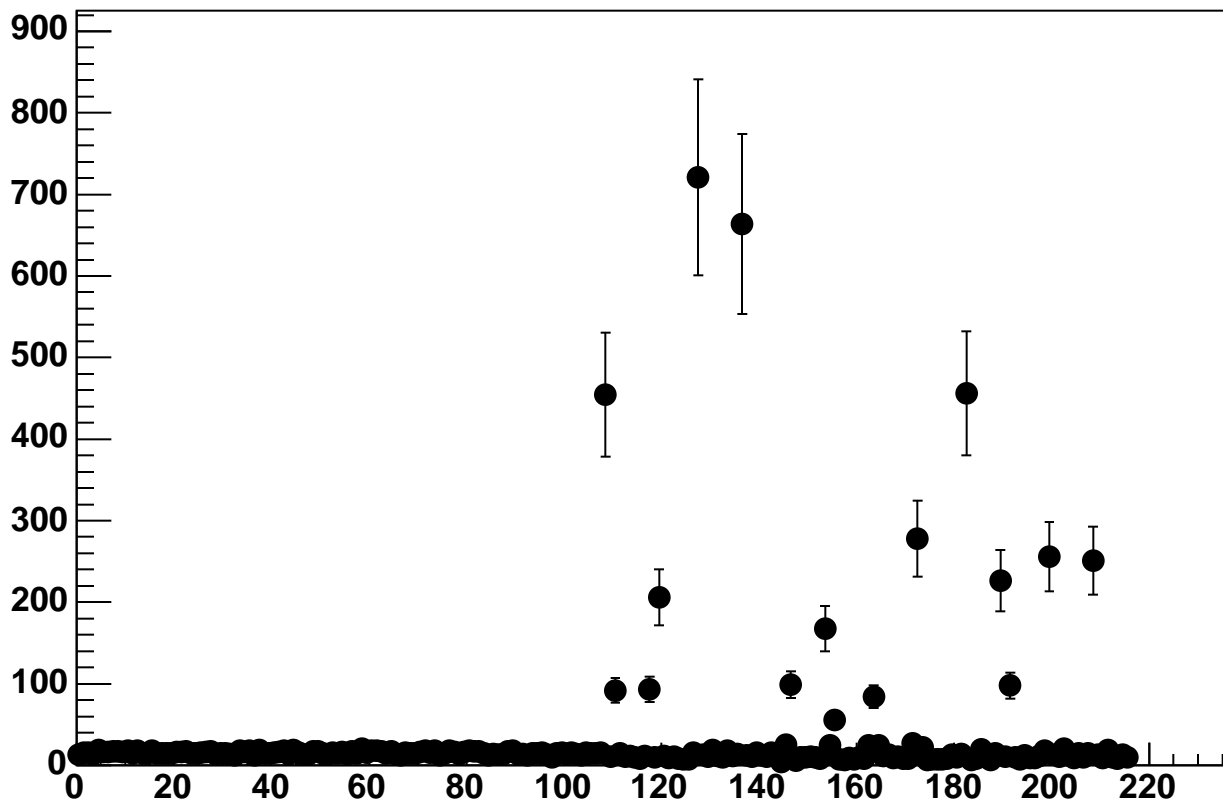
Enable 0, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



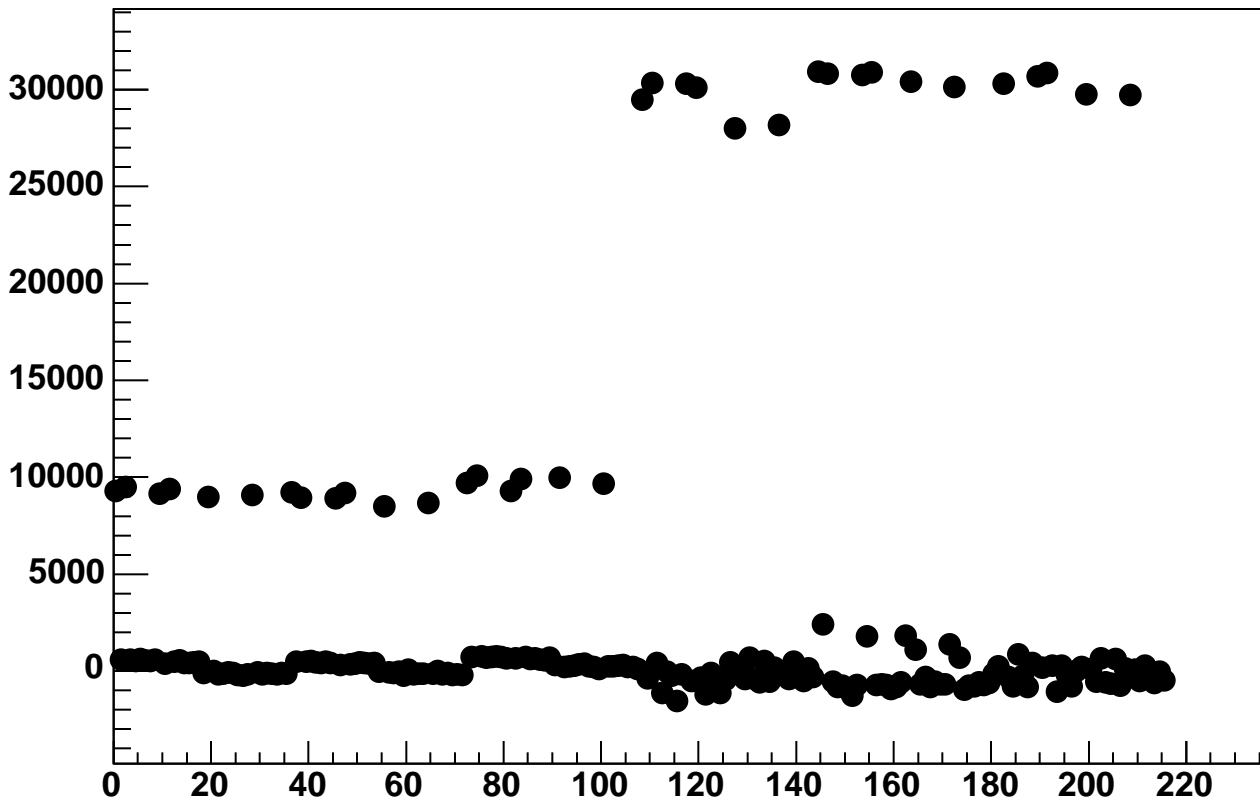
Enable 0, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



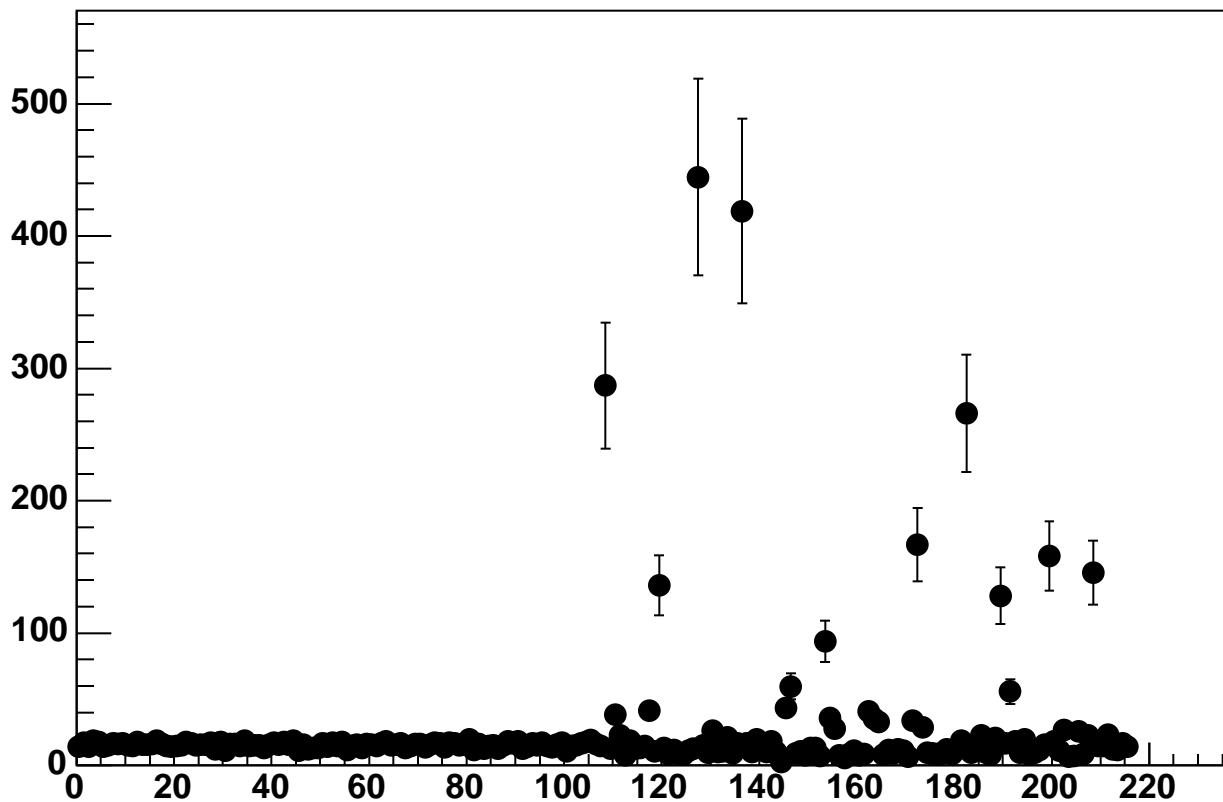
Enable 0, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



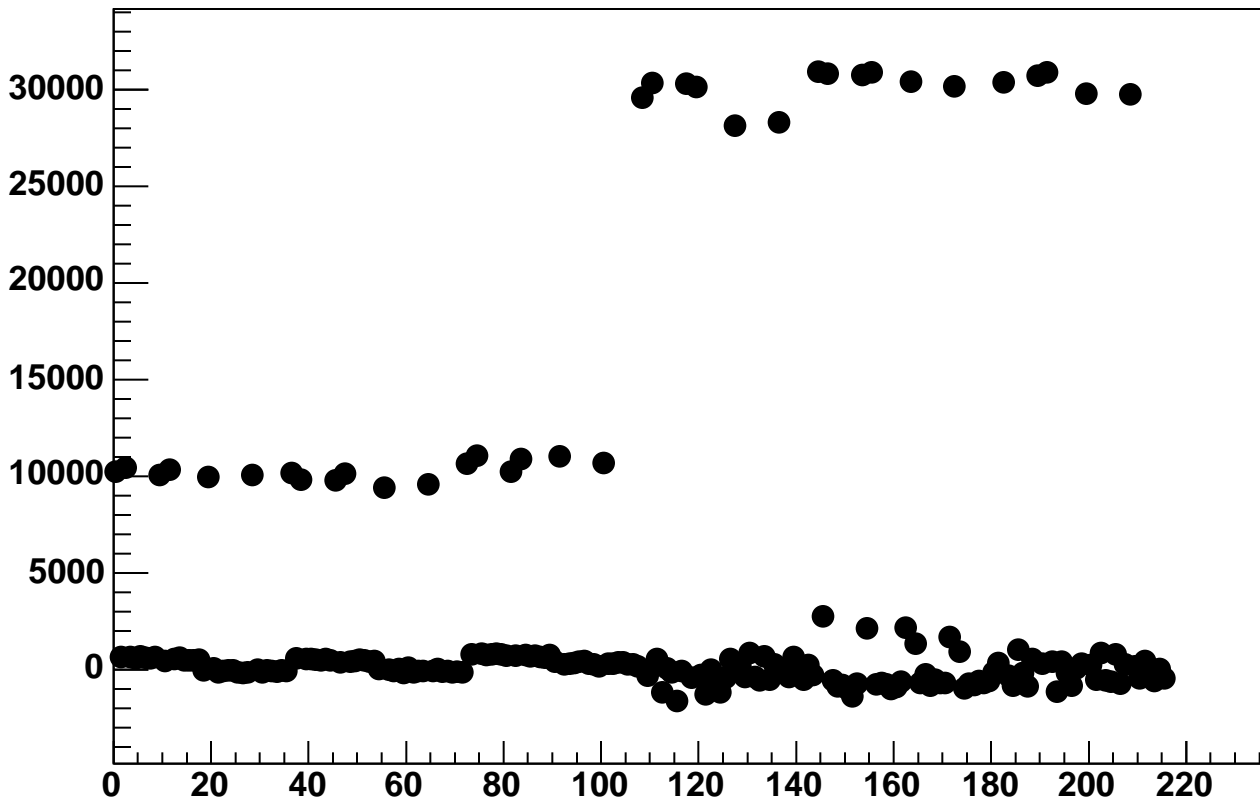
Enable 0, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



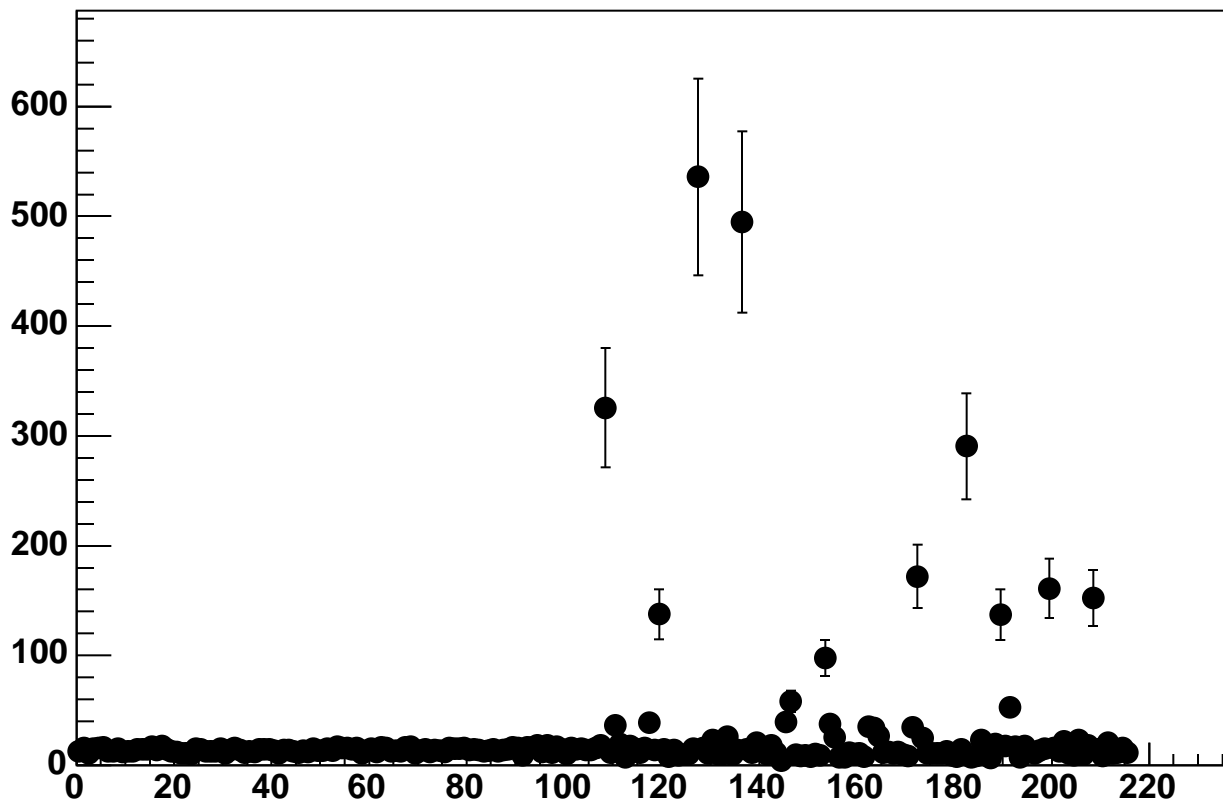
Enable 0, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



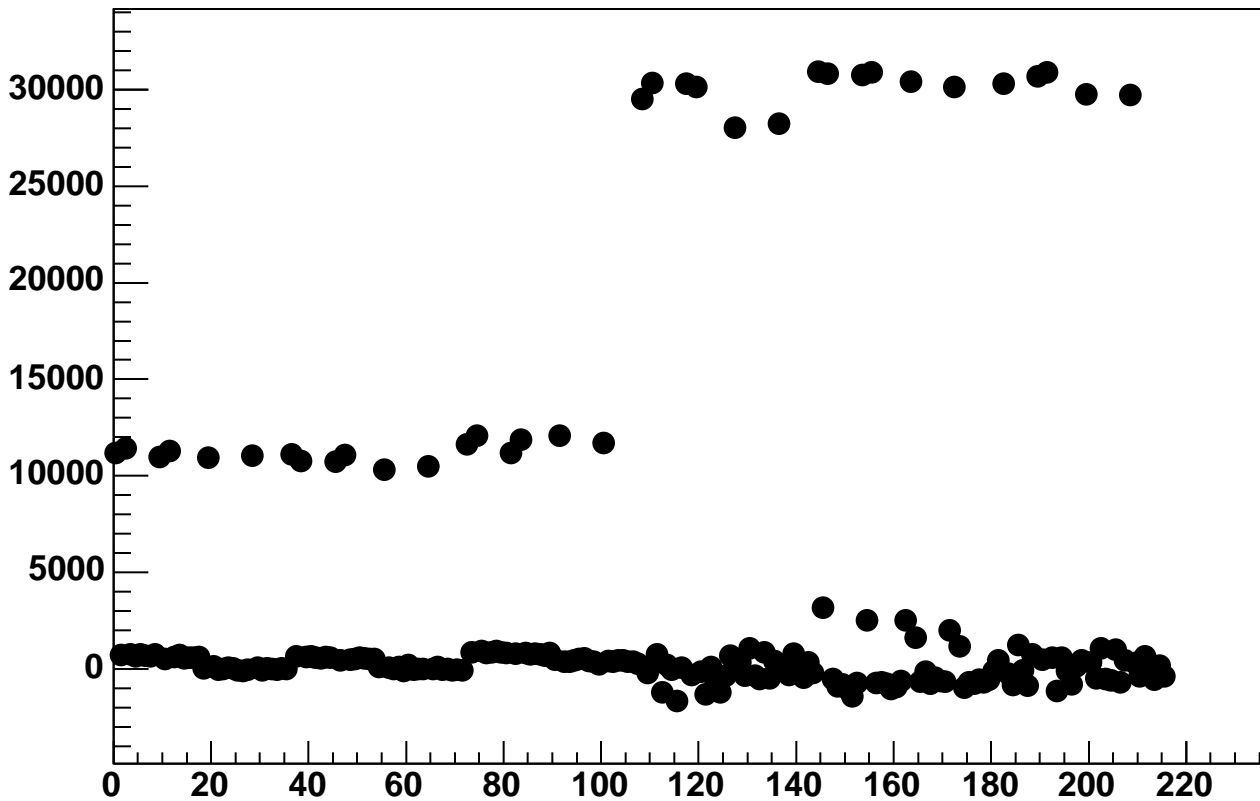
Enable 0, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



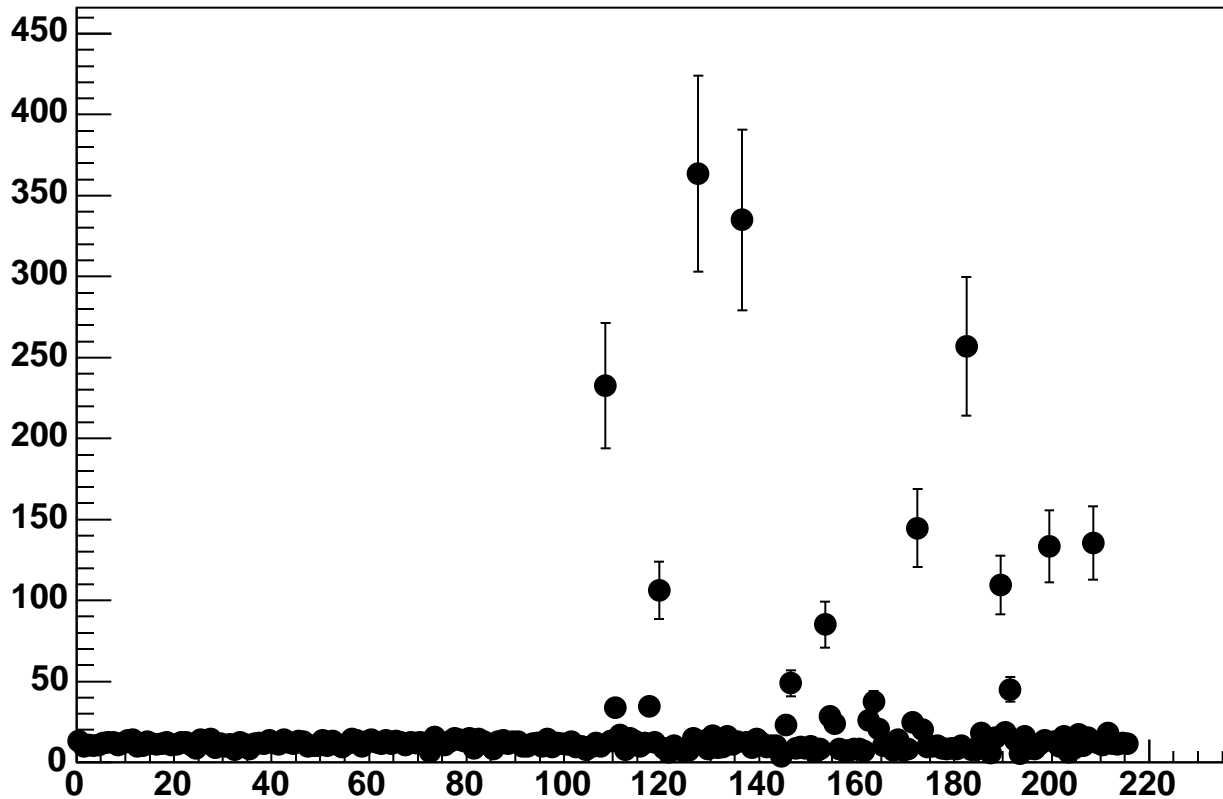
Enable 0, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



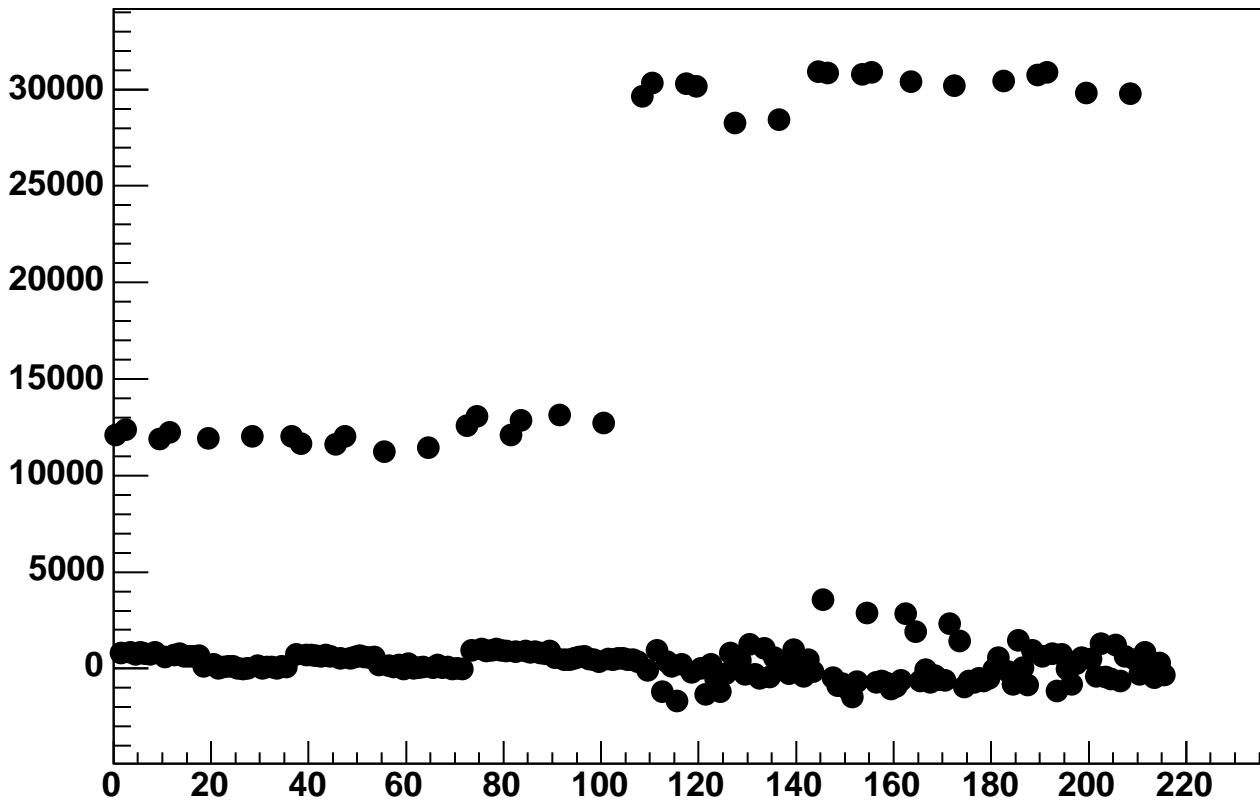
Enable 0, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



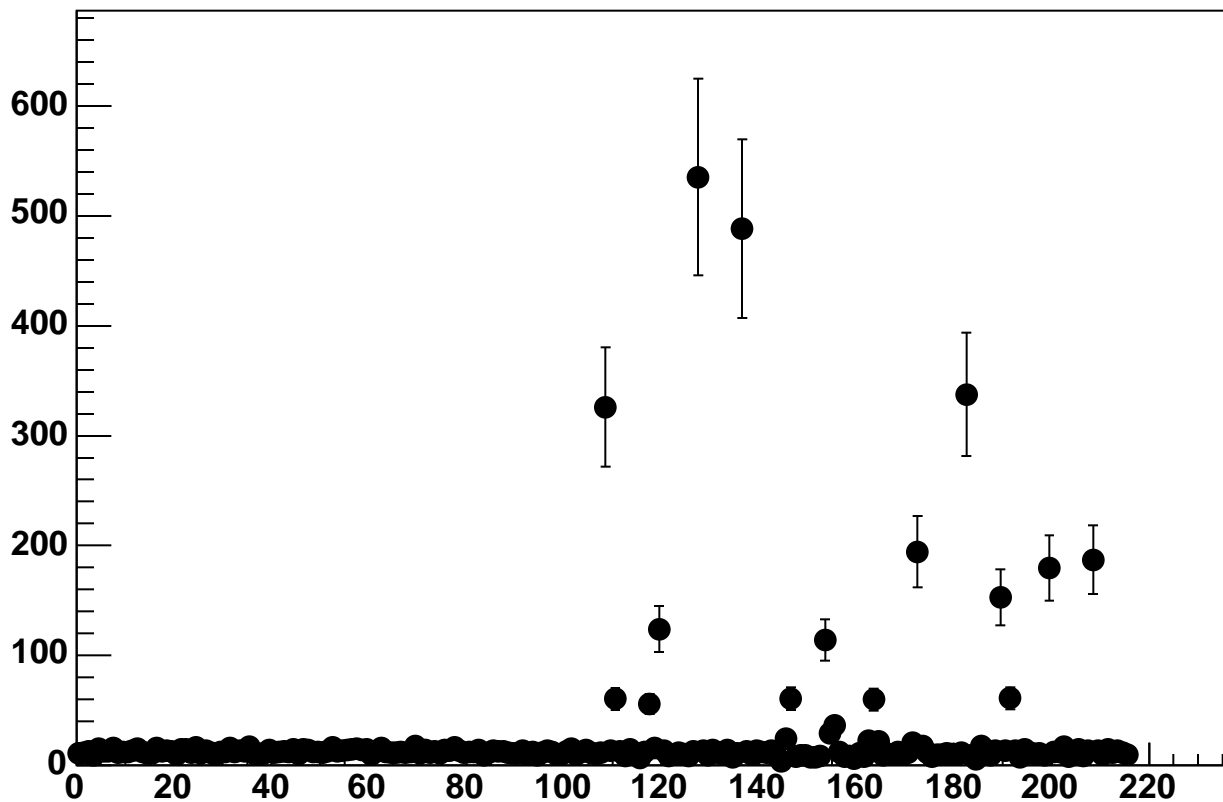
Enable 0, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



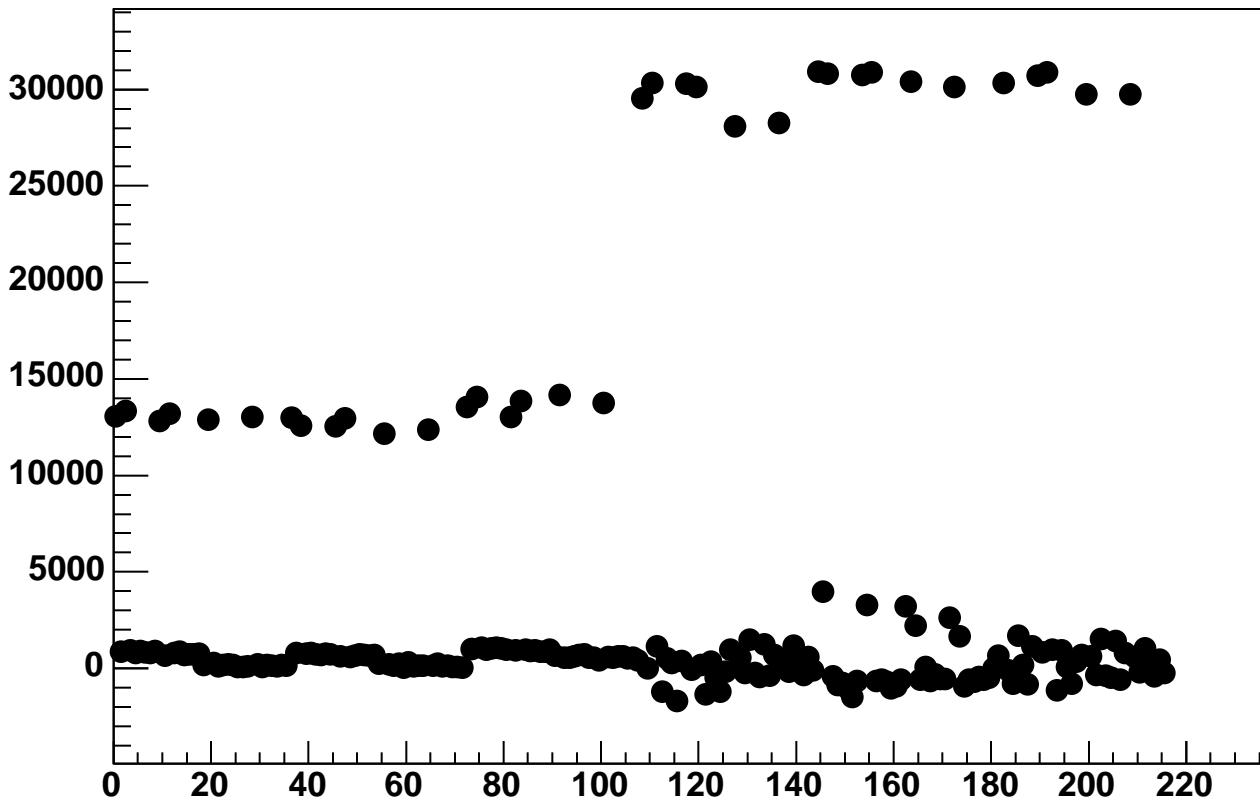
Enable 0, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



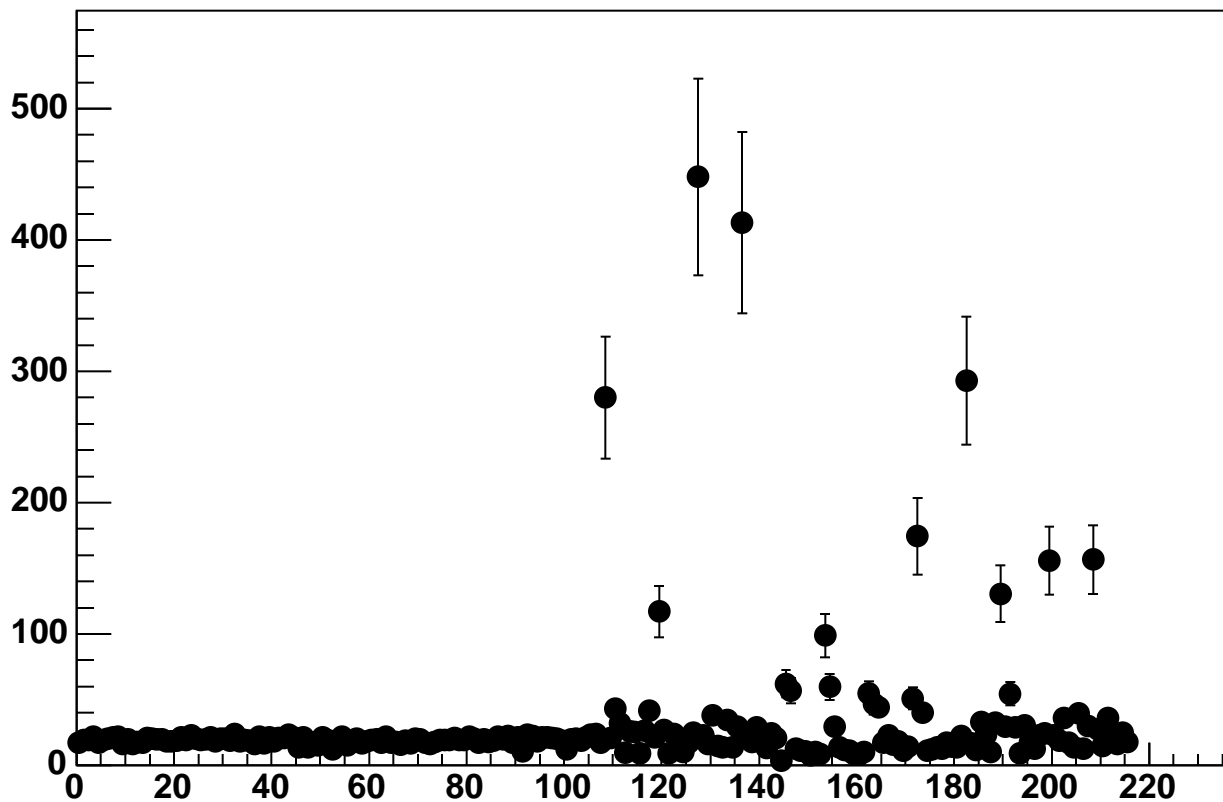
Enable 0, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



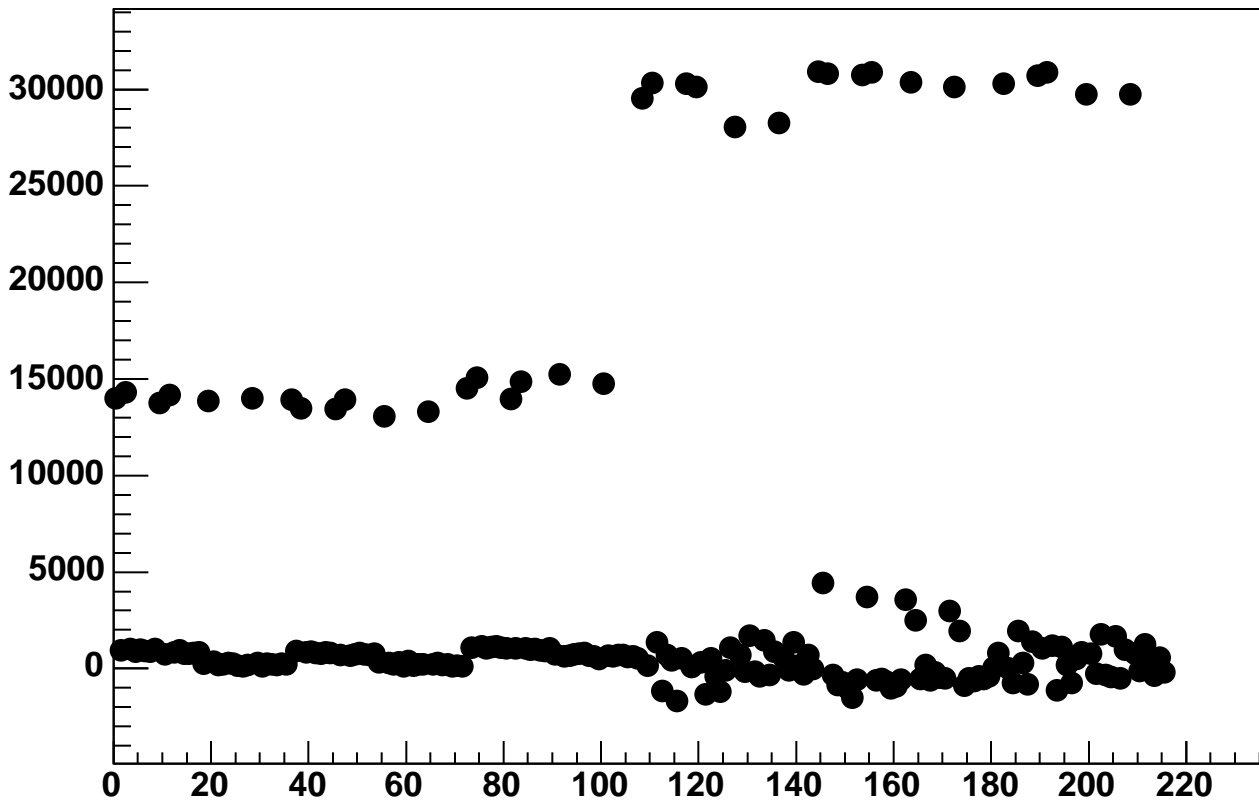
Enable 0, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



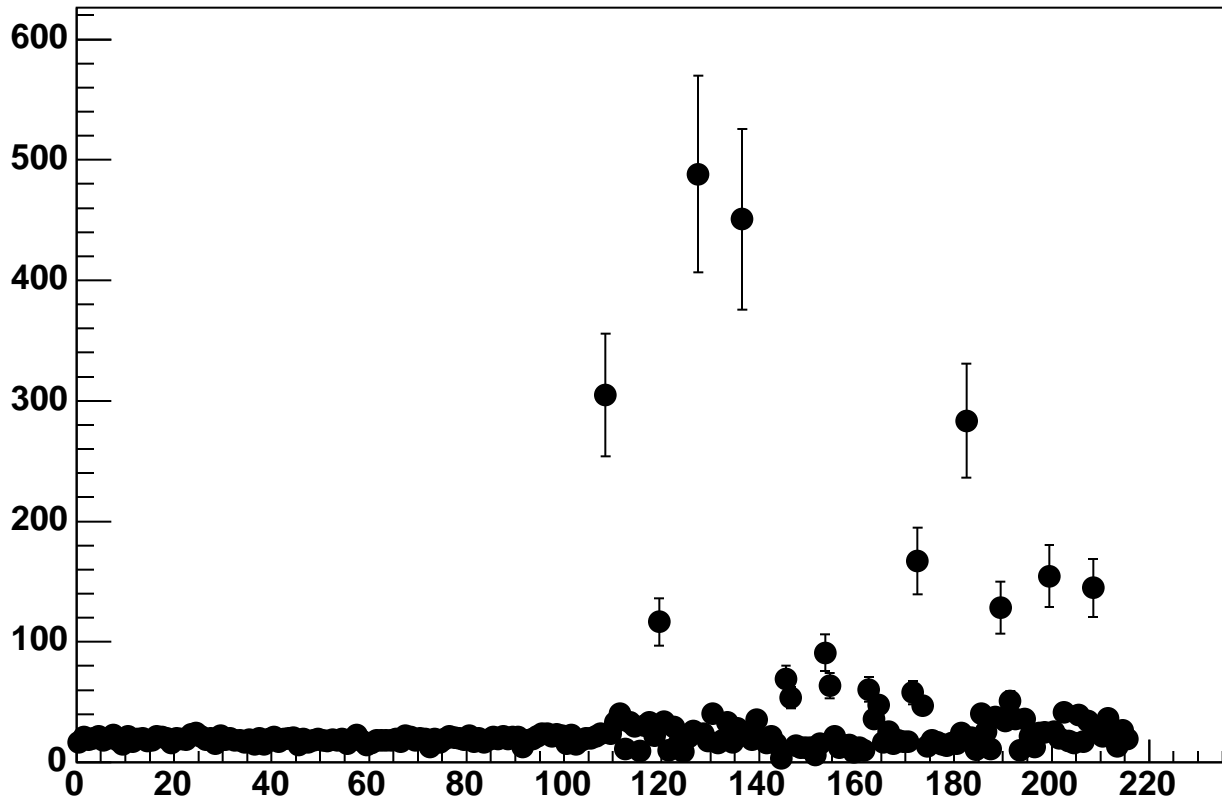
Enable 0, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



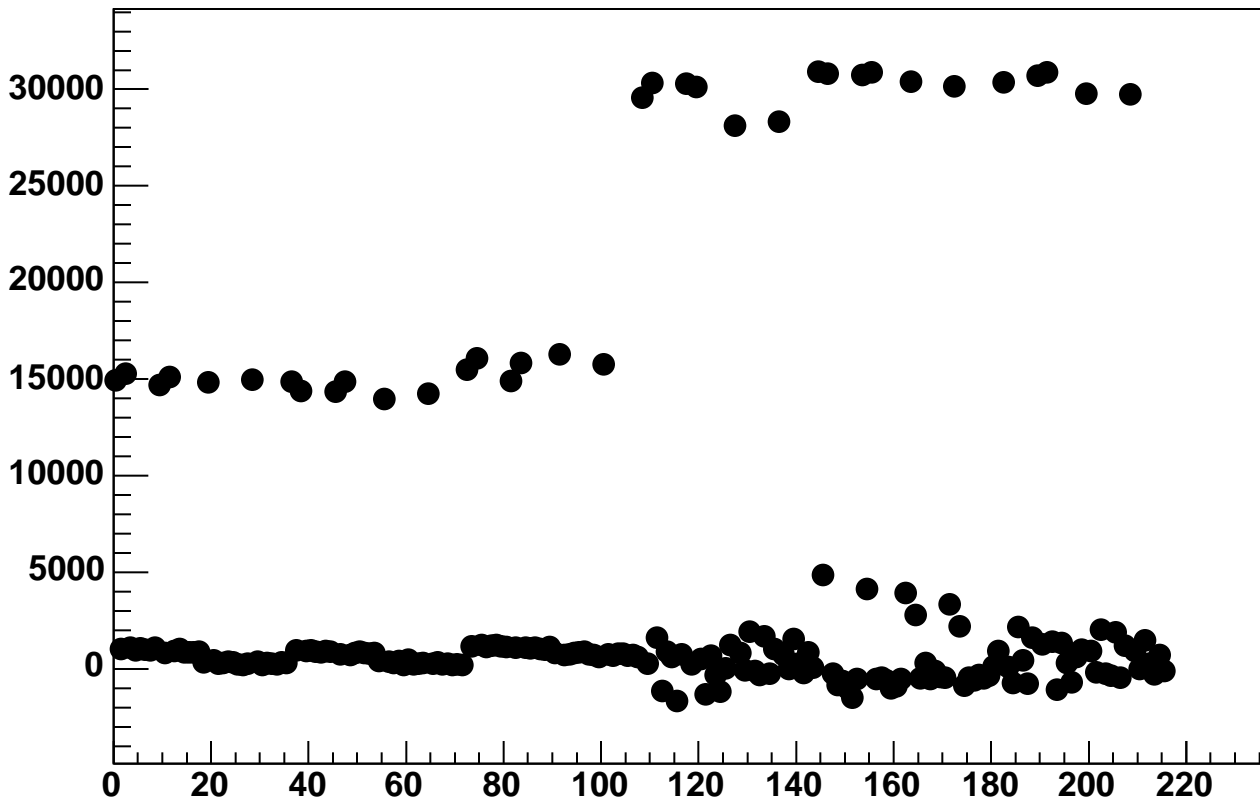
Enable 0, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



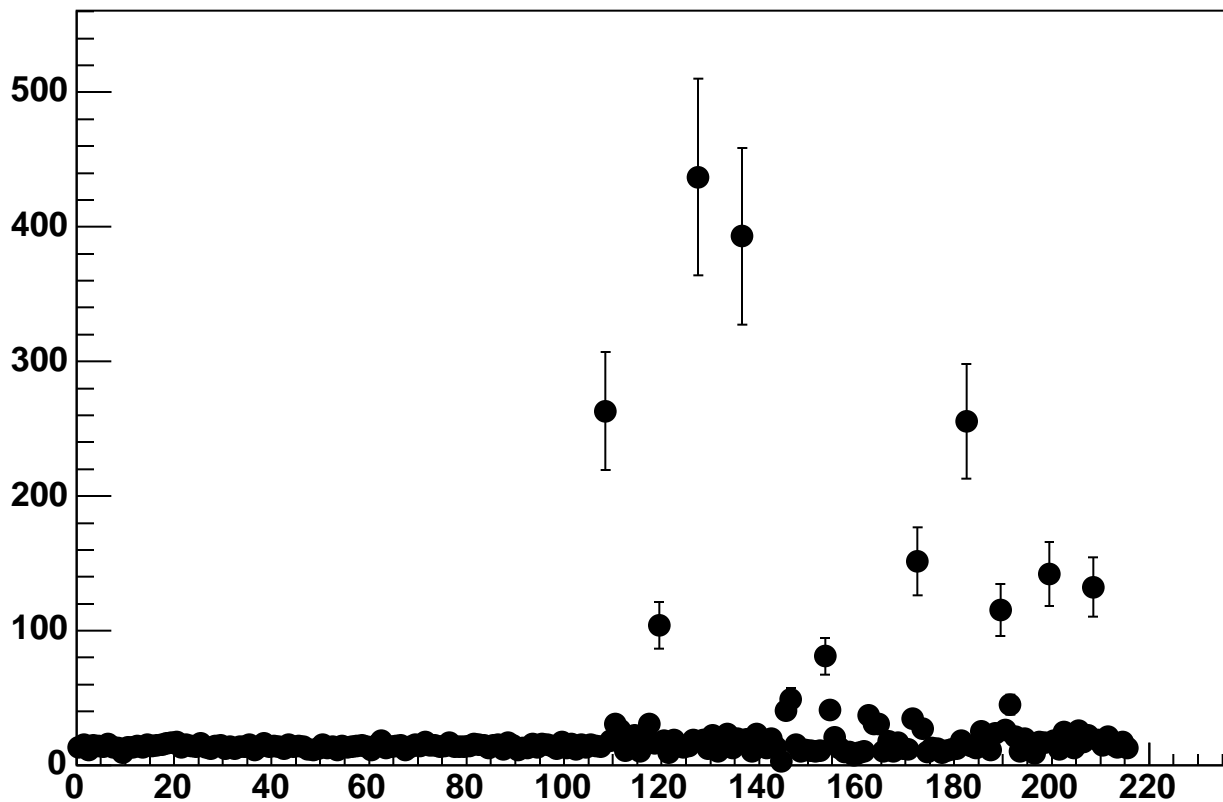
Enable 0, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



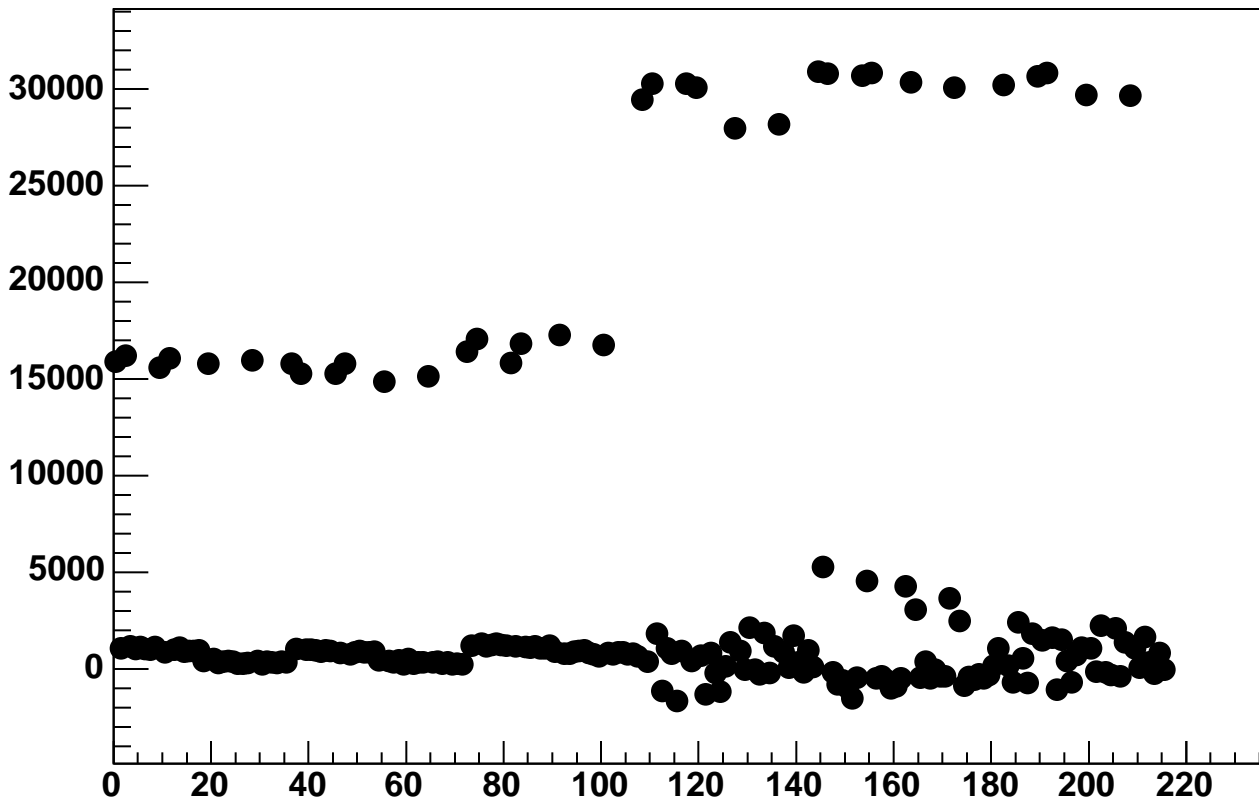
Enable 0, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



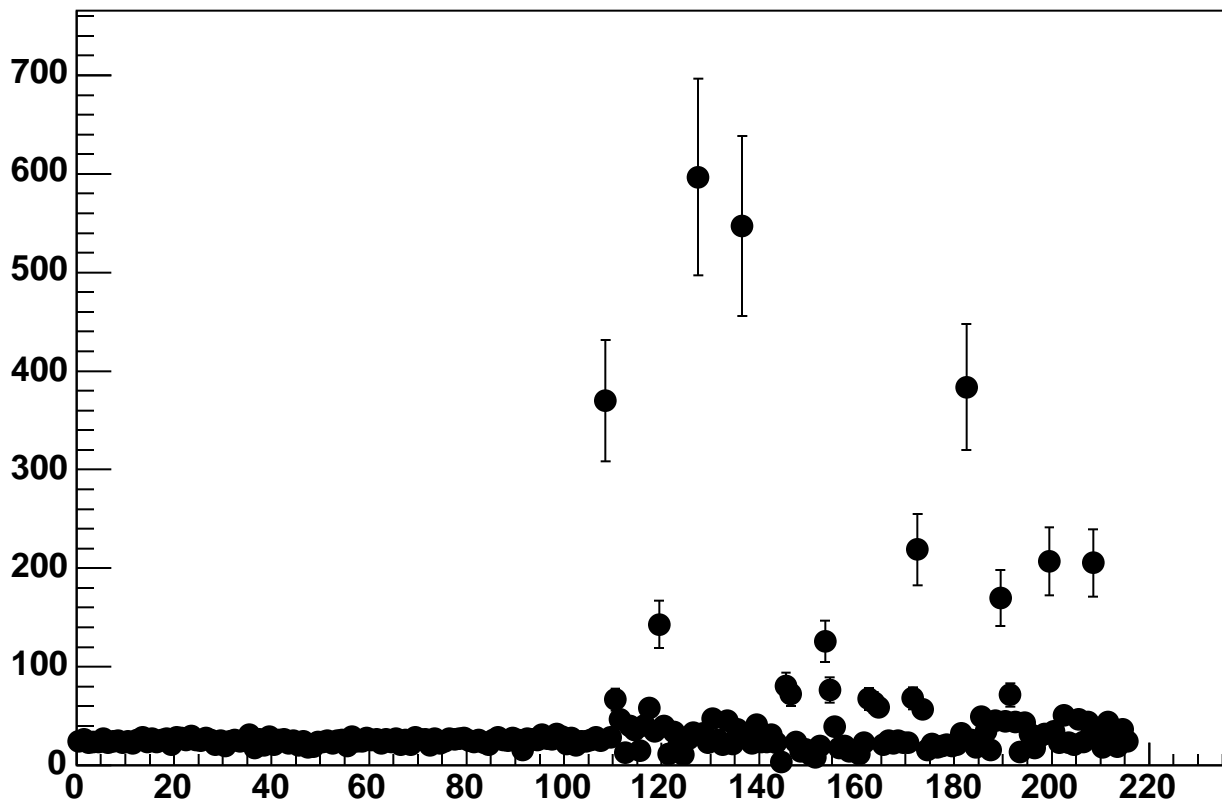
Enable 0, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



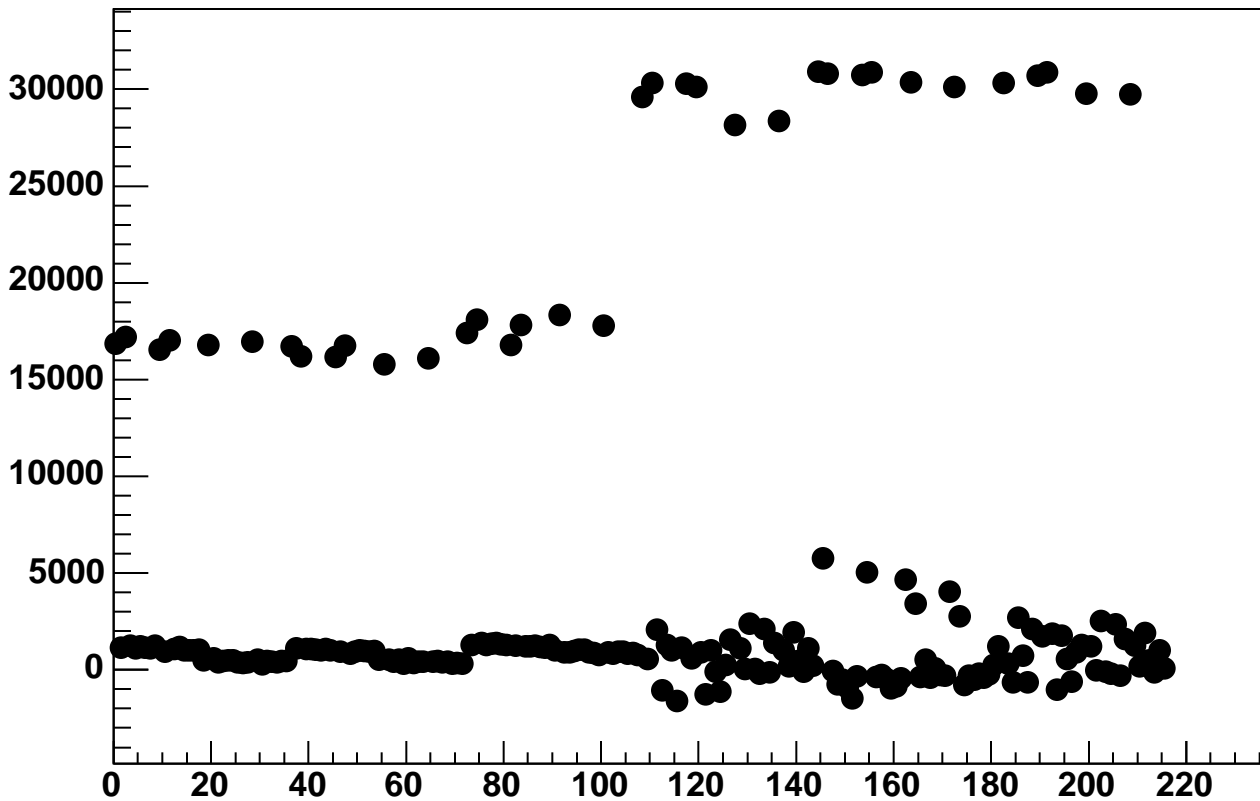
Enable 0, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



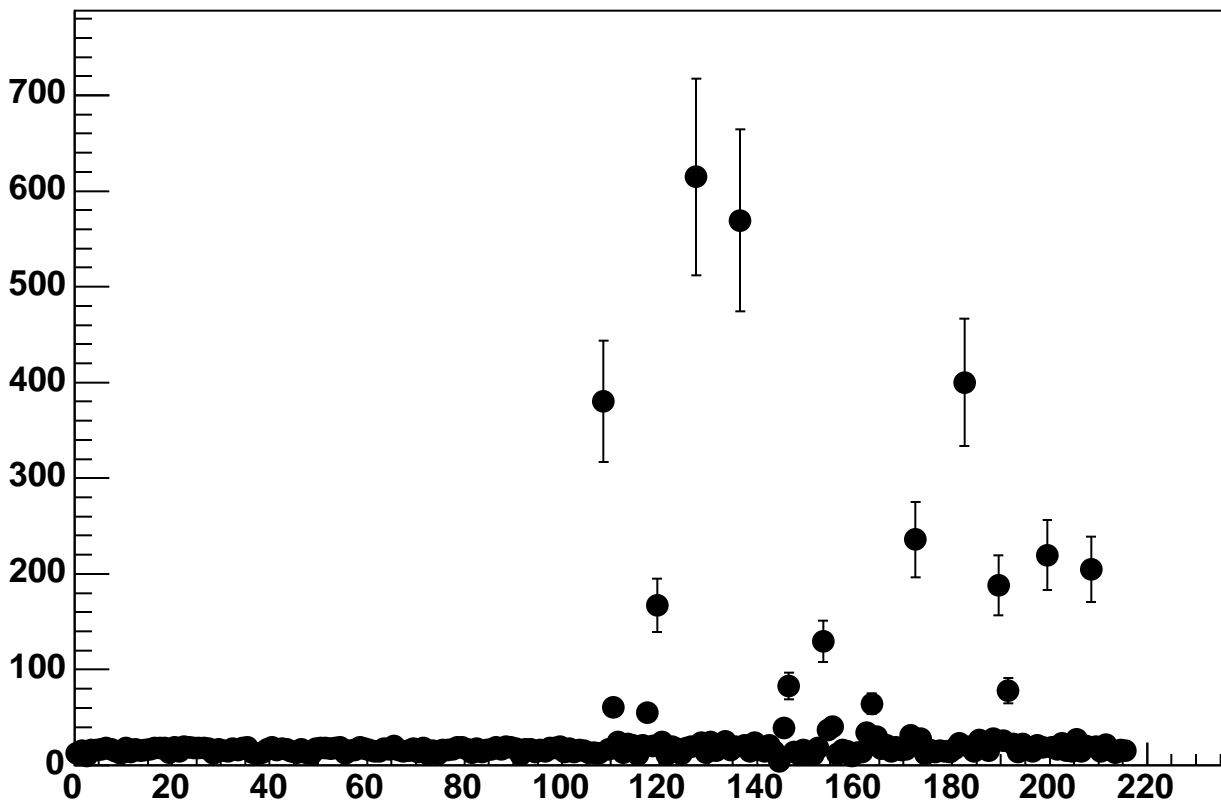
Enable 0, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



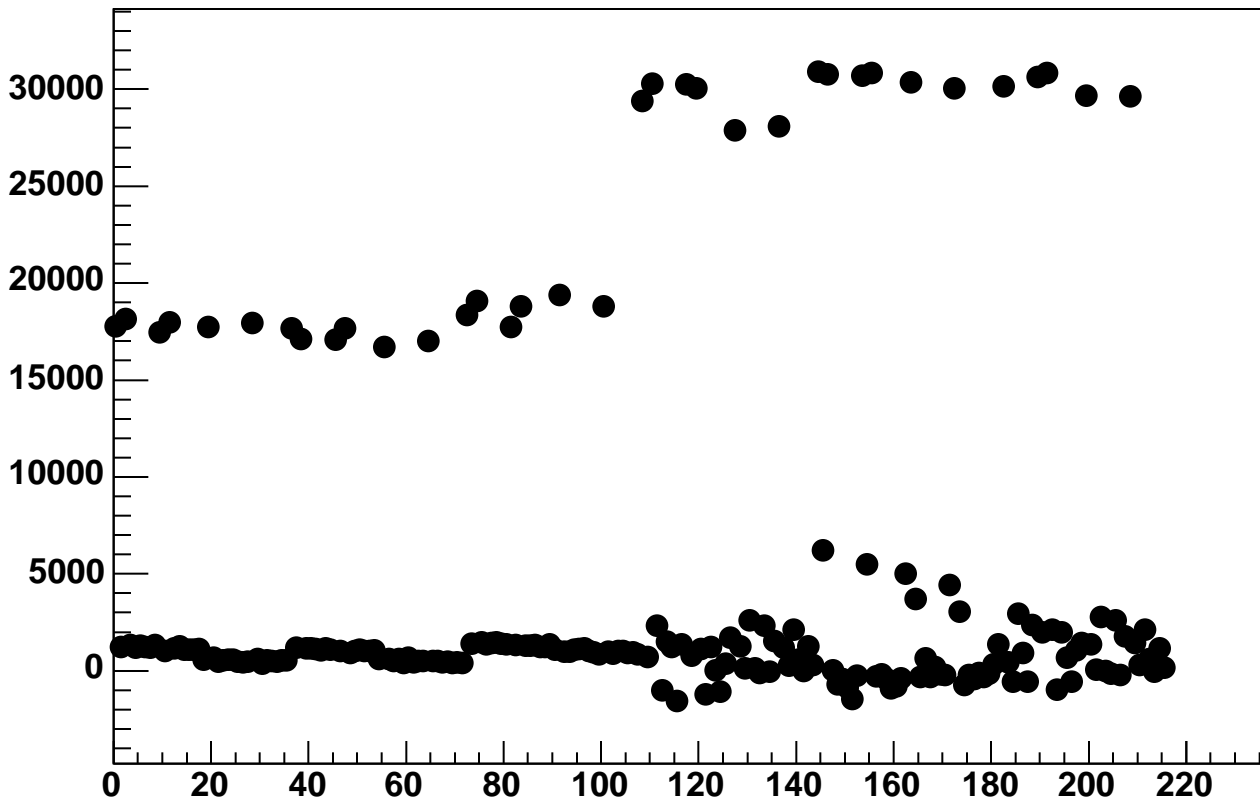
Enable 0, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



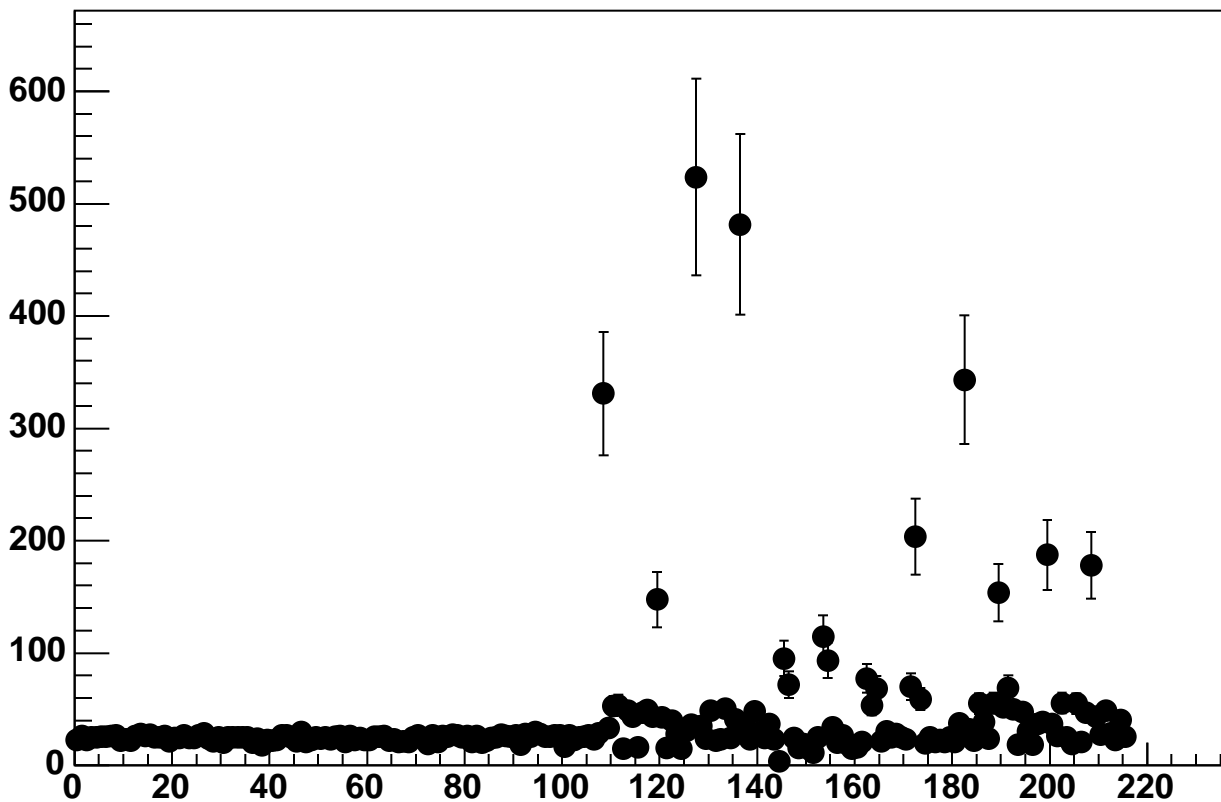
Enable 0, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



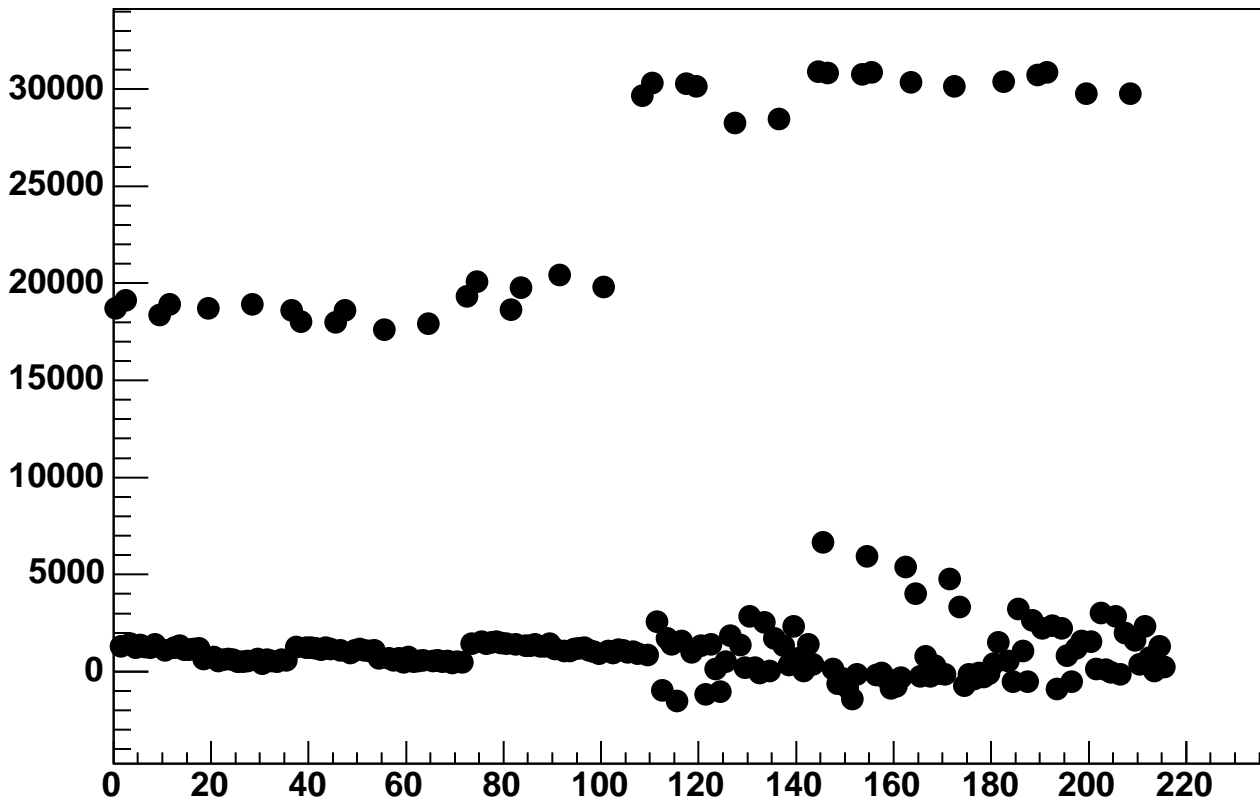
Enable 0, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



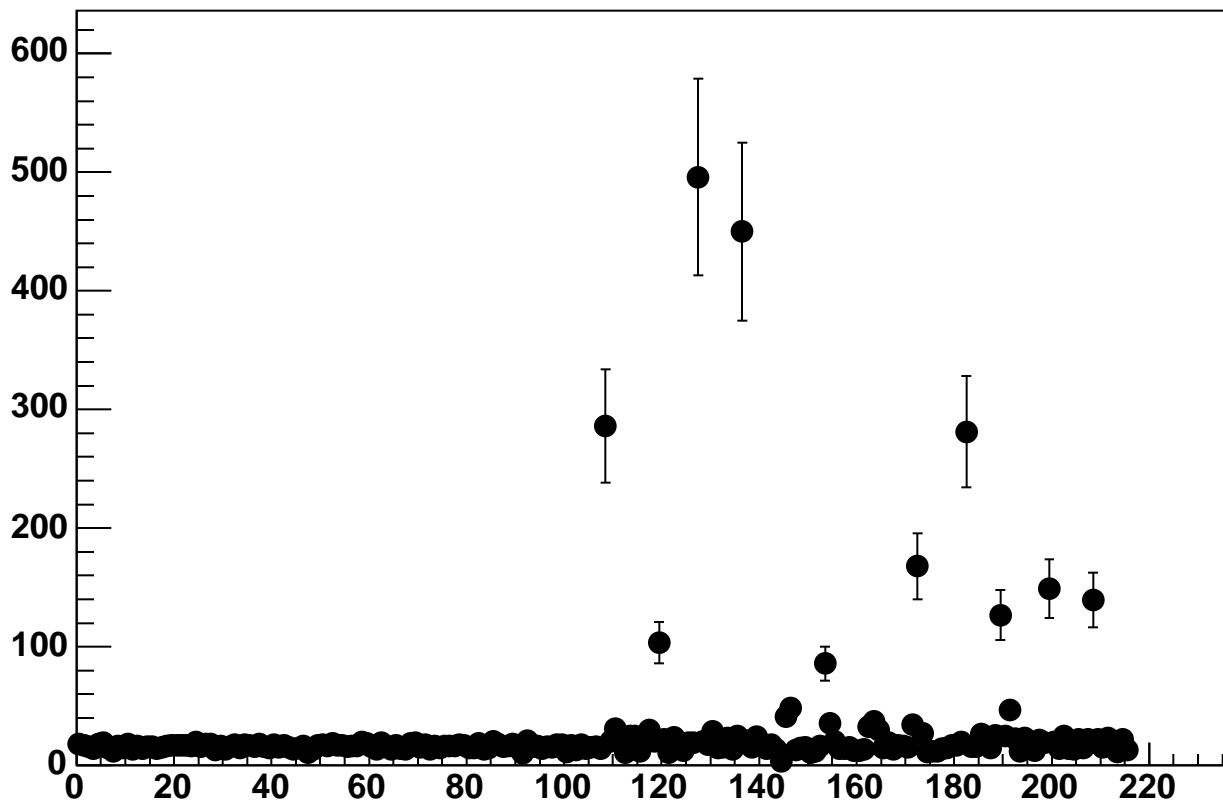
Enable 0, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



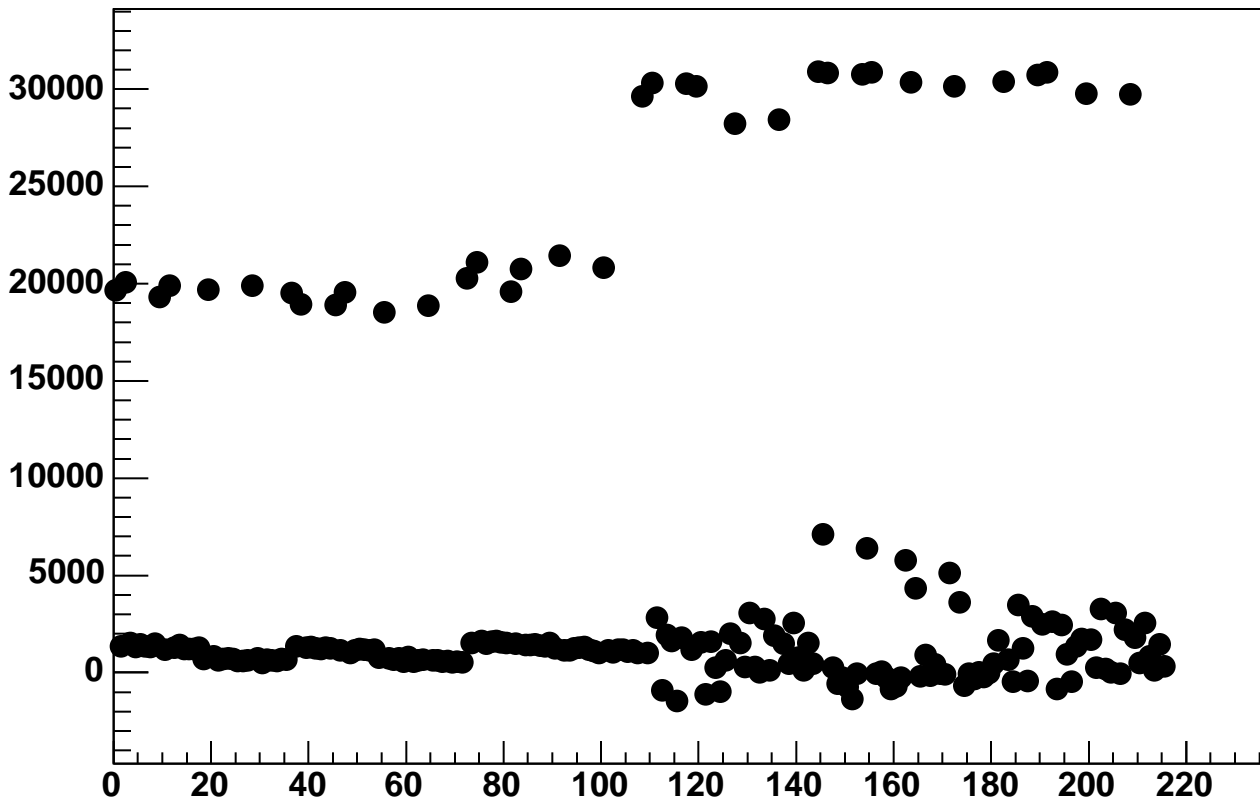
Enable 0, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



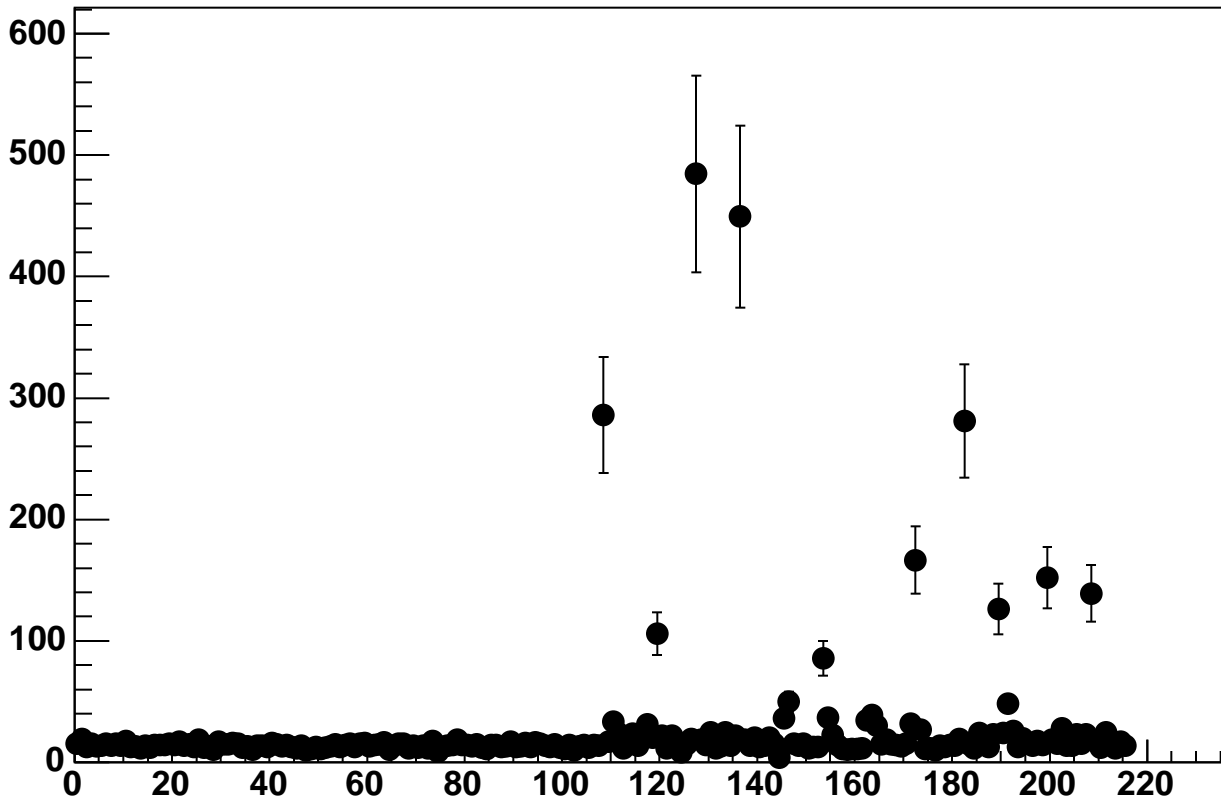
Enable 0, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



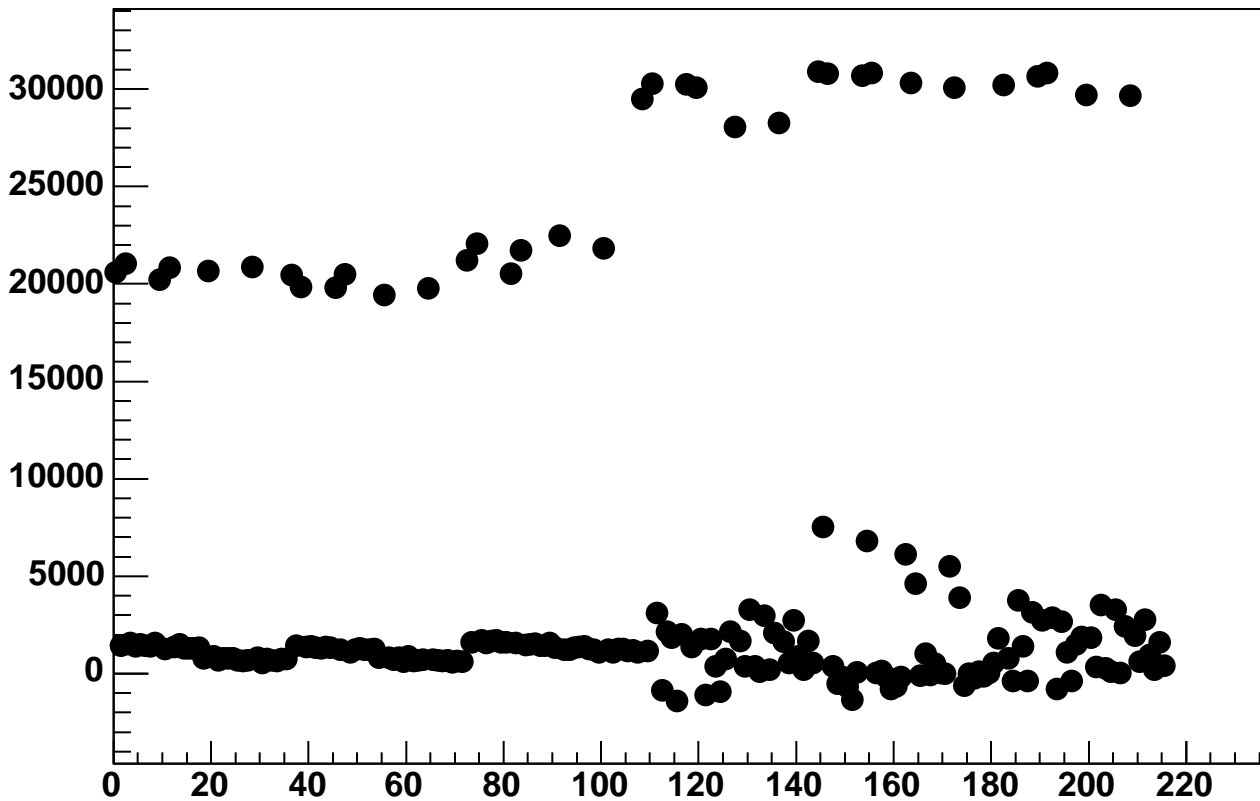
Enable 0, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



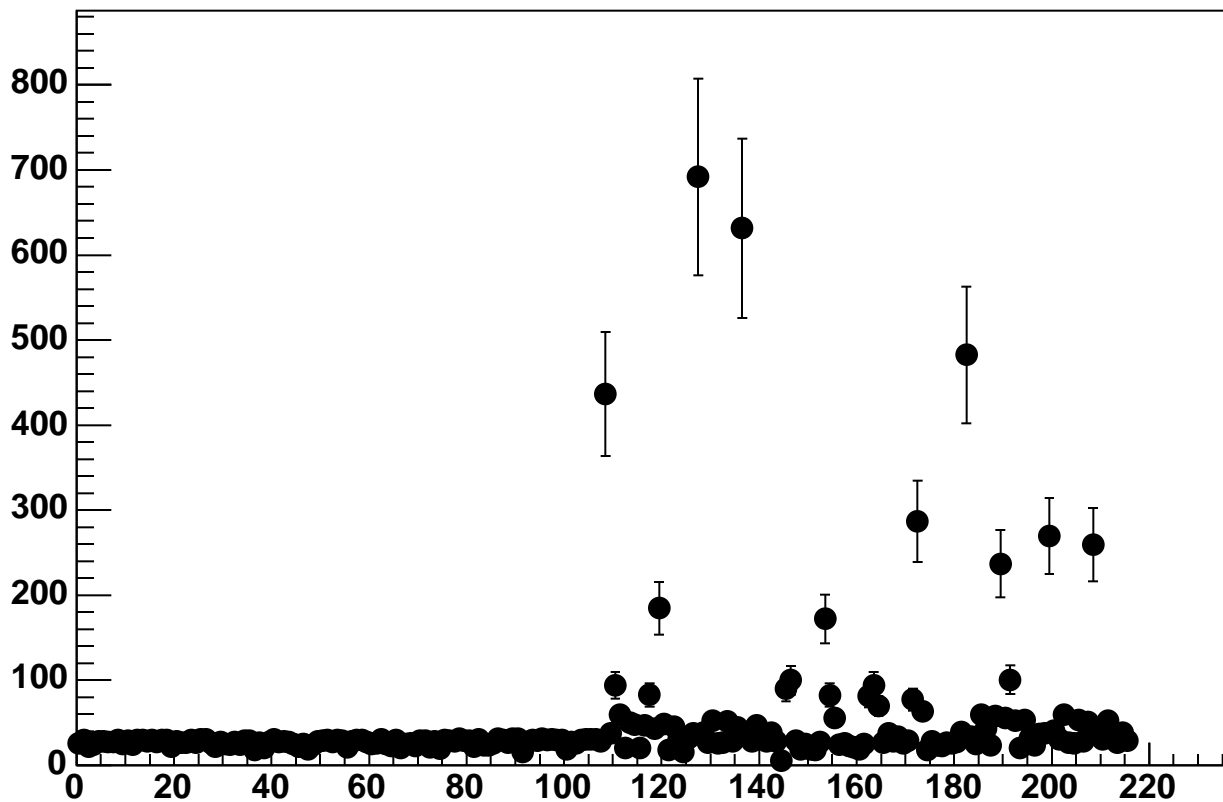
Enable 0, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



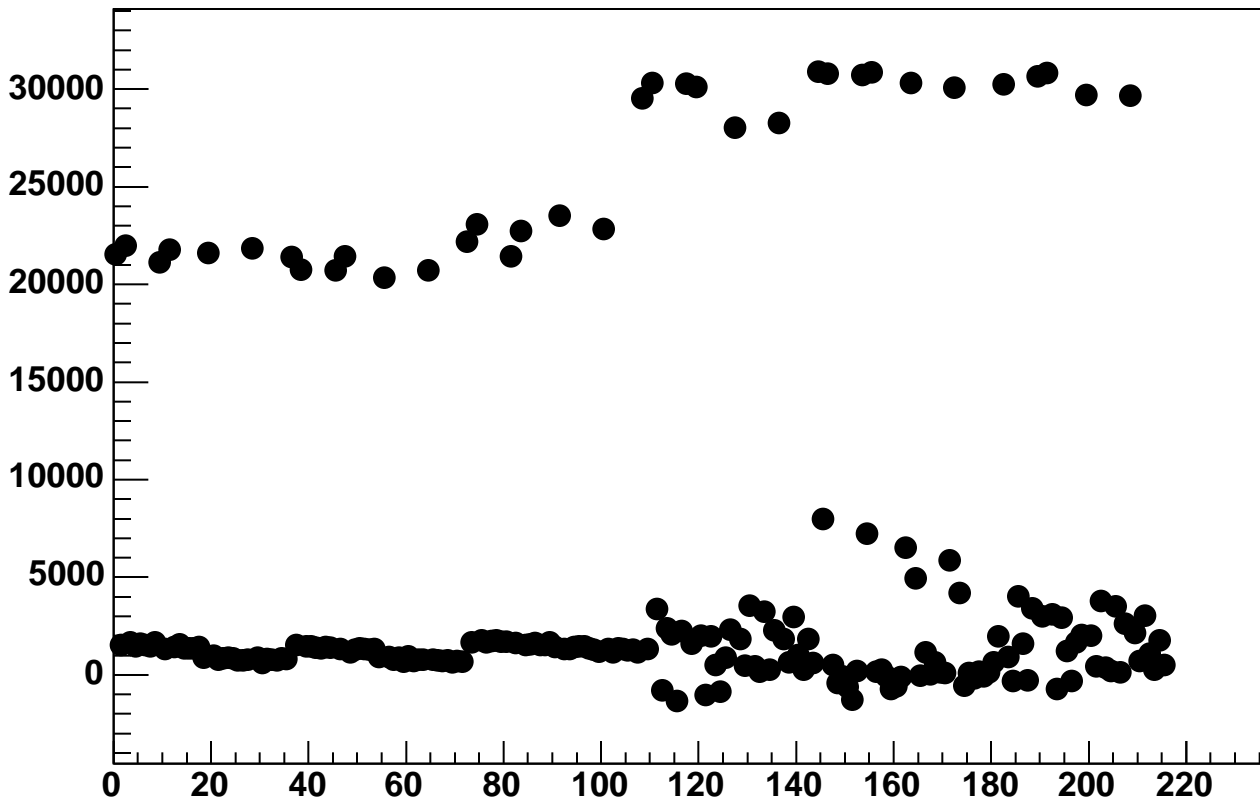
Enable 0, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



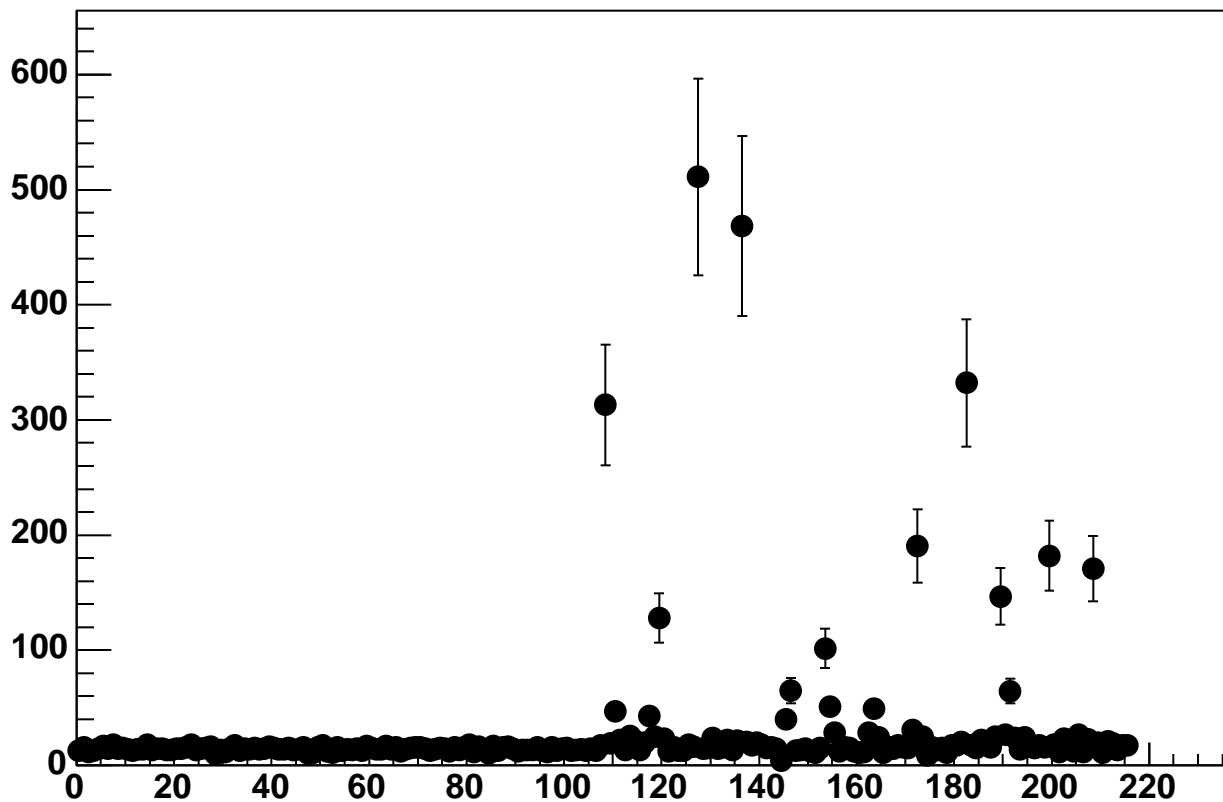
Enable 0, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



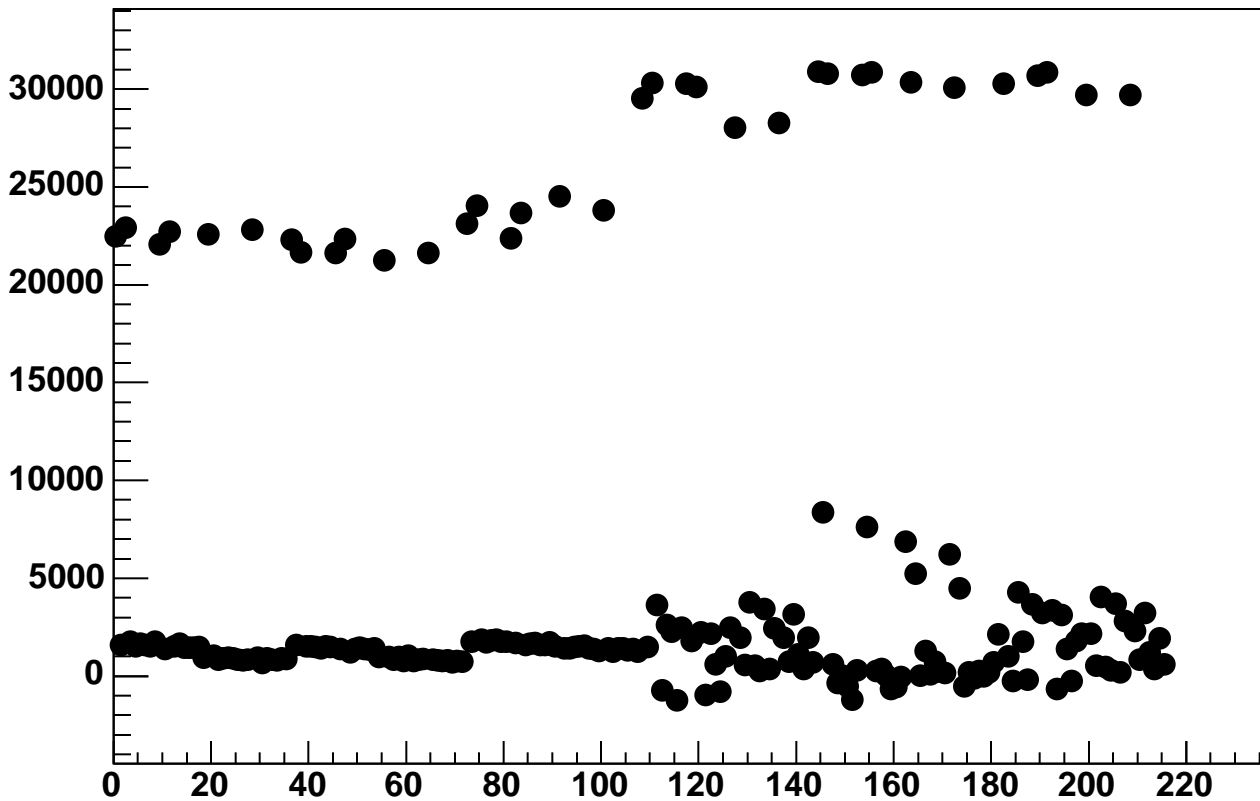
Enable 0, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



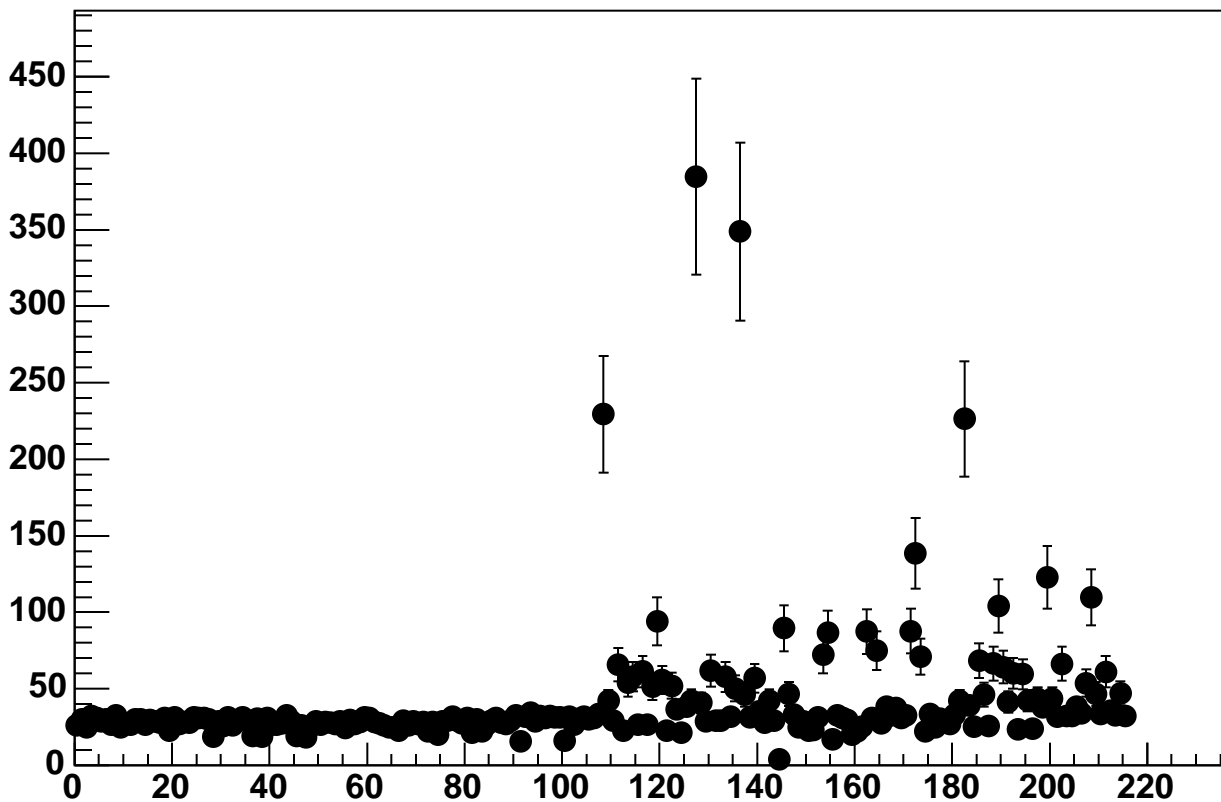
Enable 0, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



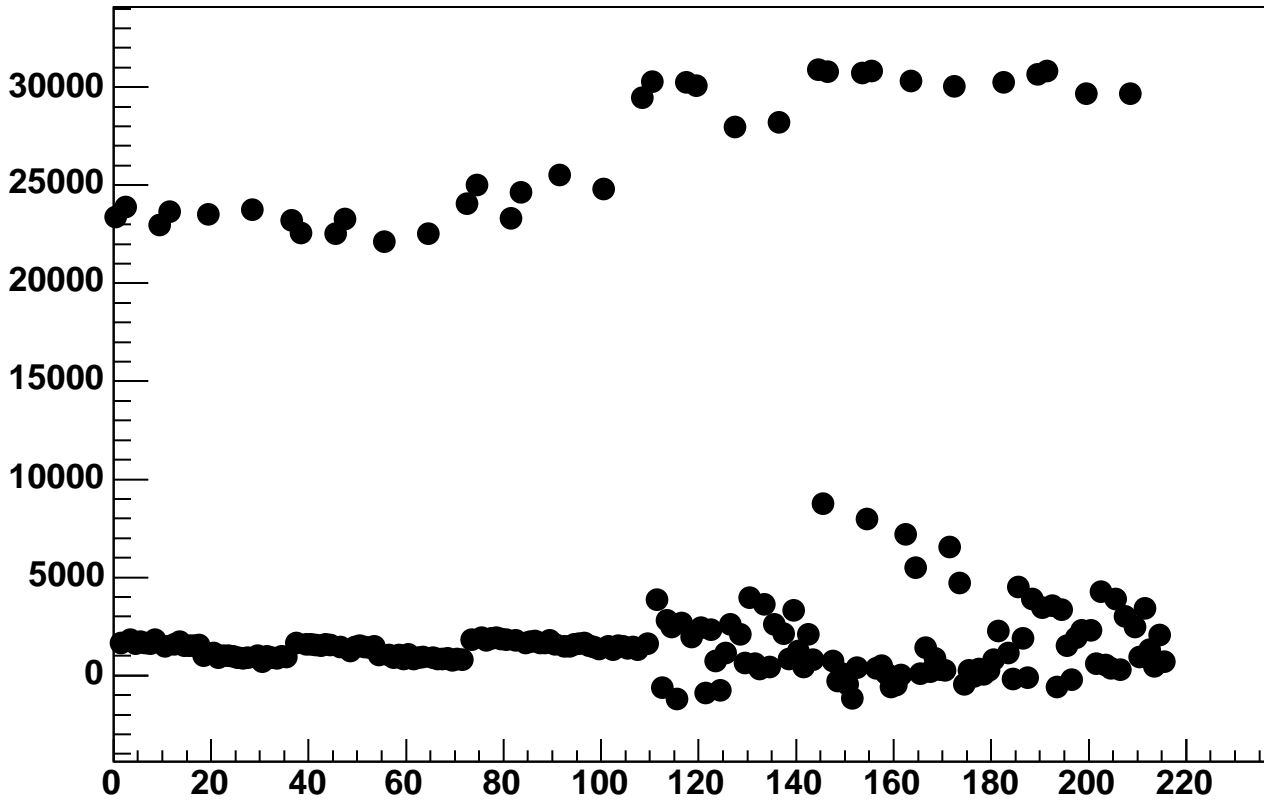
Enable 0, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



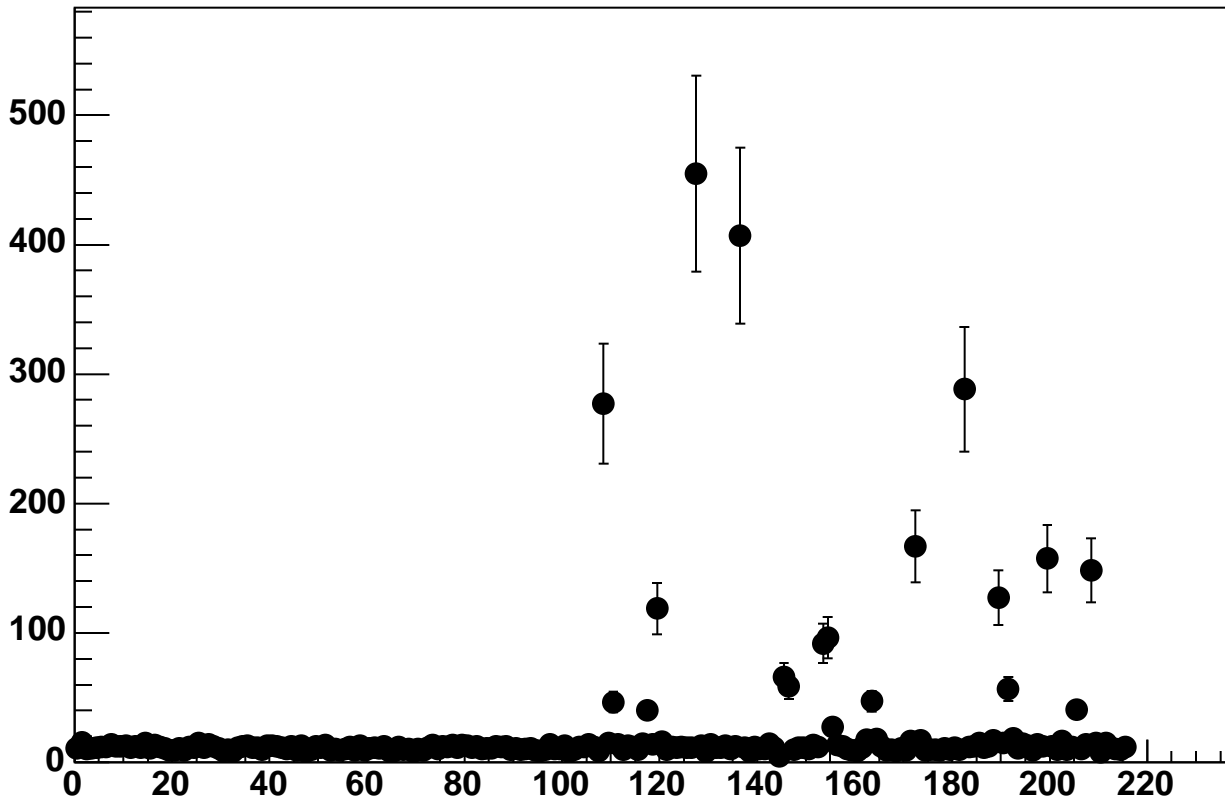
Enable 0, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



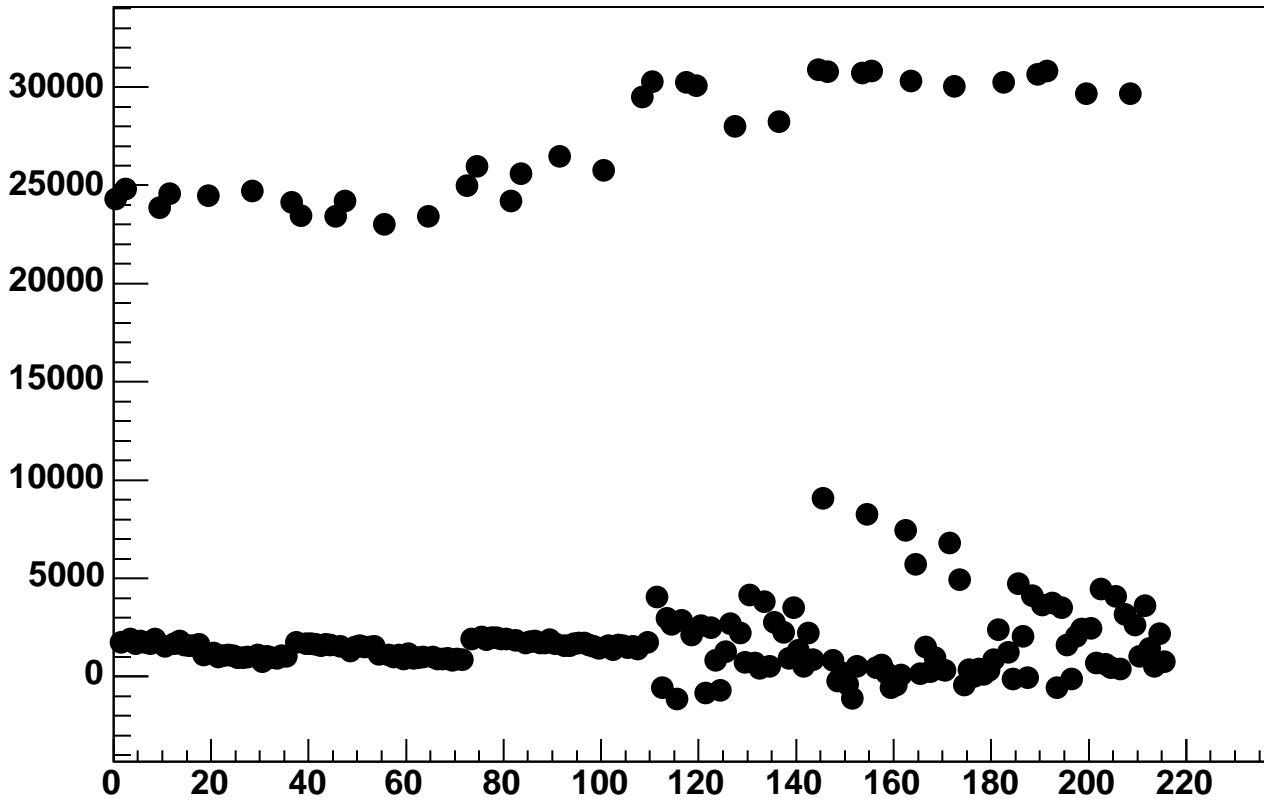
Enable 0, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



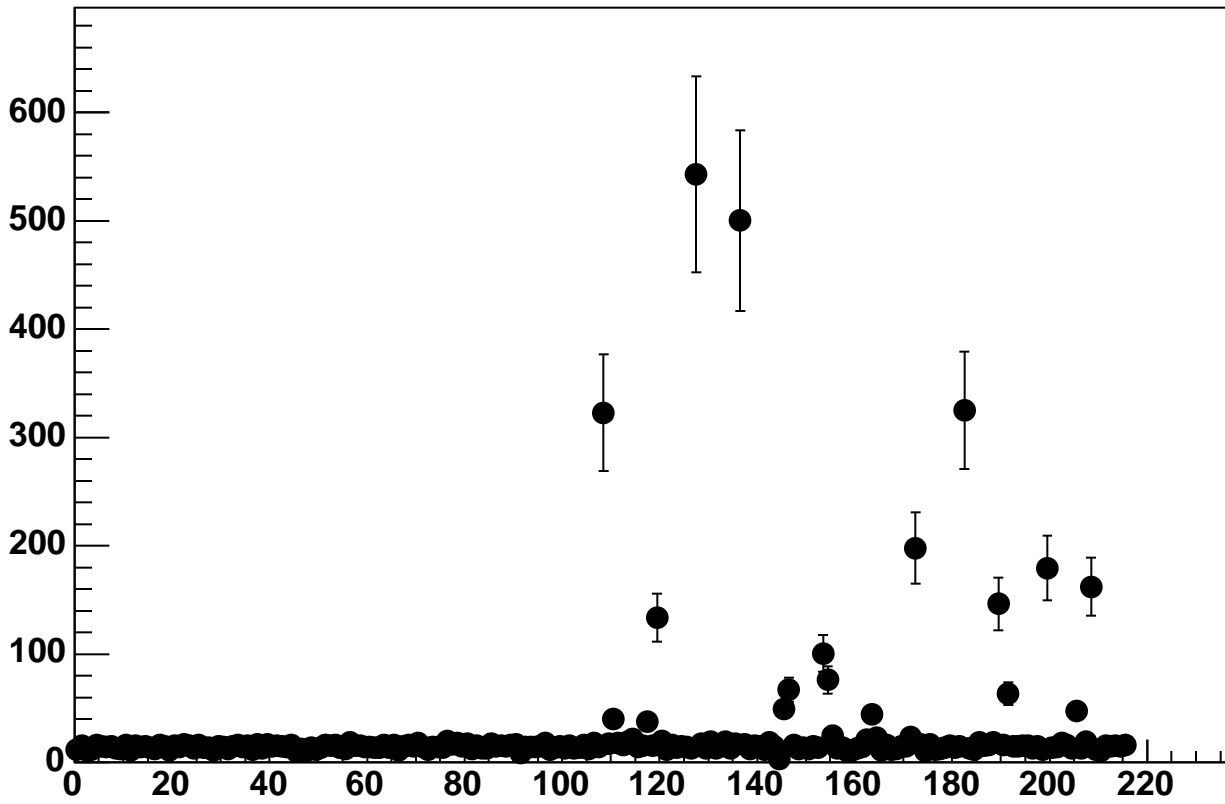
Enable 0, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



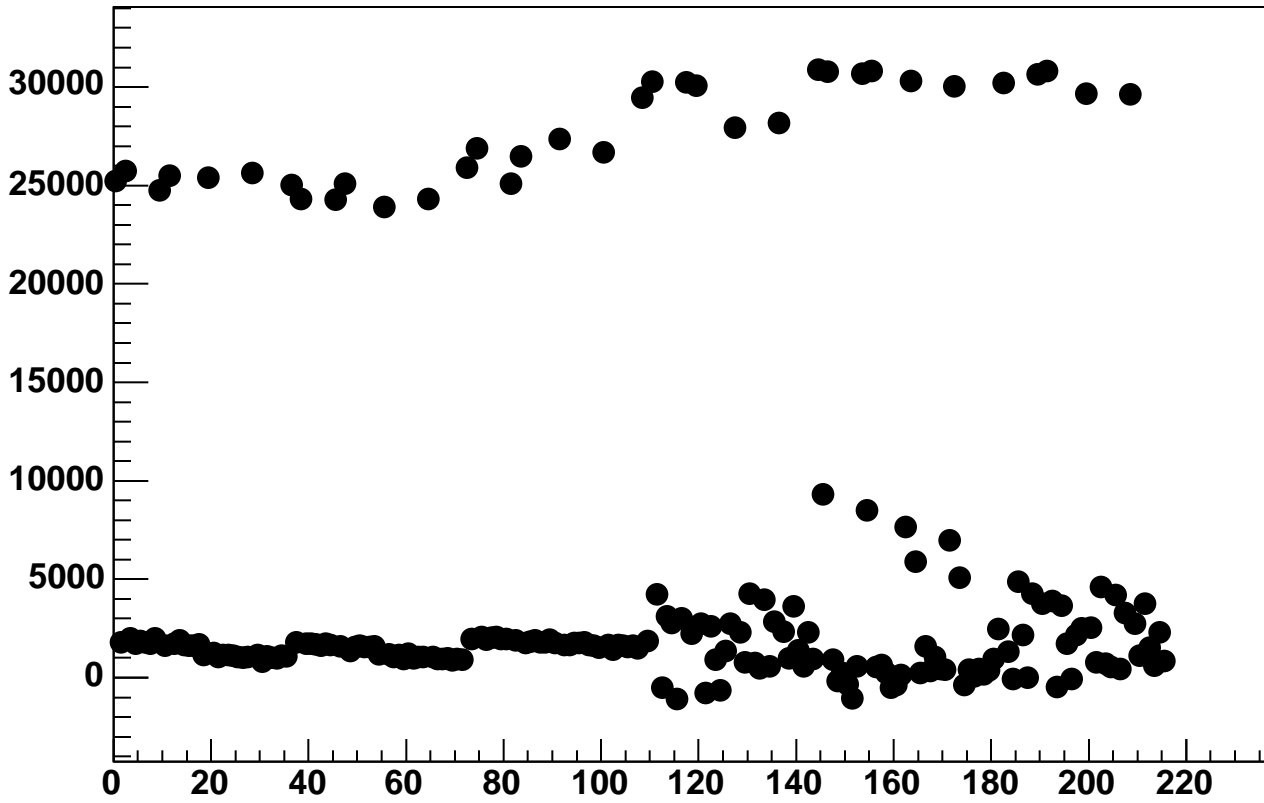
Enable 0, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



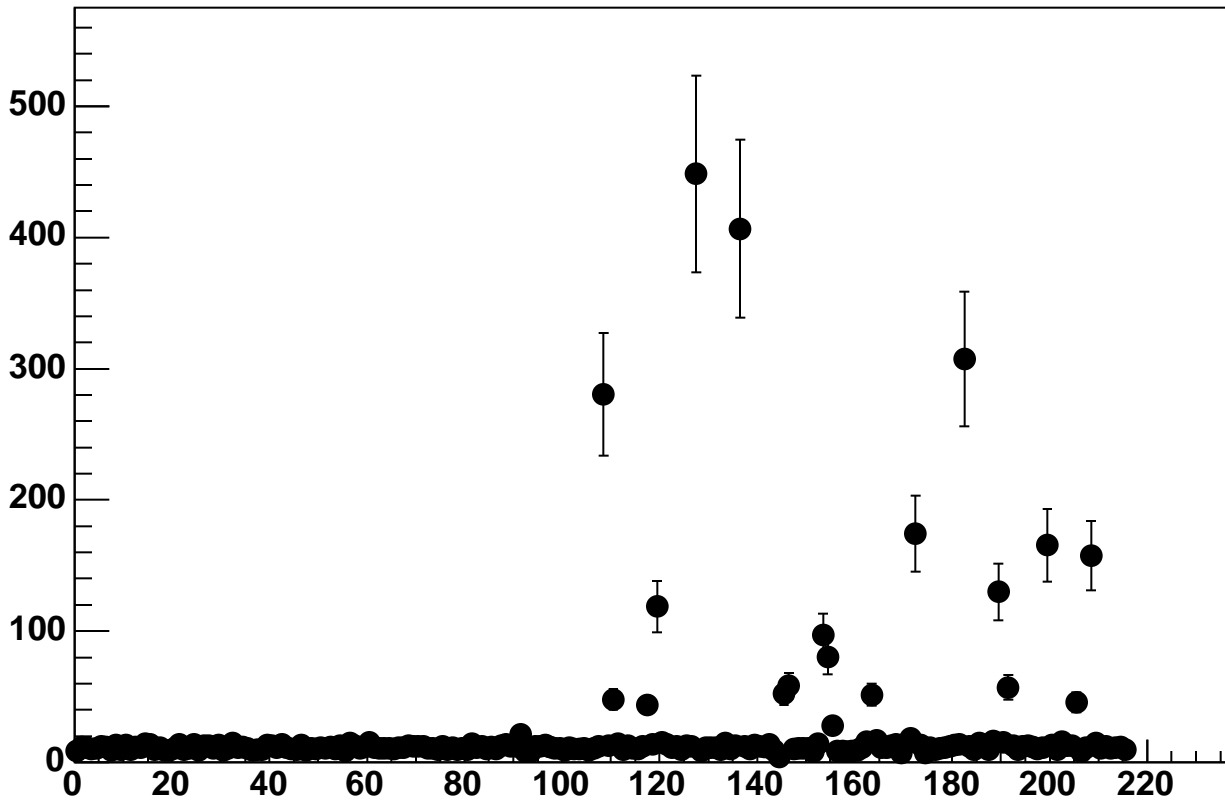
Enable 0, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



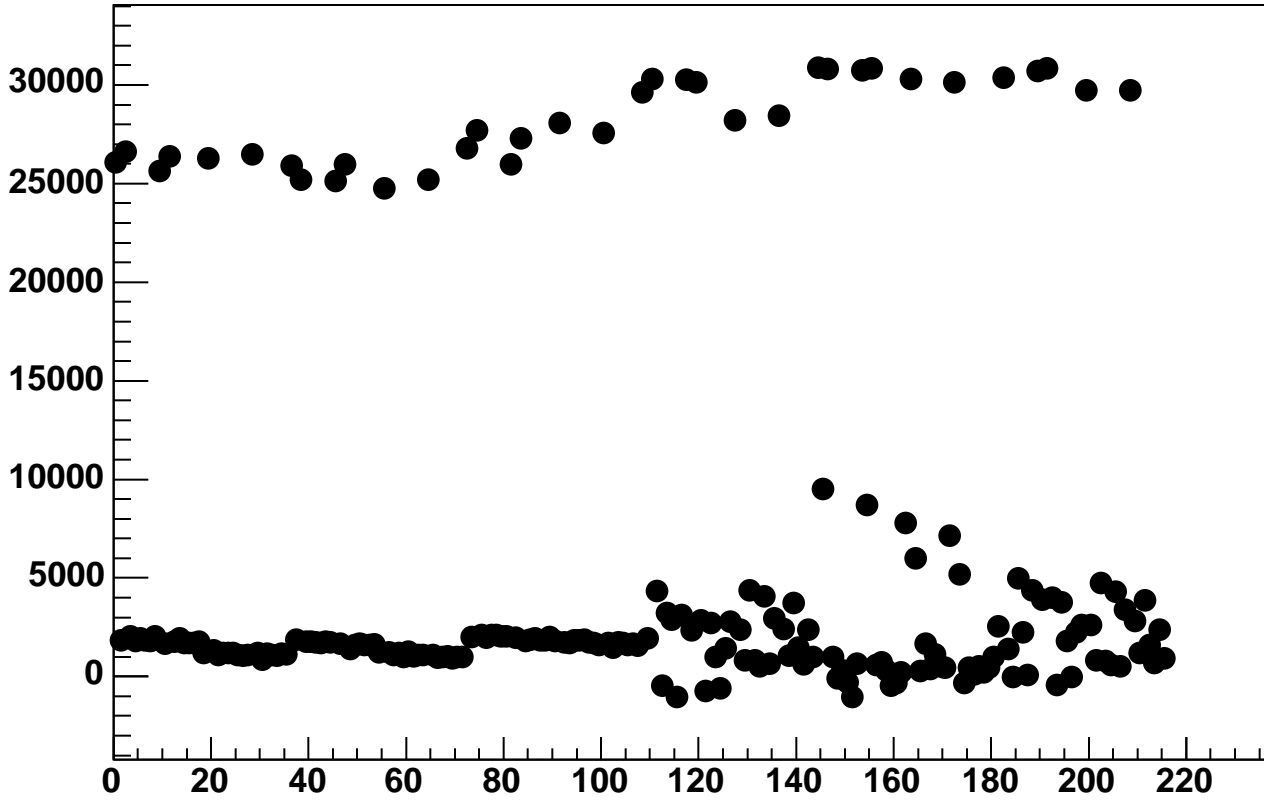
Enable 0, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



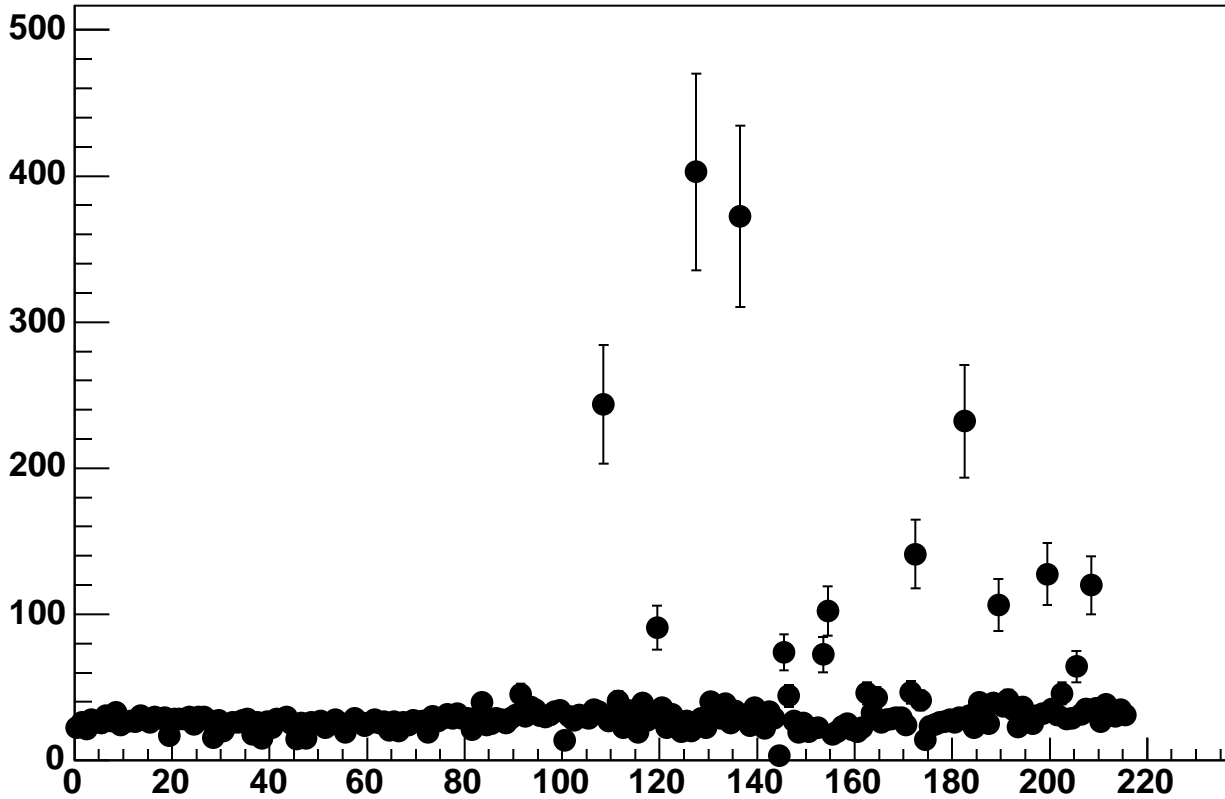
Enable 0, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



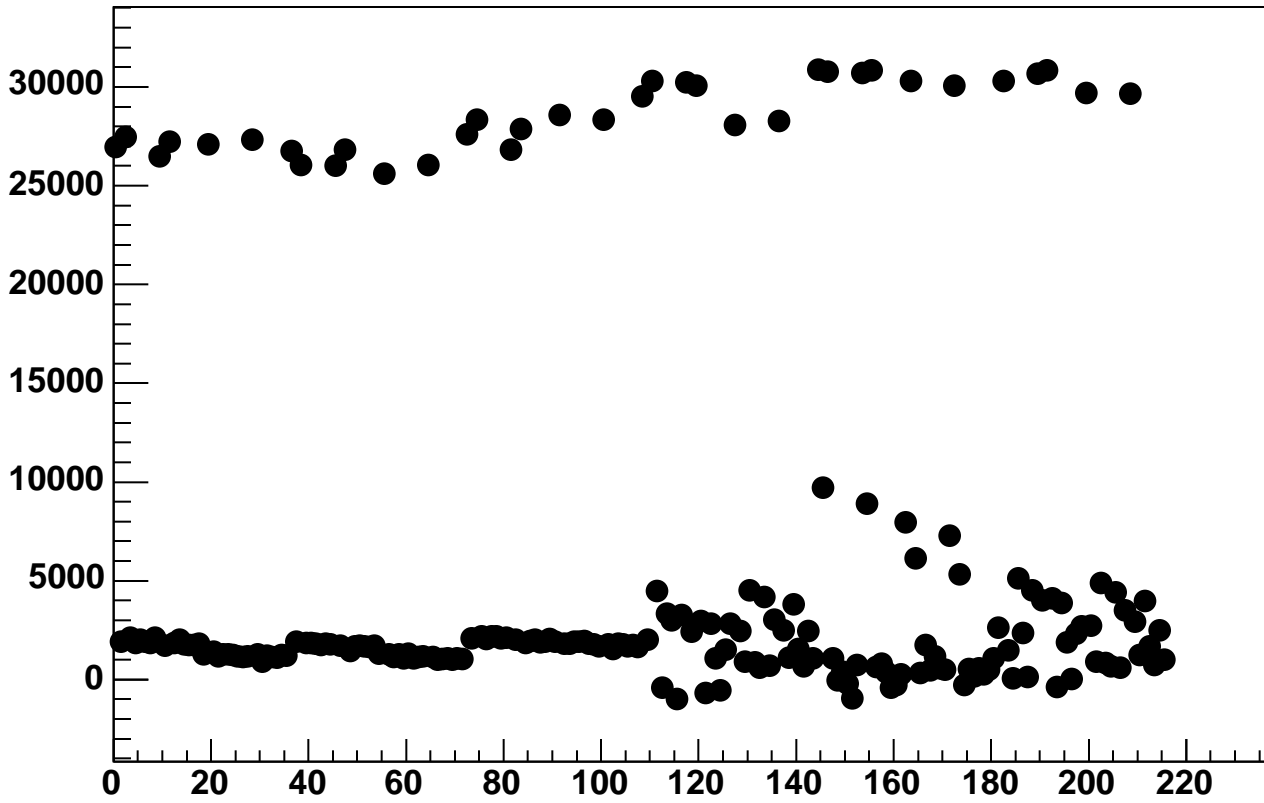
Enable 0, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



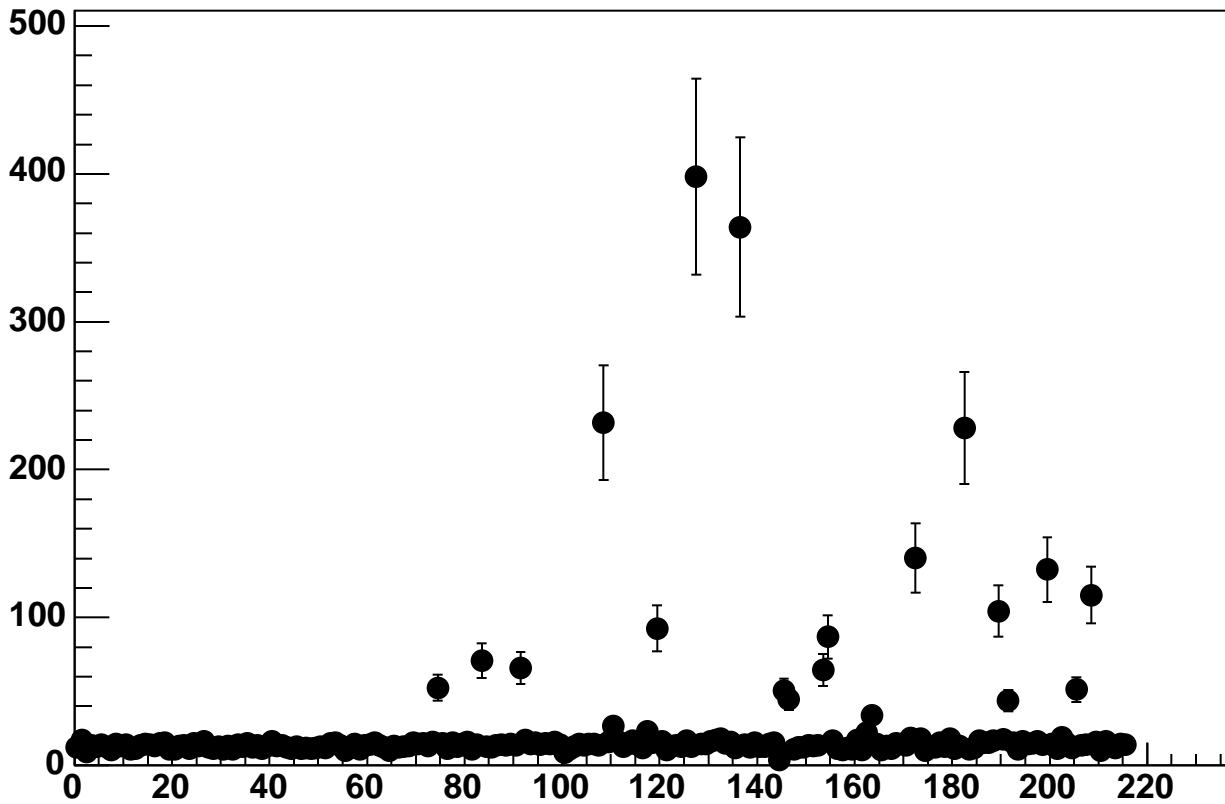
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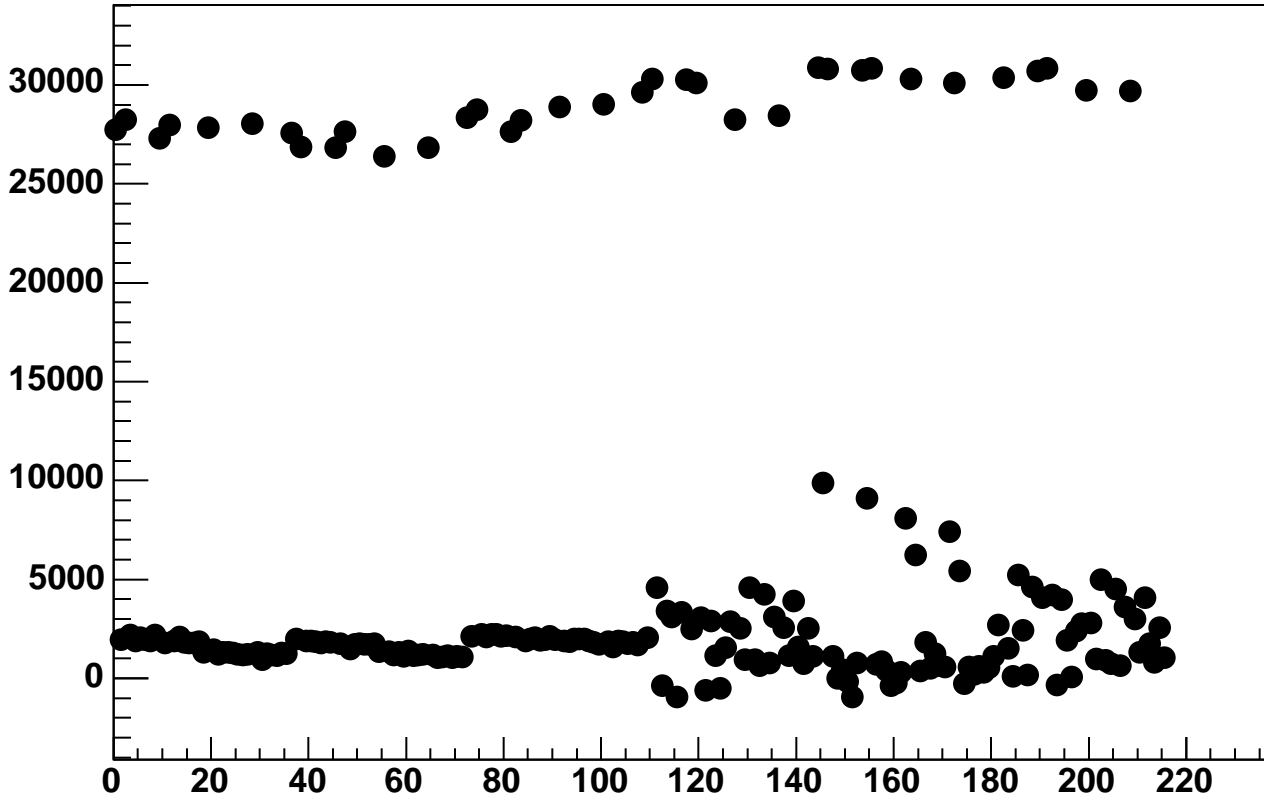
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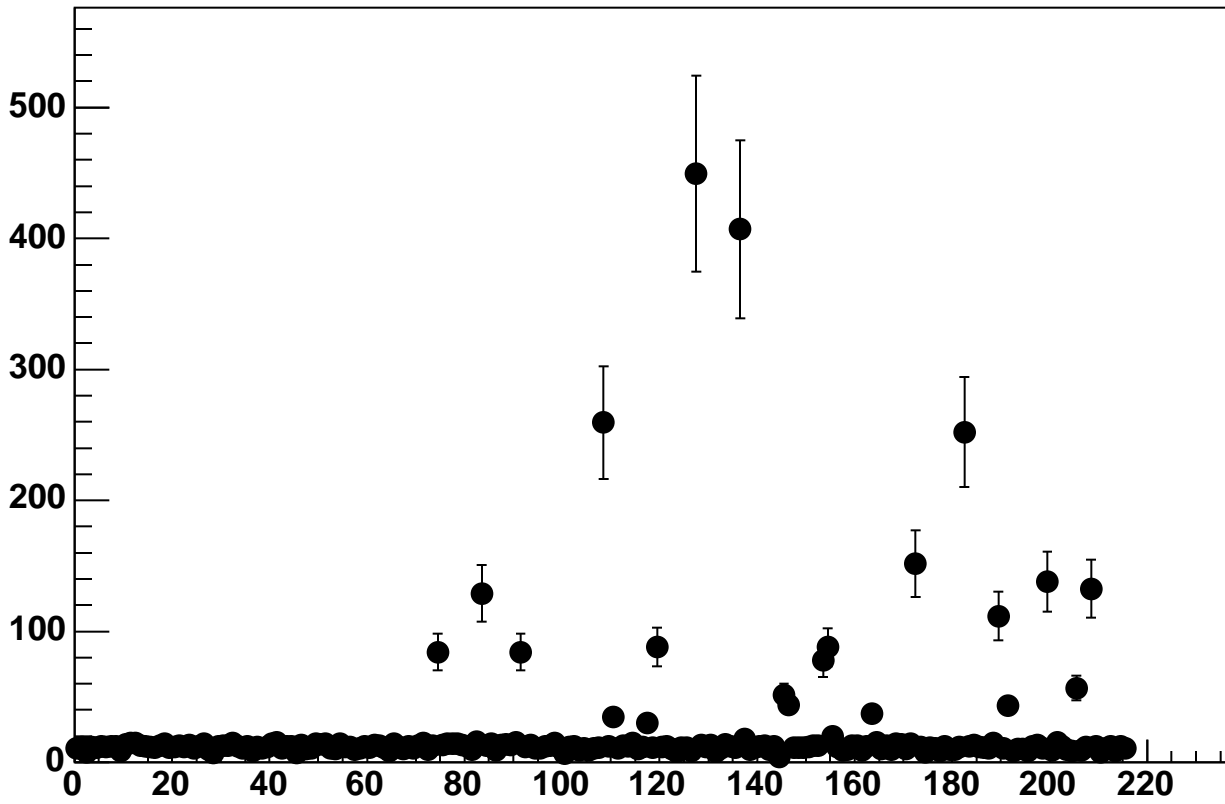
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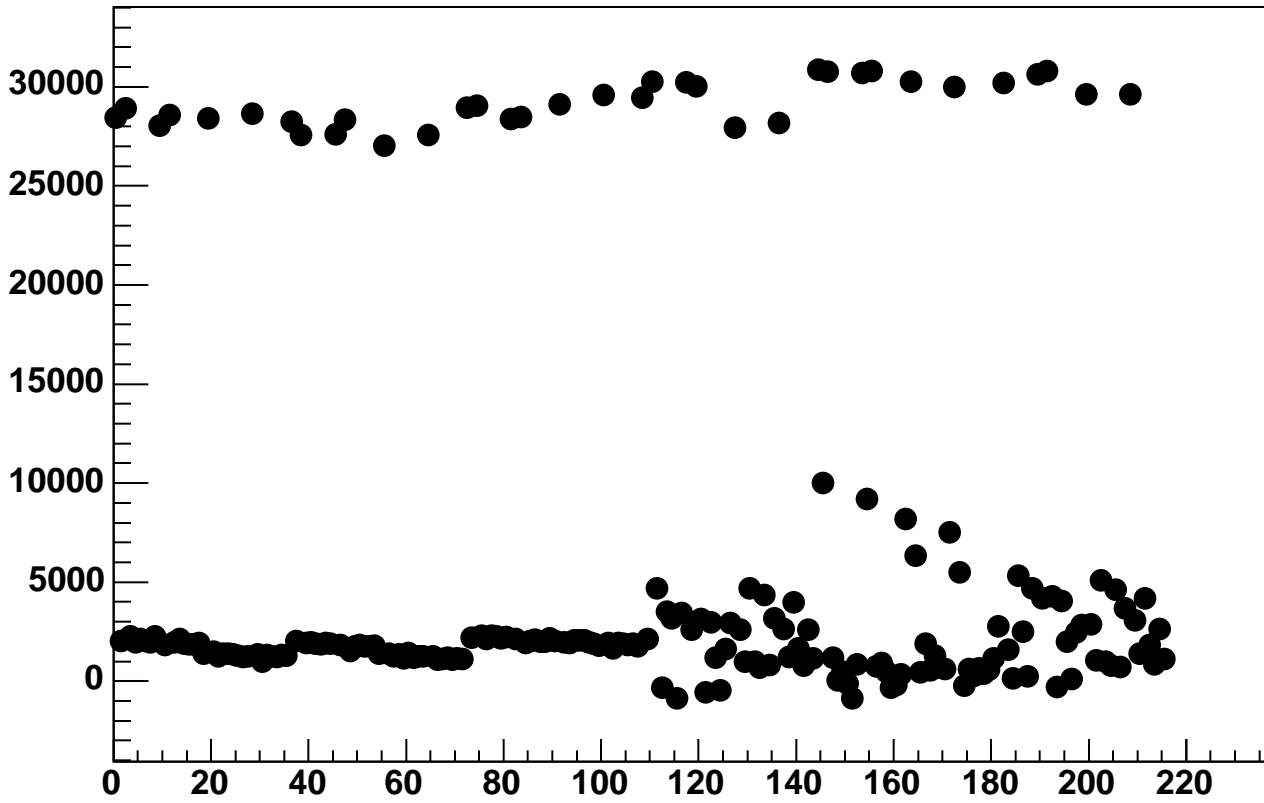
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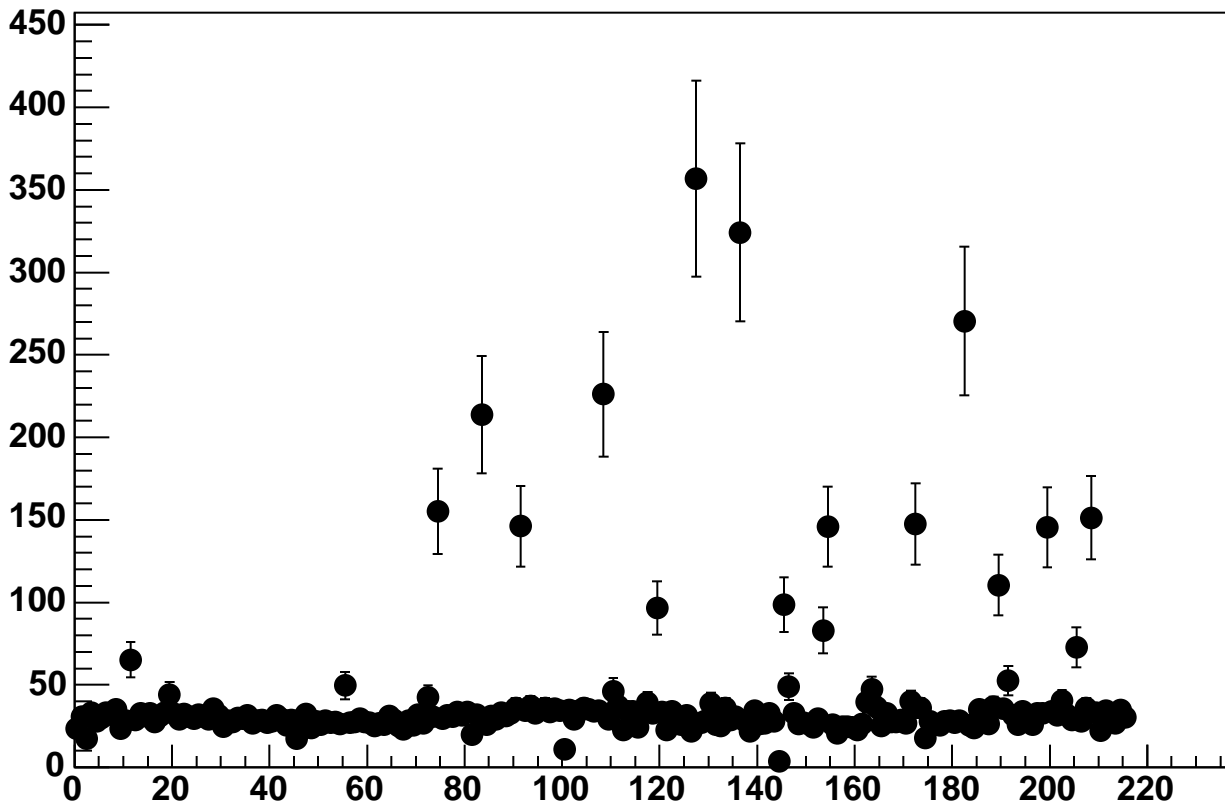
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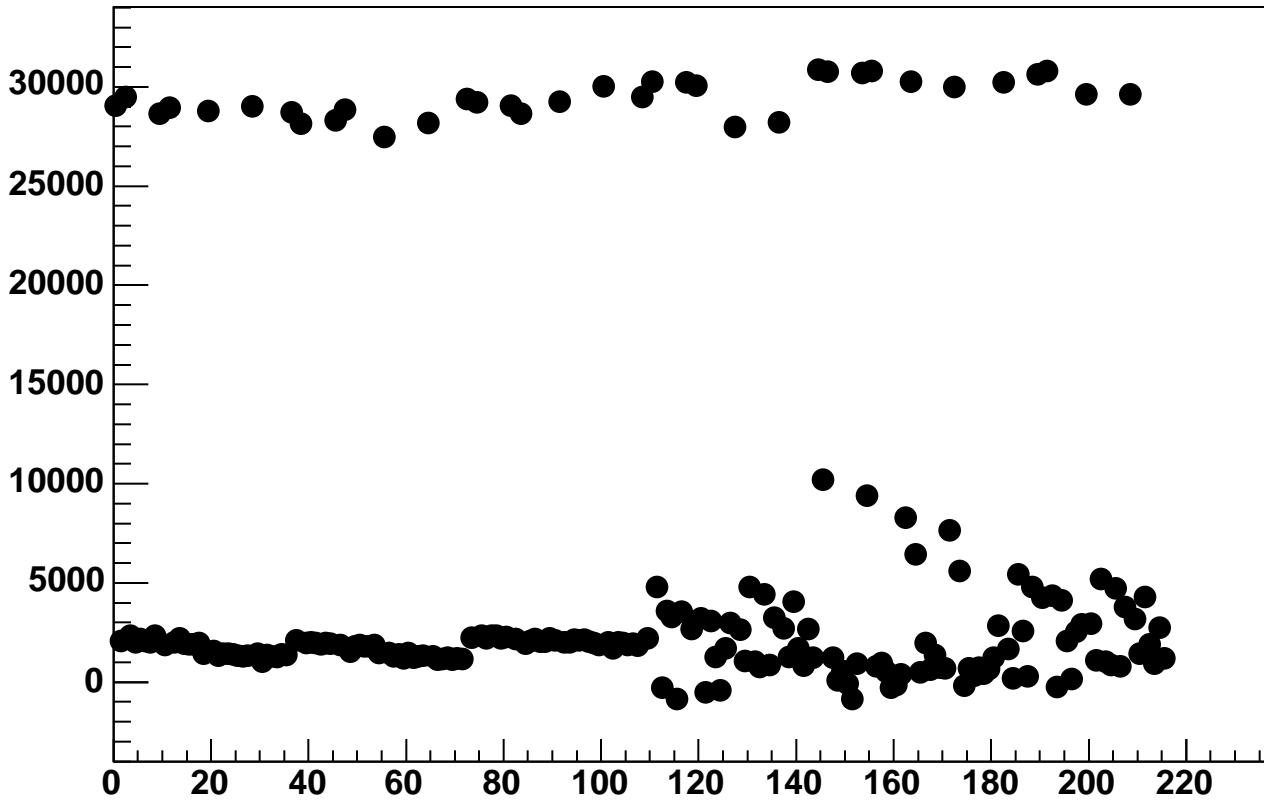
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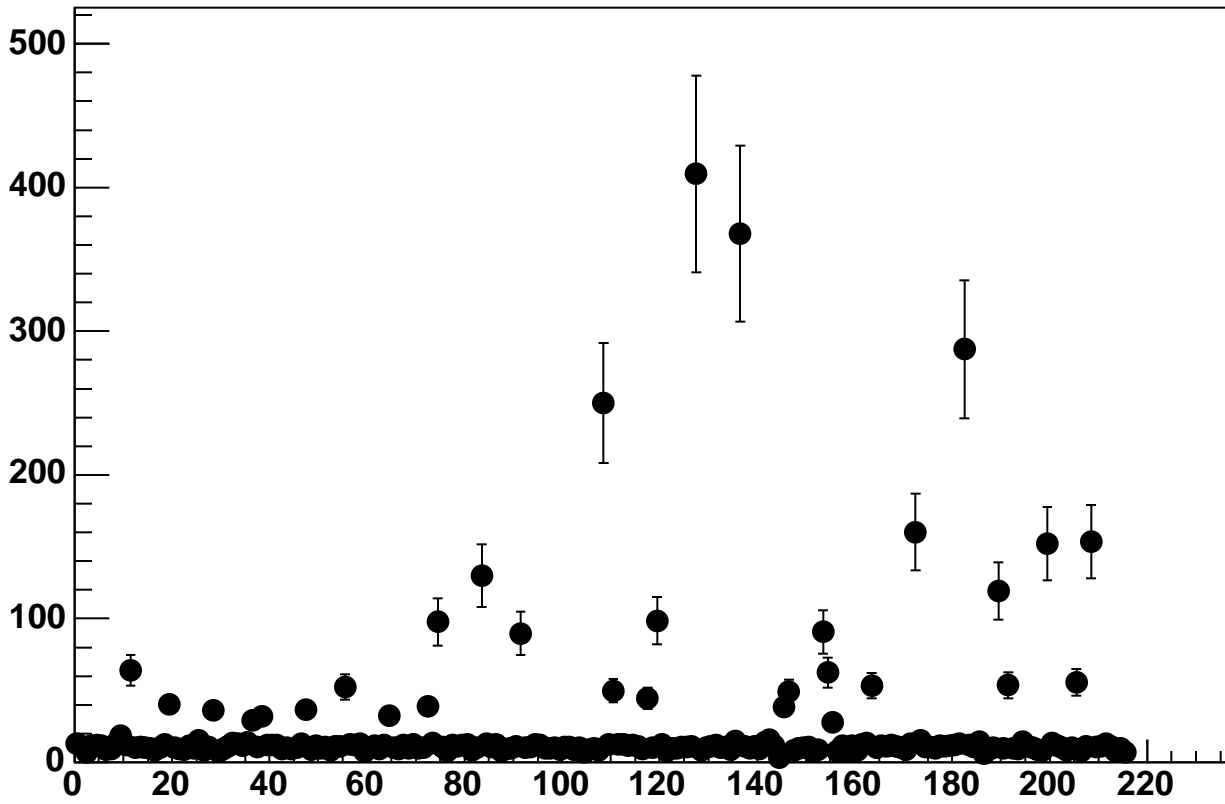
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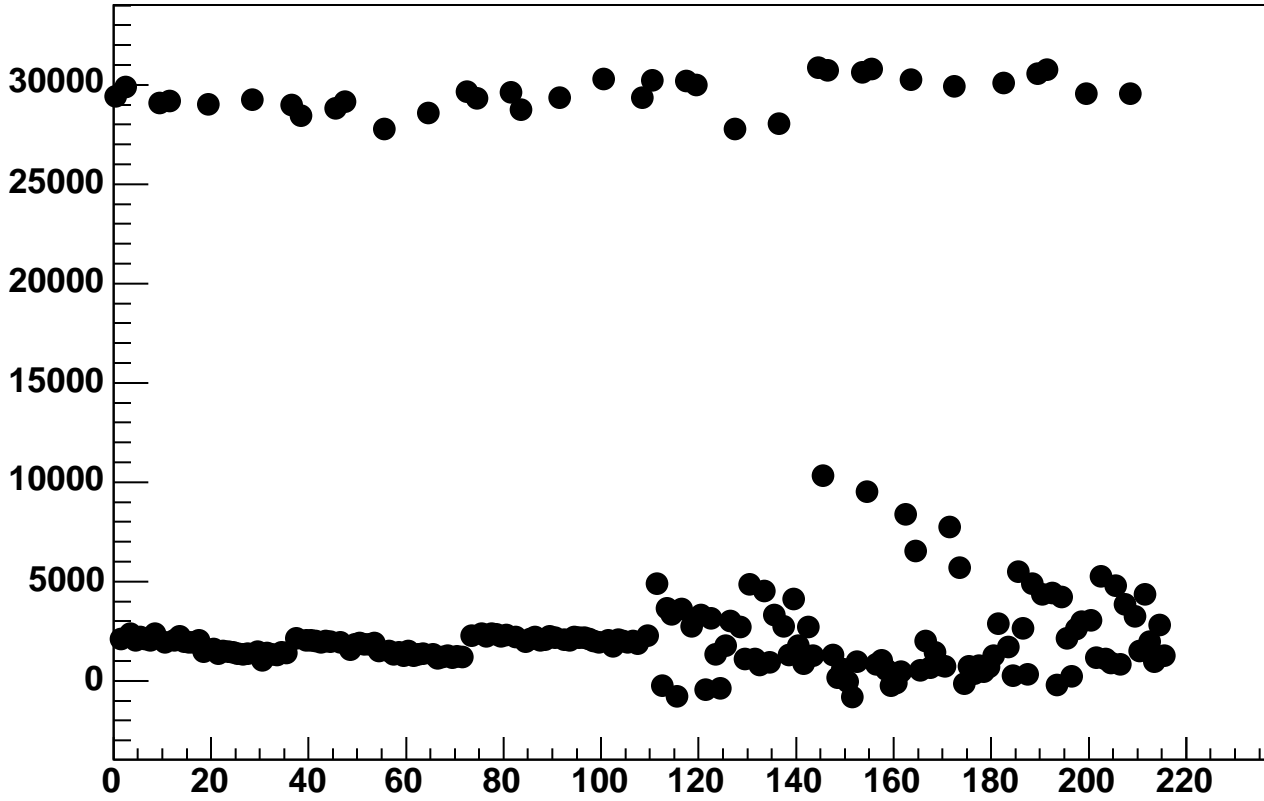
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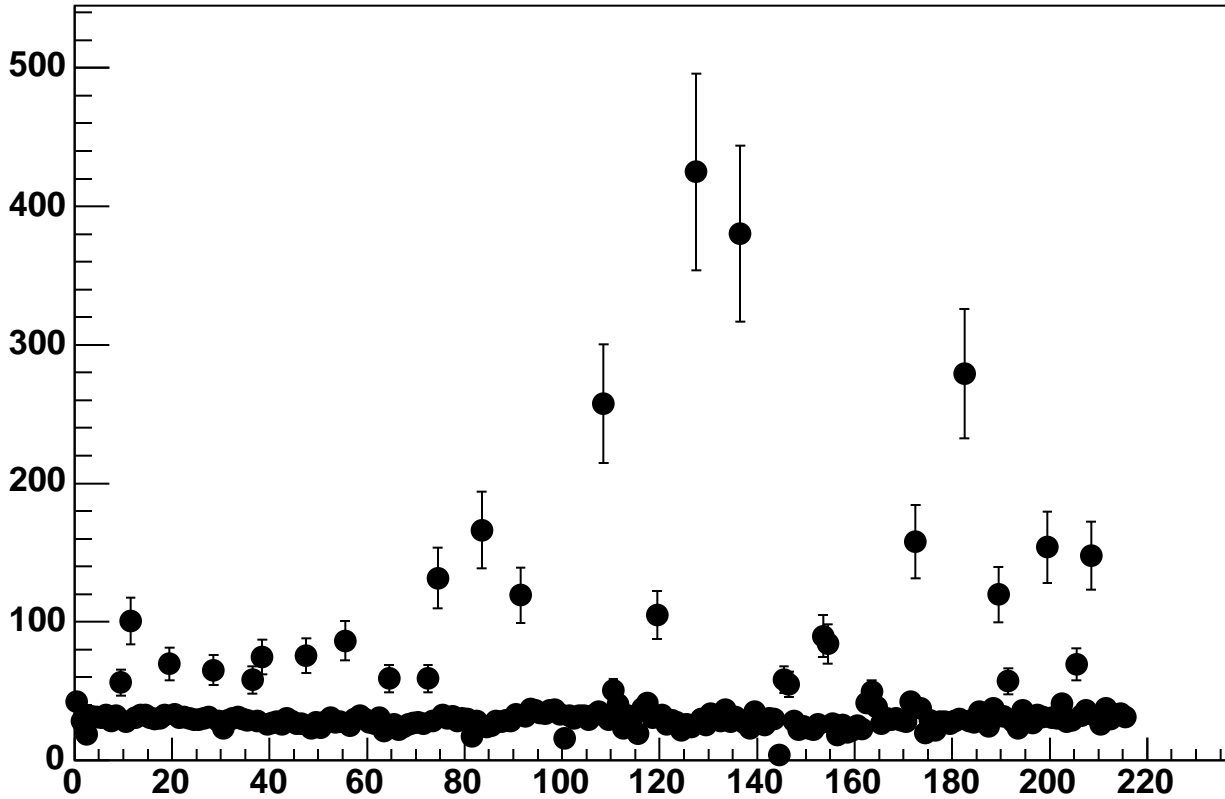
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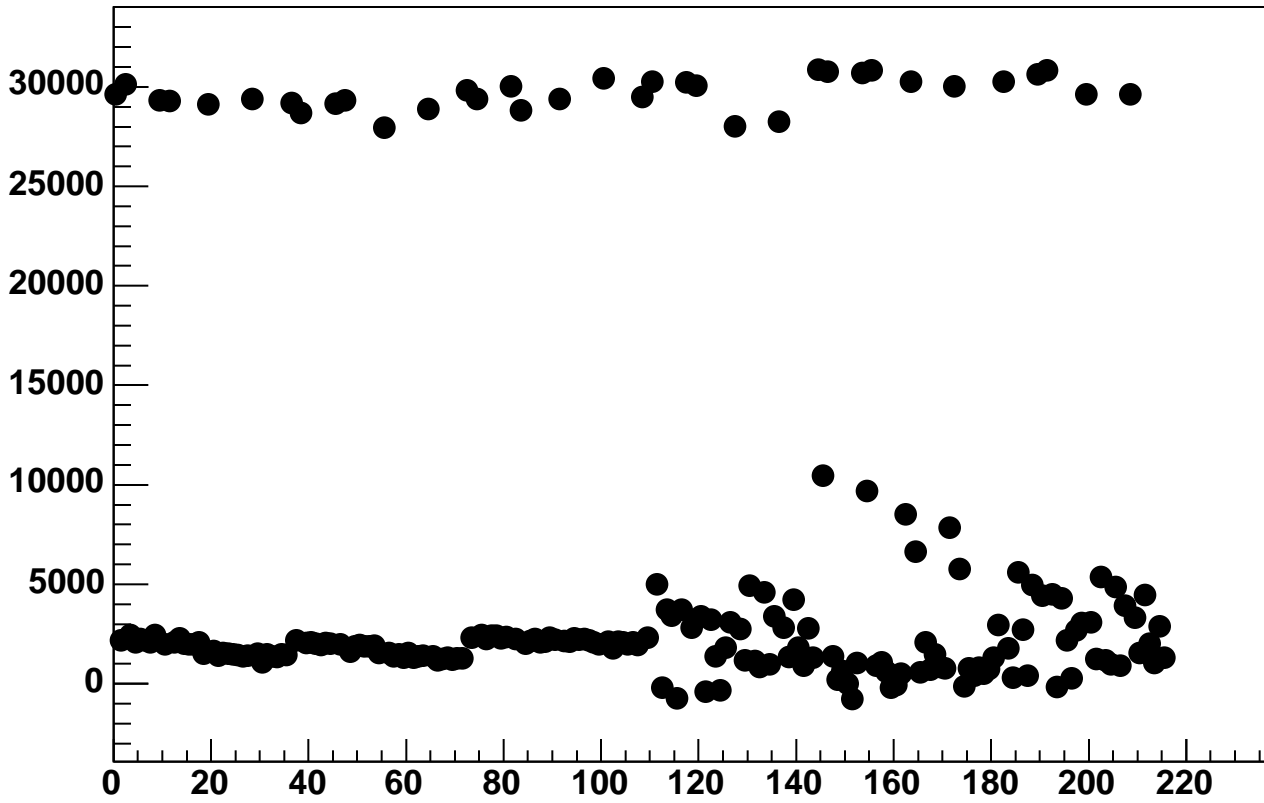
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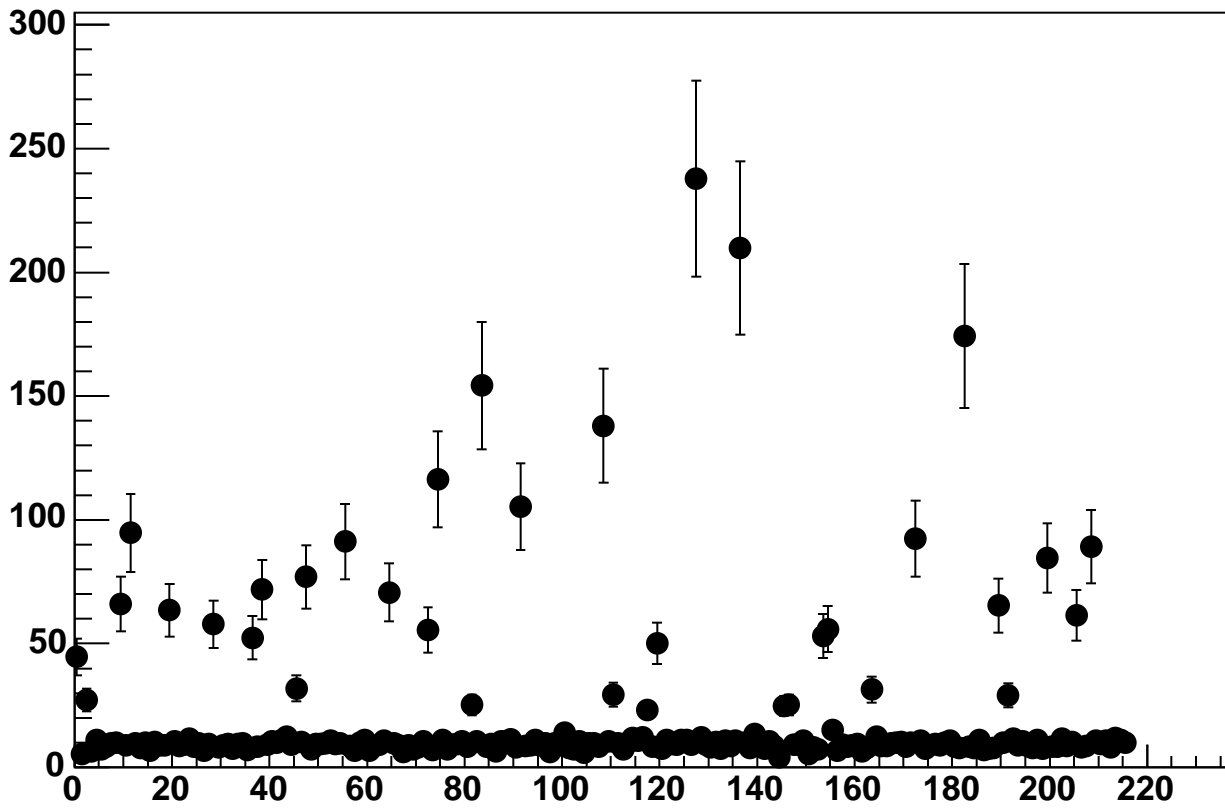
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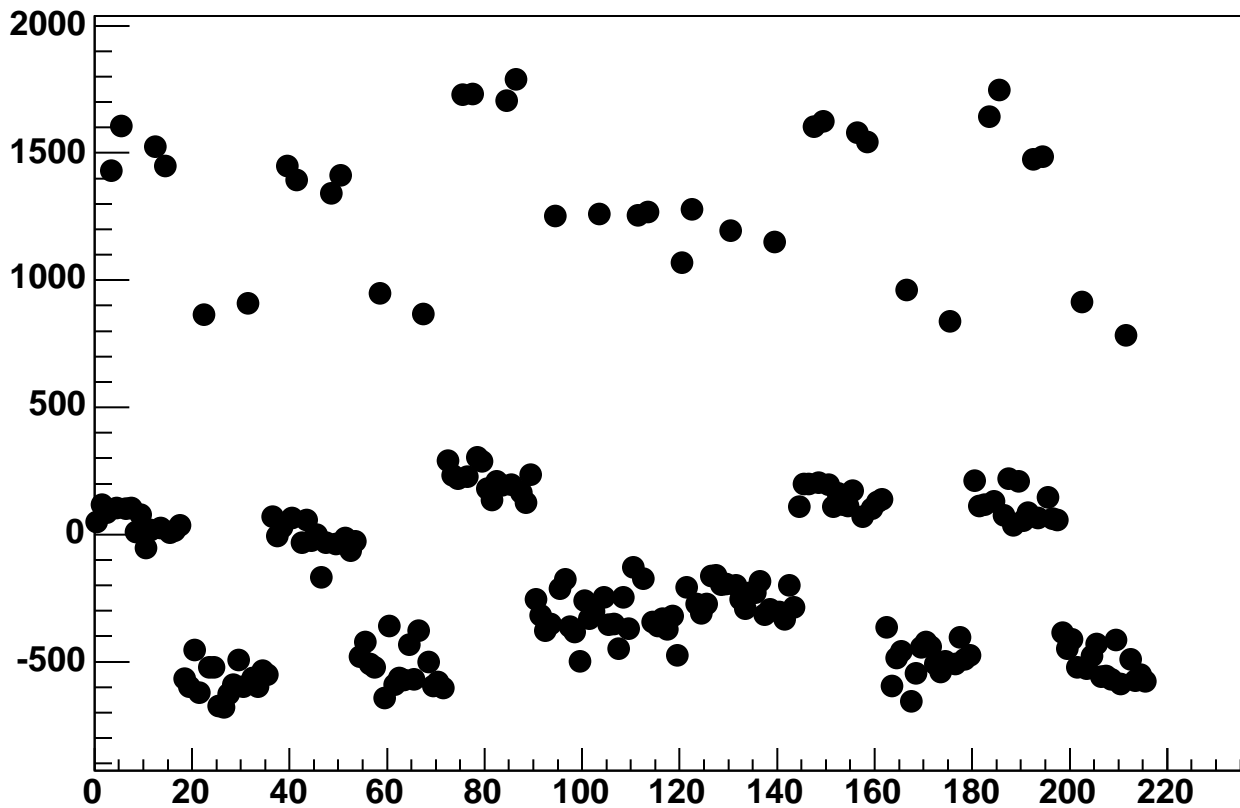
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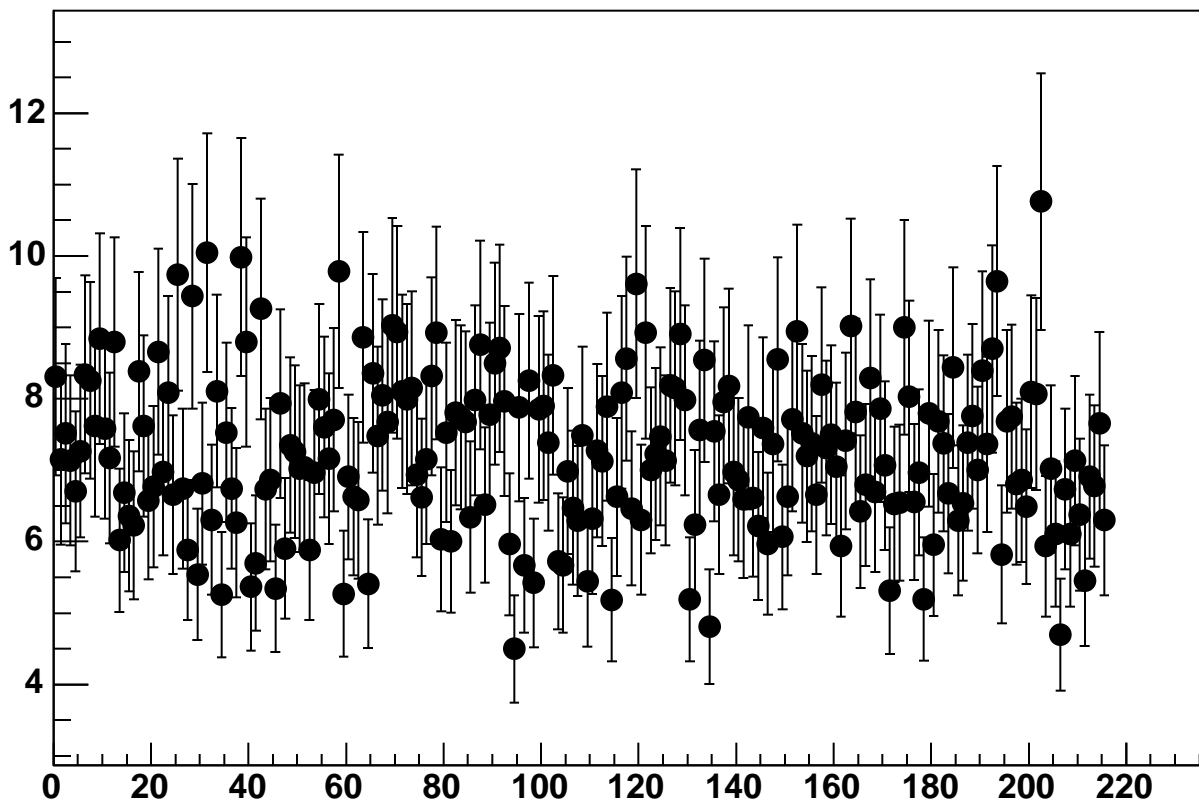
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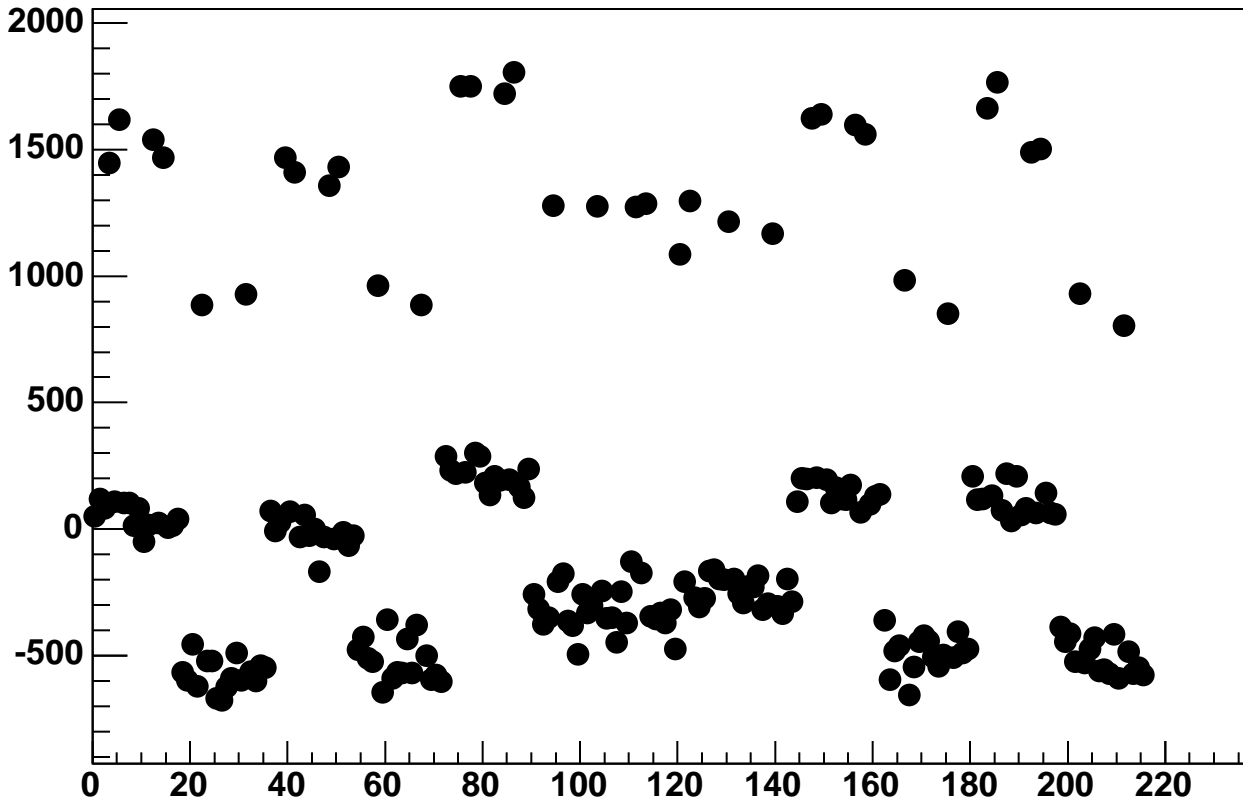
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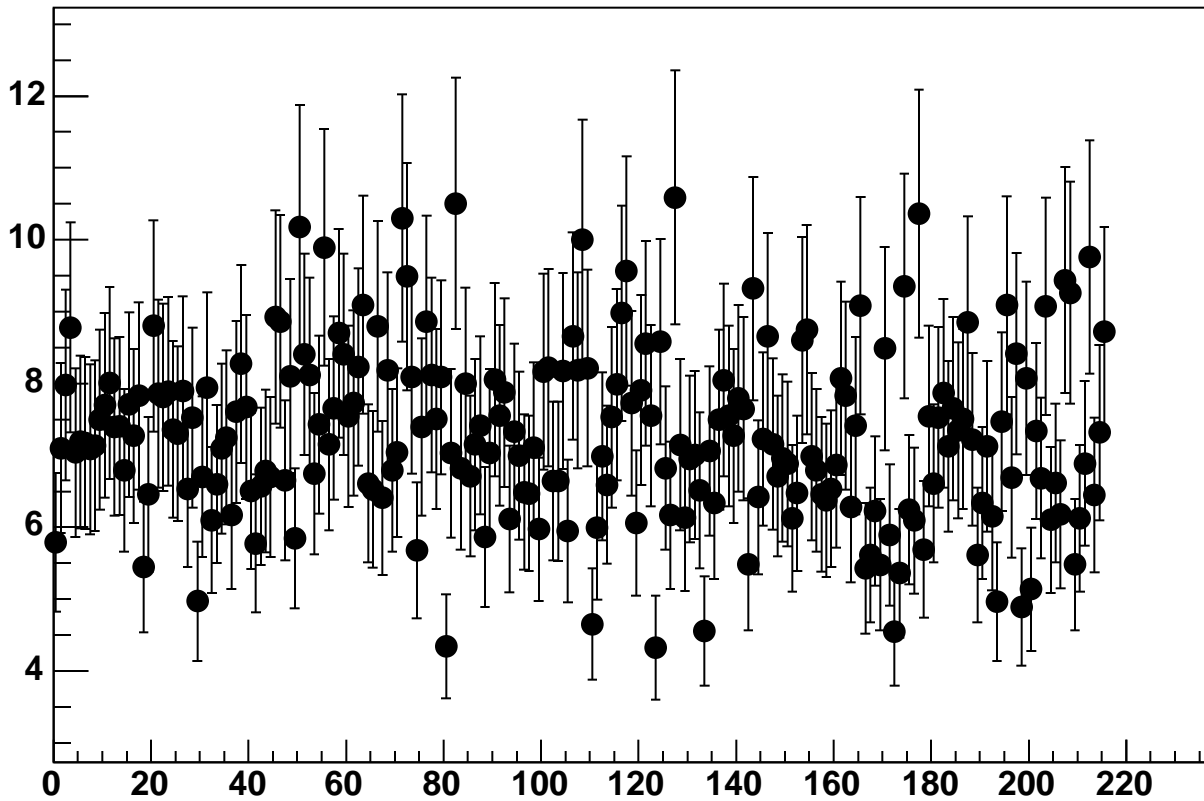
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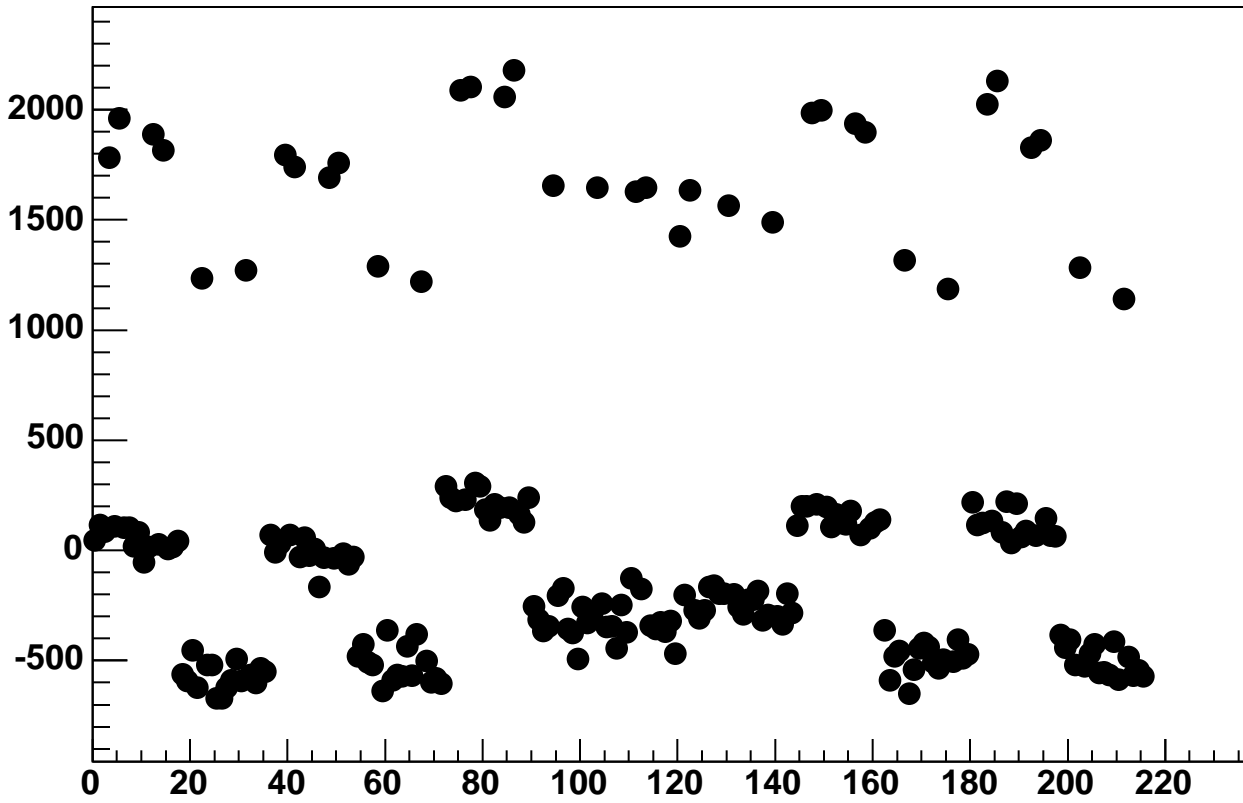
Enable 1, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



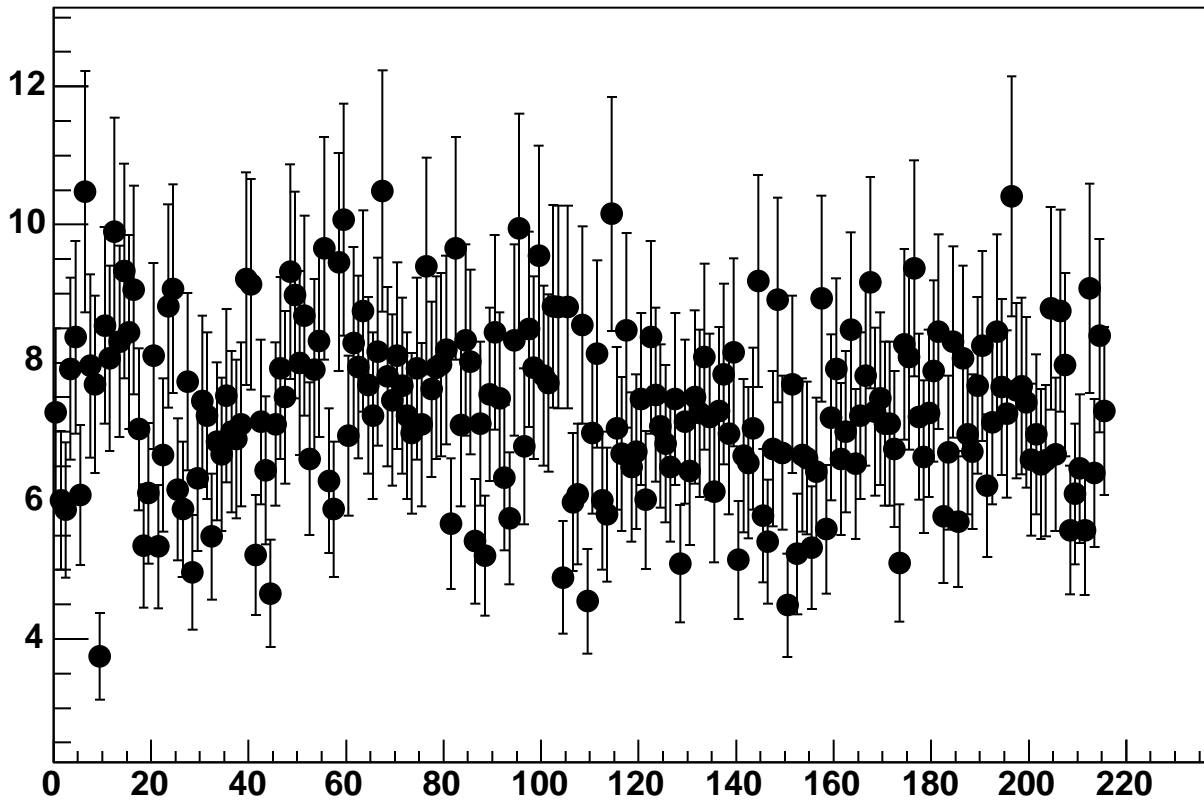
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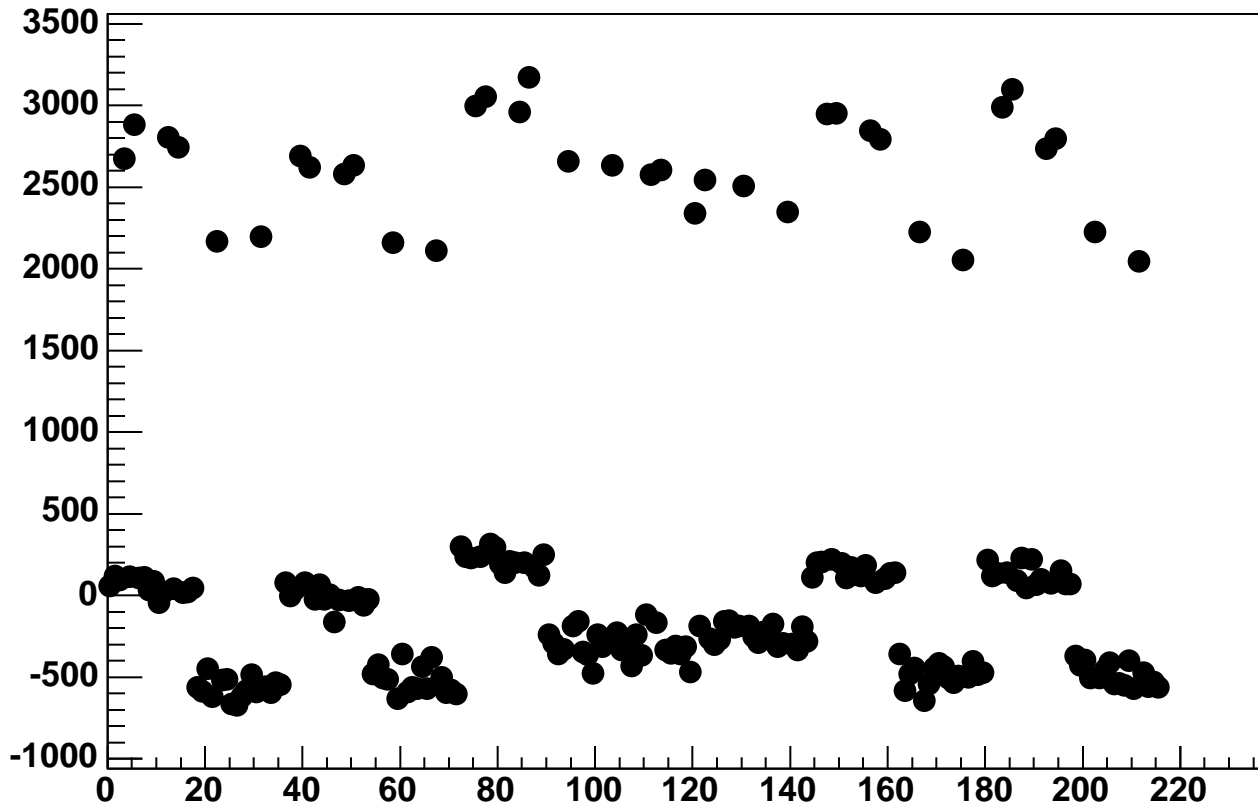
Enable 1, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



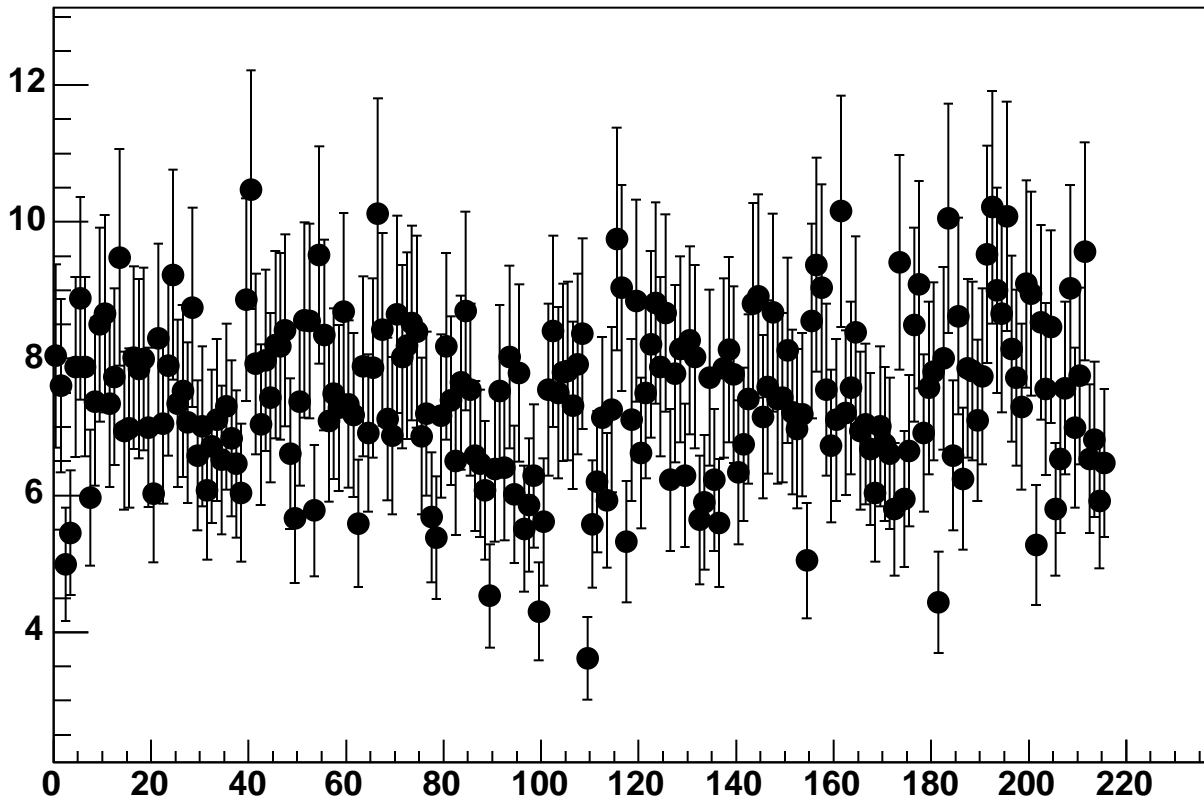
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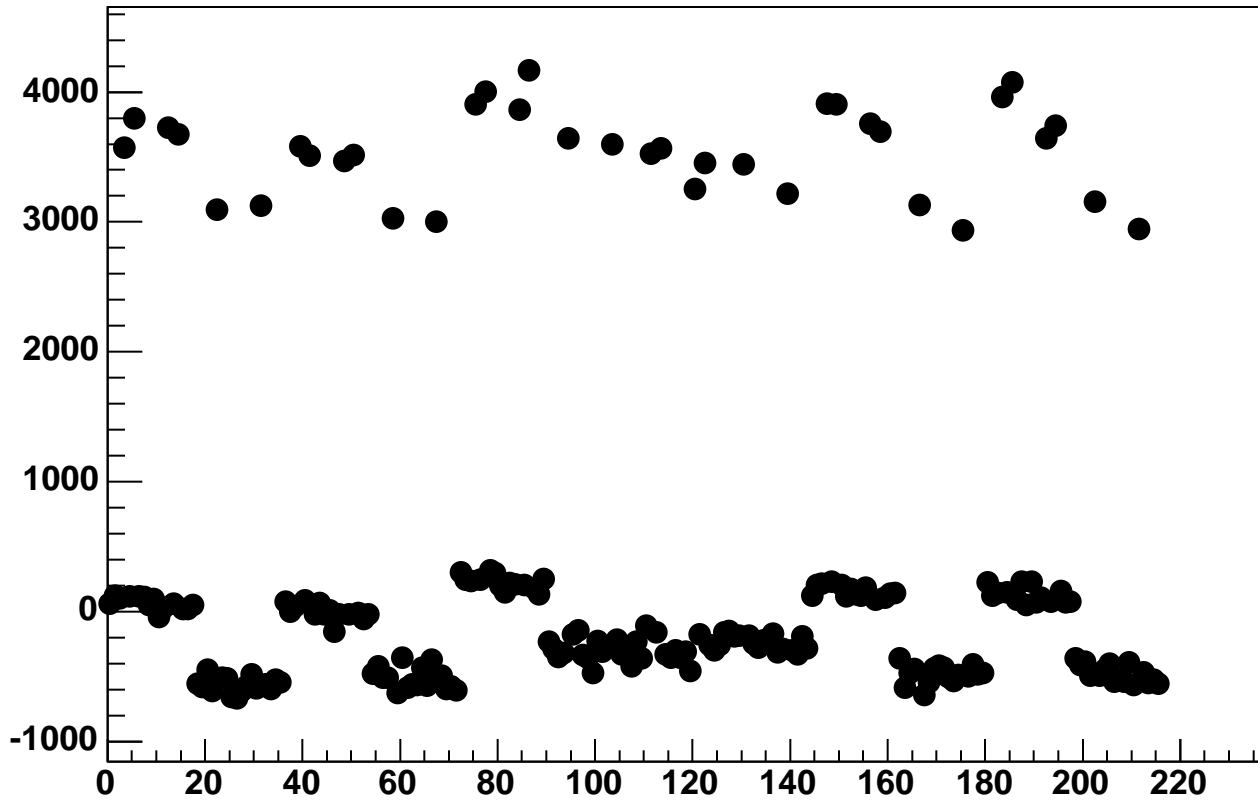
Enable 1, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



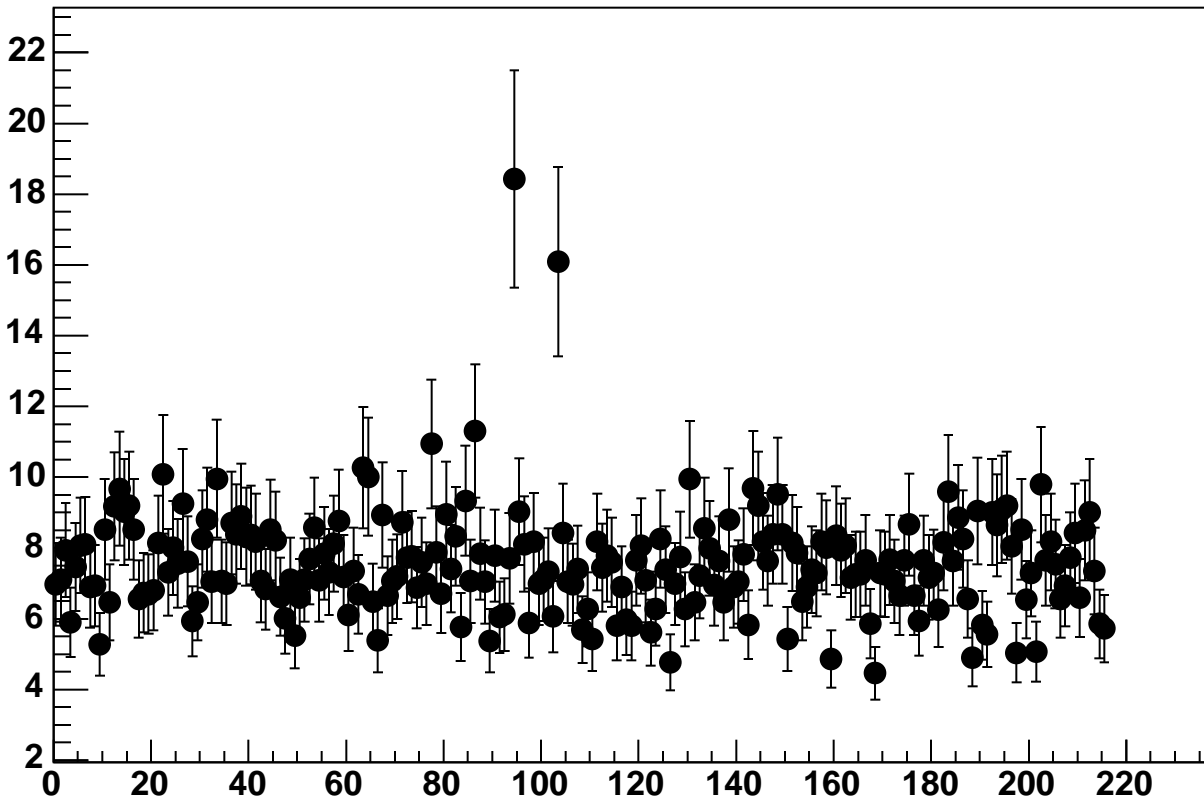
Enable 1, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



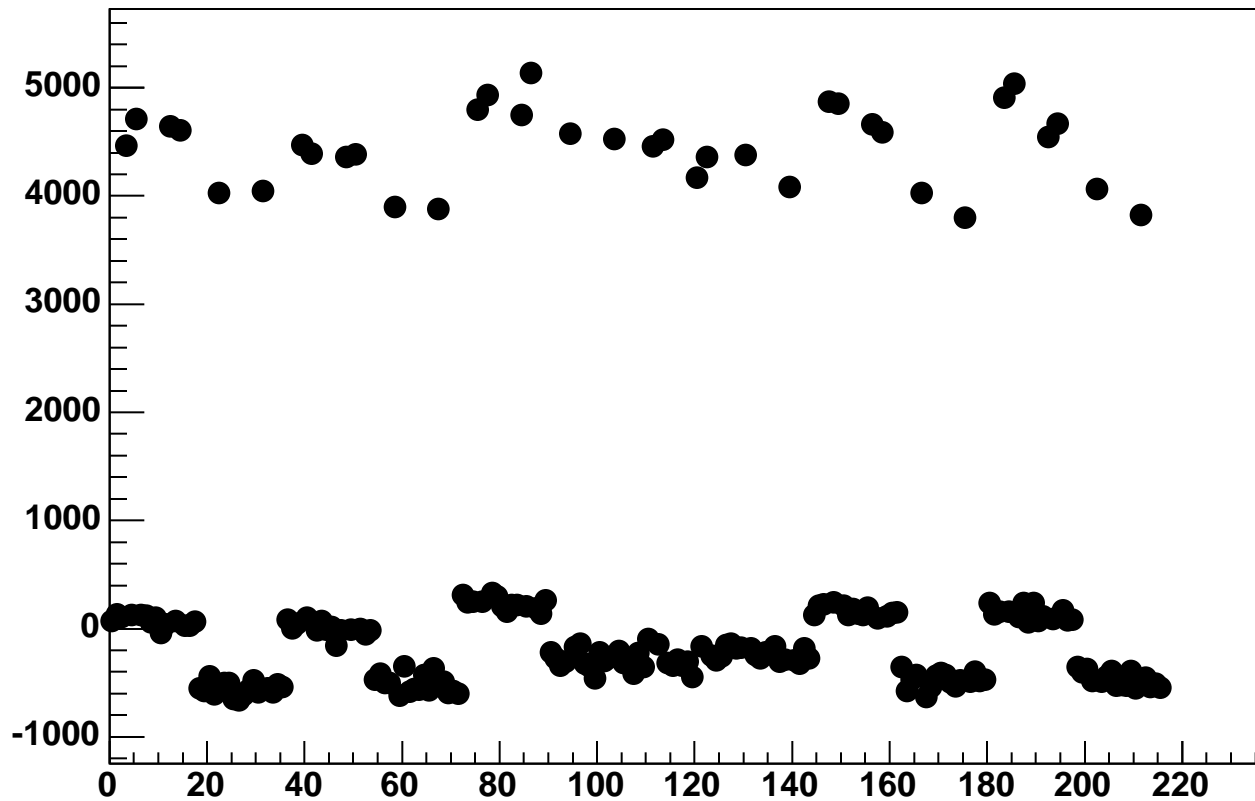
Enable 1, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



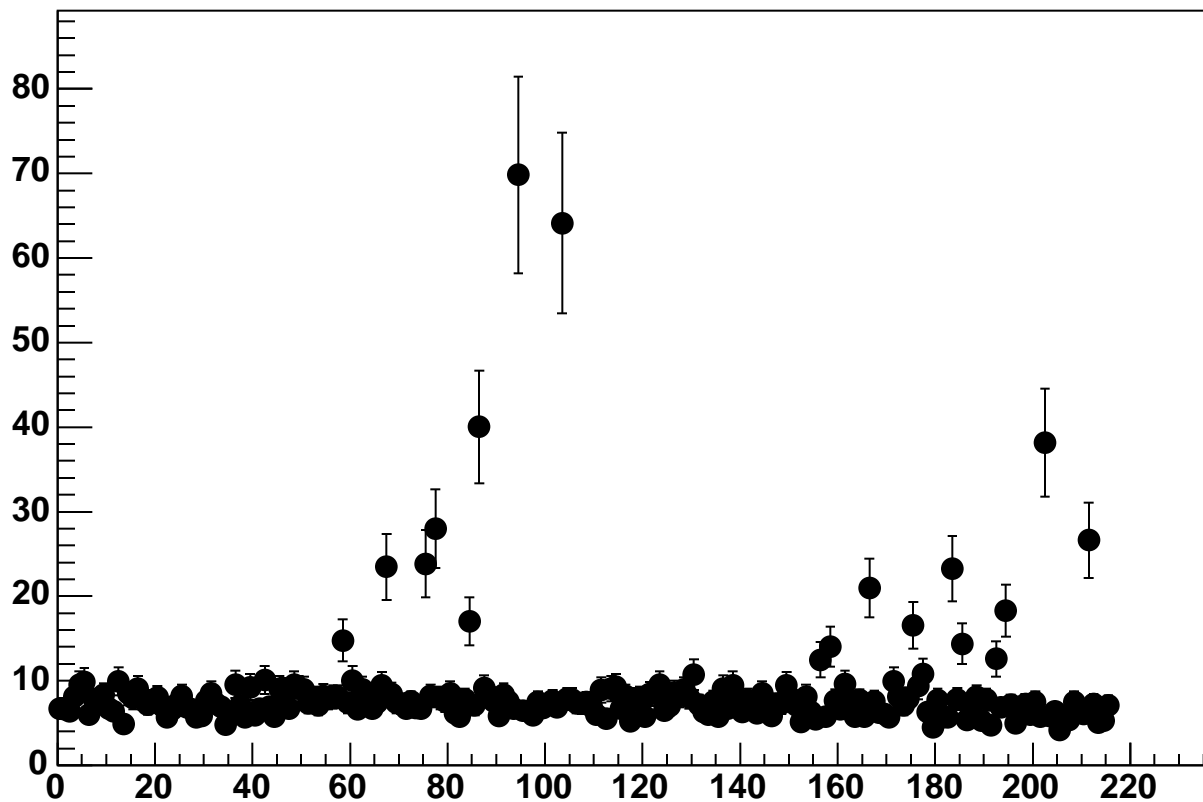
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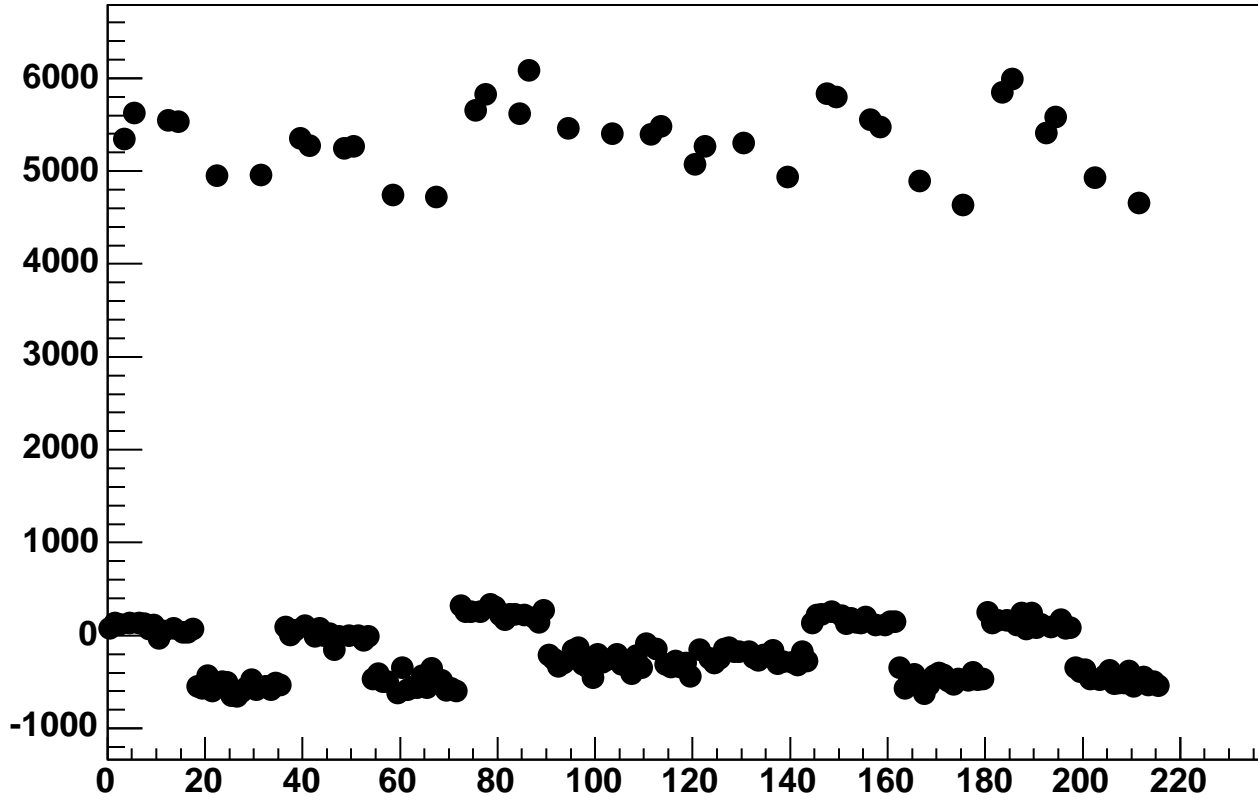
Enable 1, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



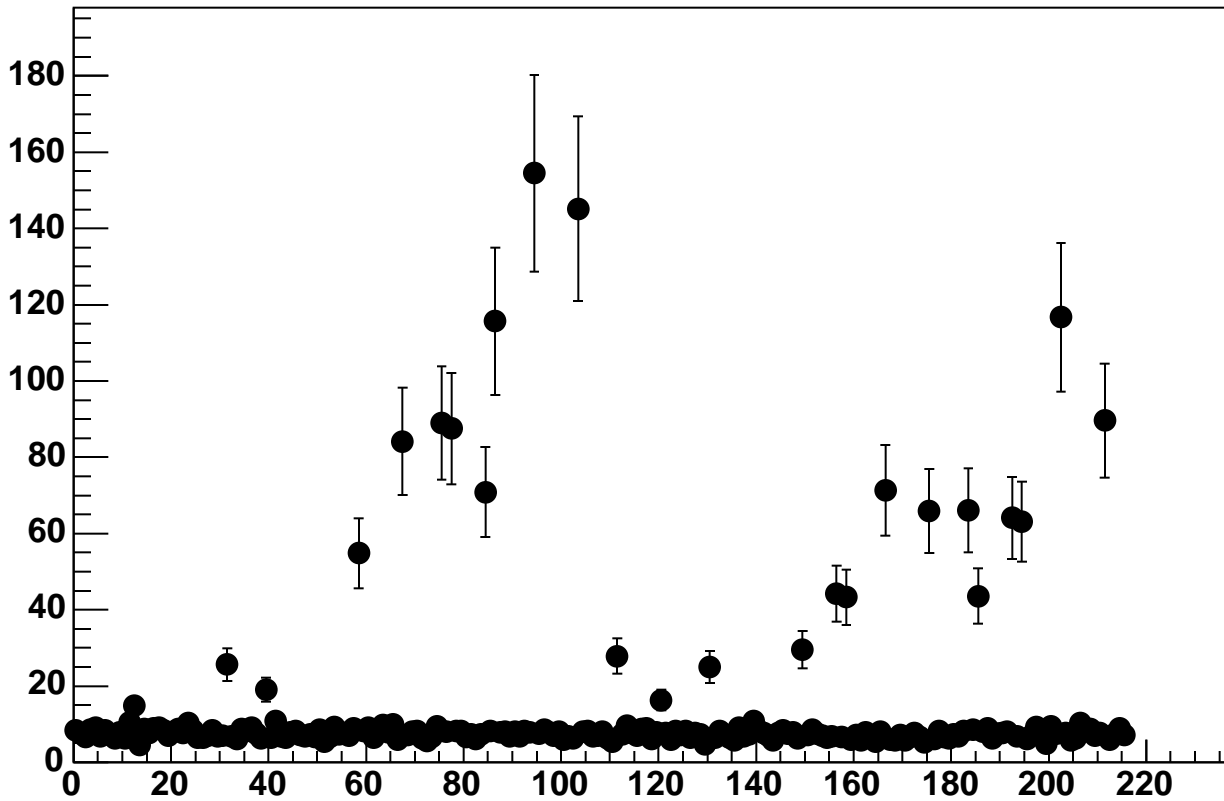
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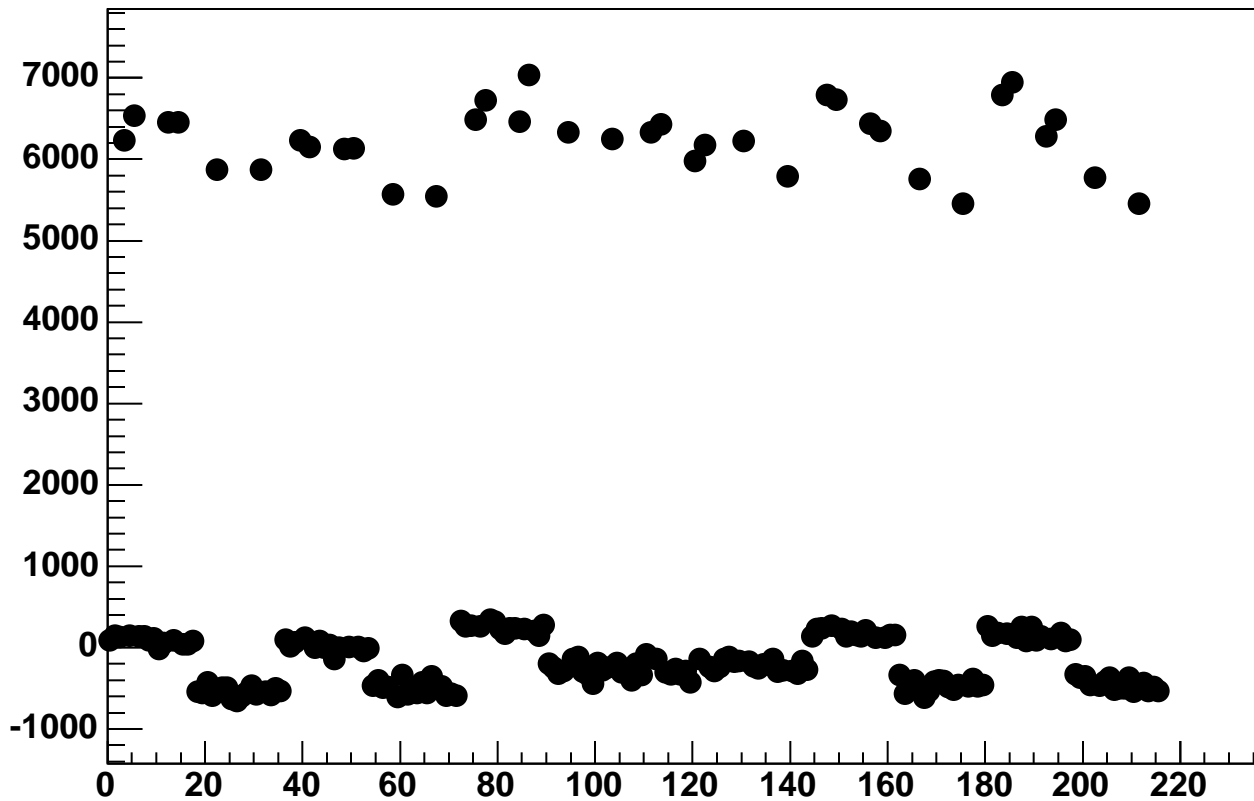
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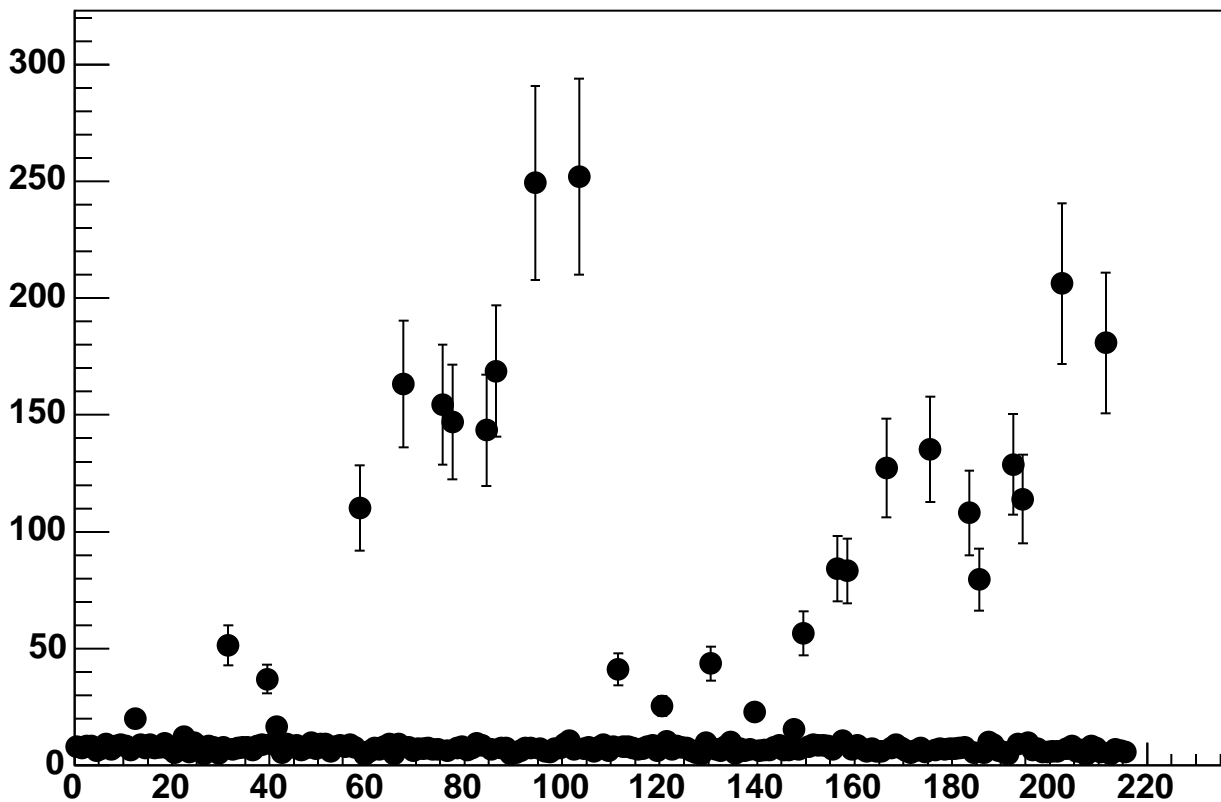
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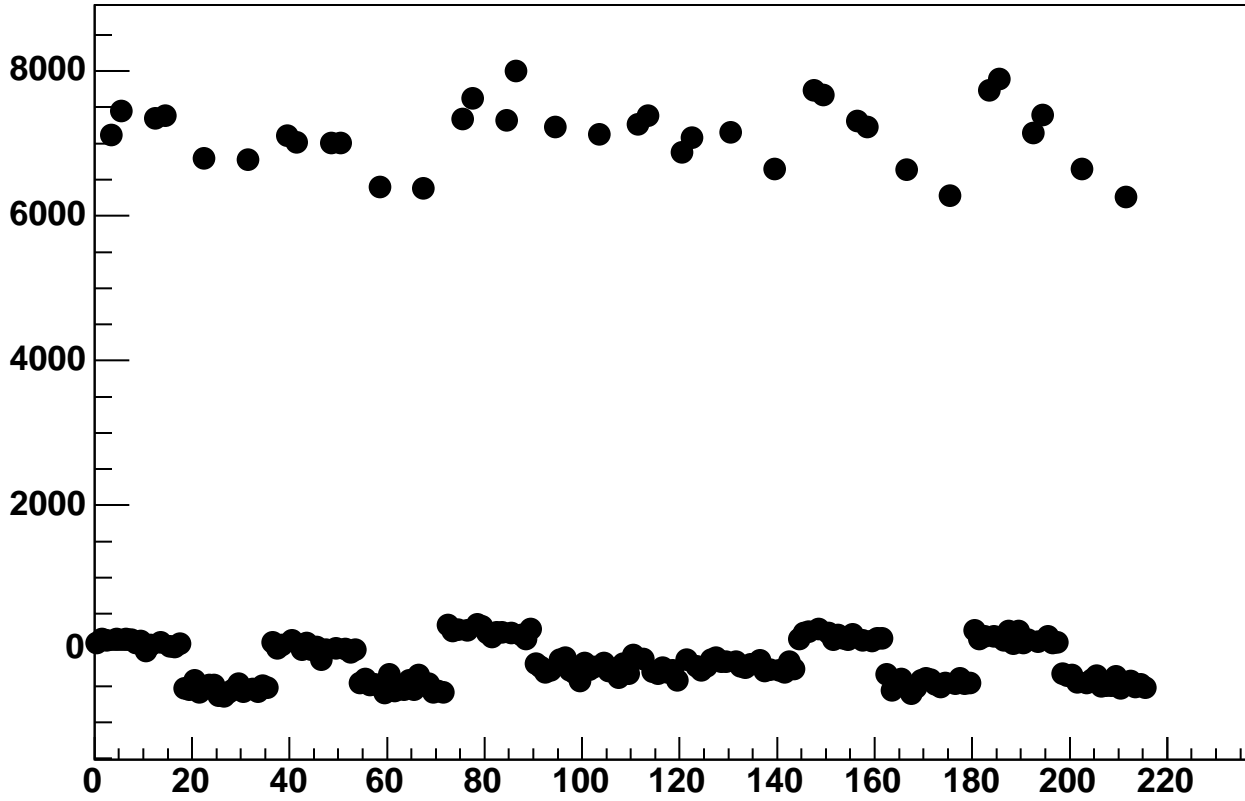
Enable 1, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



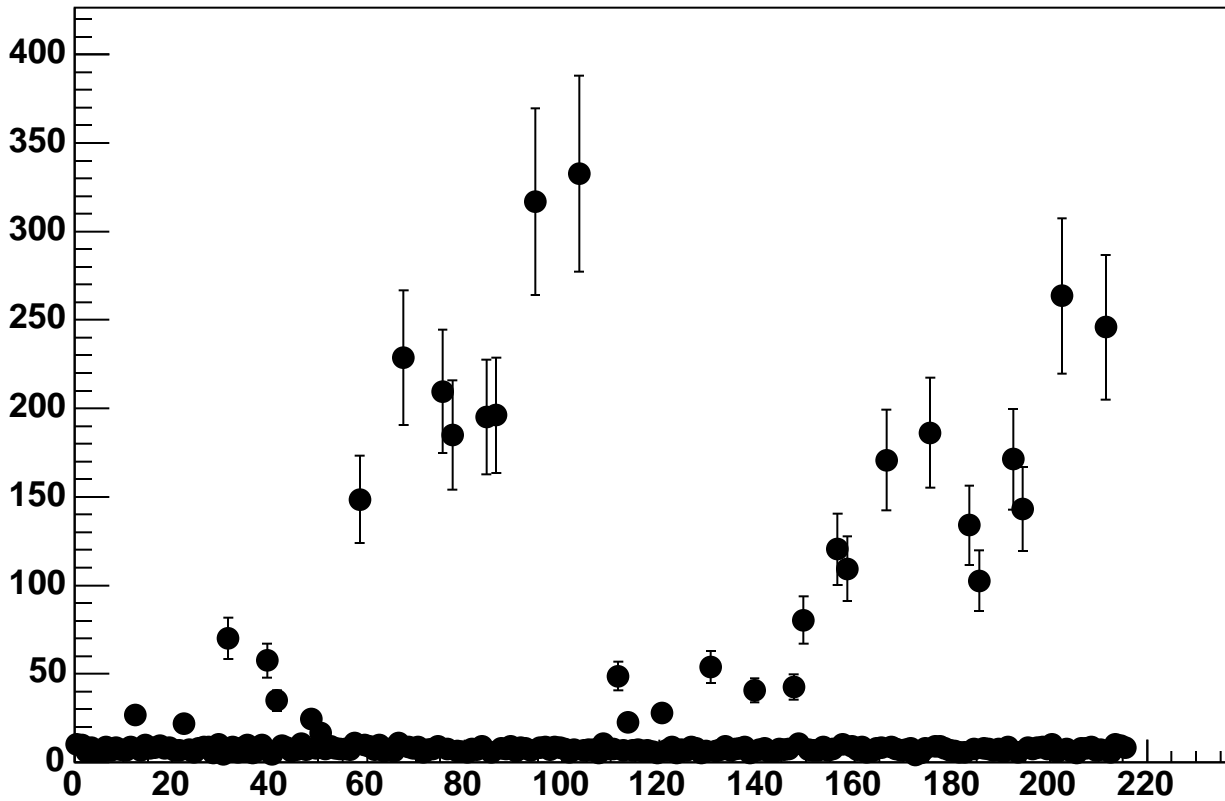
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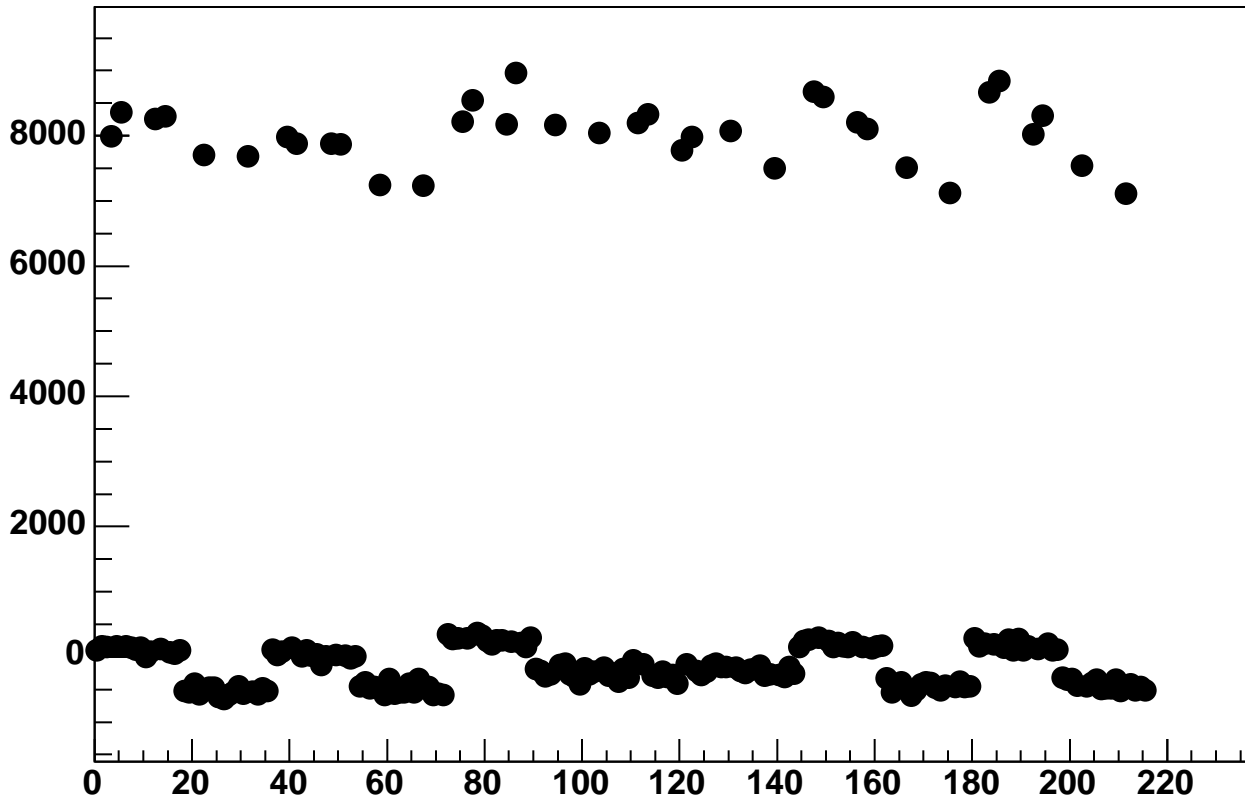
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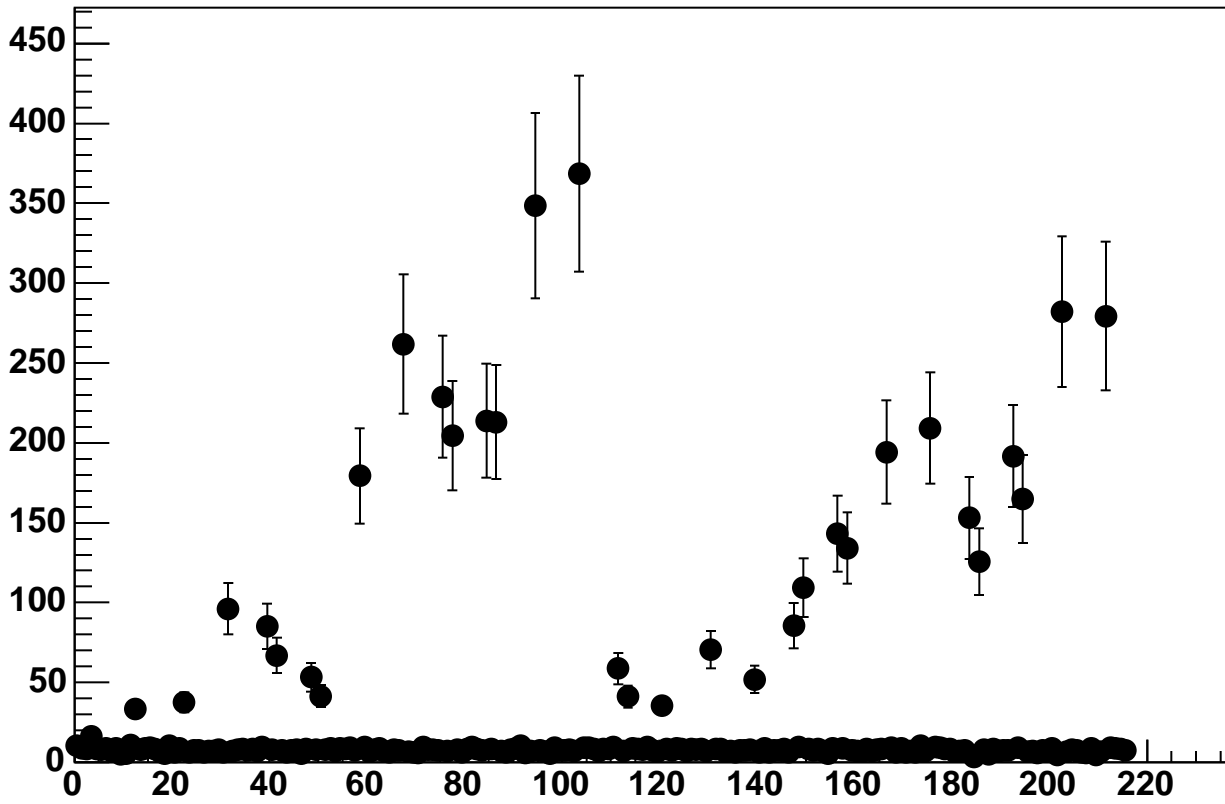
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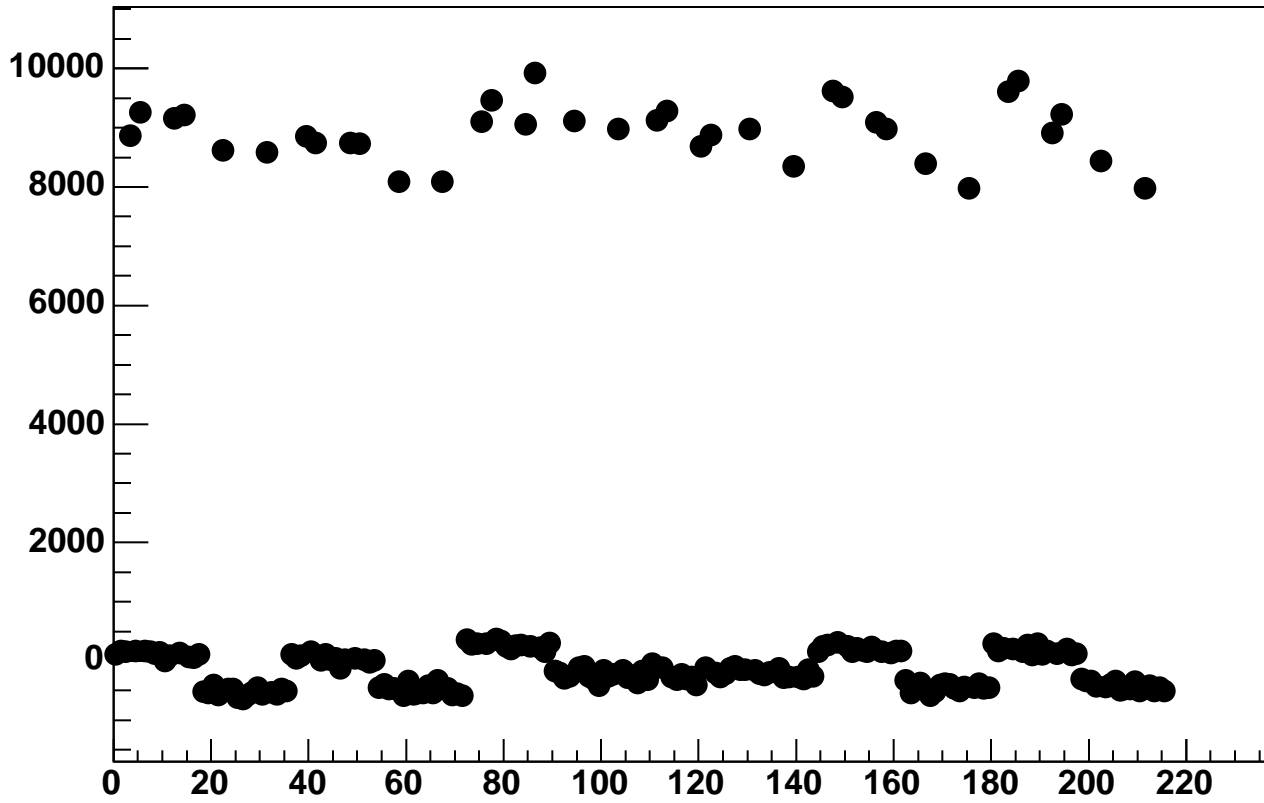
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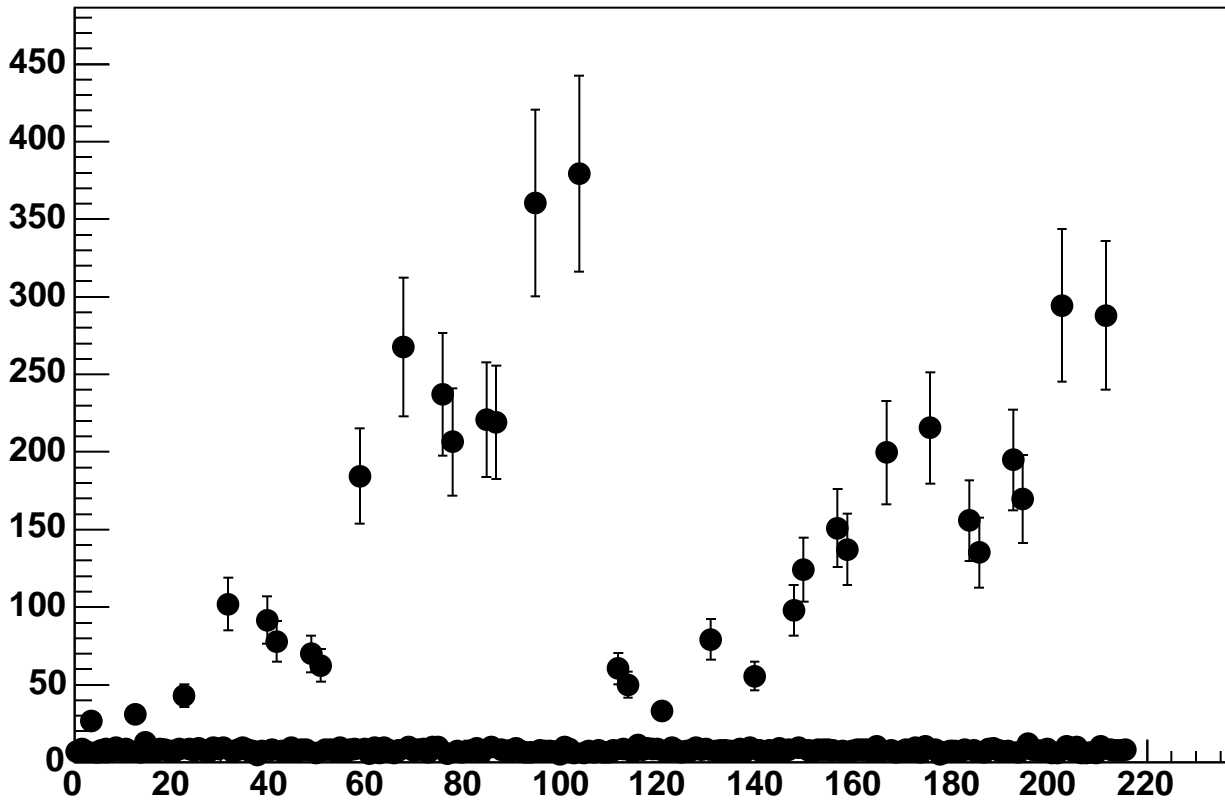
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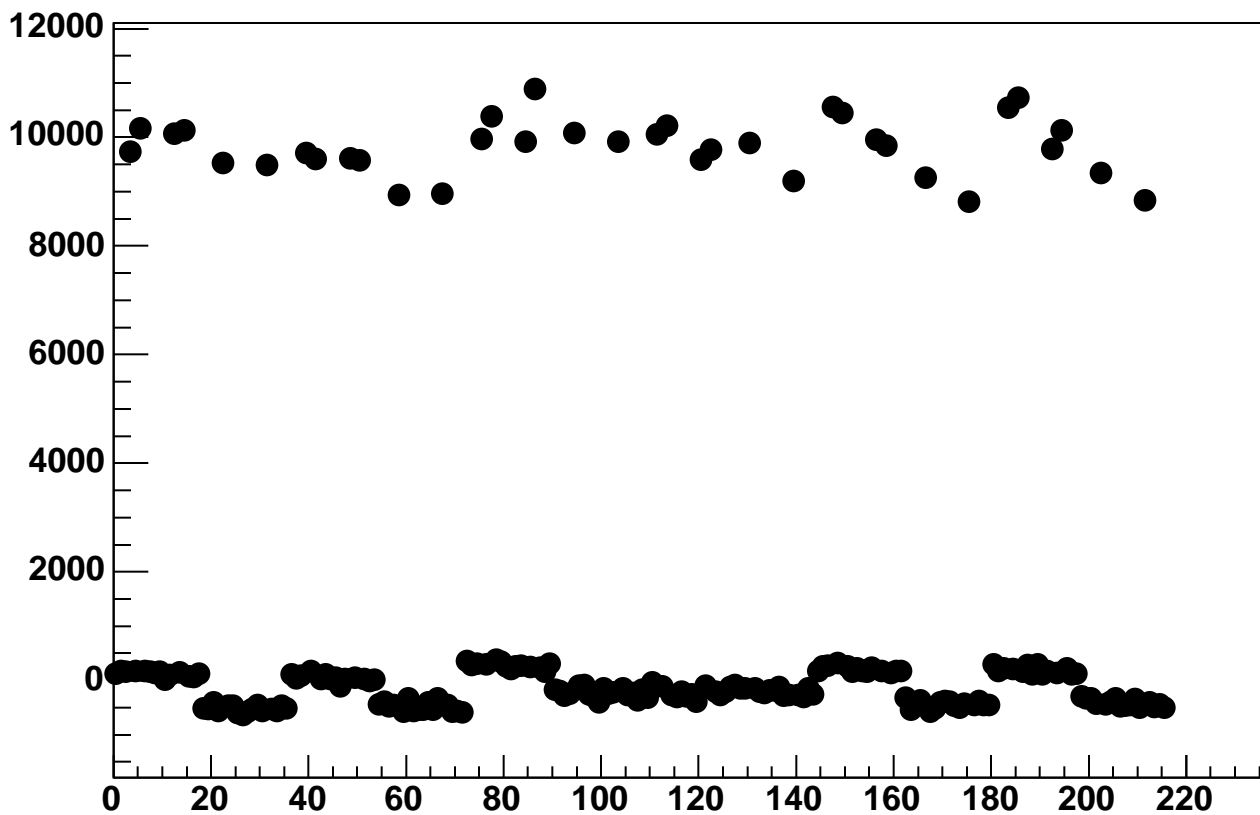
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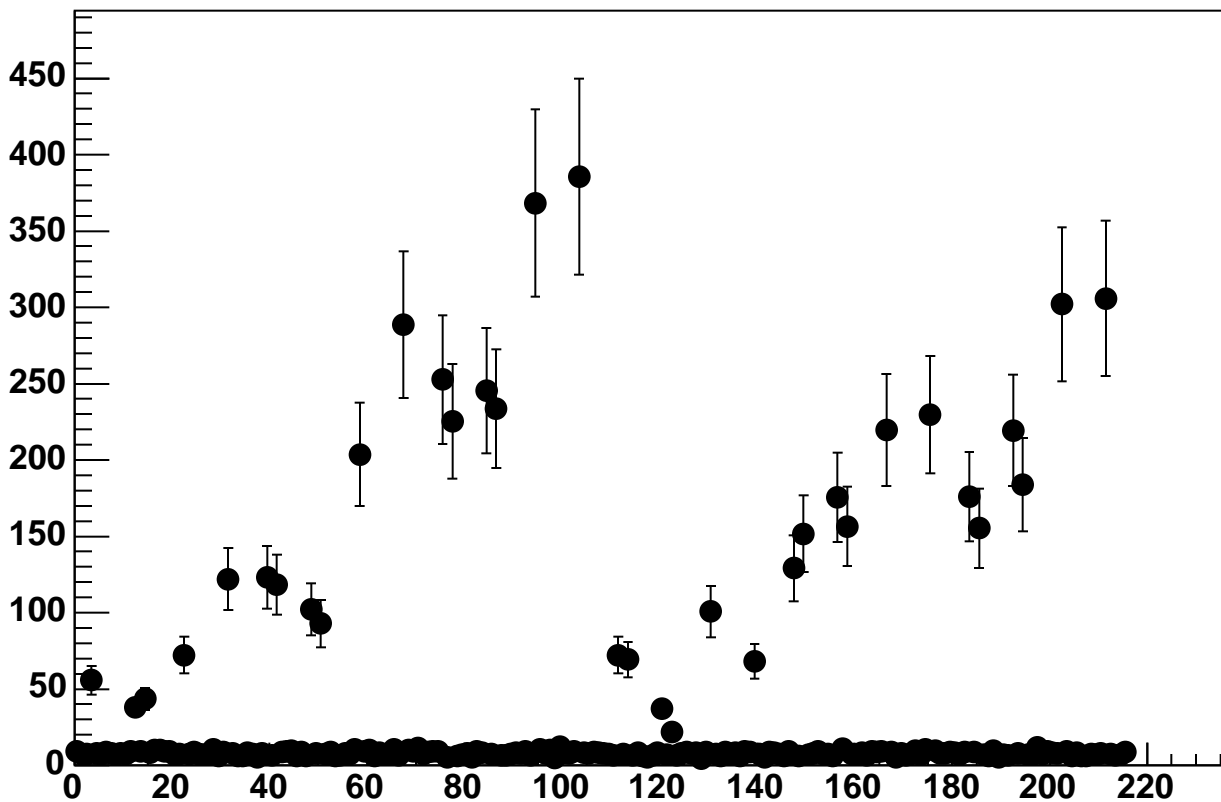
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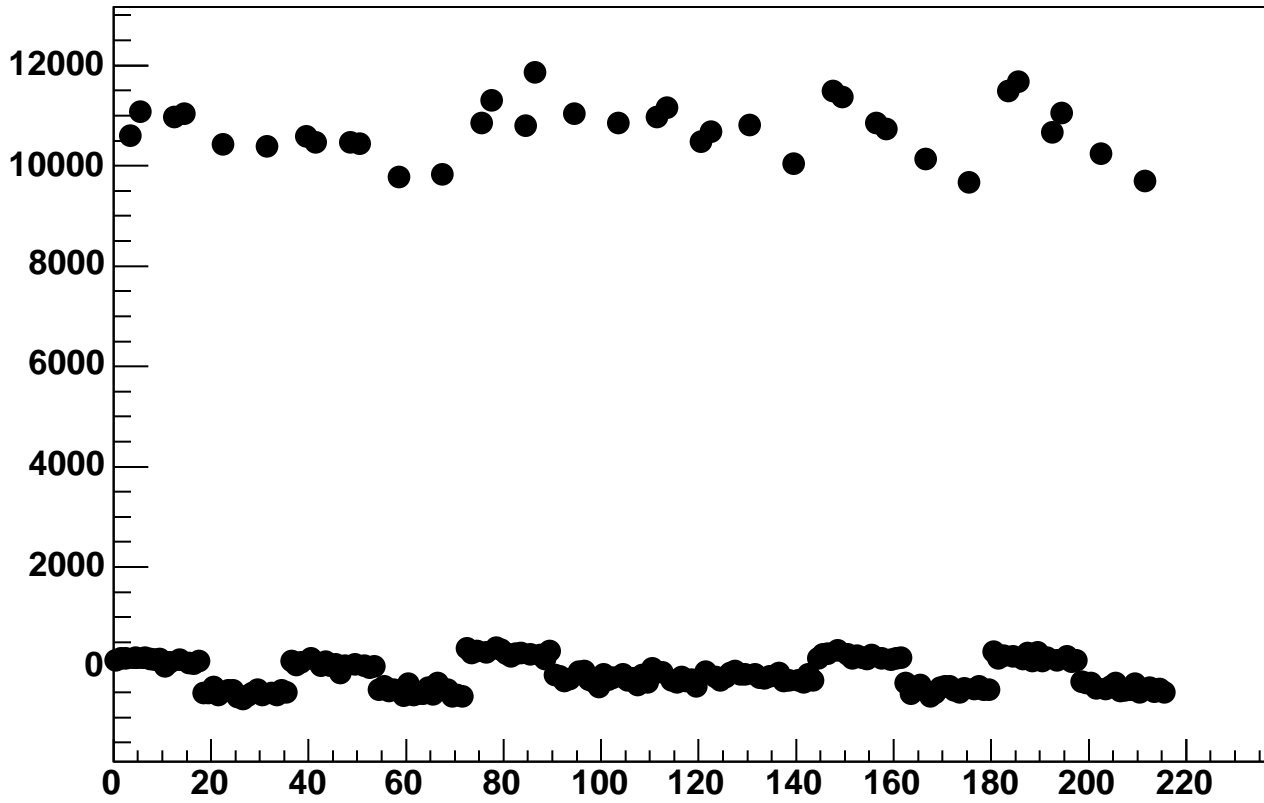
Enable 1, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



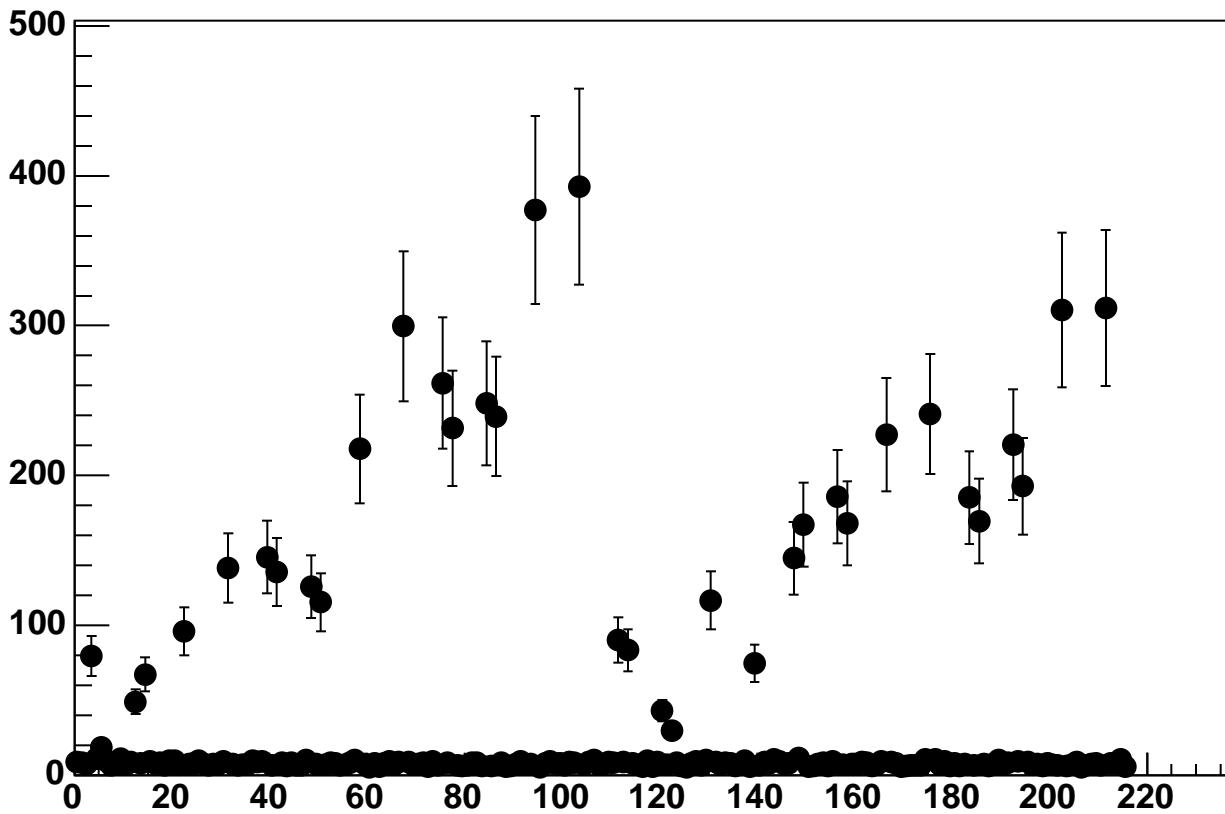
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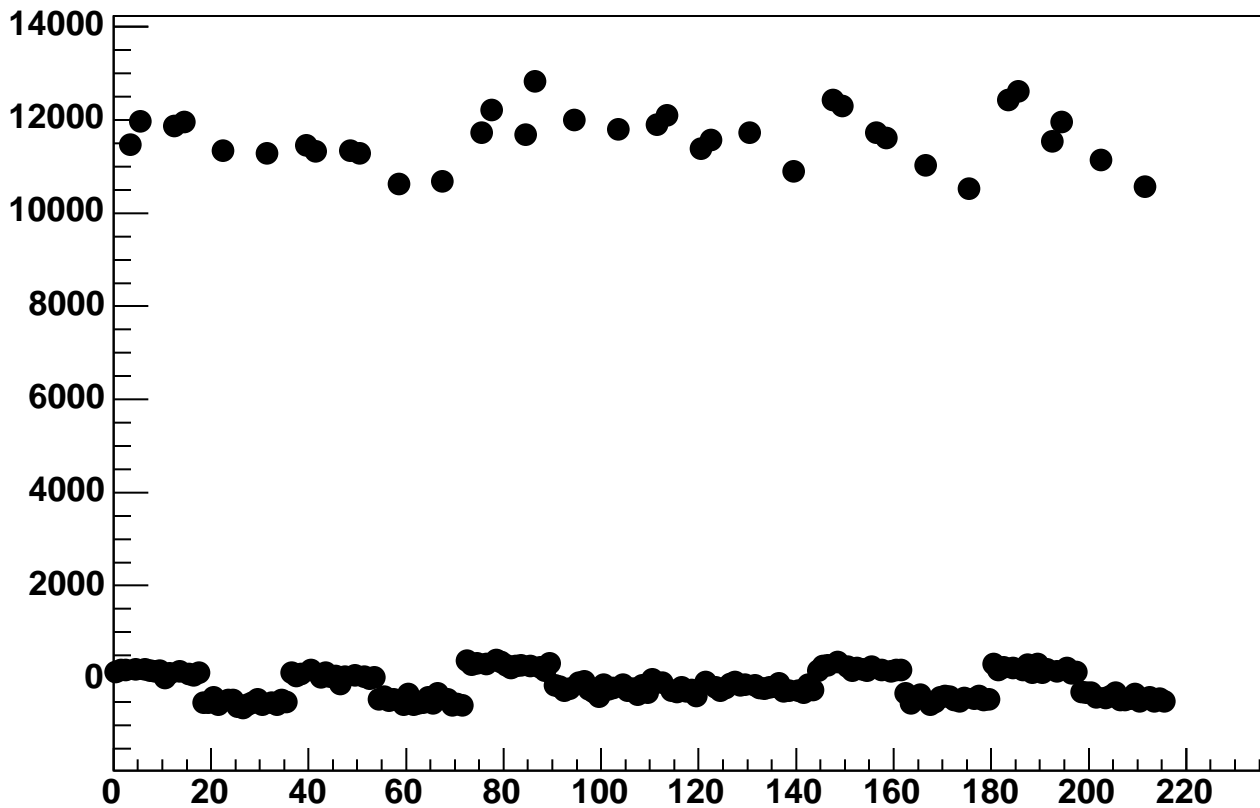
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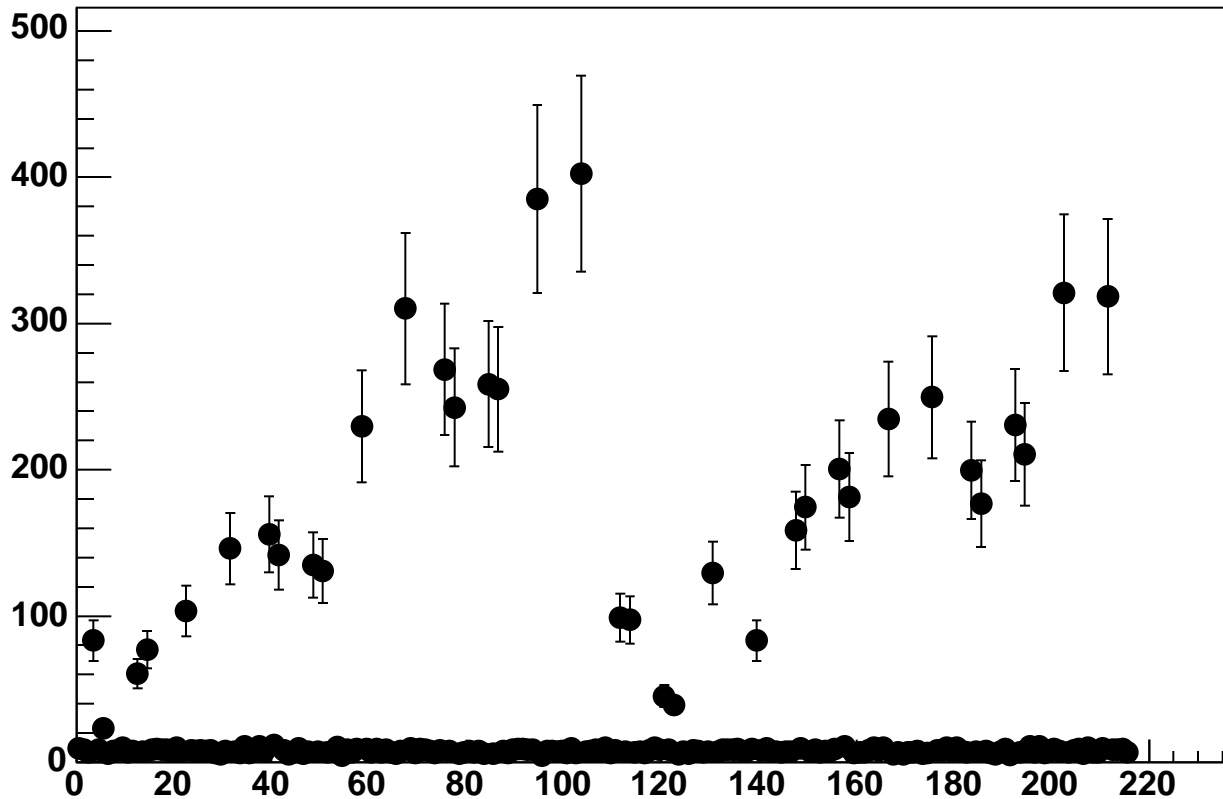
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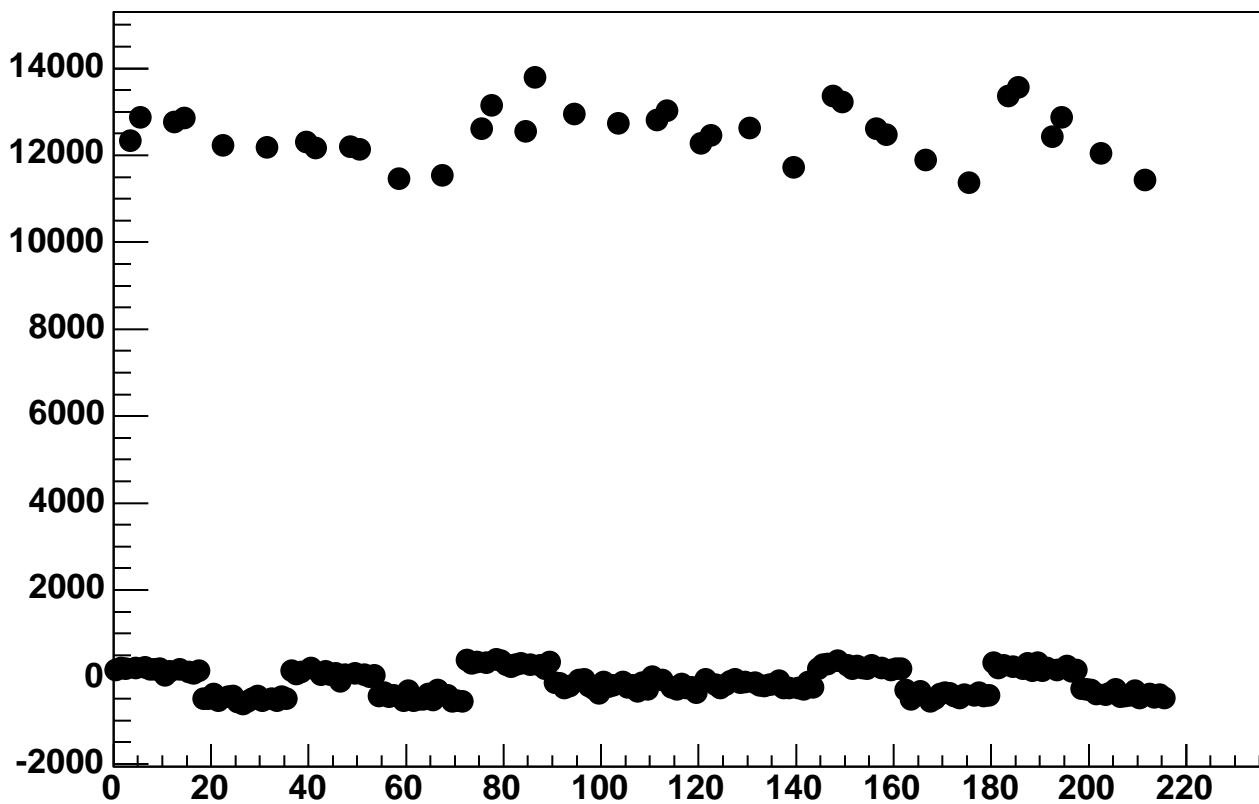
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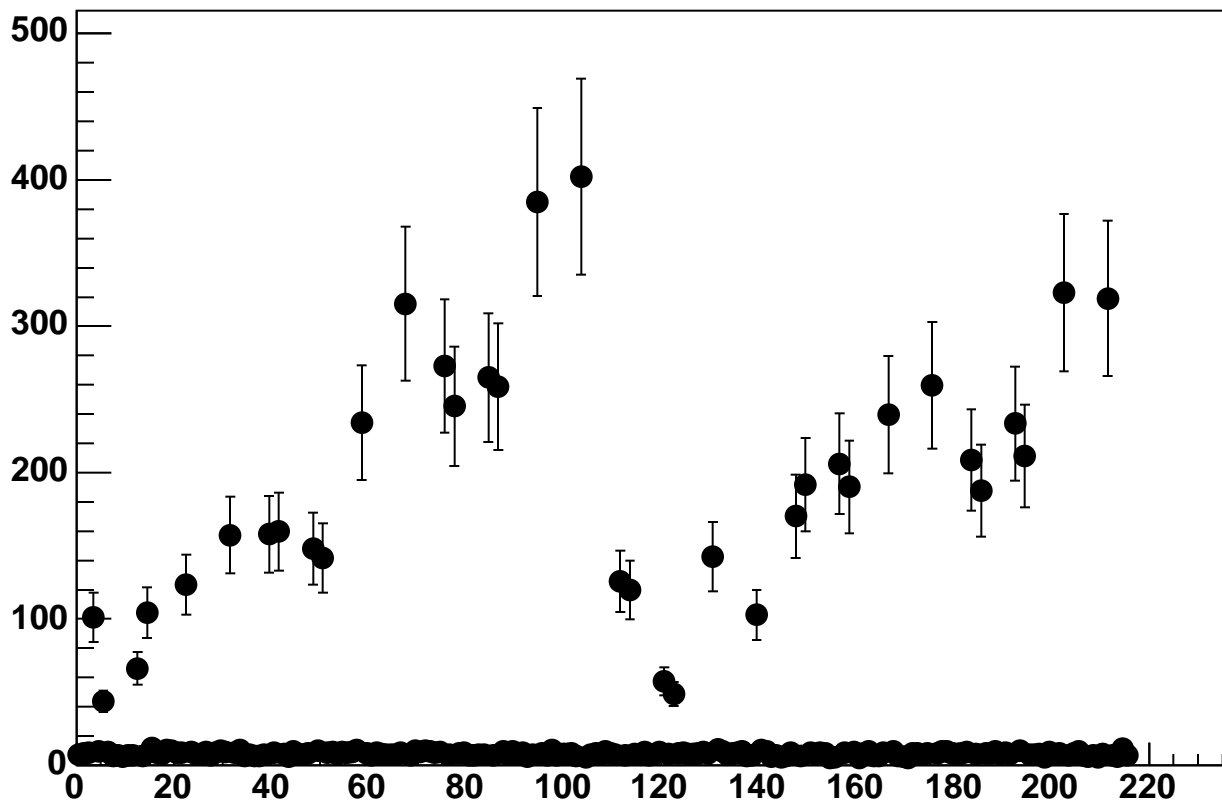
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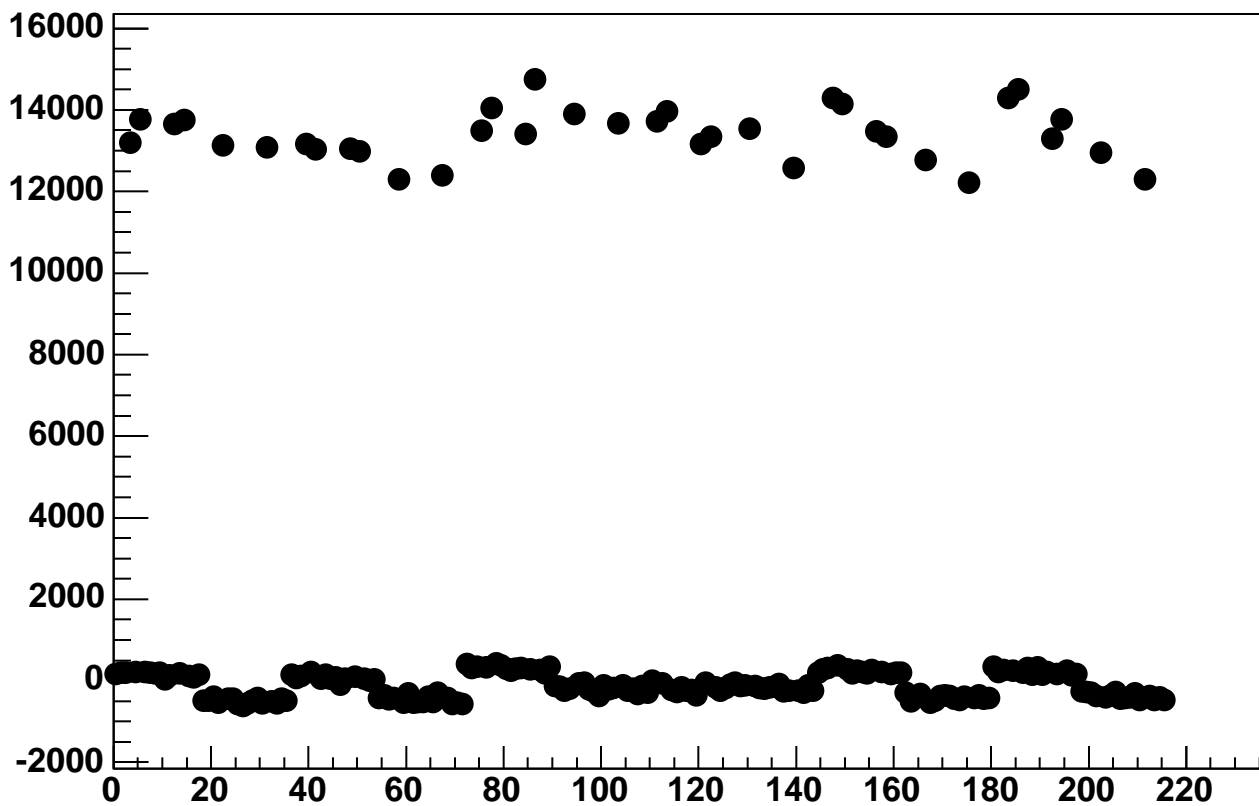
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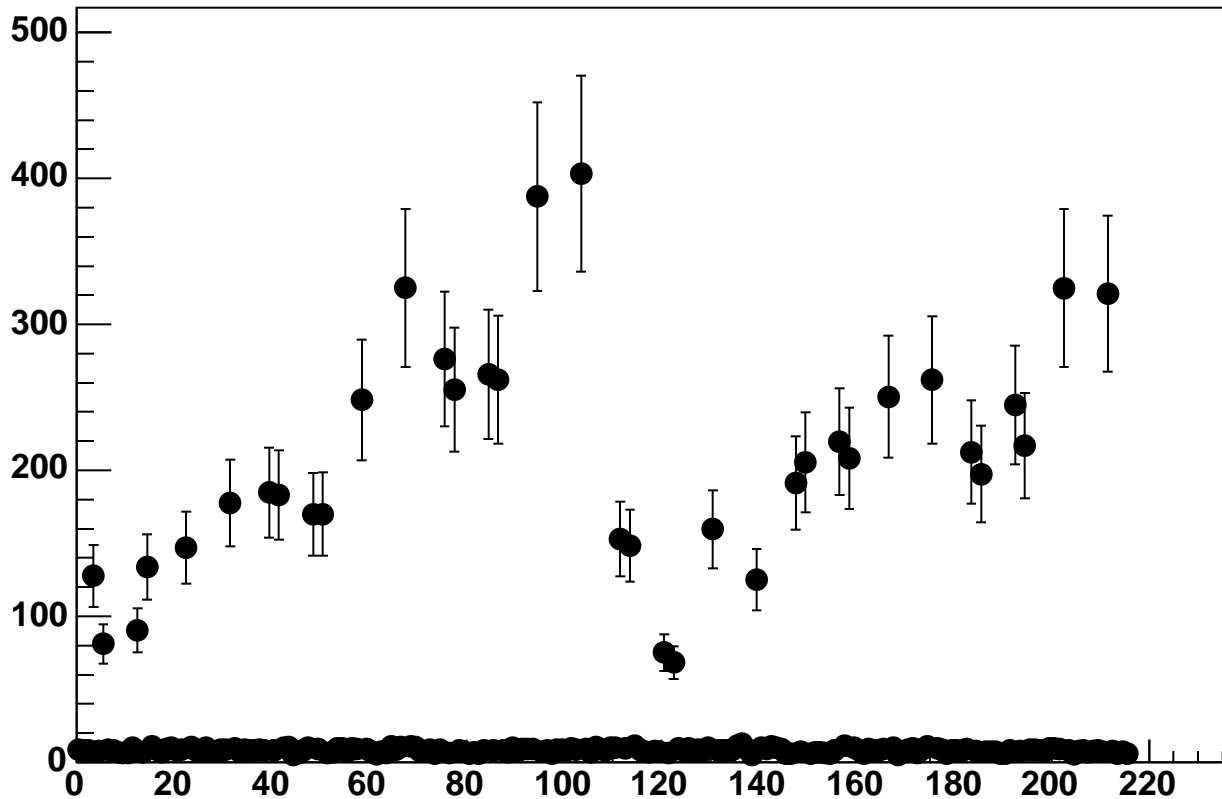
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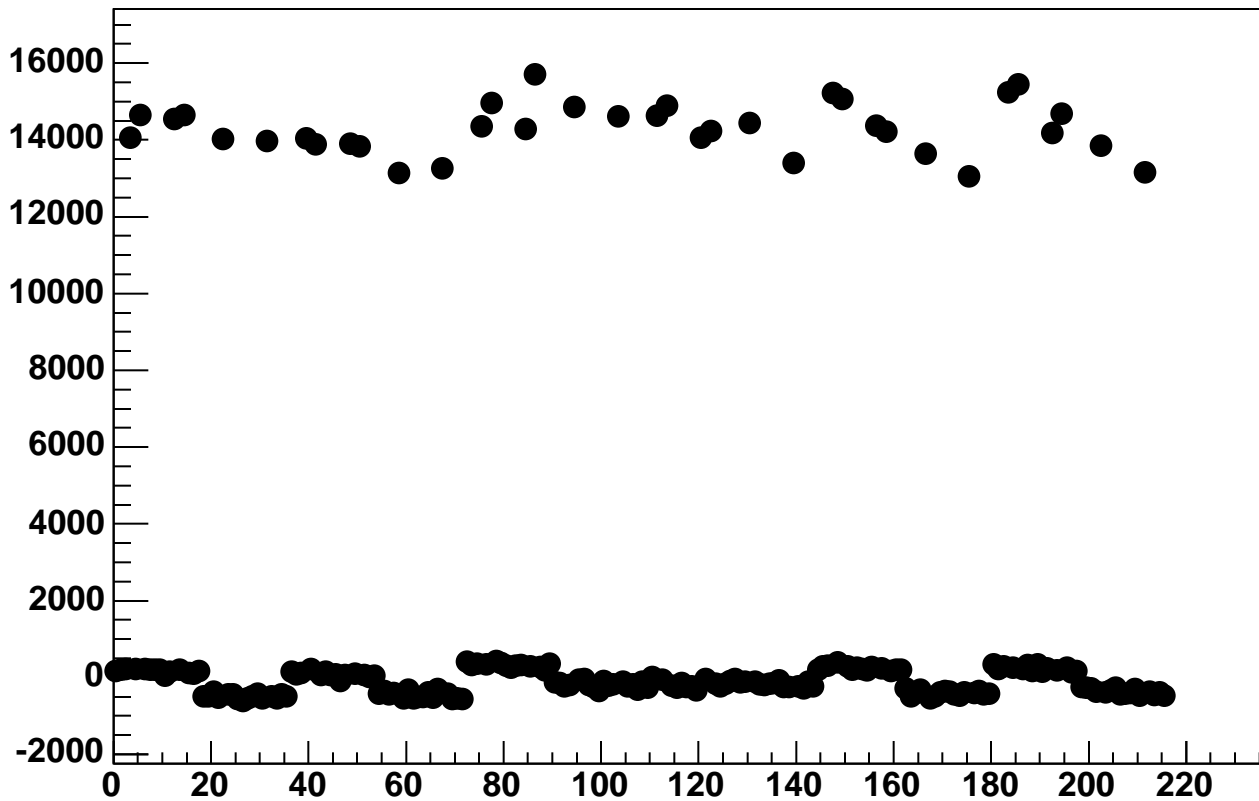
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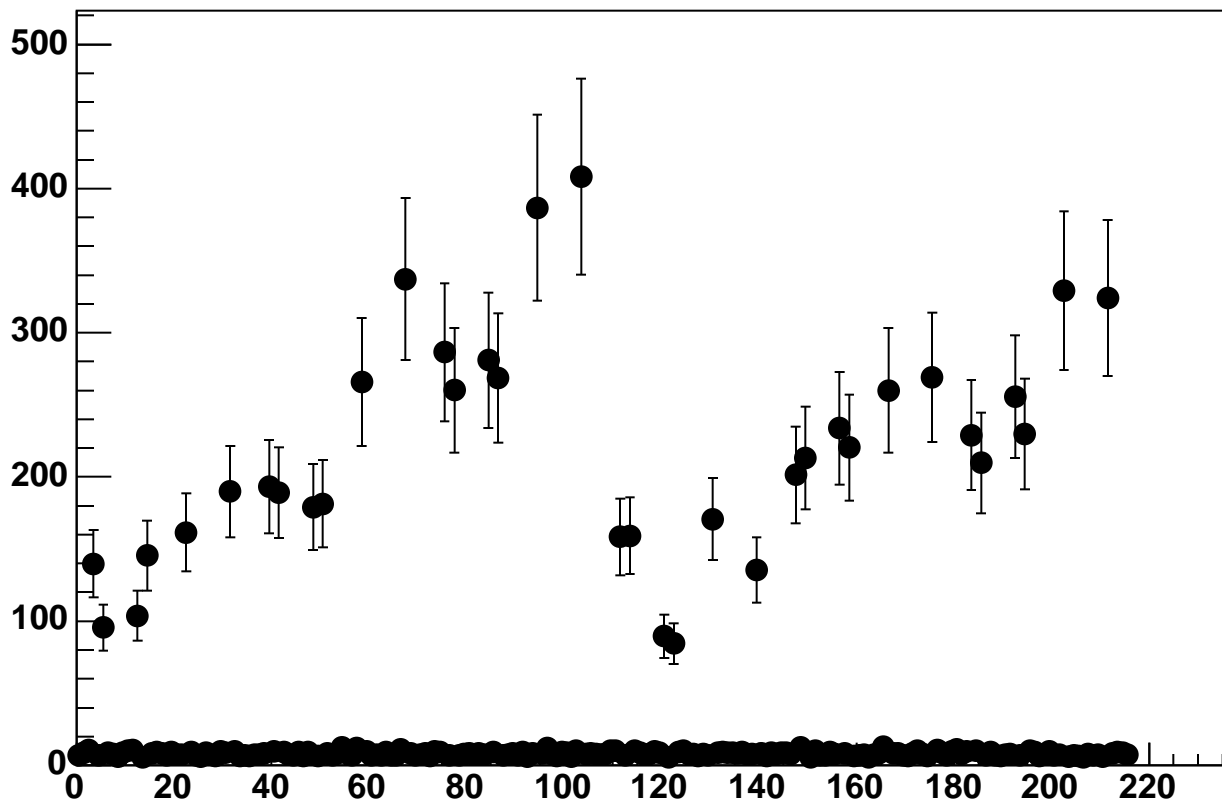
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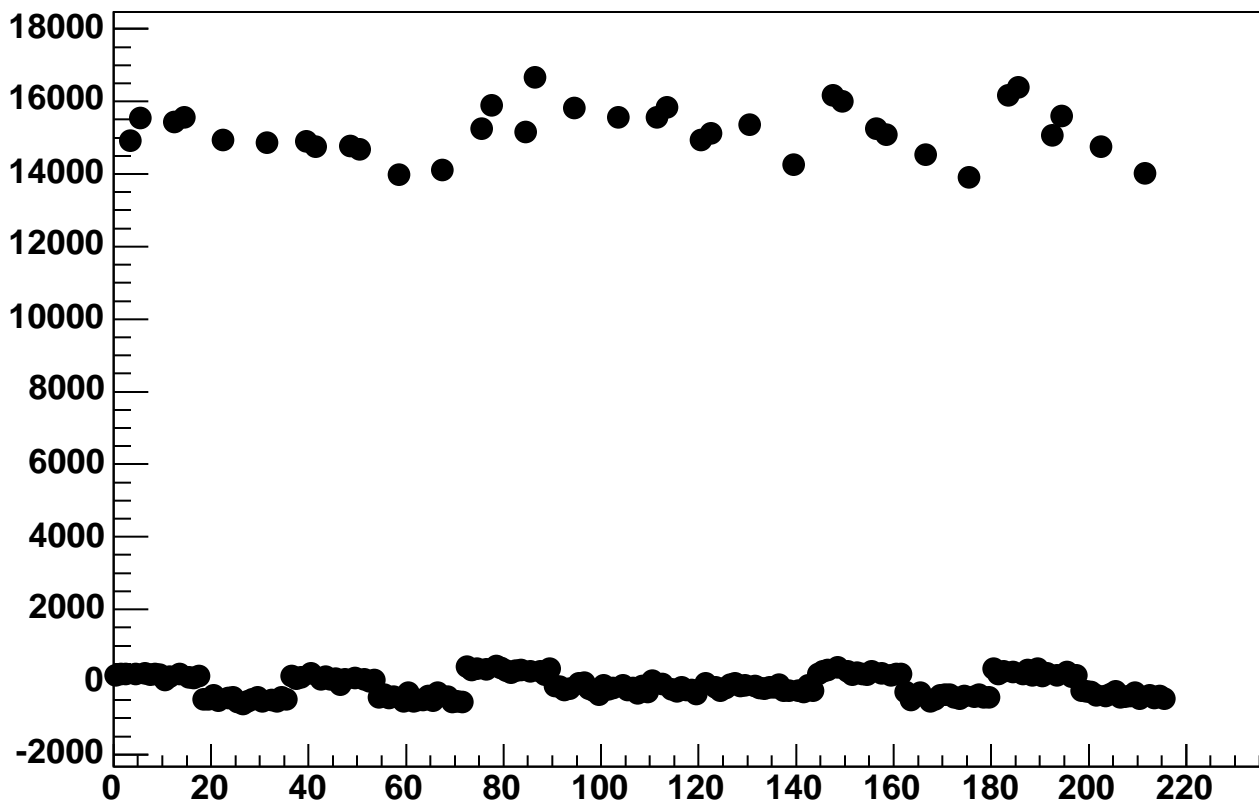
Enable 1, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



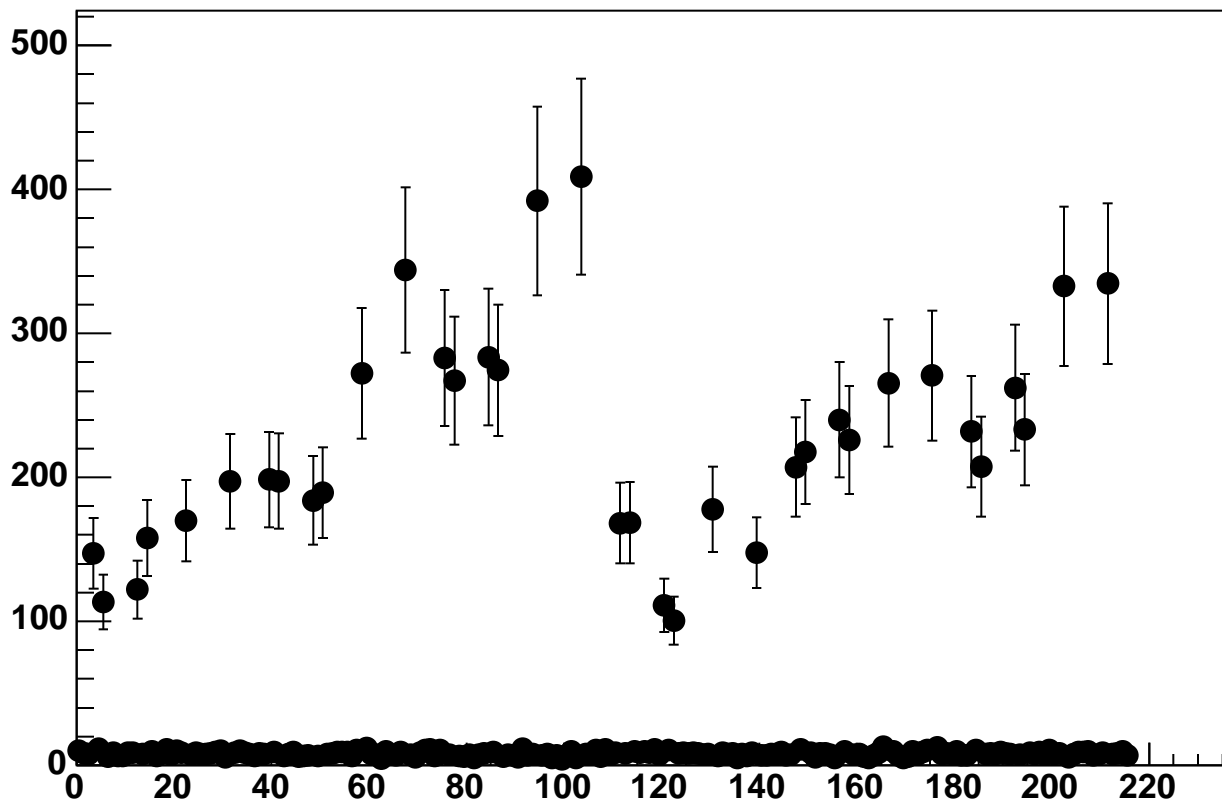
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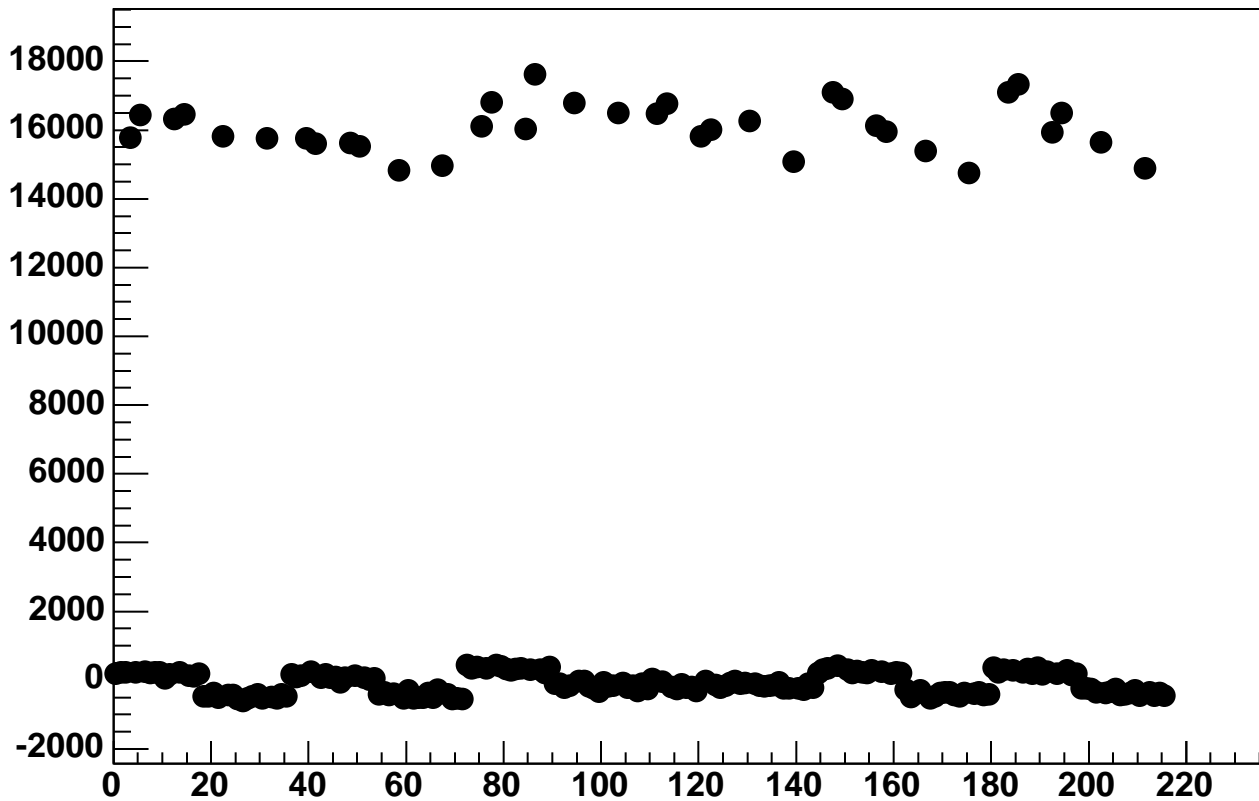
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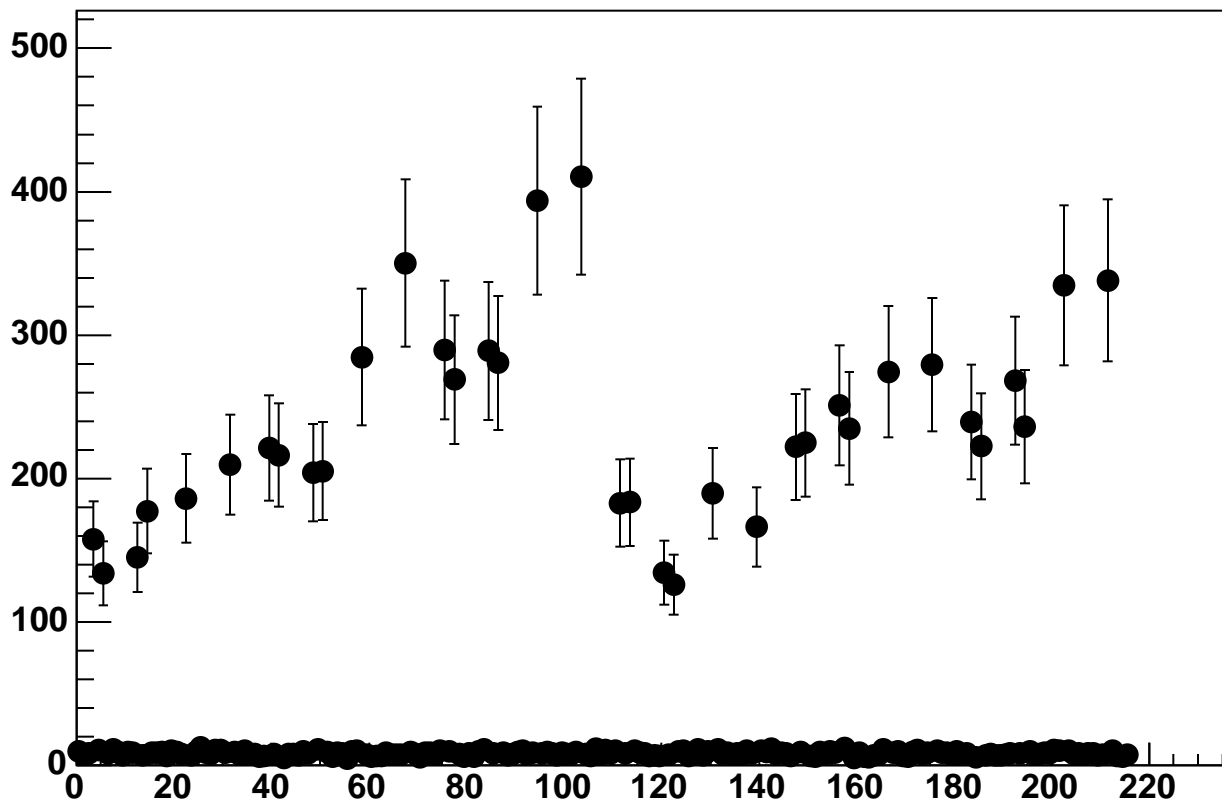
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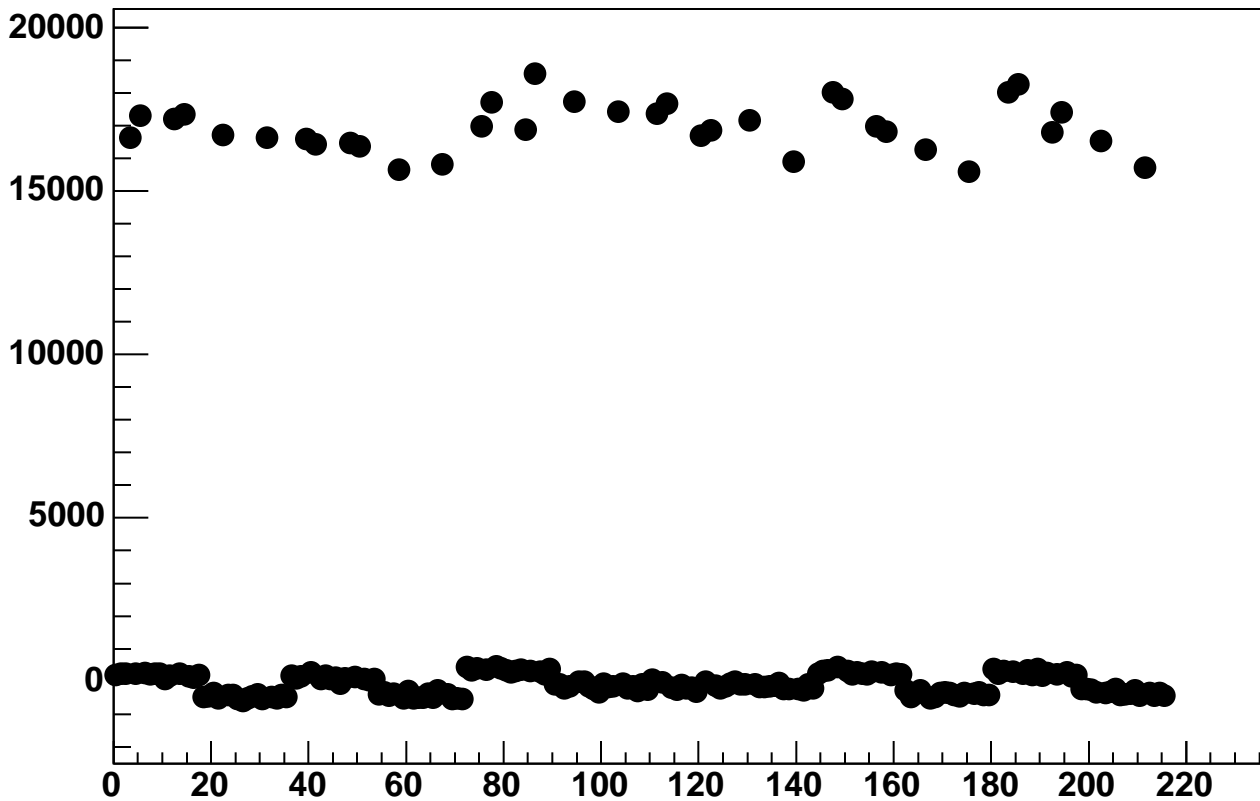
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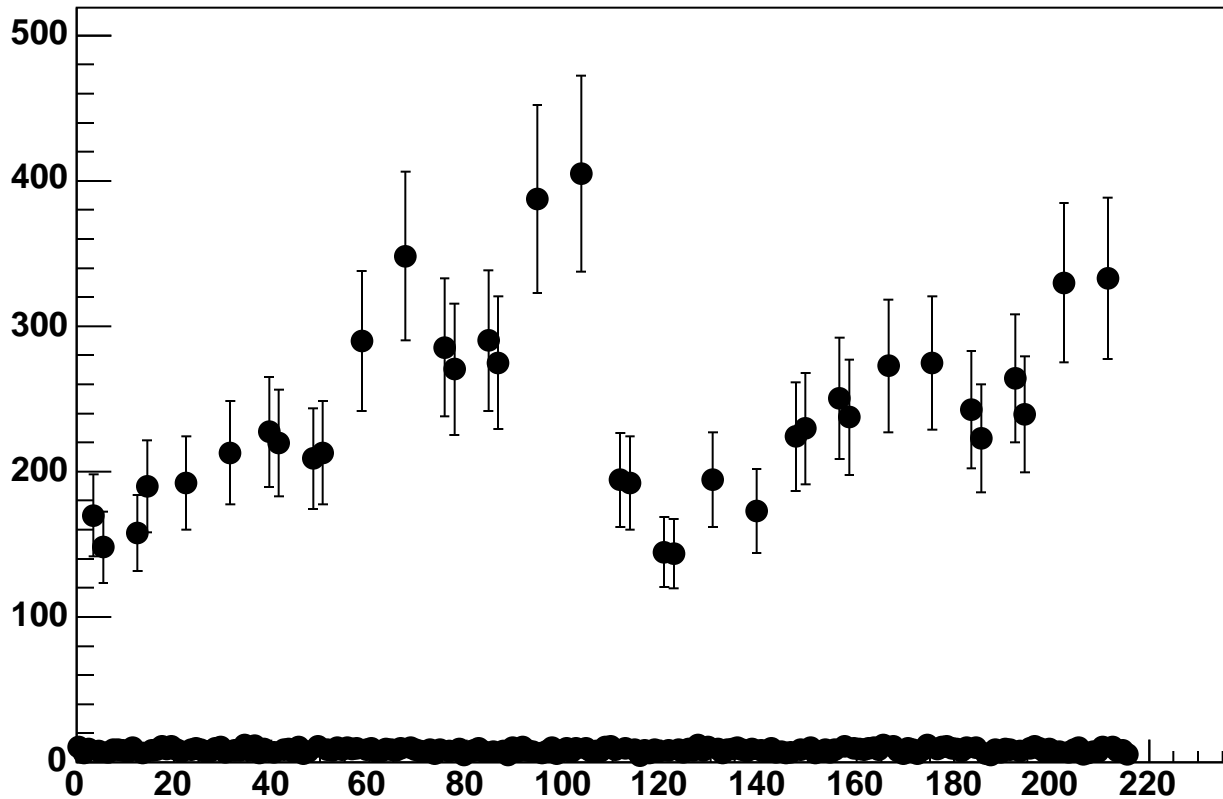
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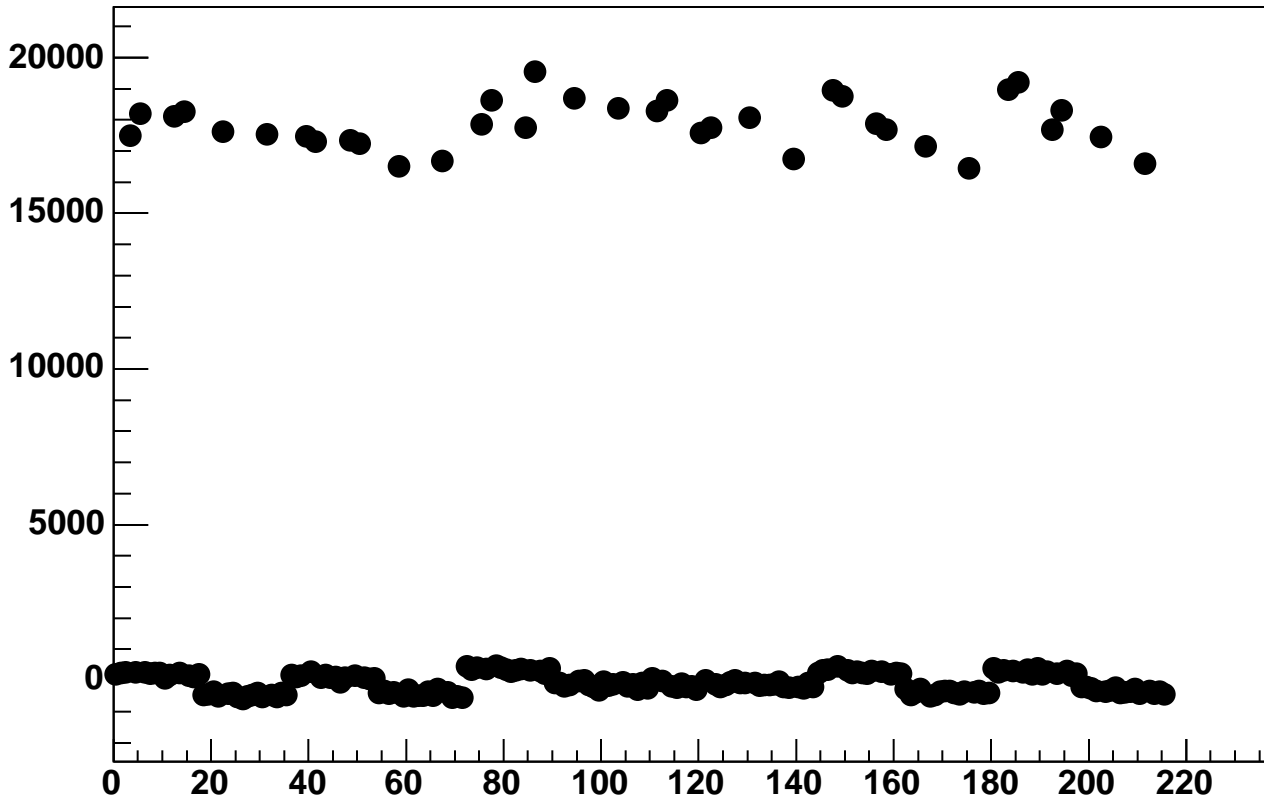
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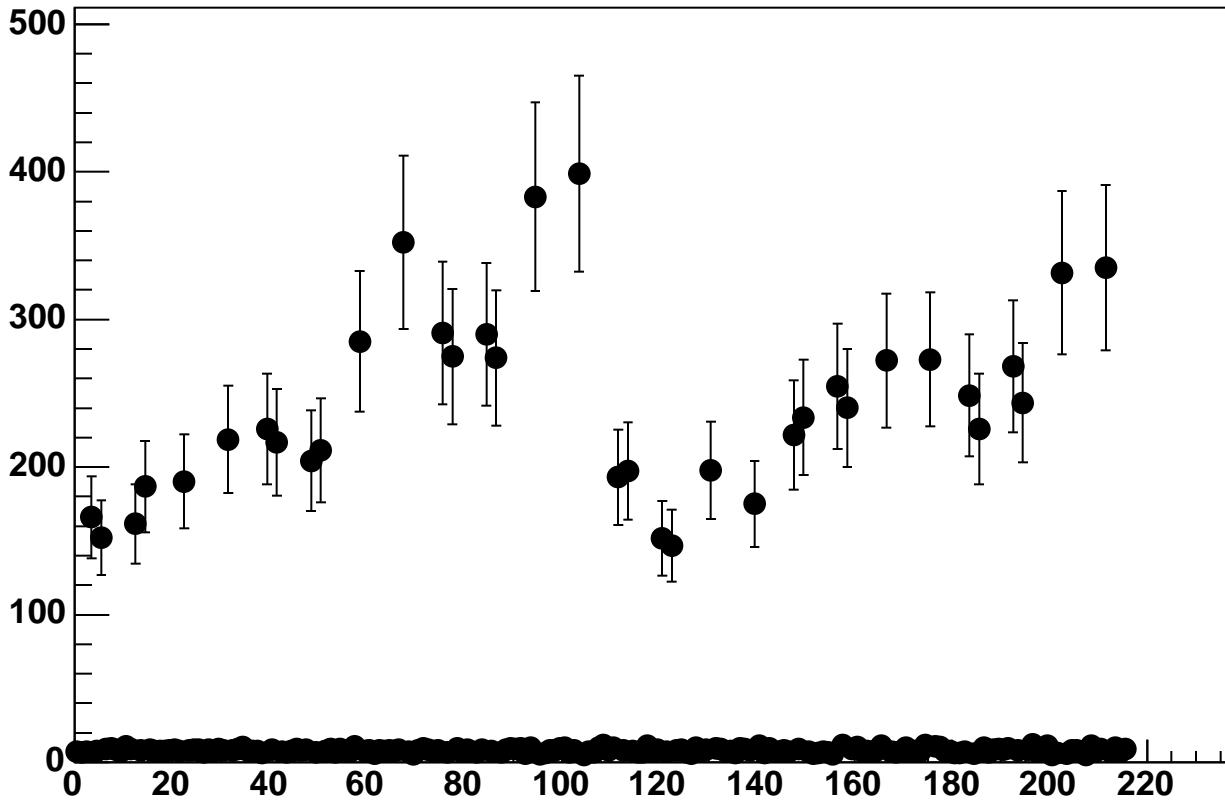
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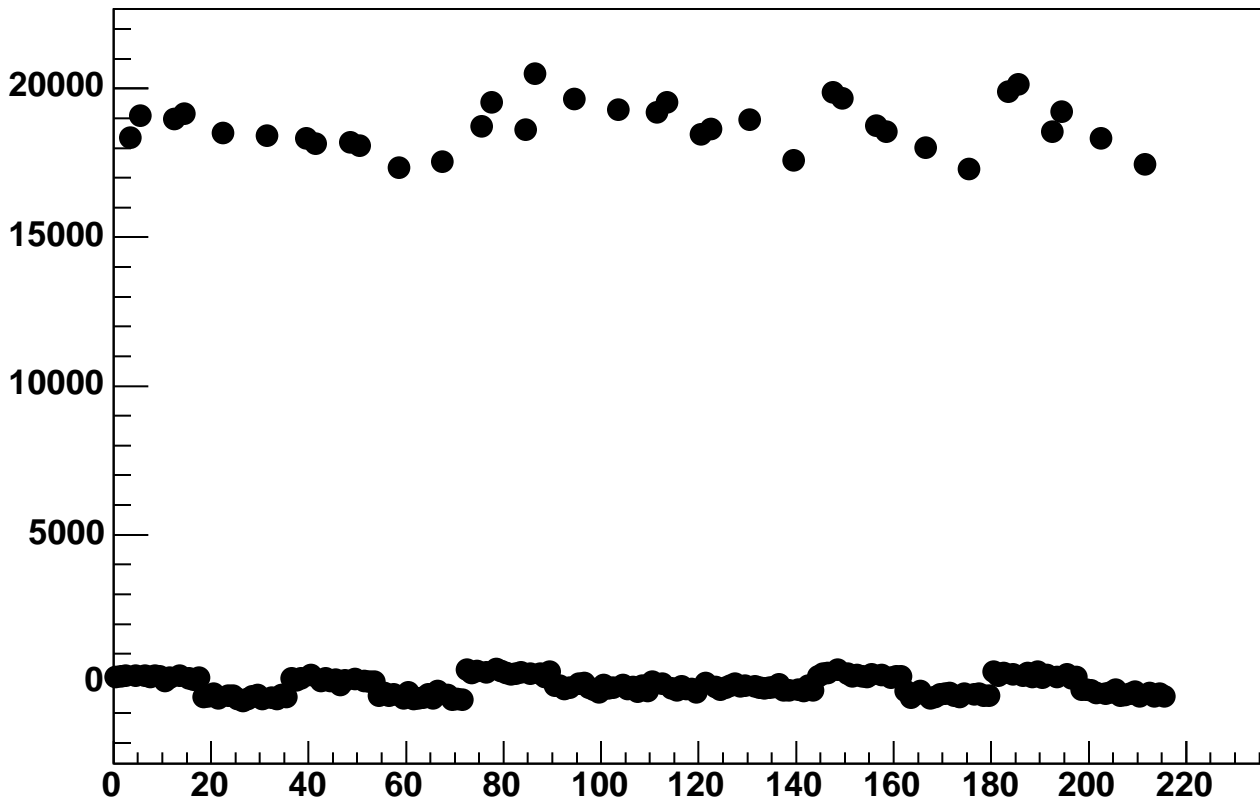
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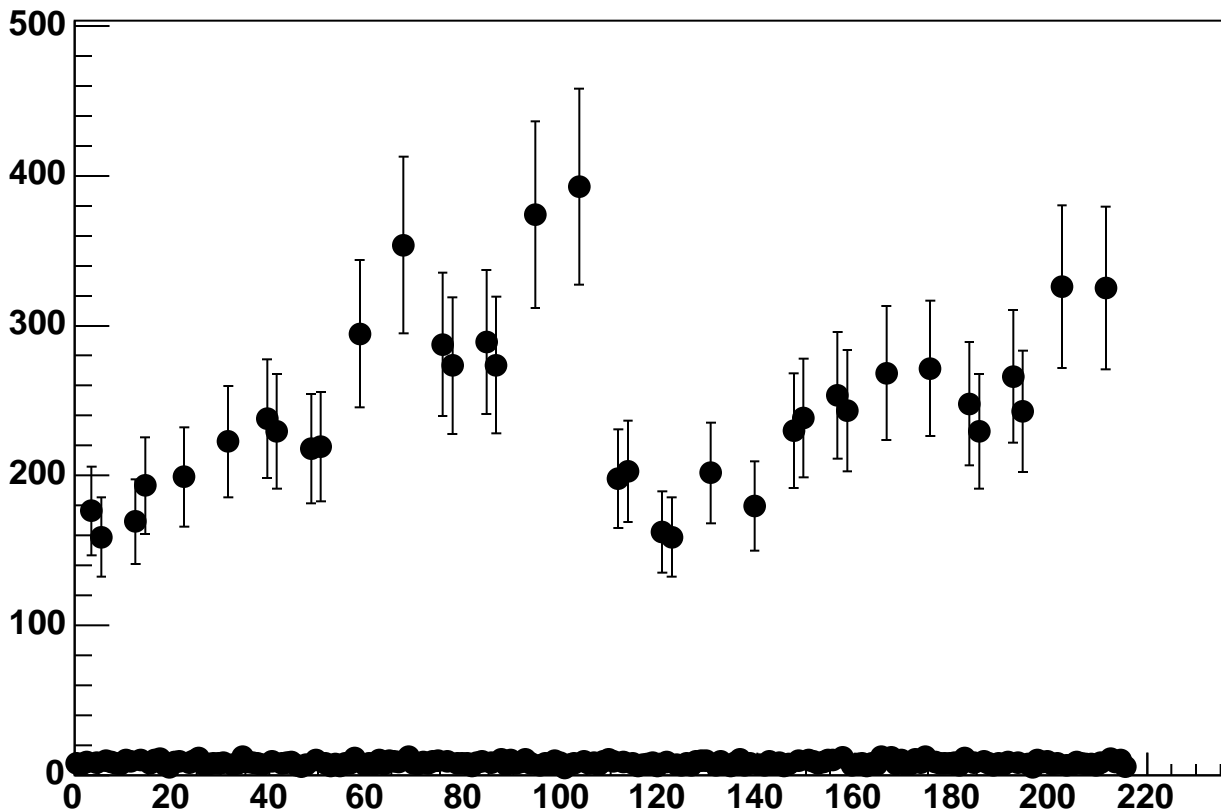
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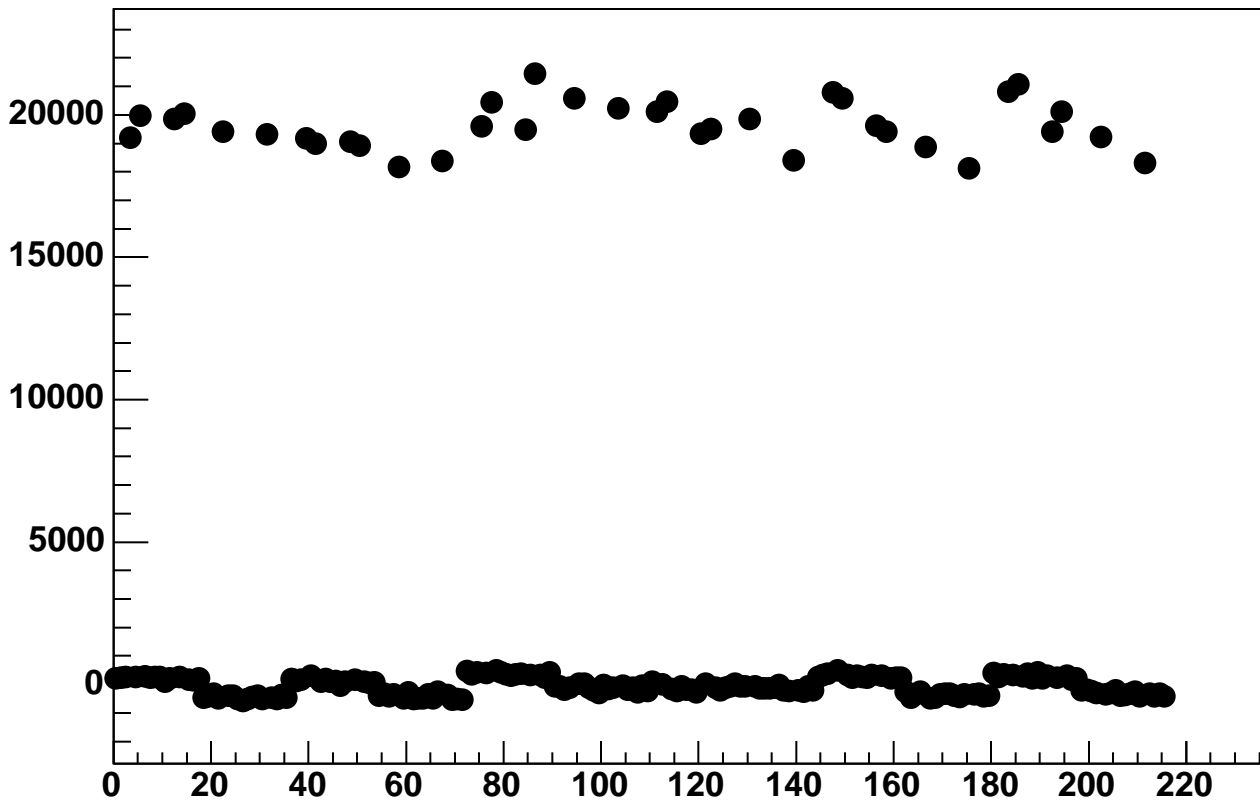
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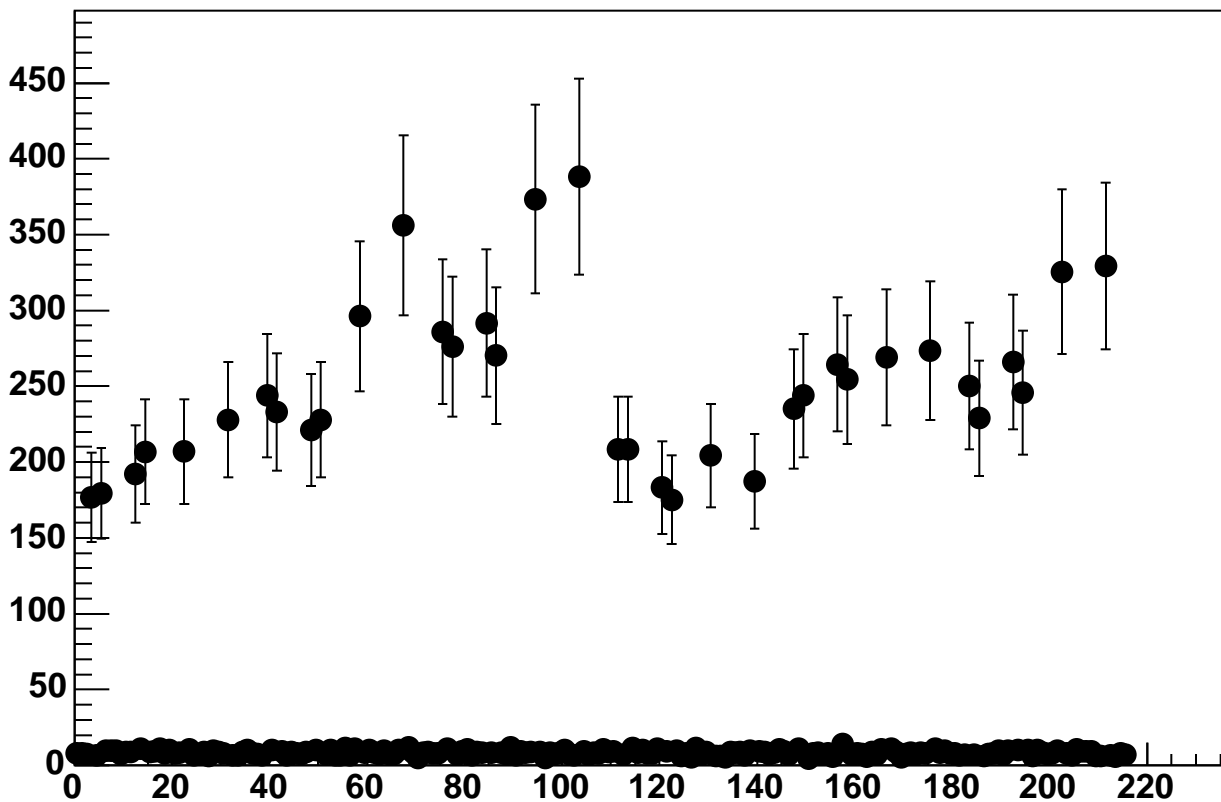
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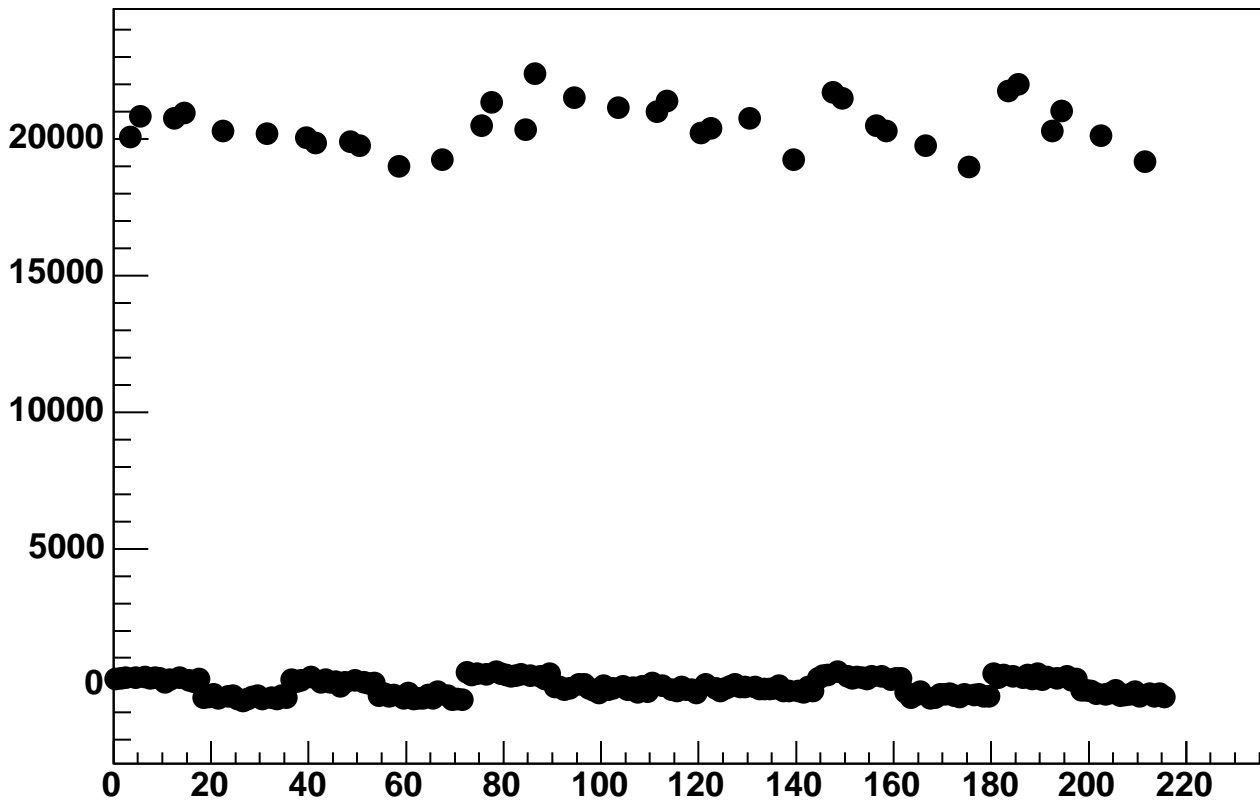
Enable 1, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



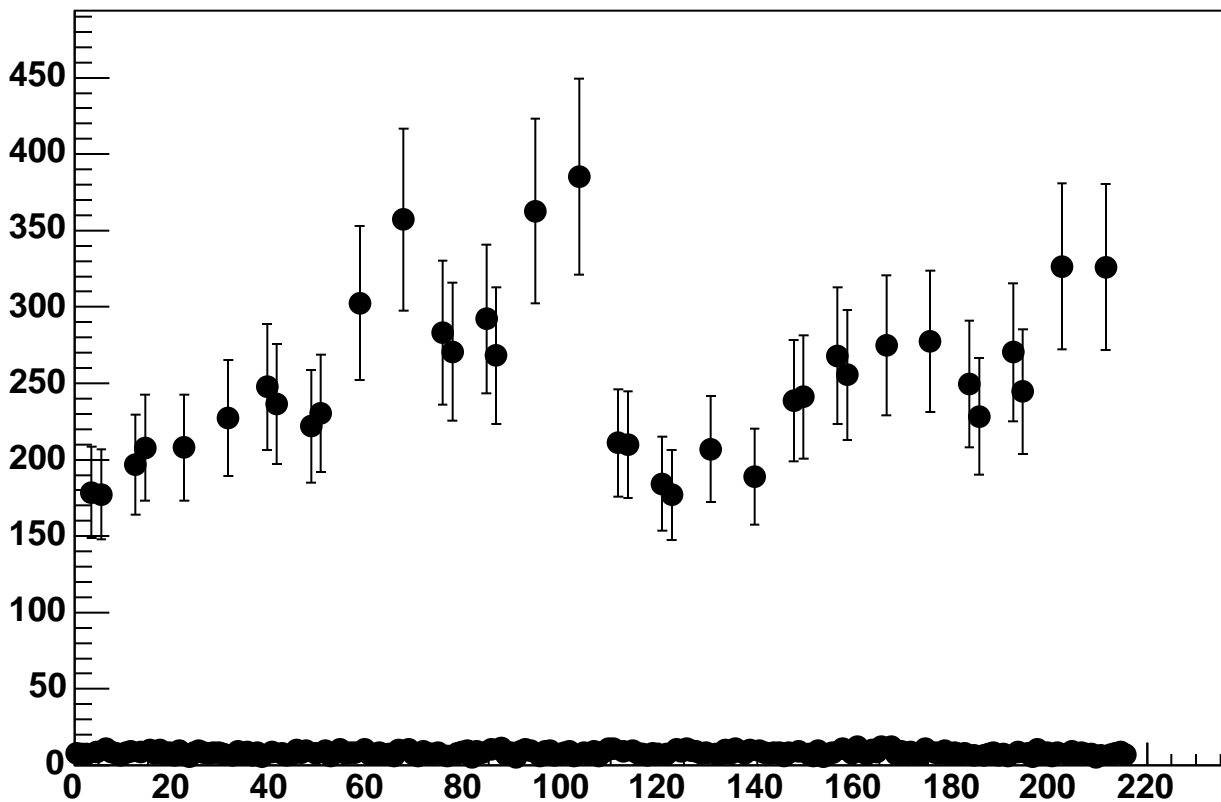
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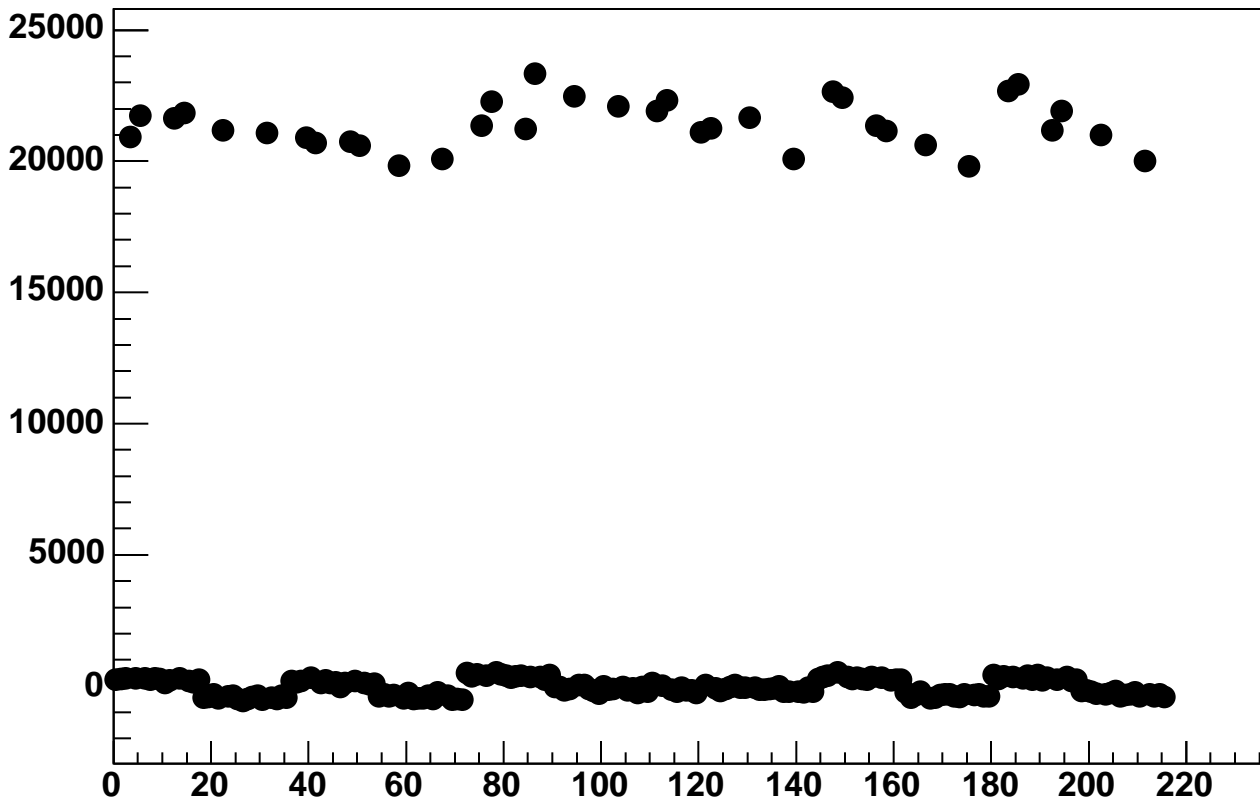
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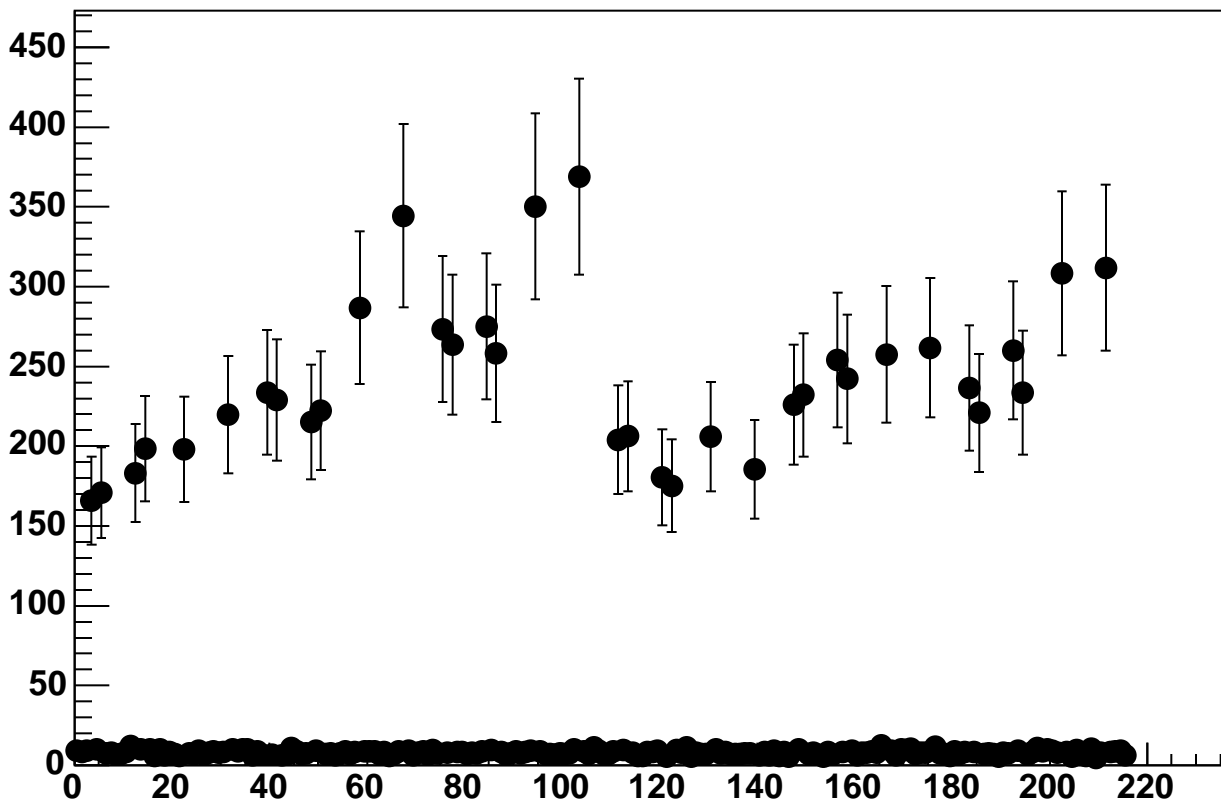
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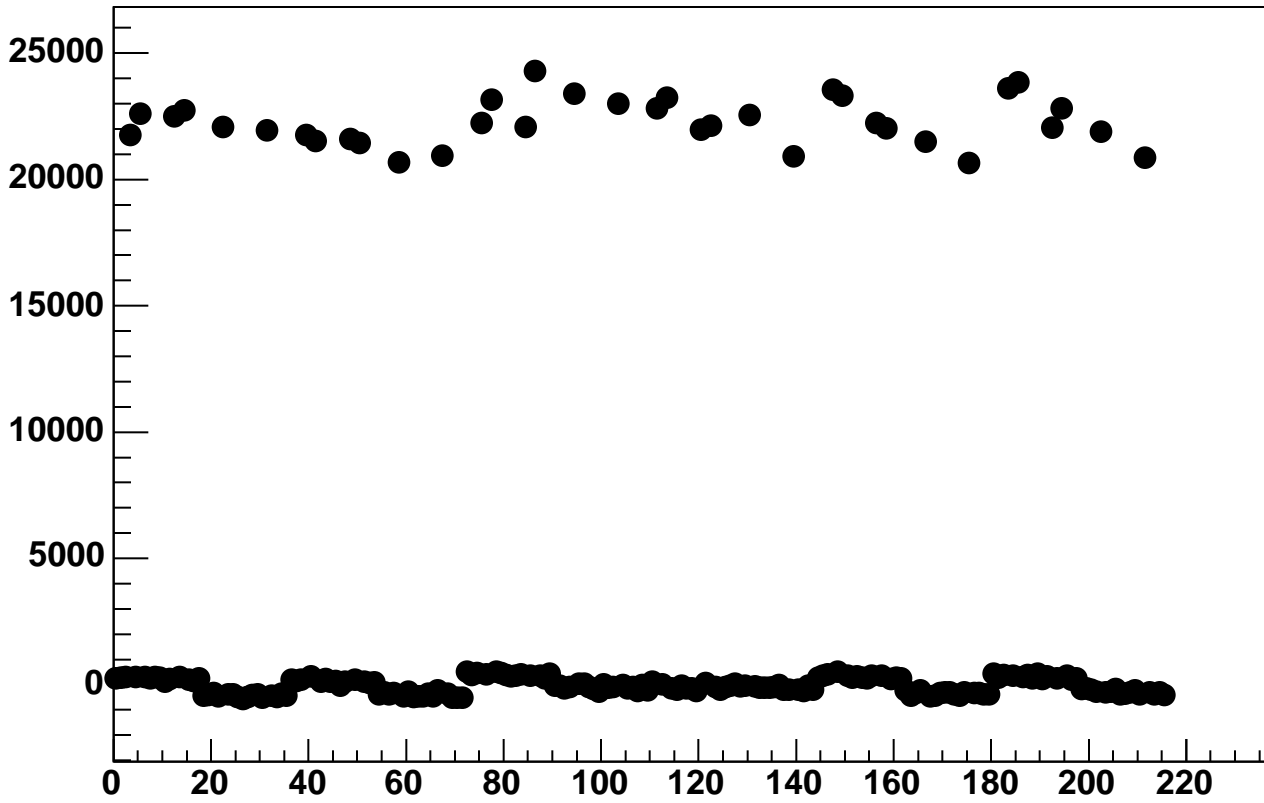
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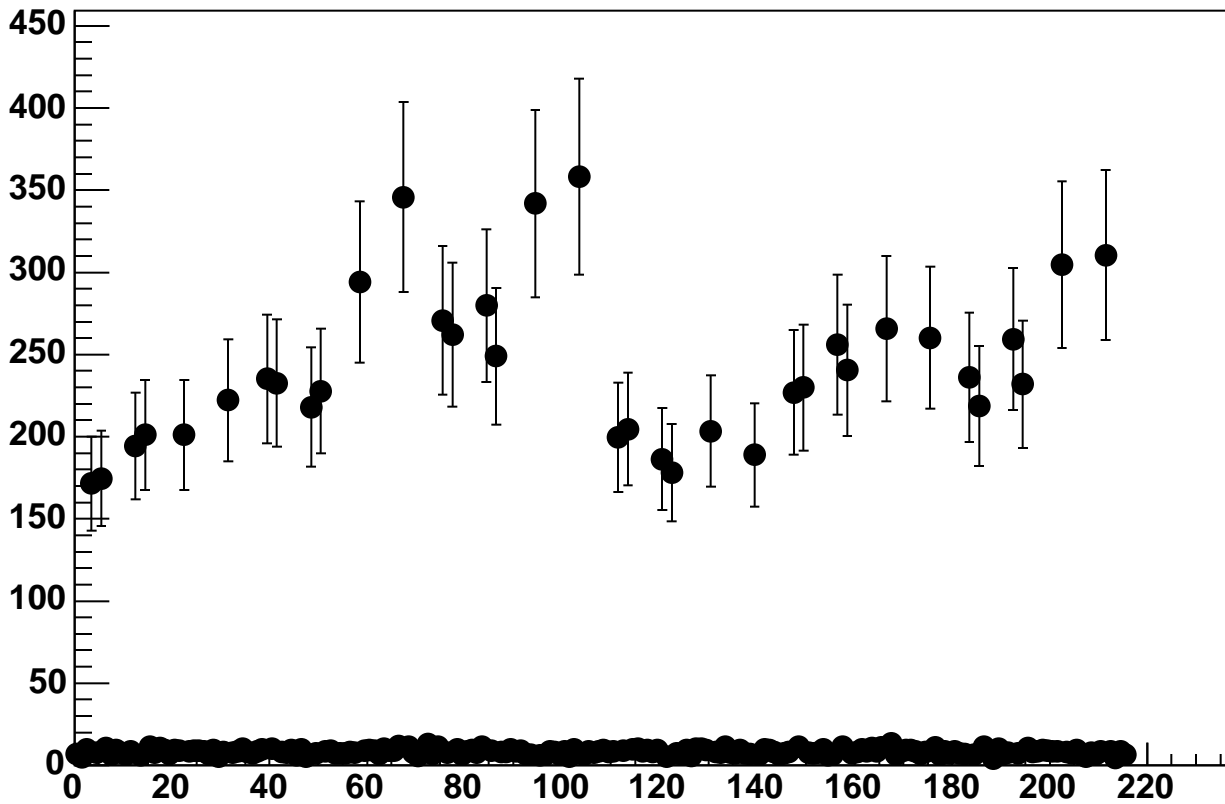
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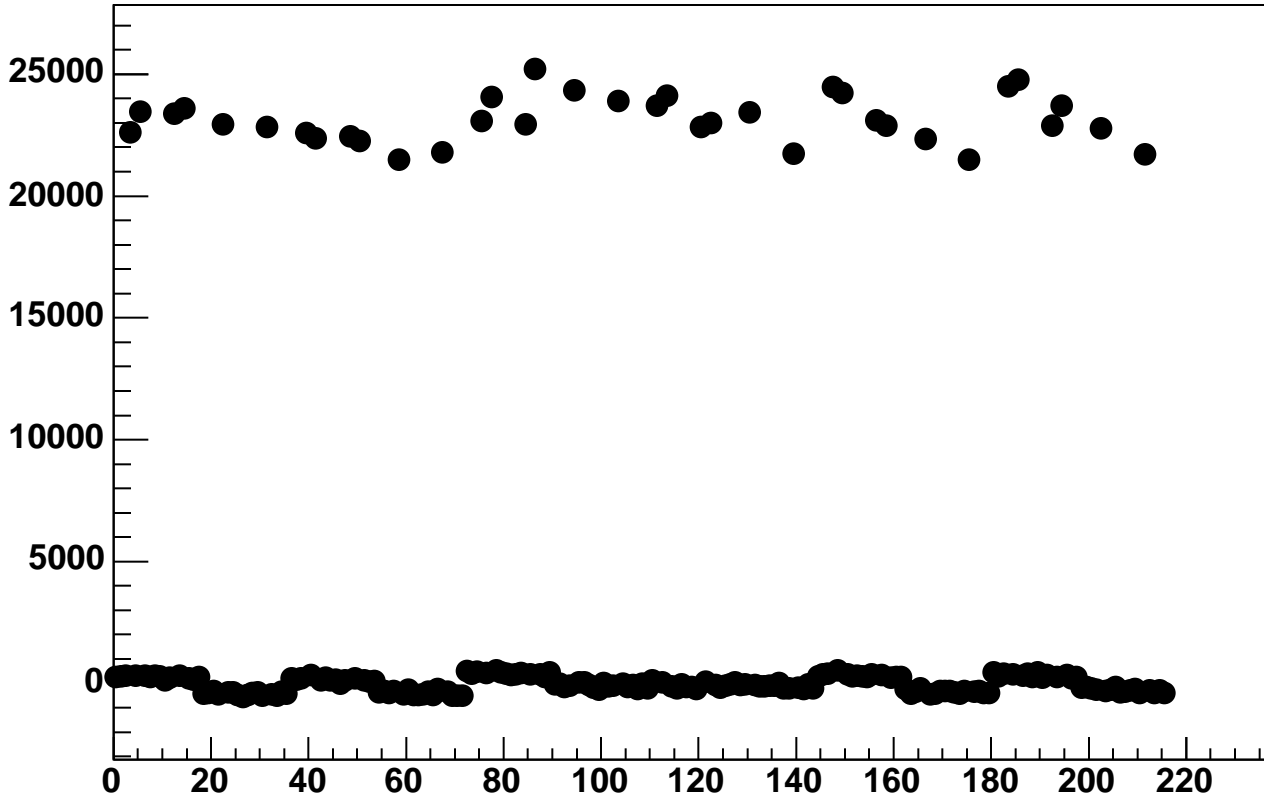
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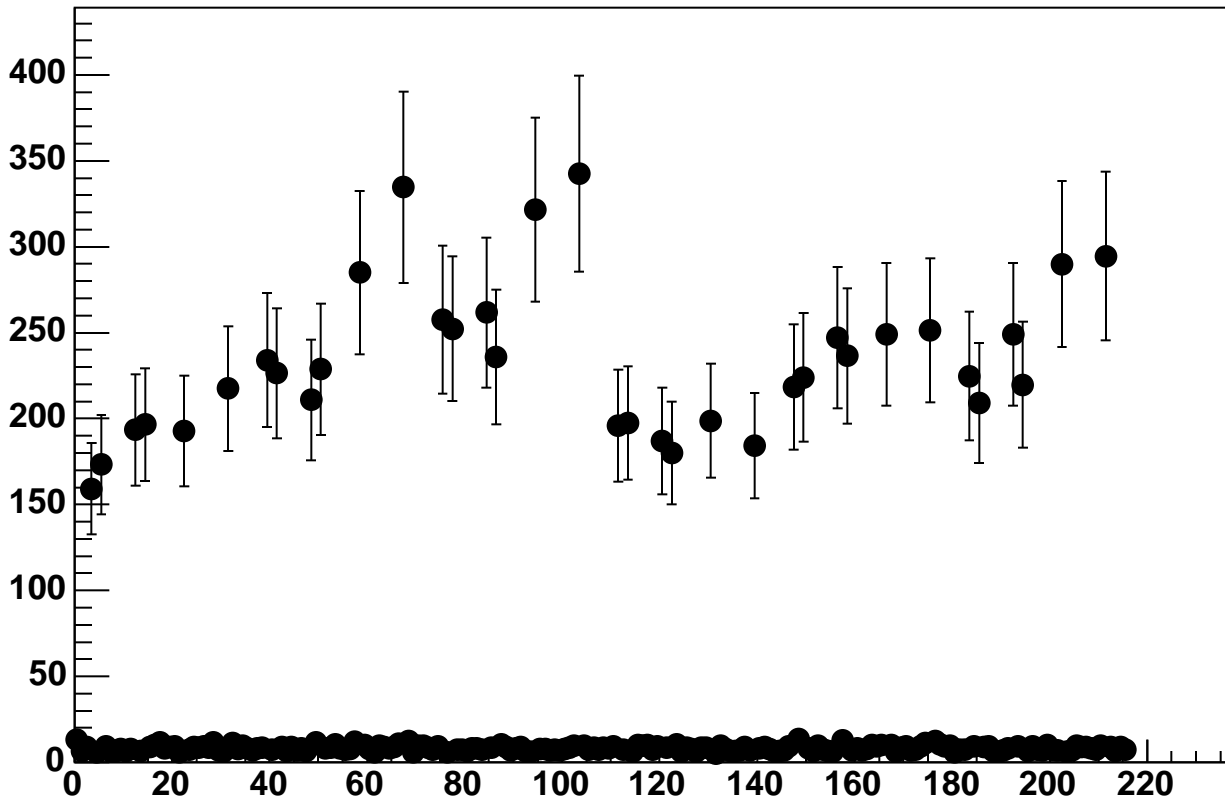
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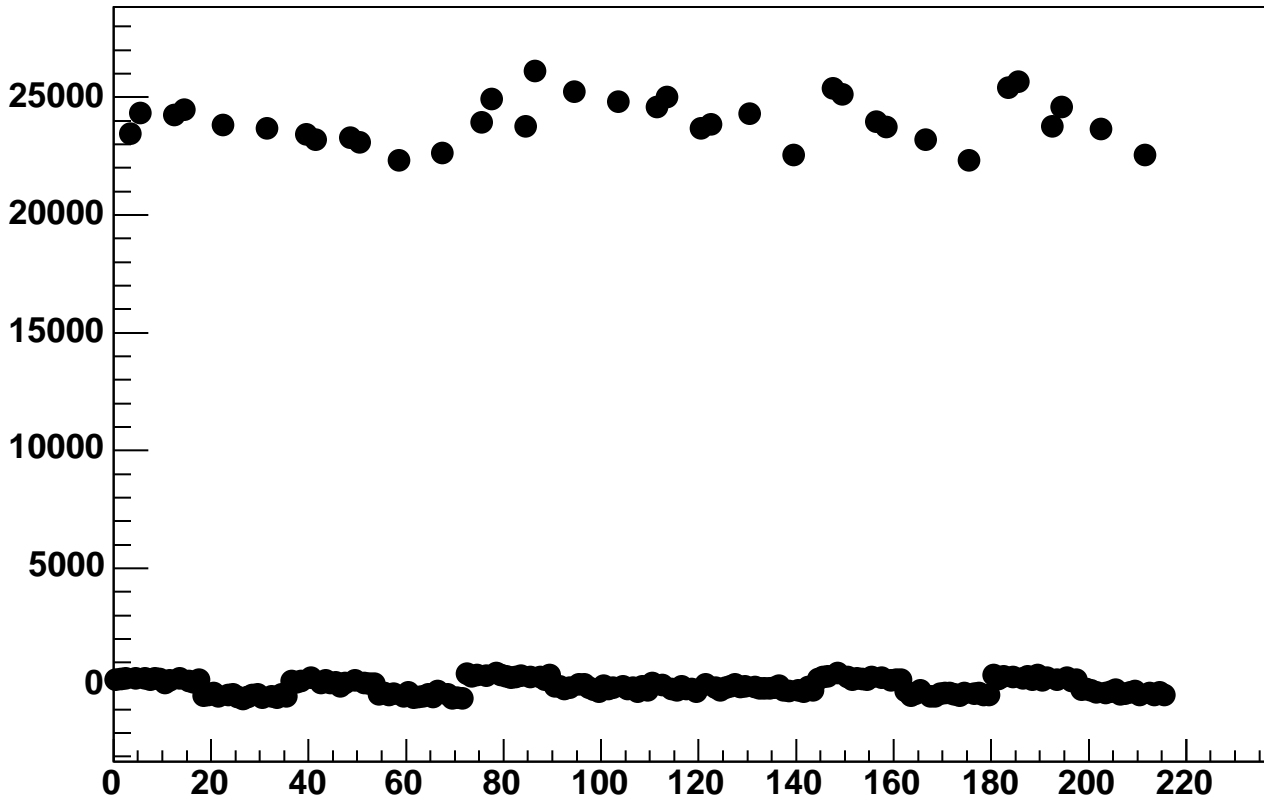
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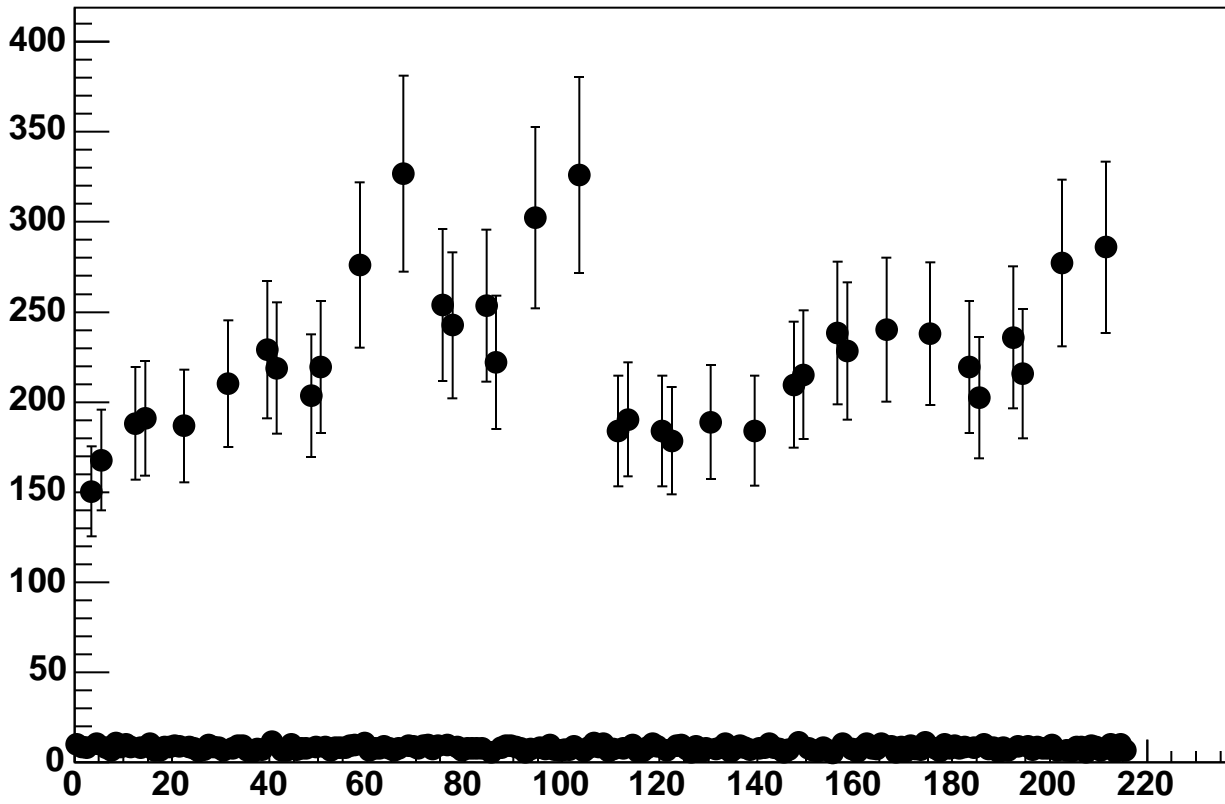
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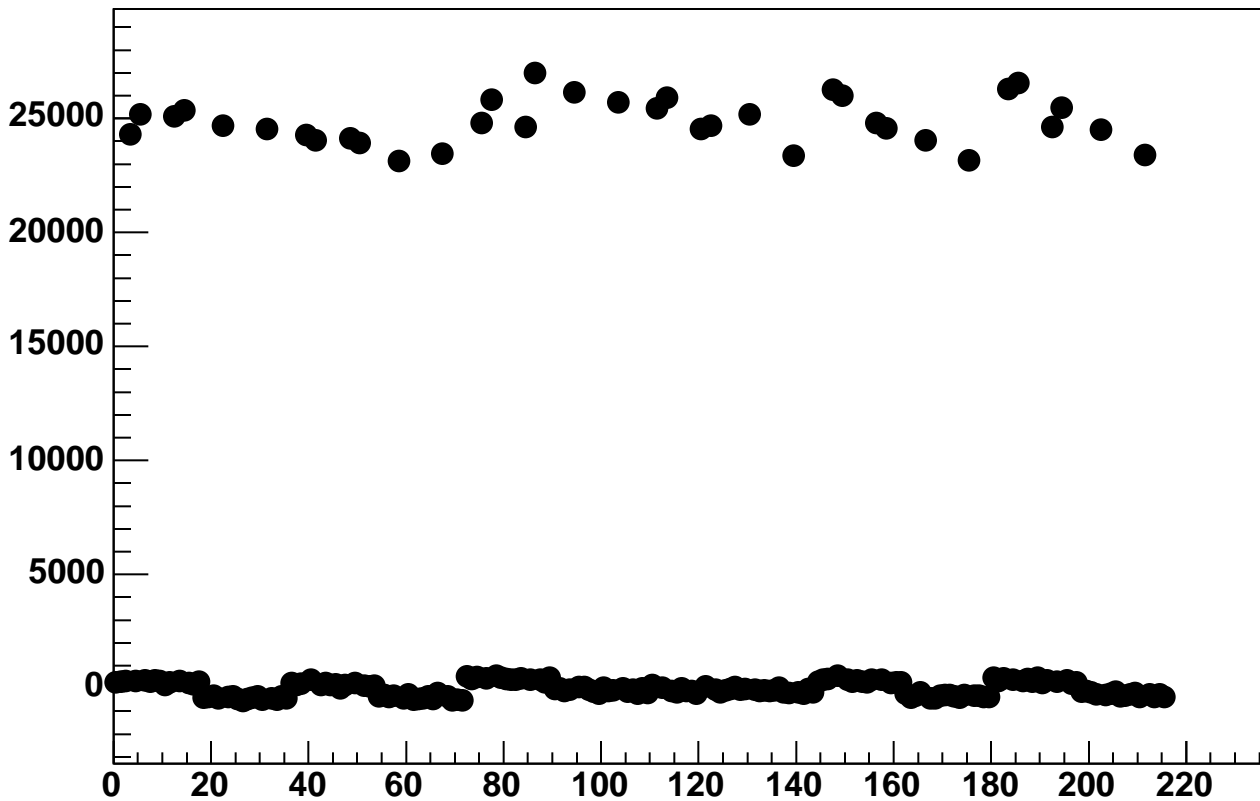
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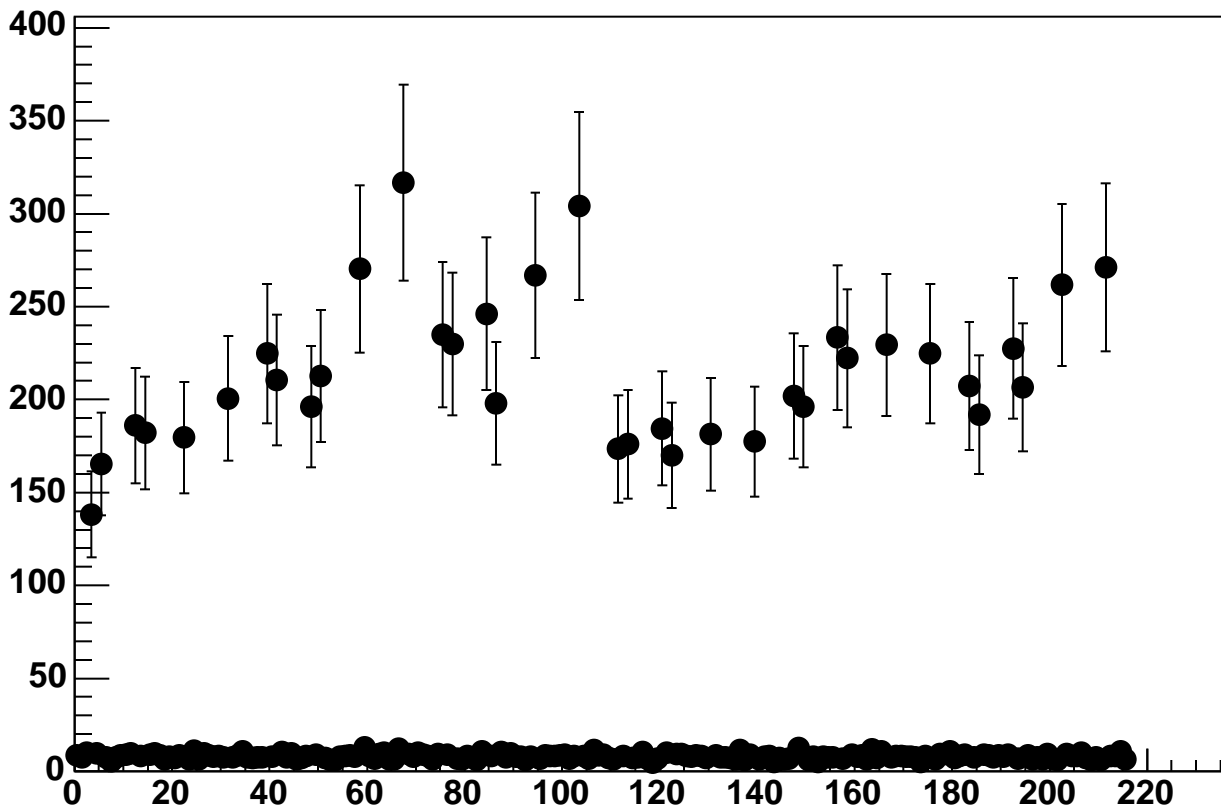
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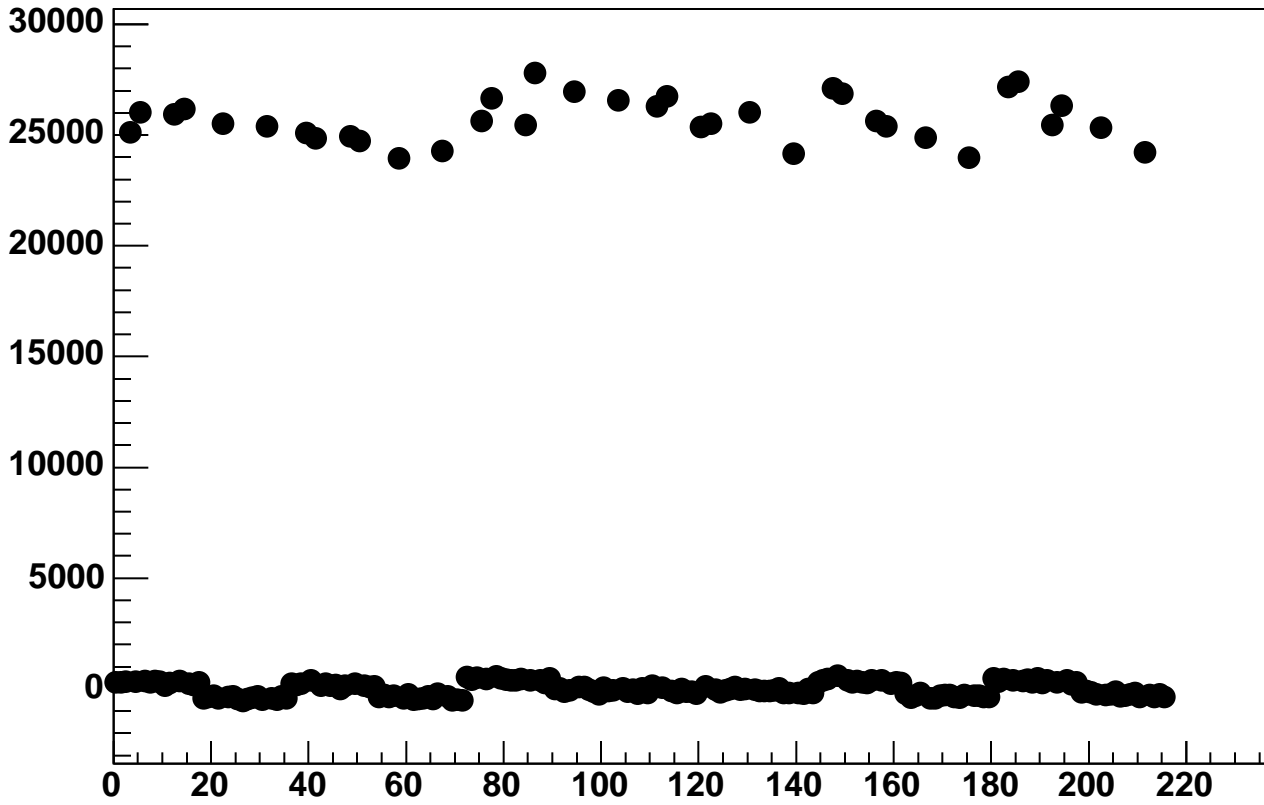
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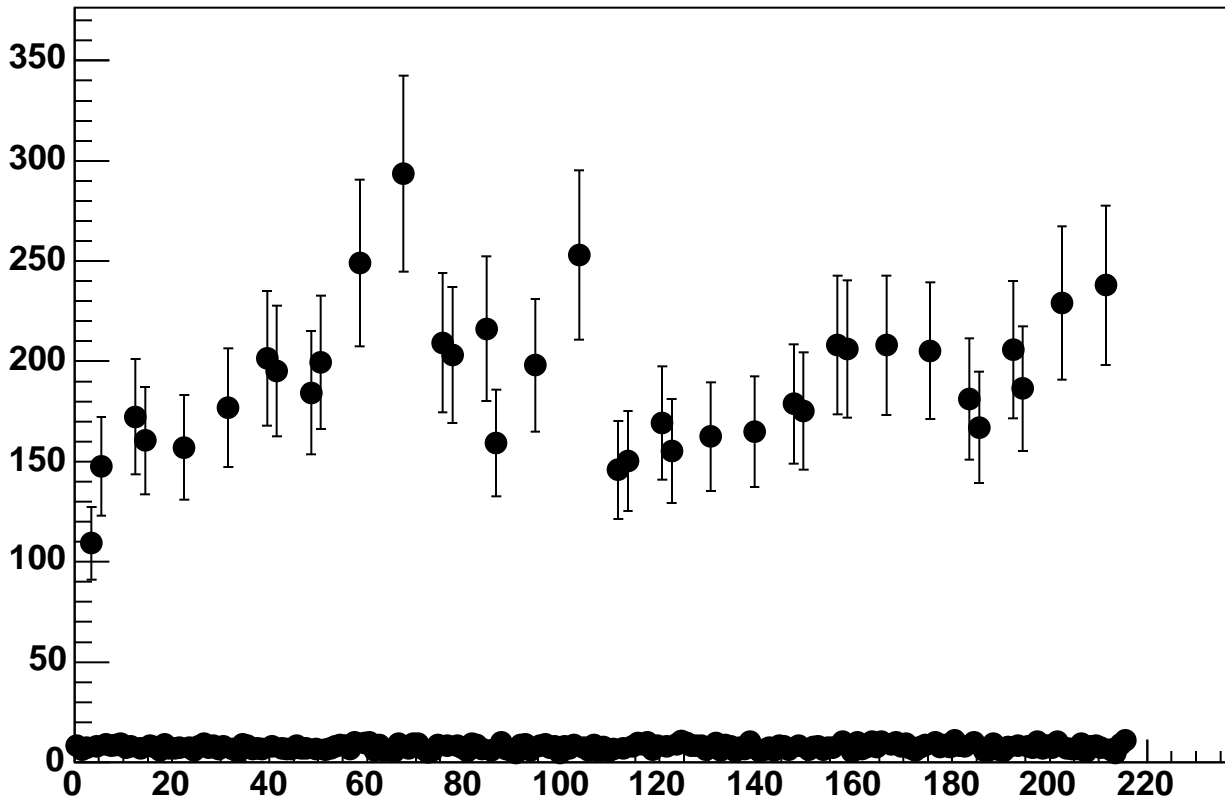
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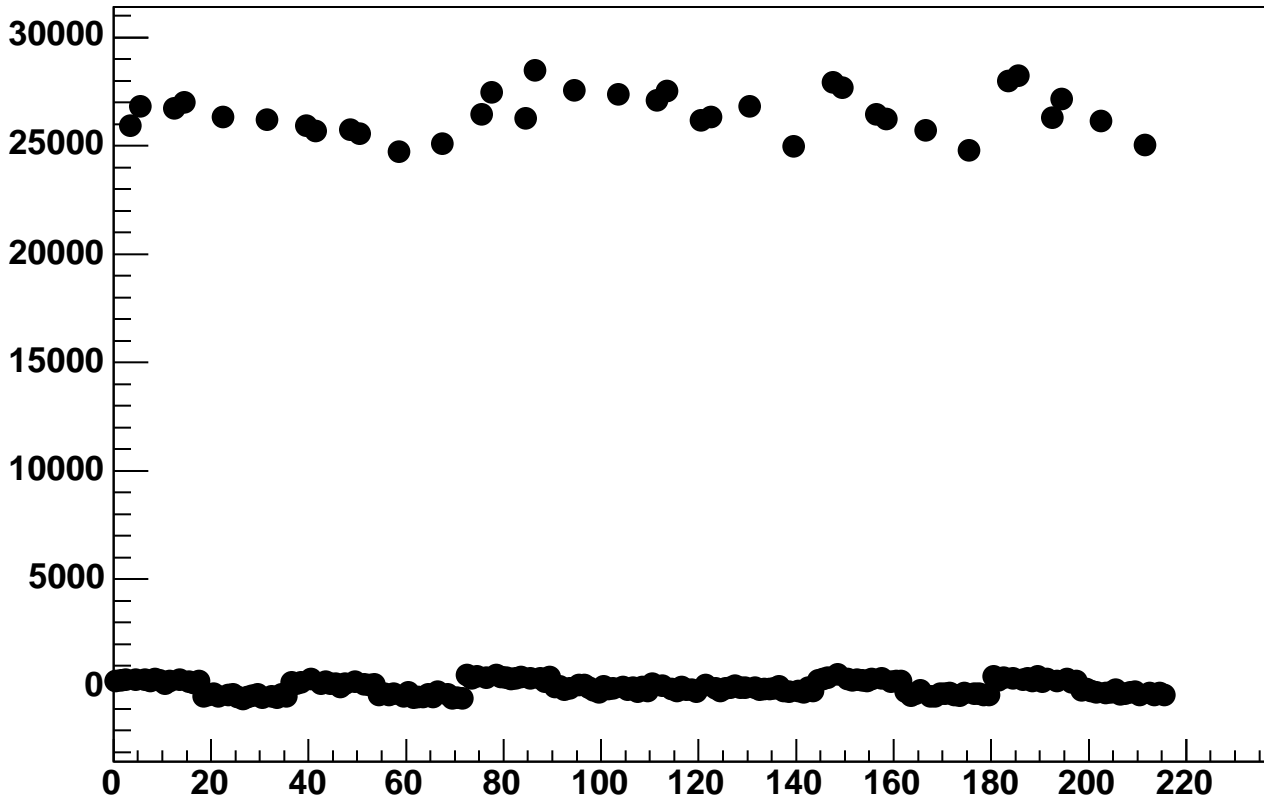
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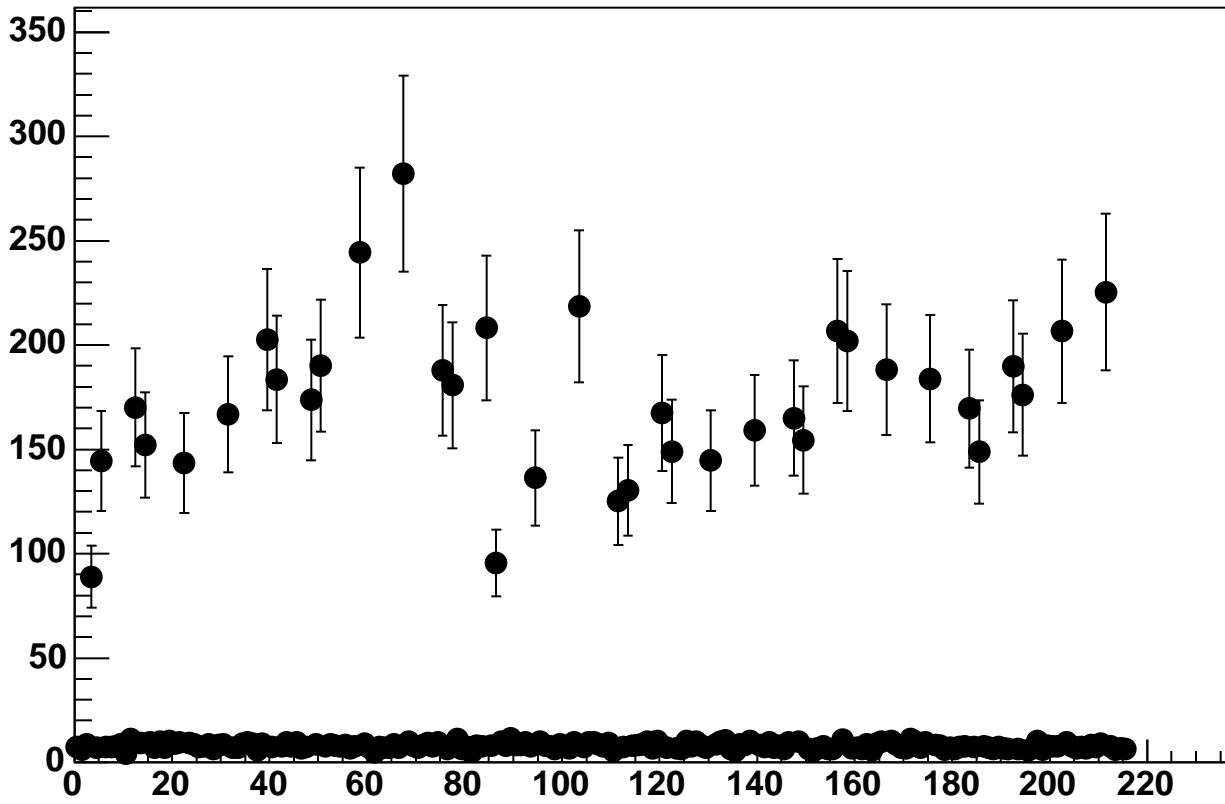
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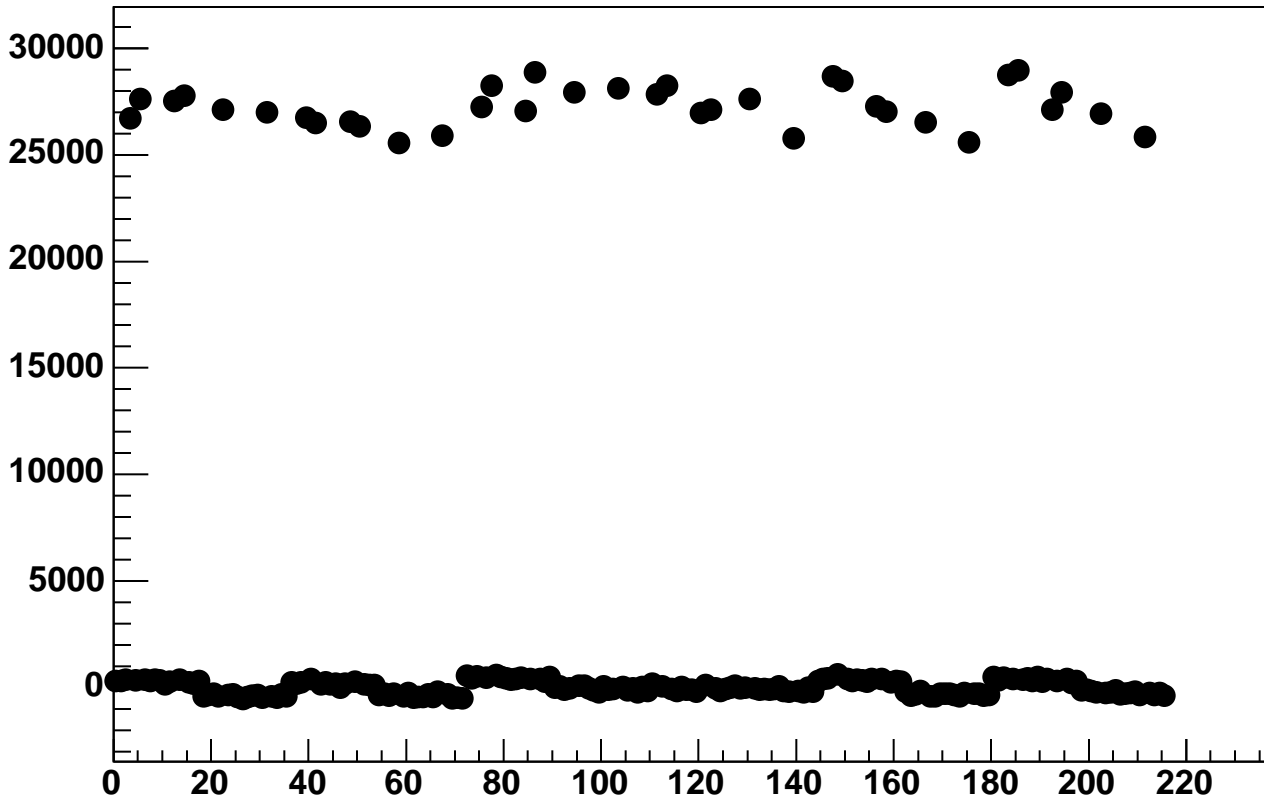
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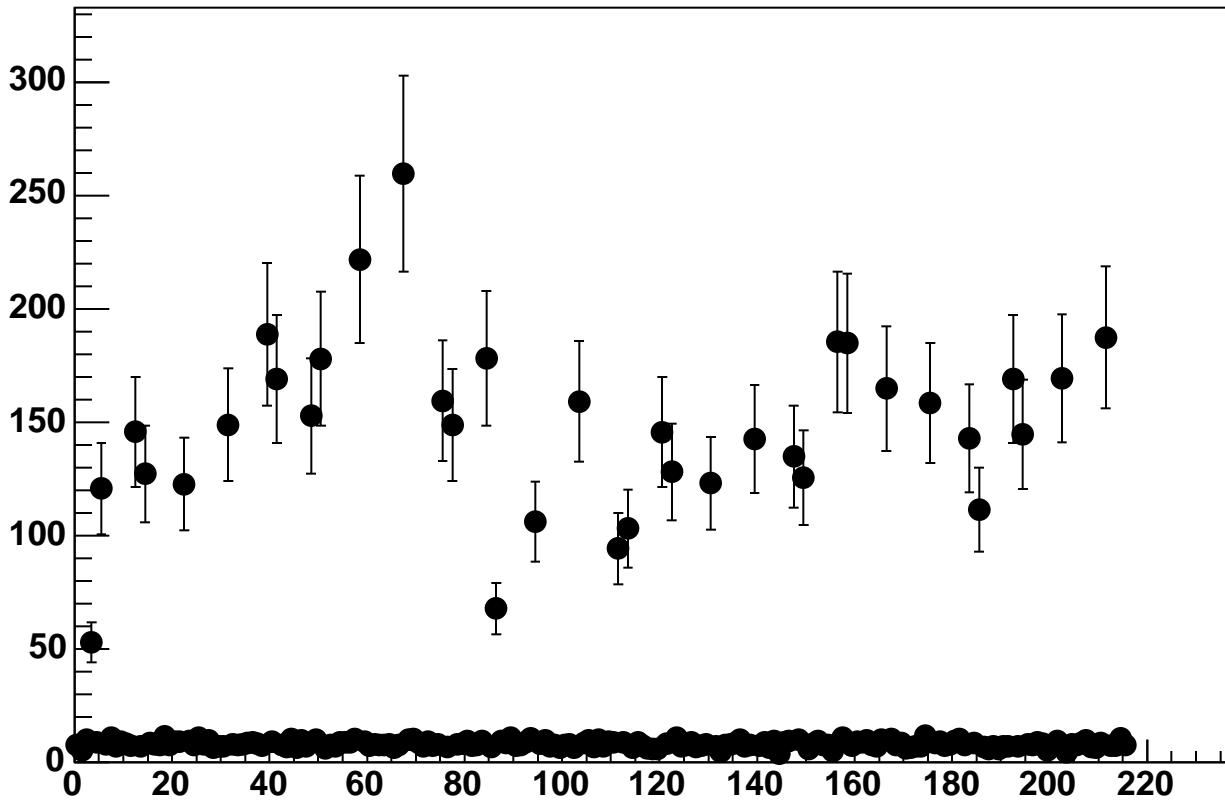
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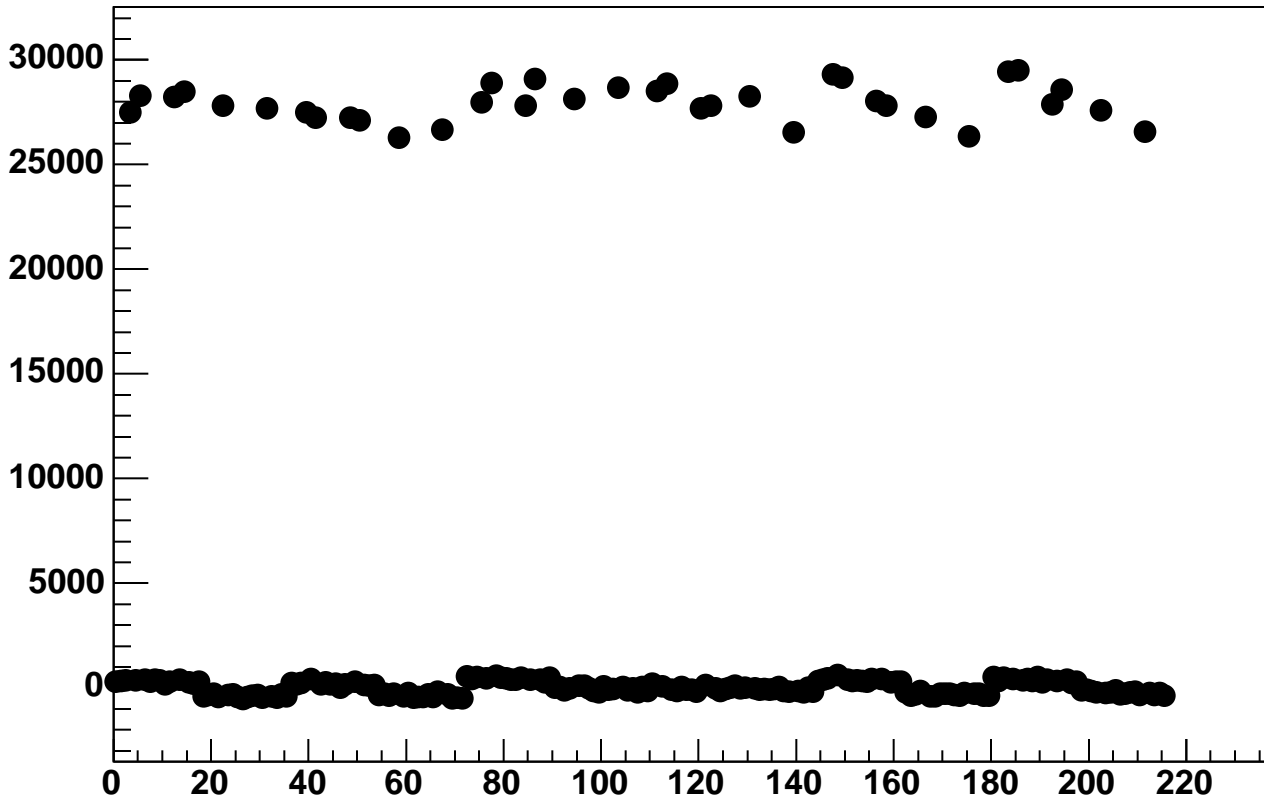
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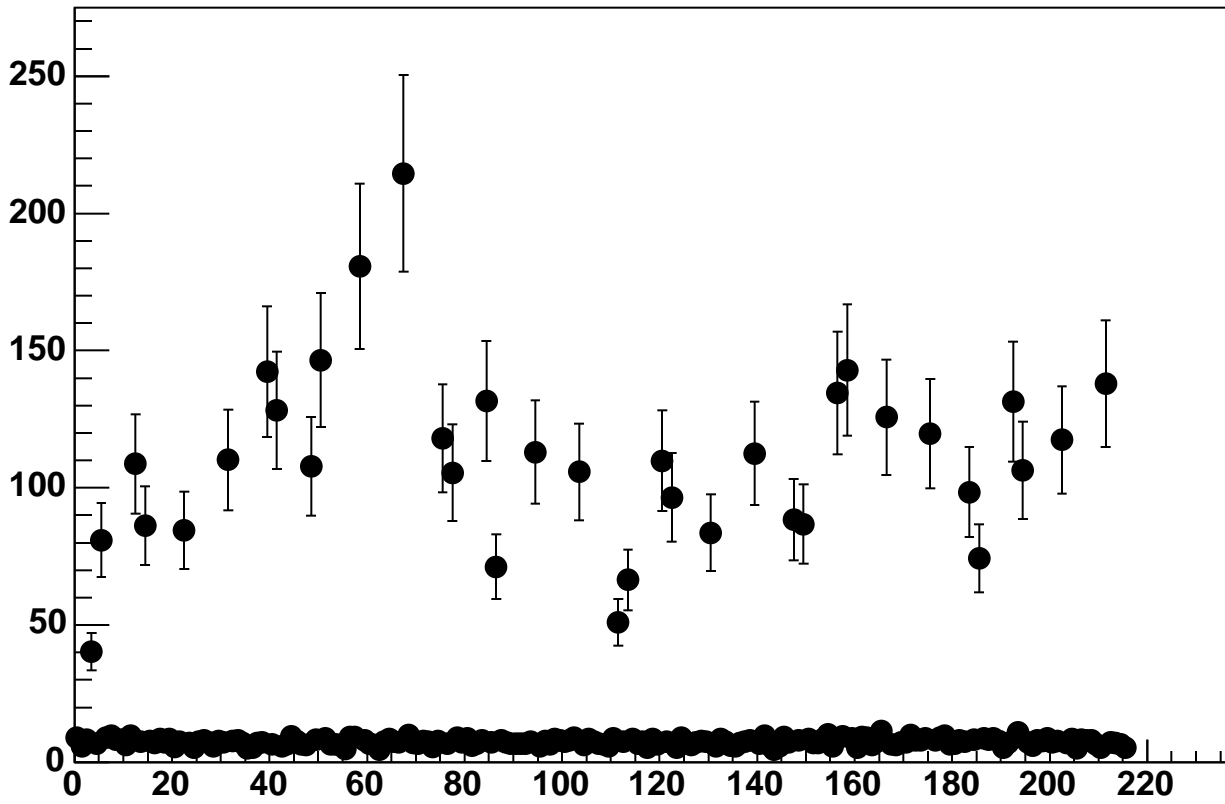
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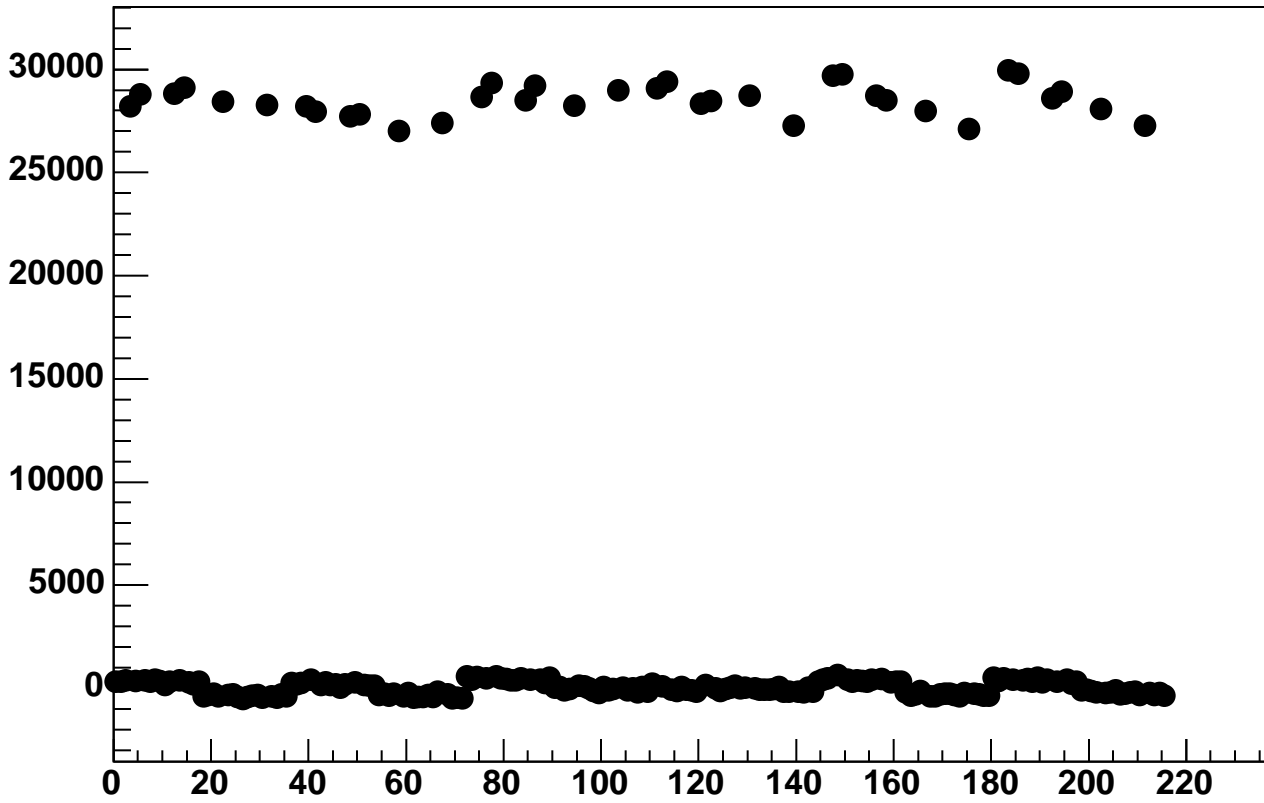
Enable 1, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



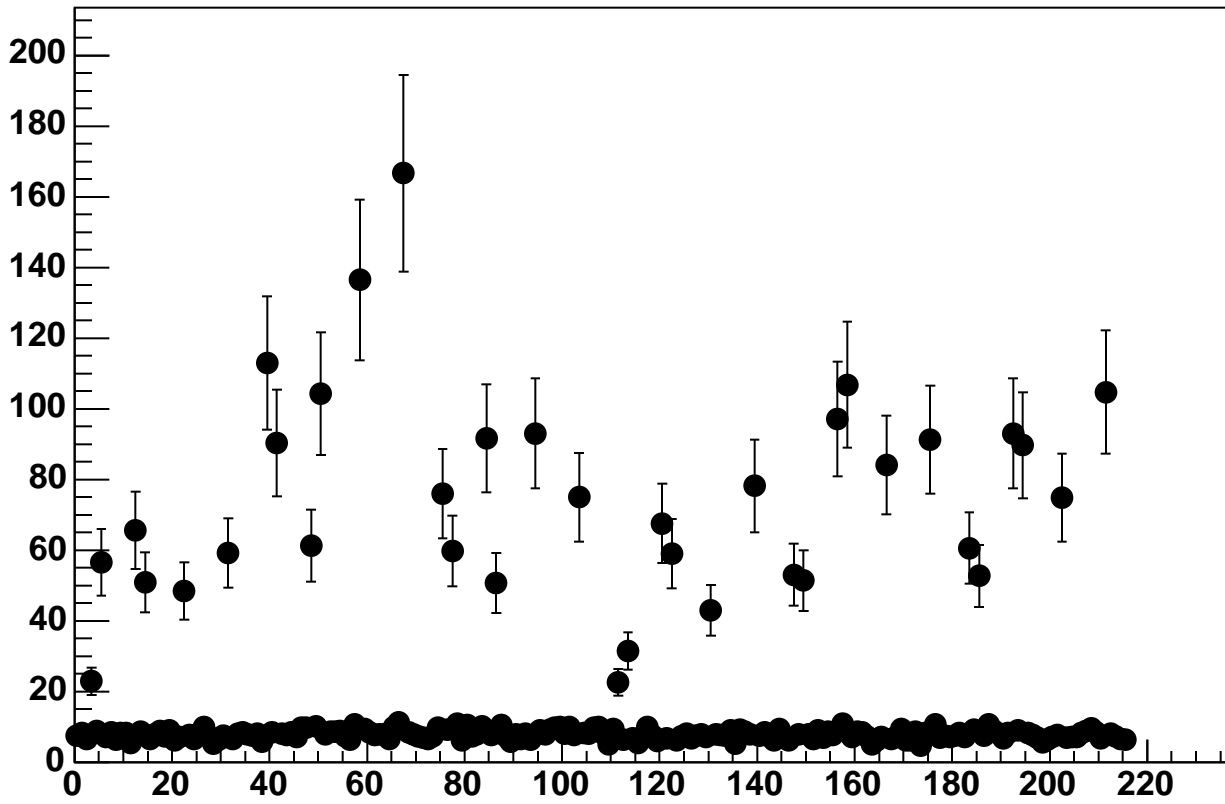
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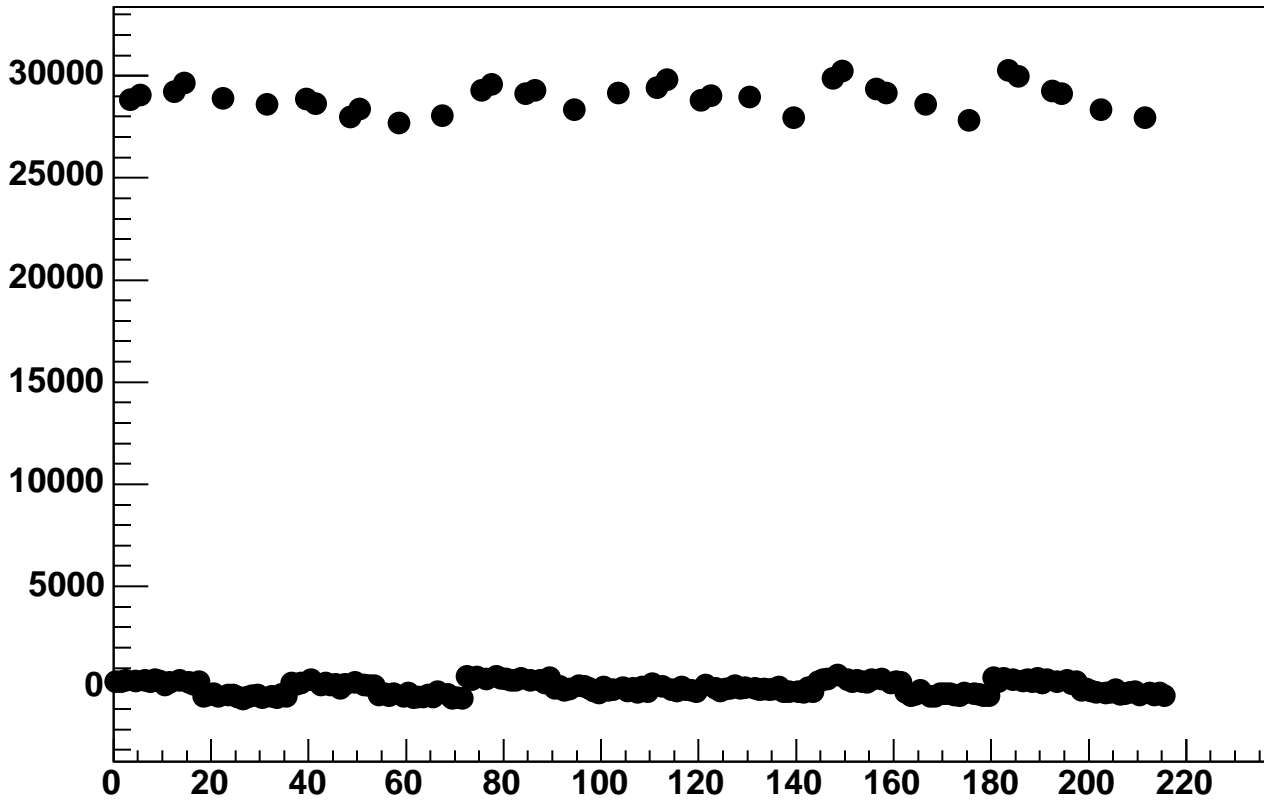
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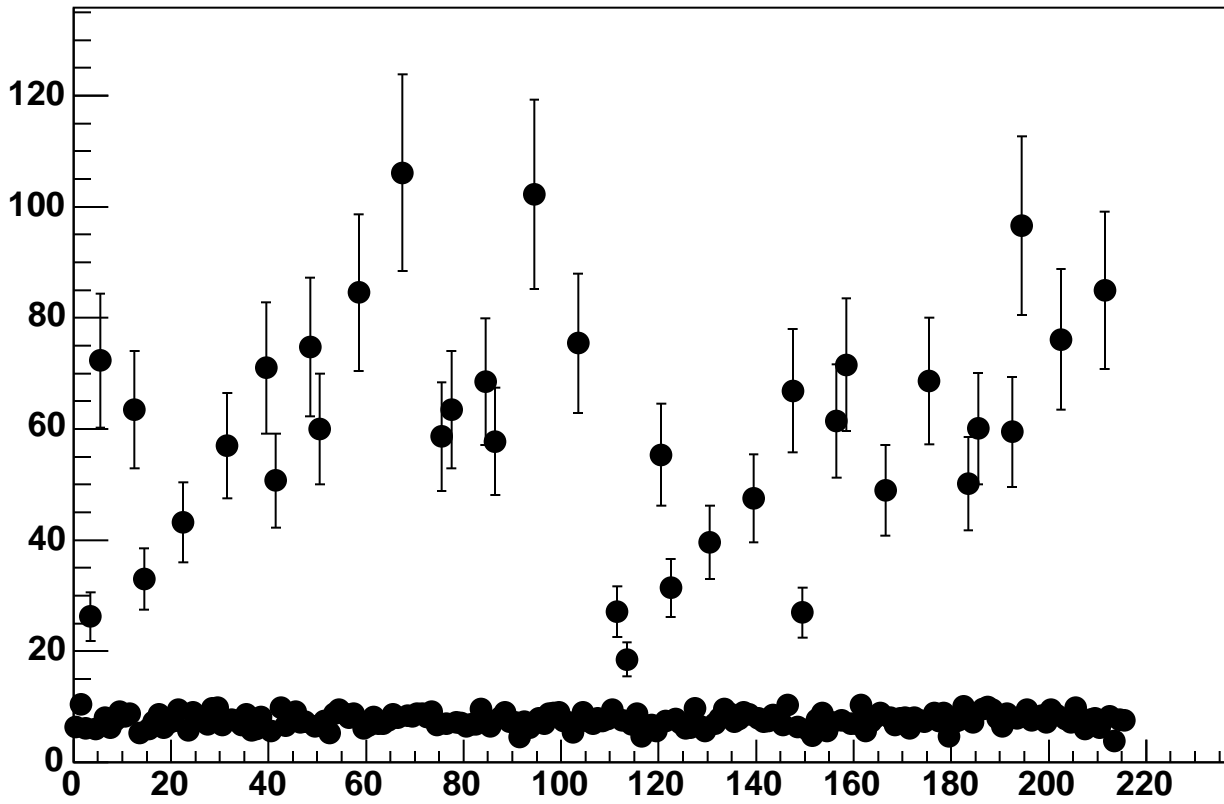
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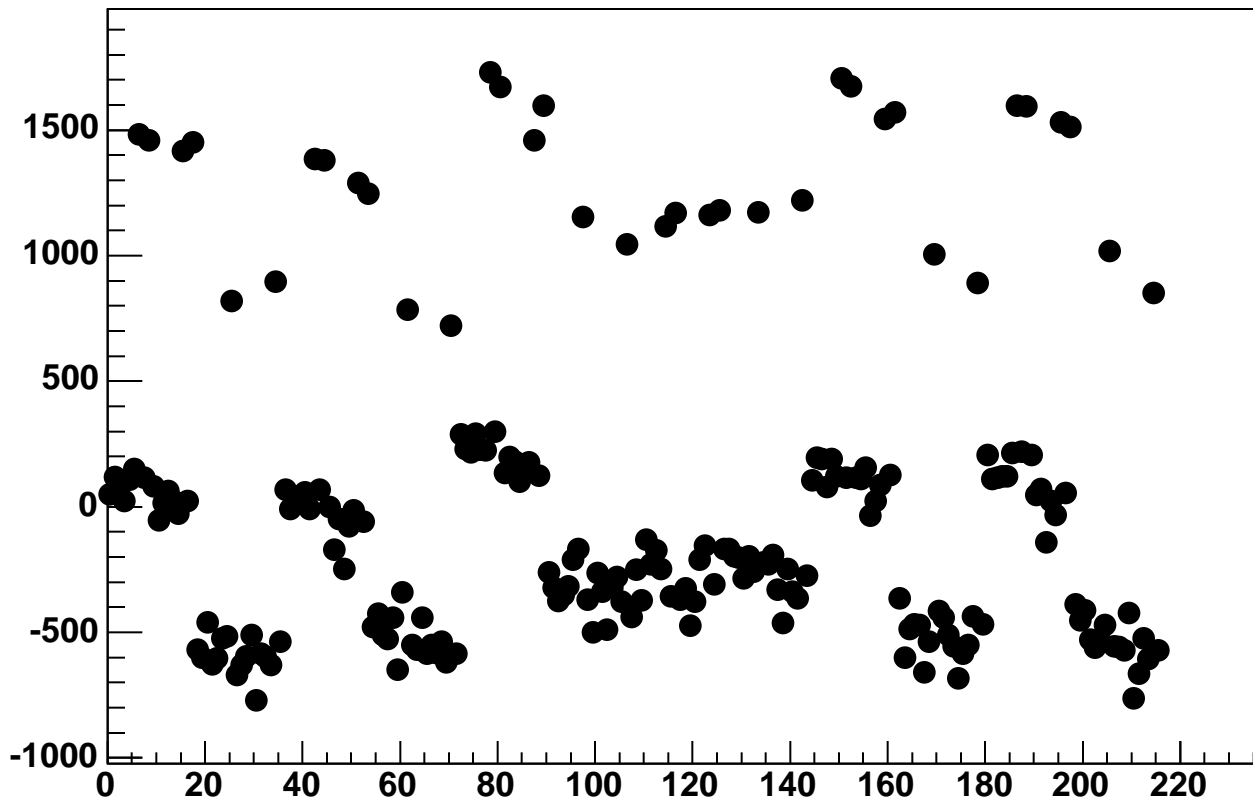
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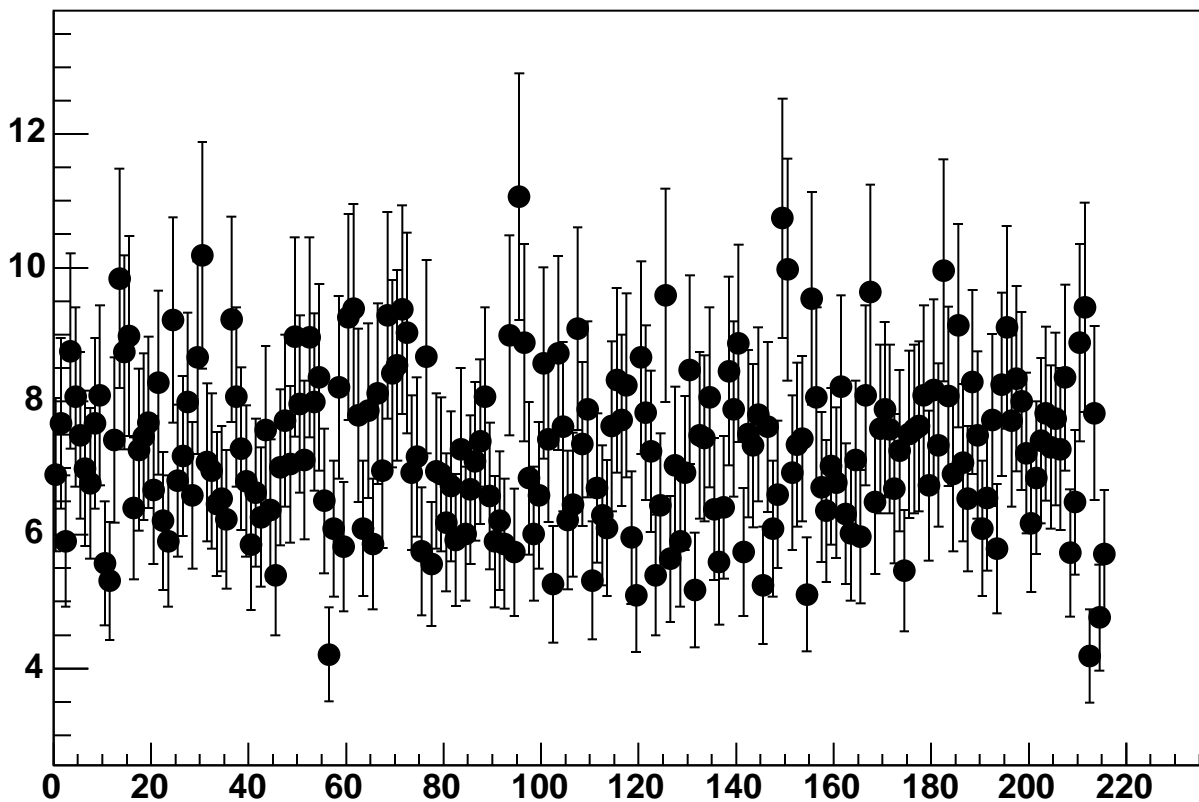
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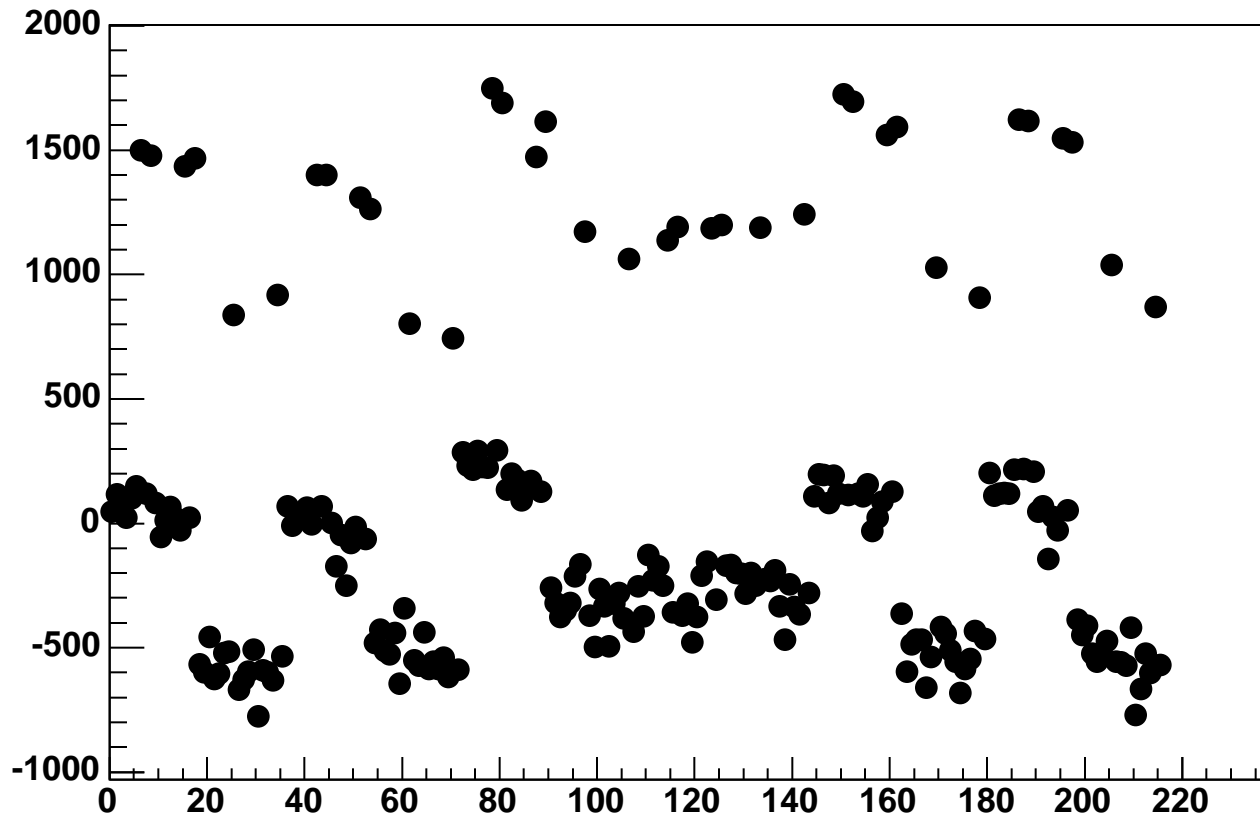
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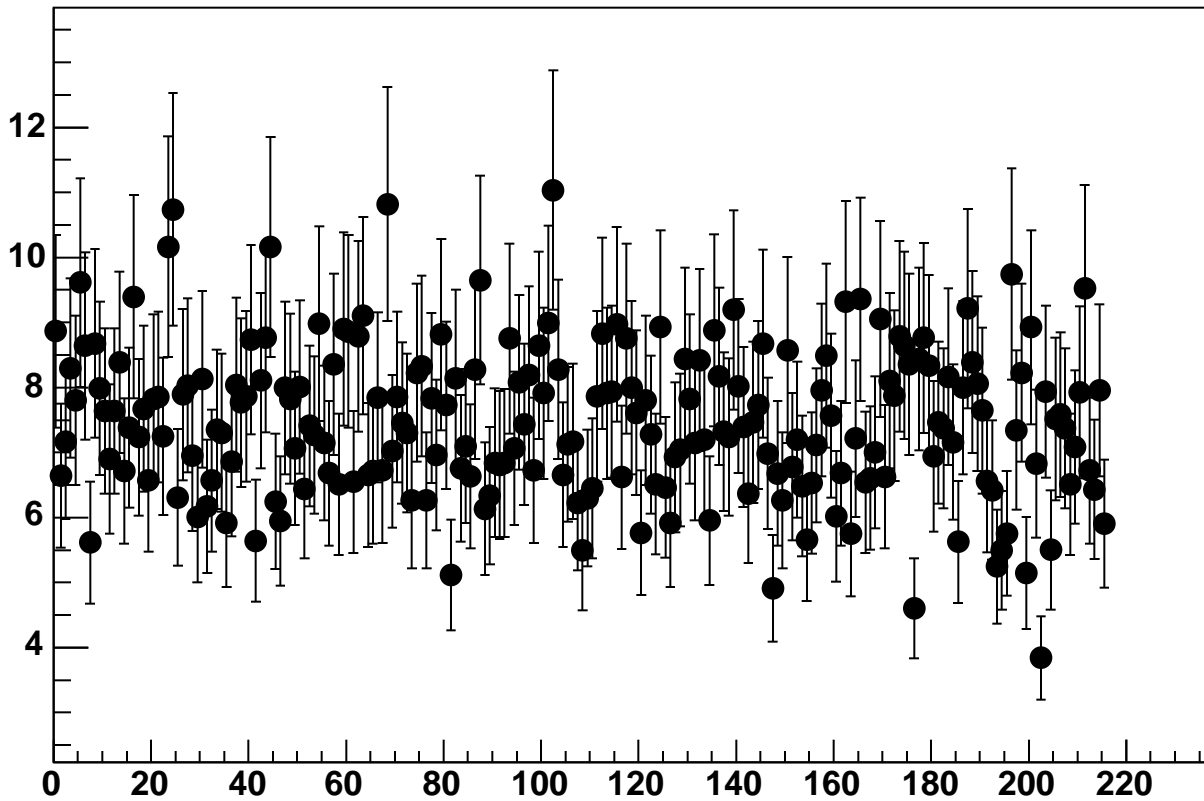
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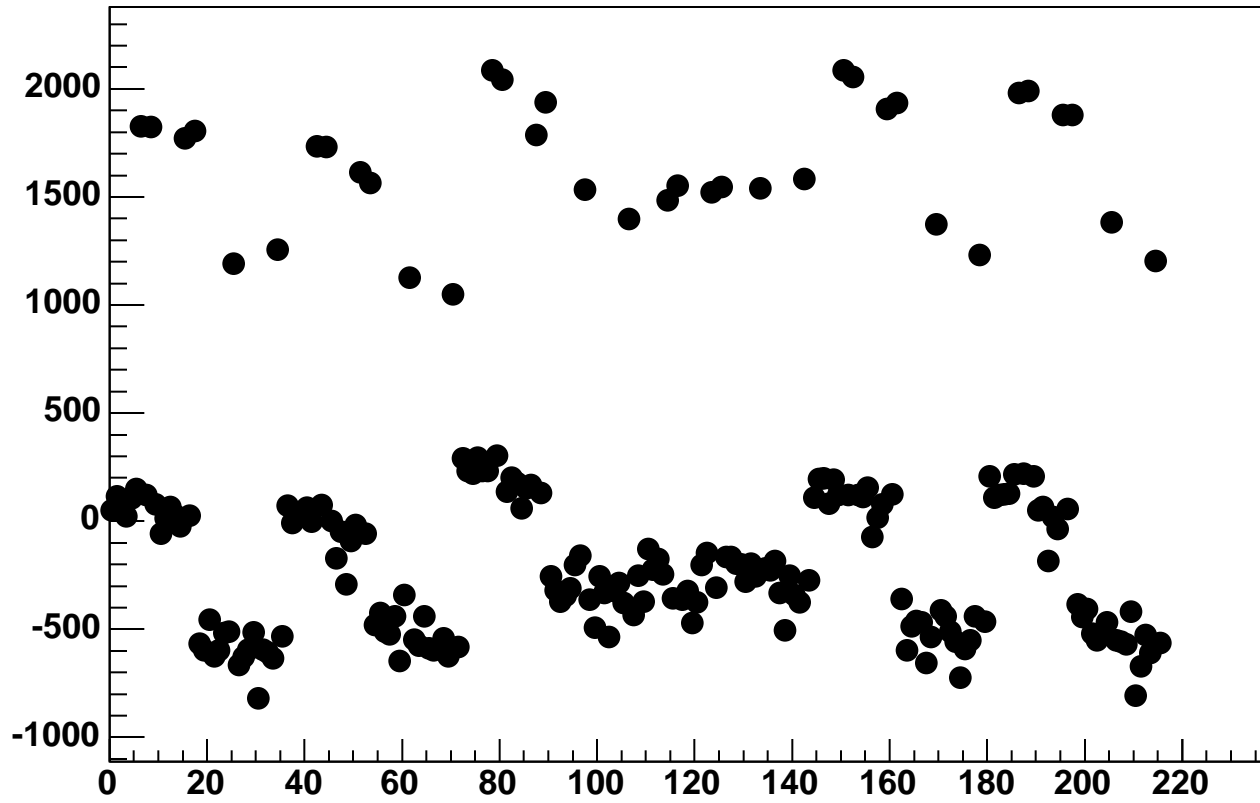
Enable 2, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



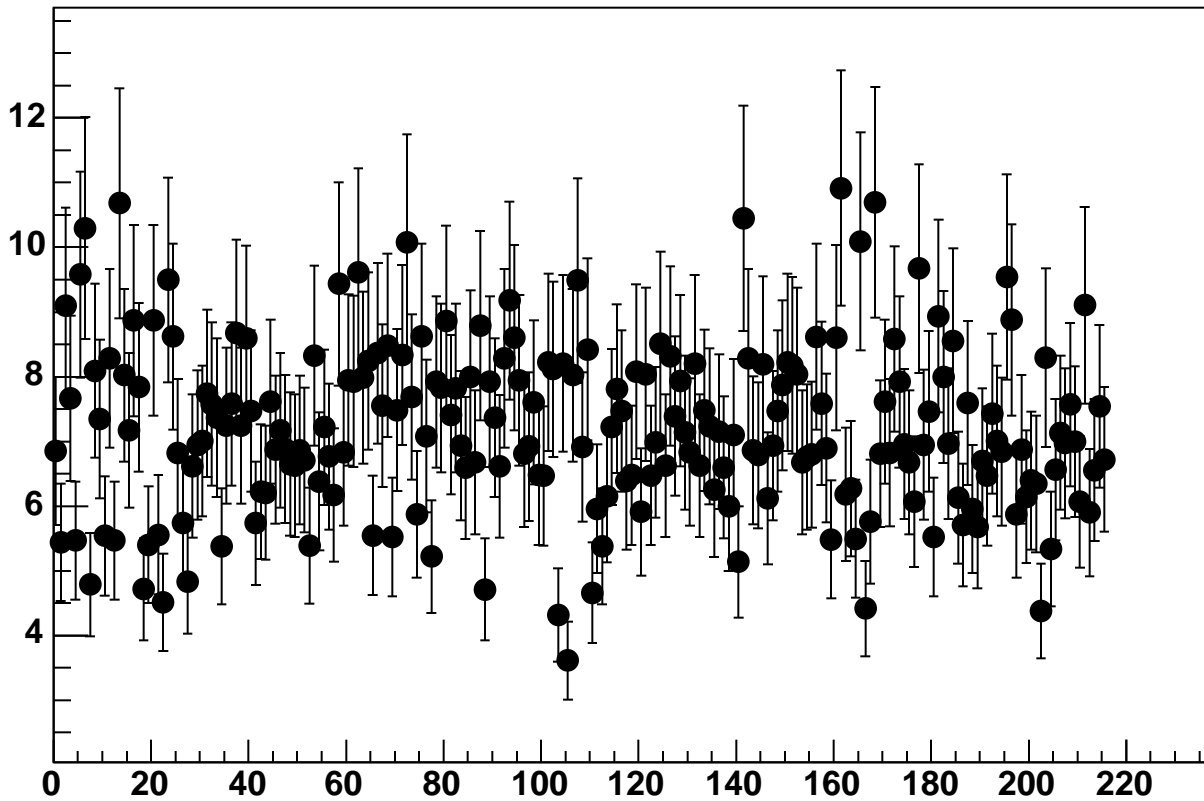
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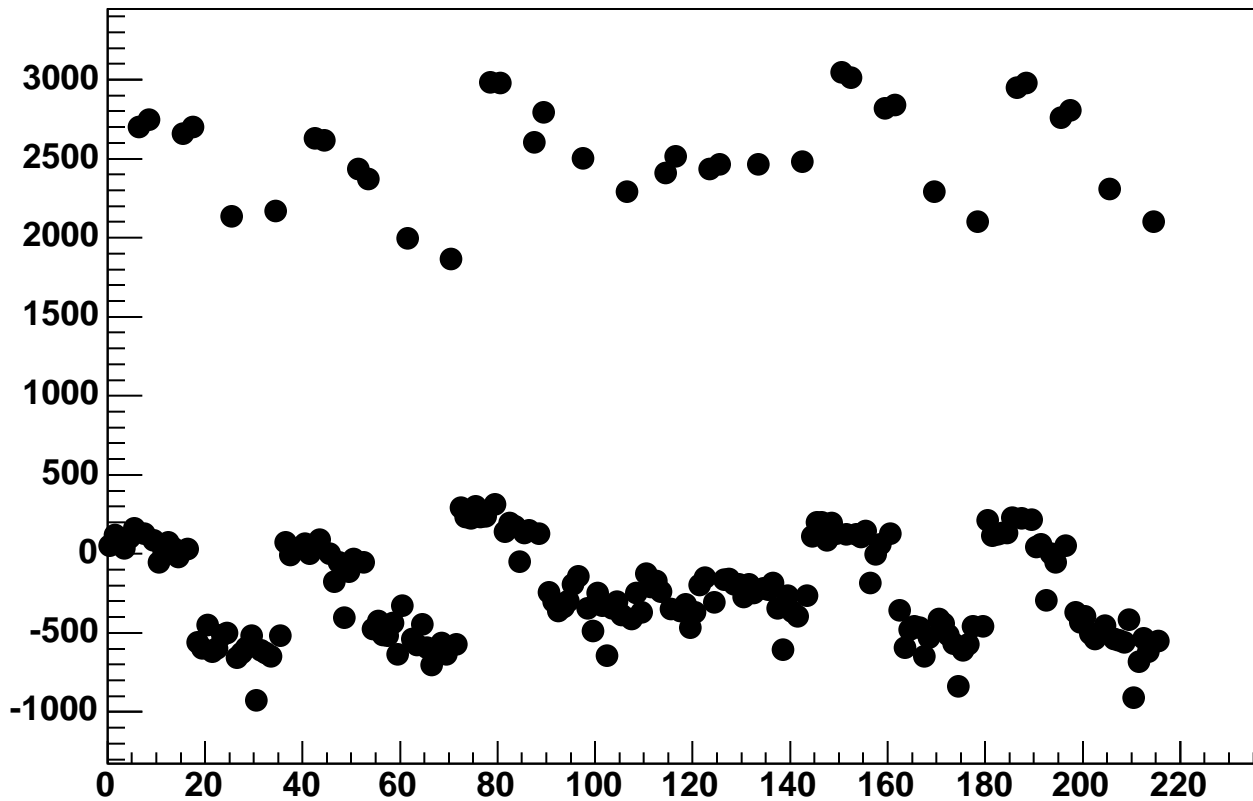
Enable 2, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



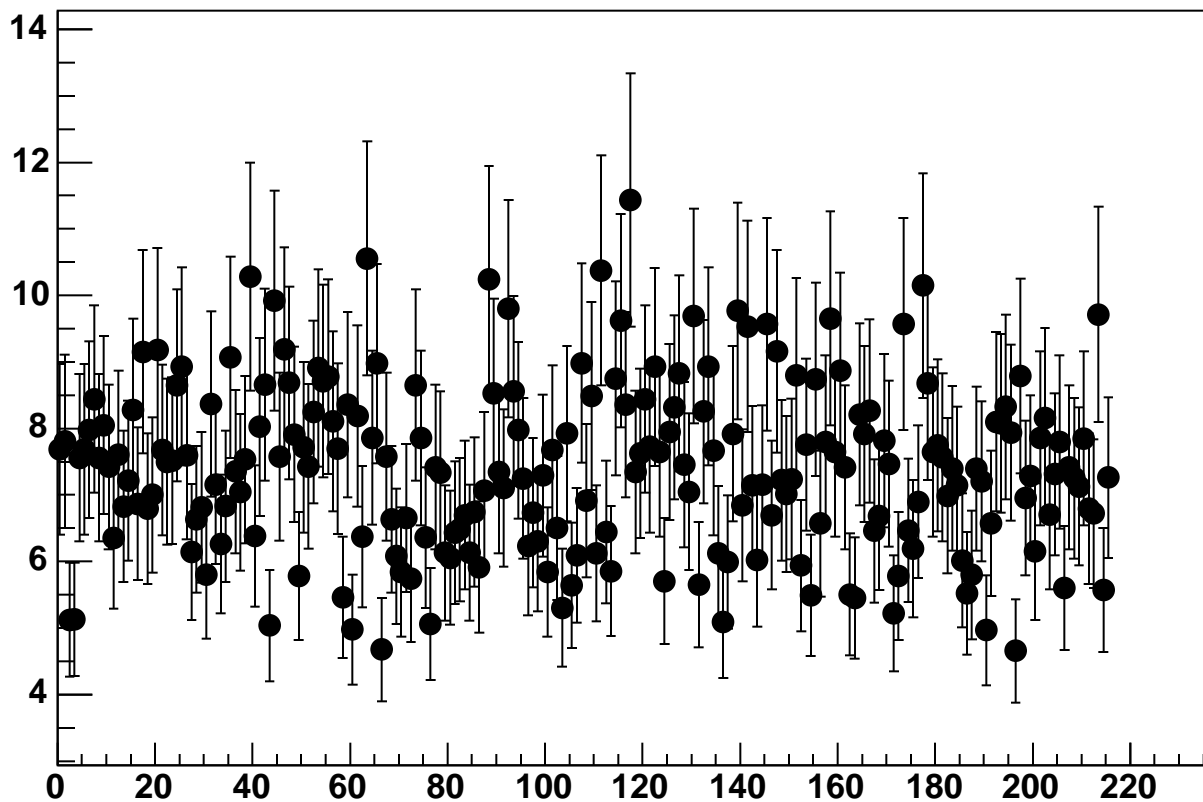
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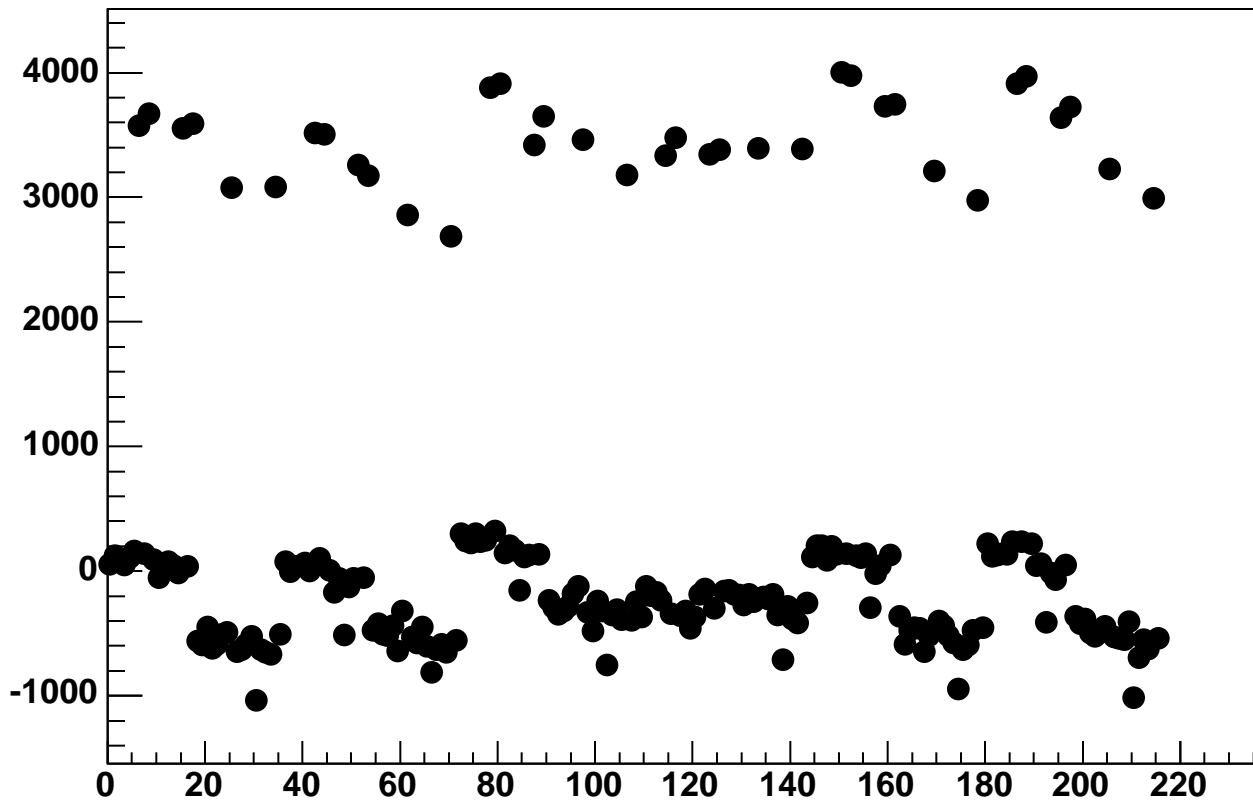
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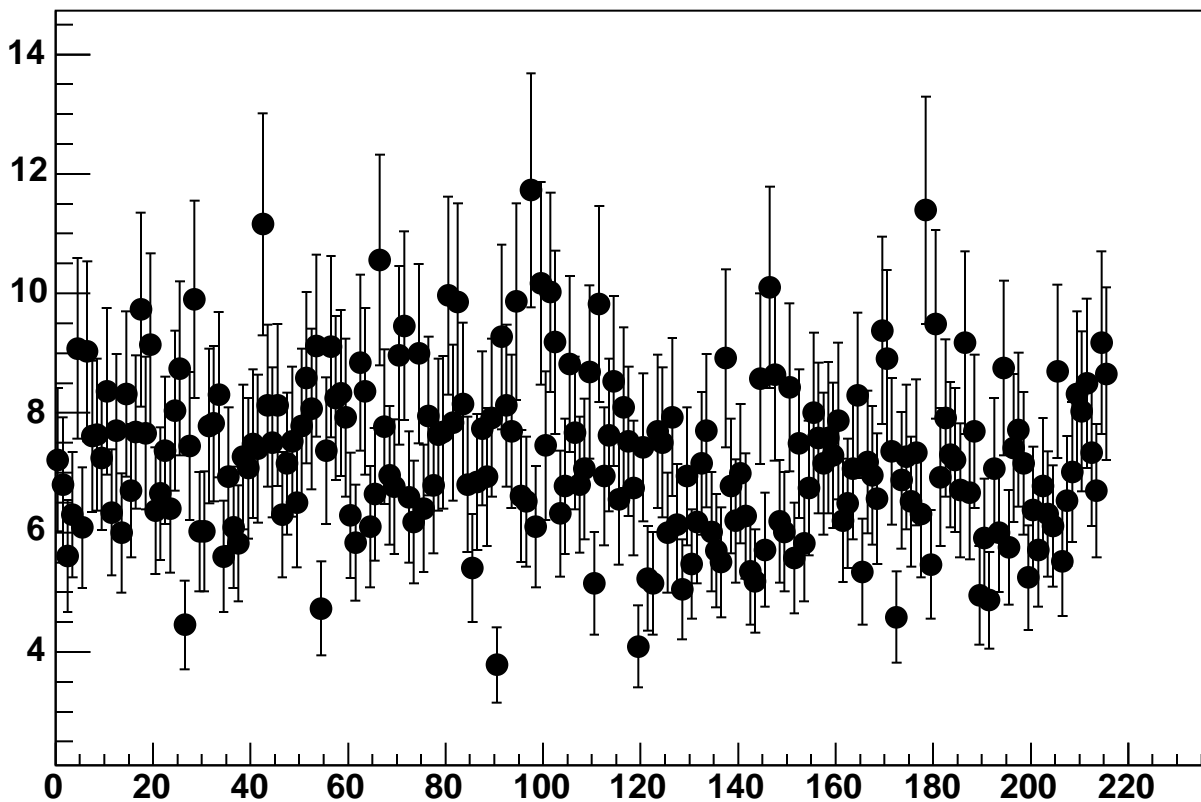
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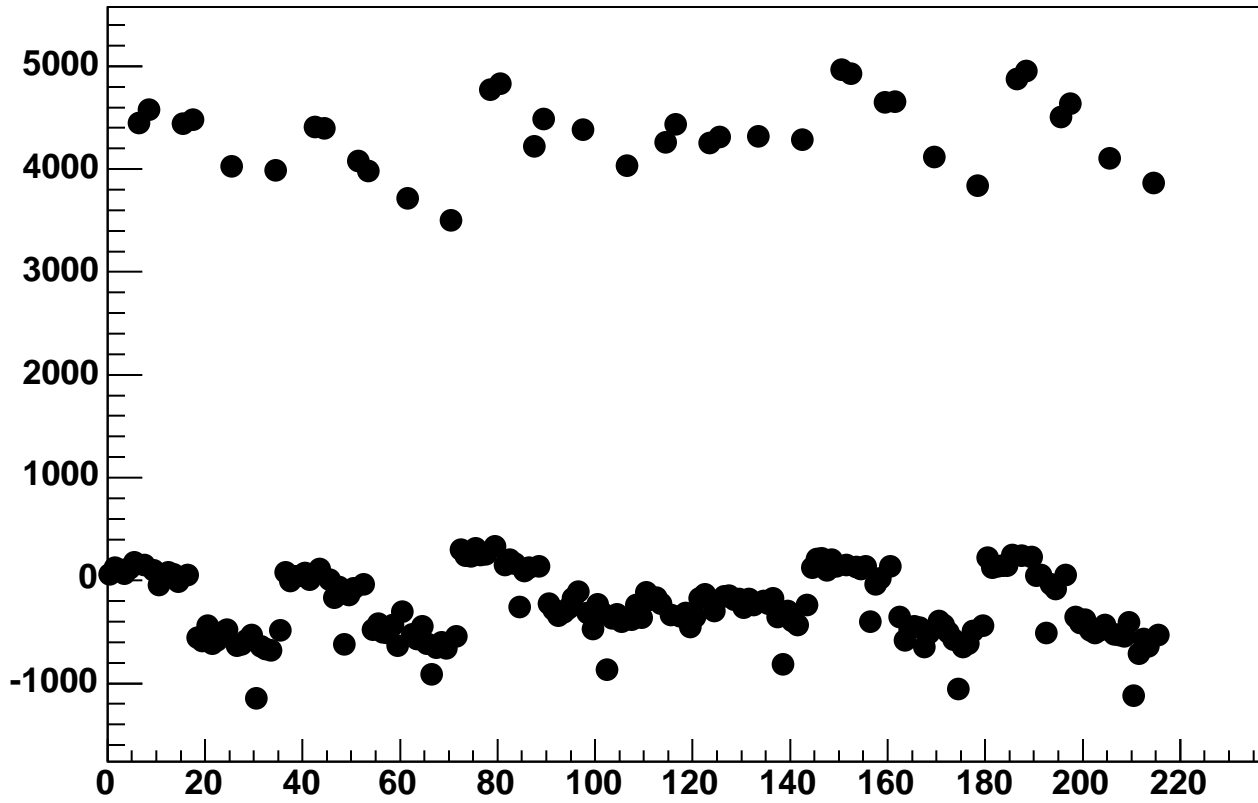
Enable 2, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



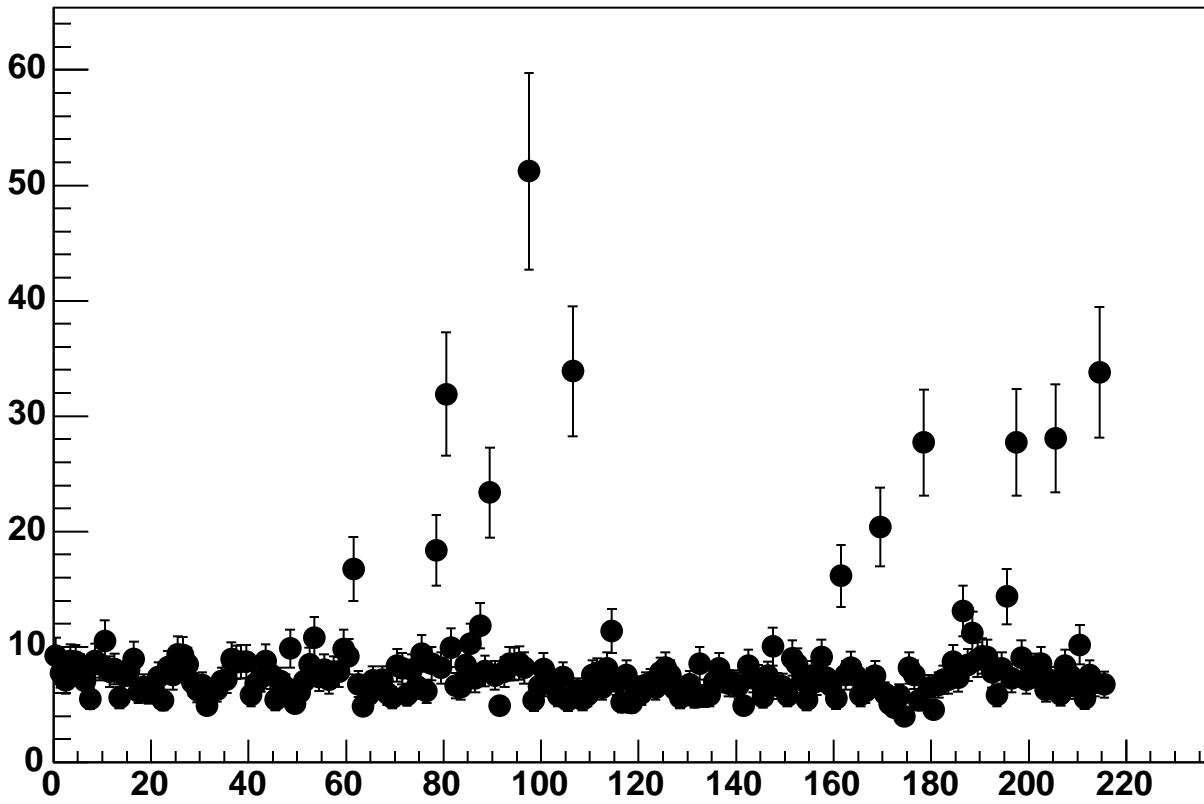
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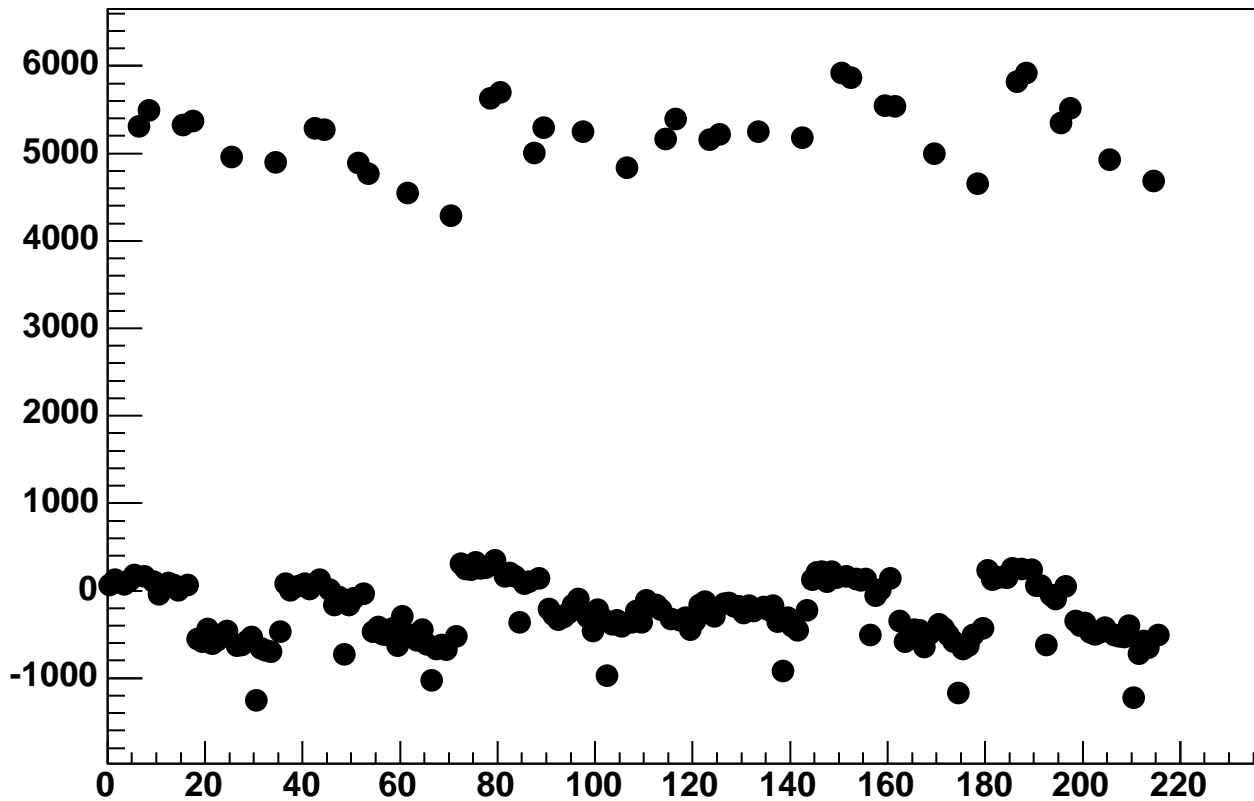
Enable 2, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



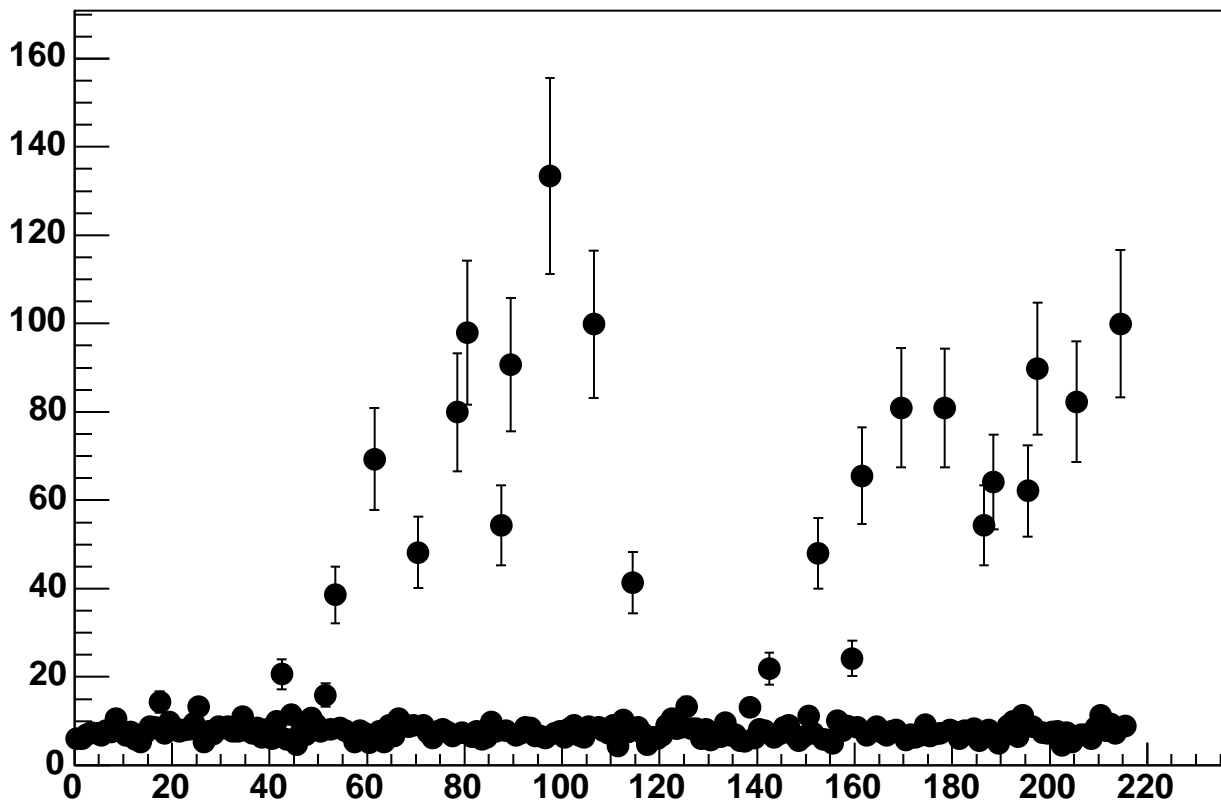
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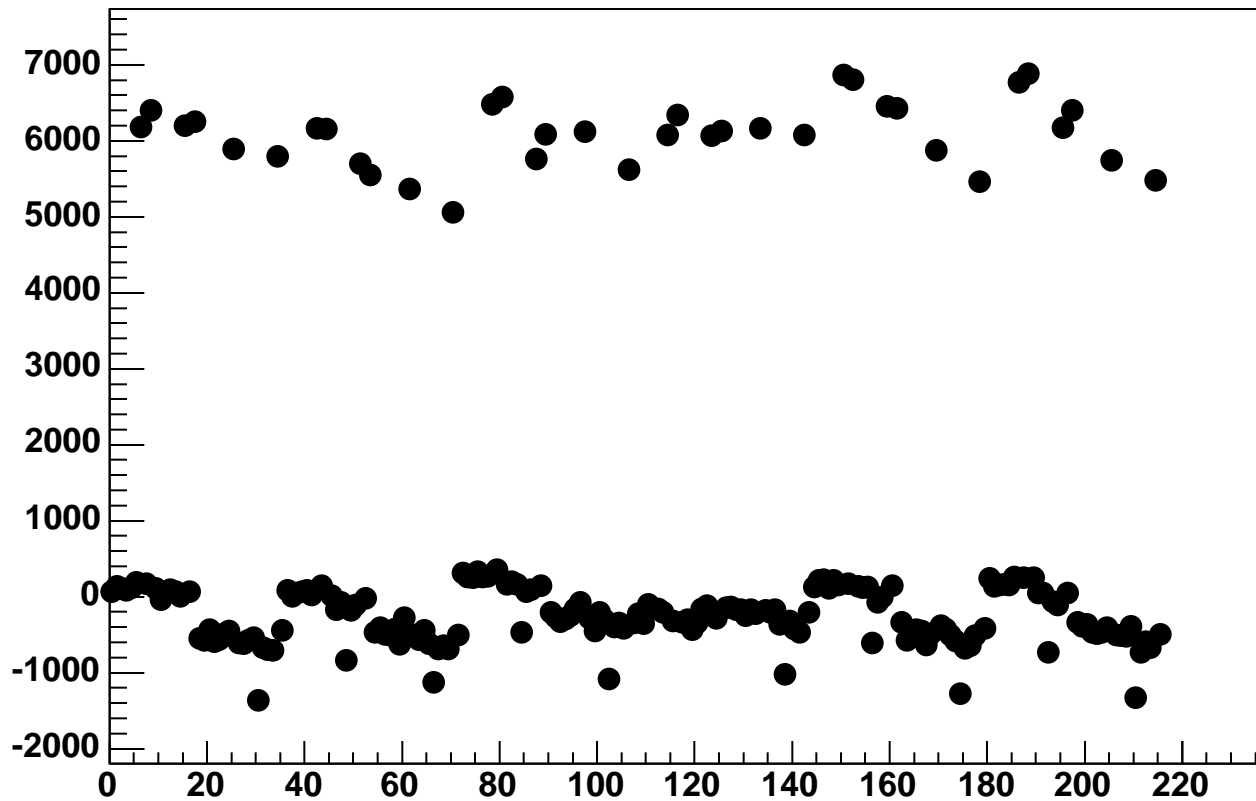
Enable 2, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



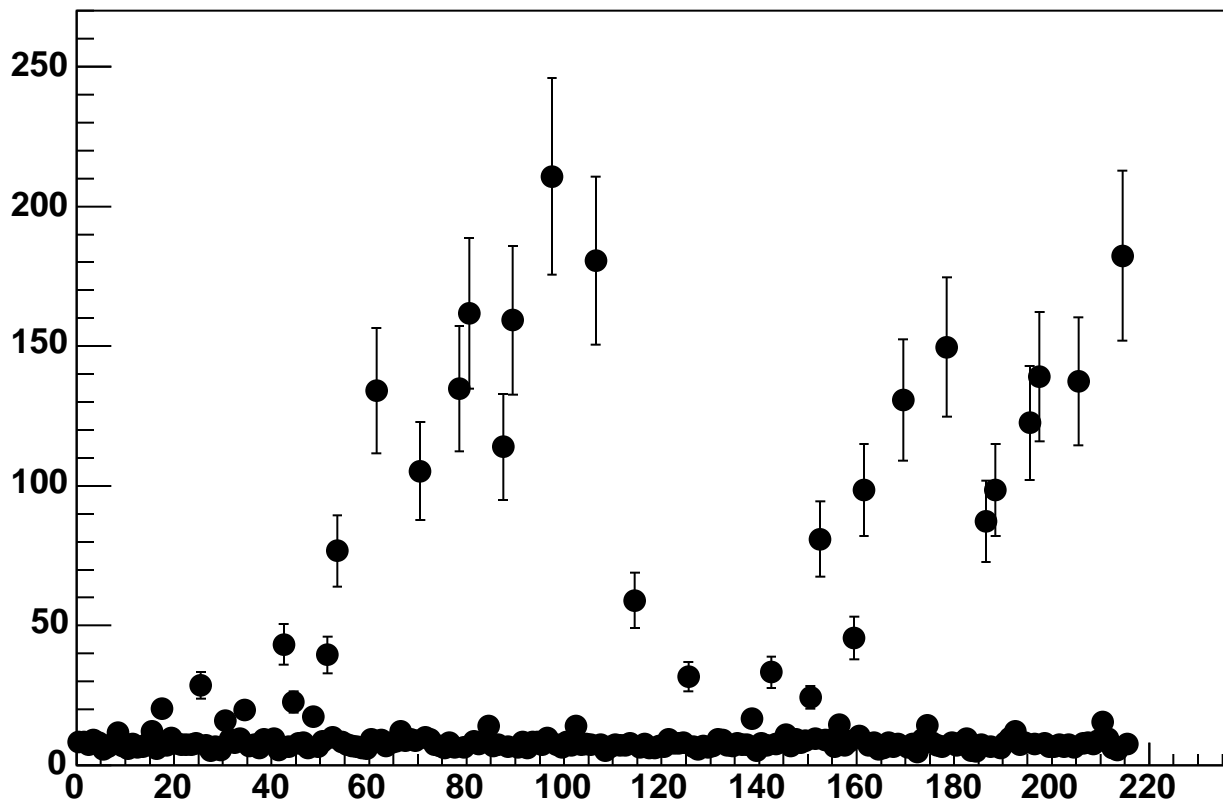
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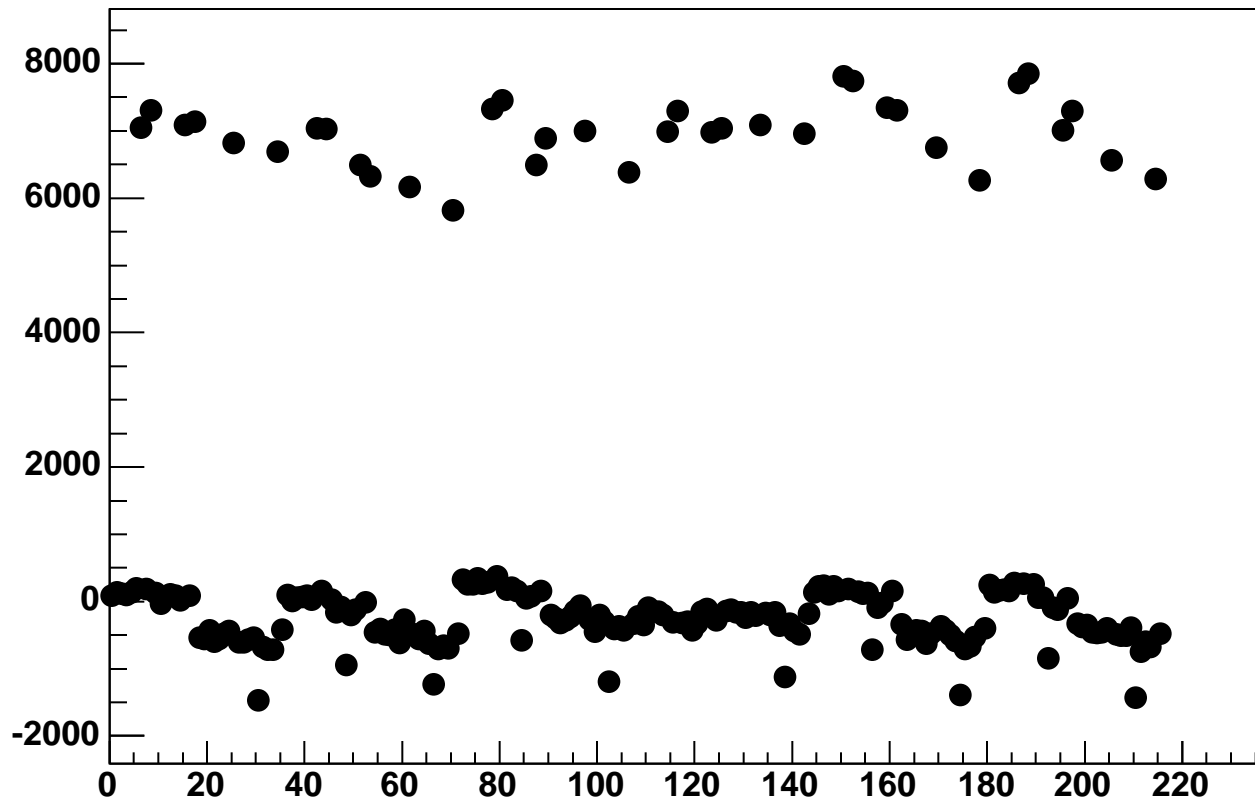
Enable 2, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



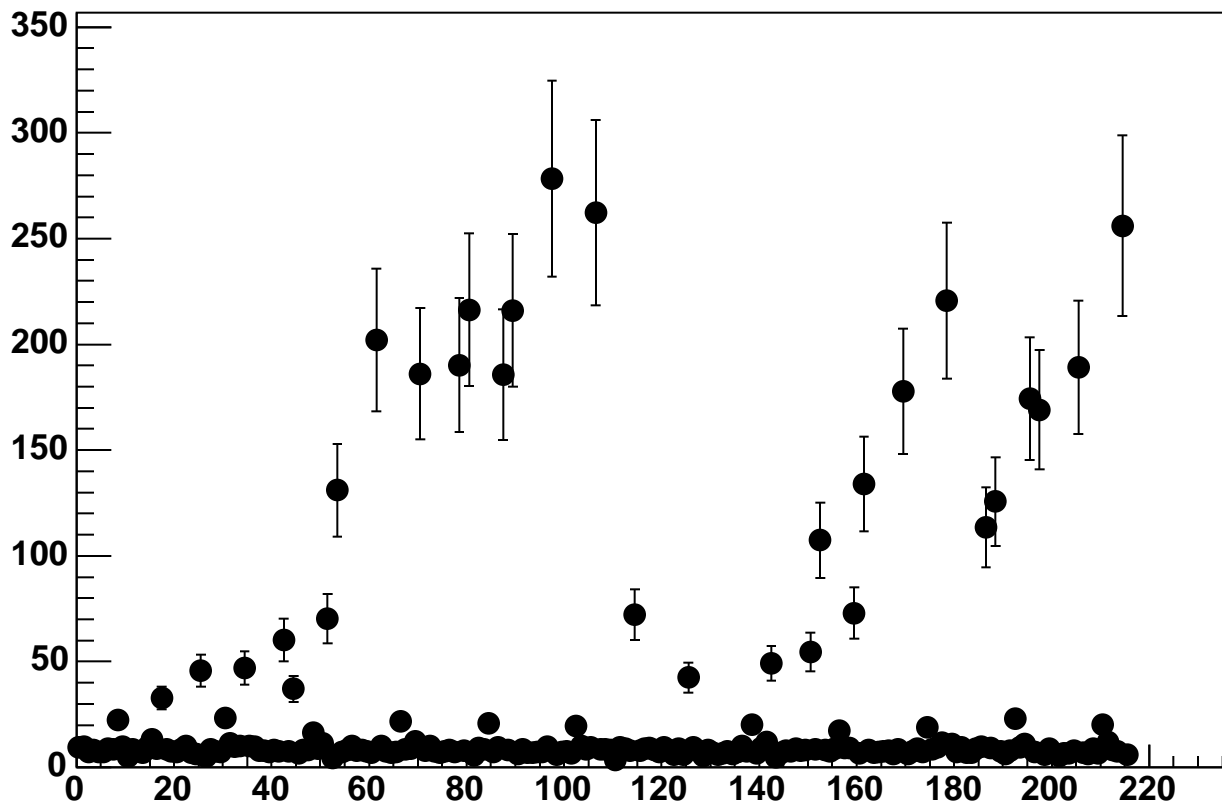
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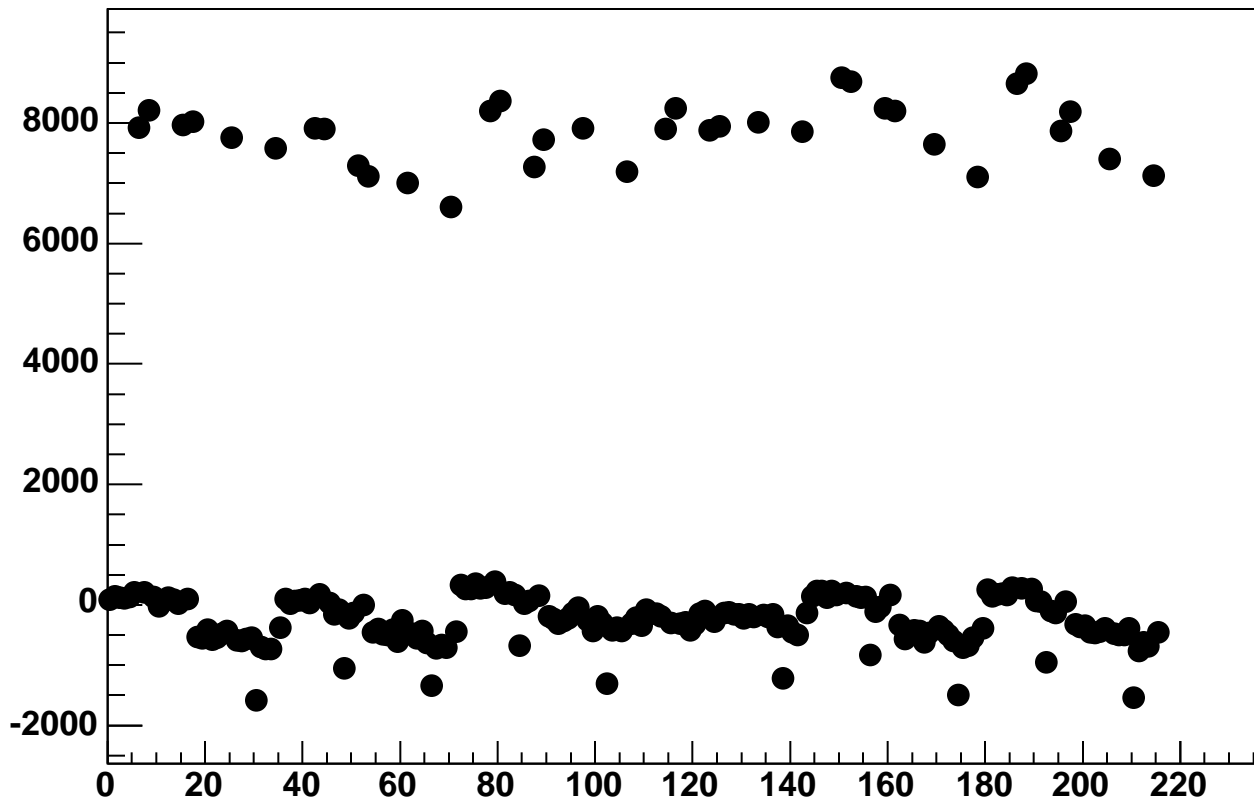
Enable 2, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



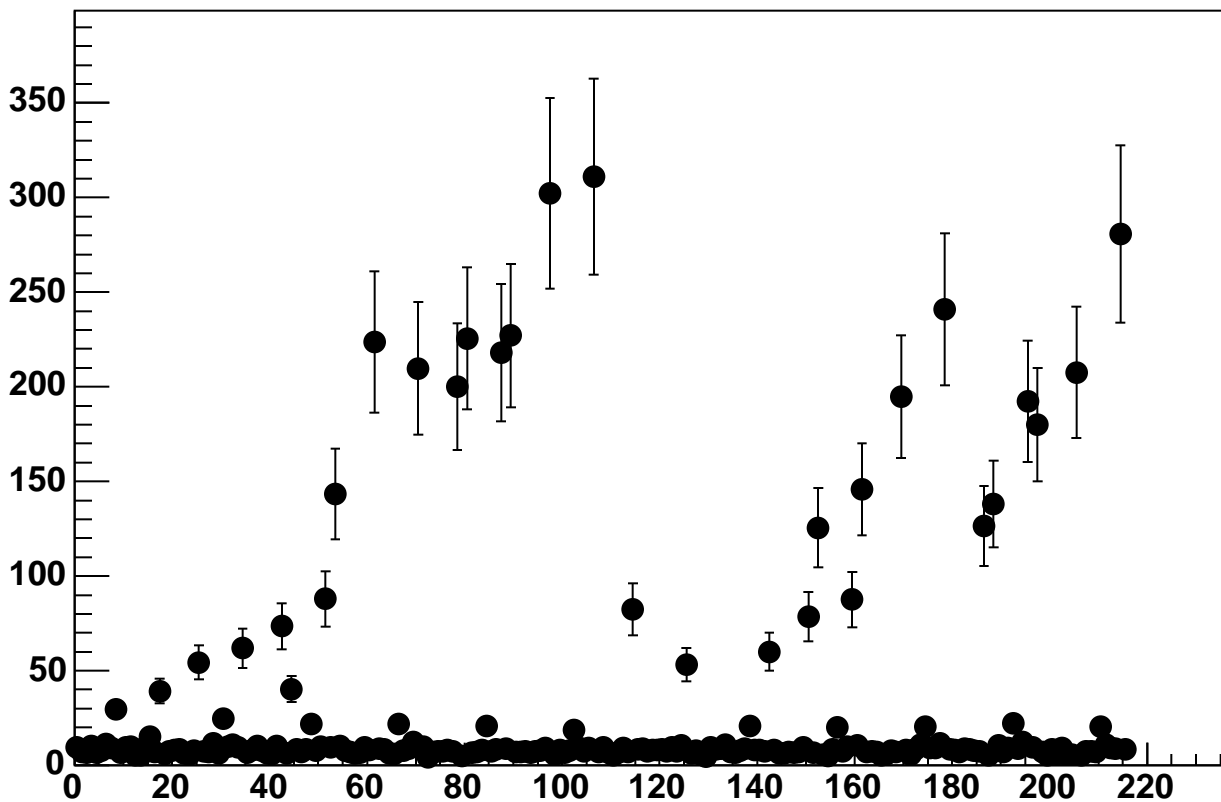
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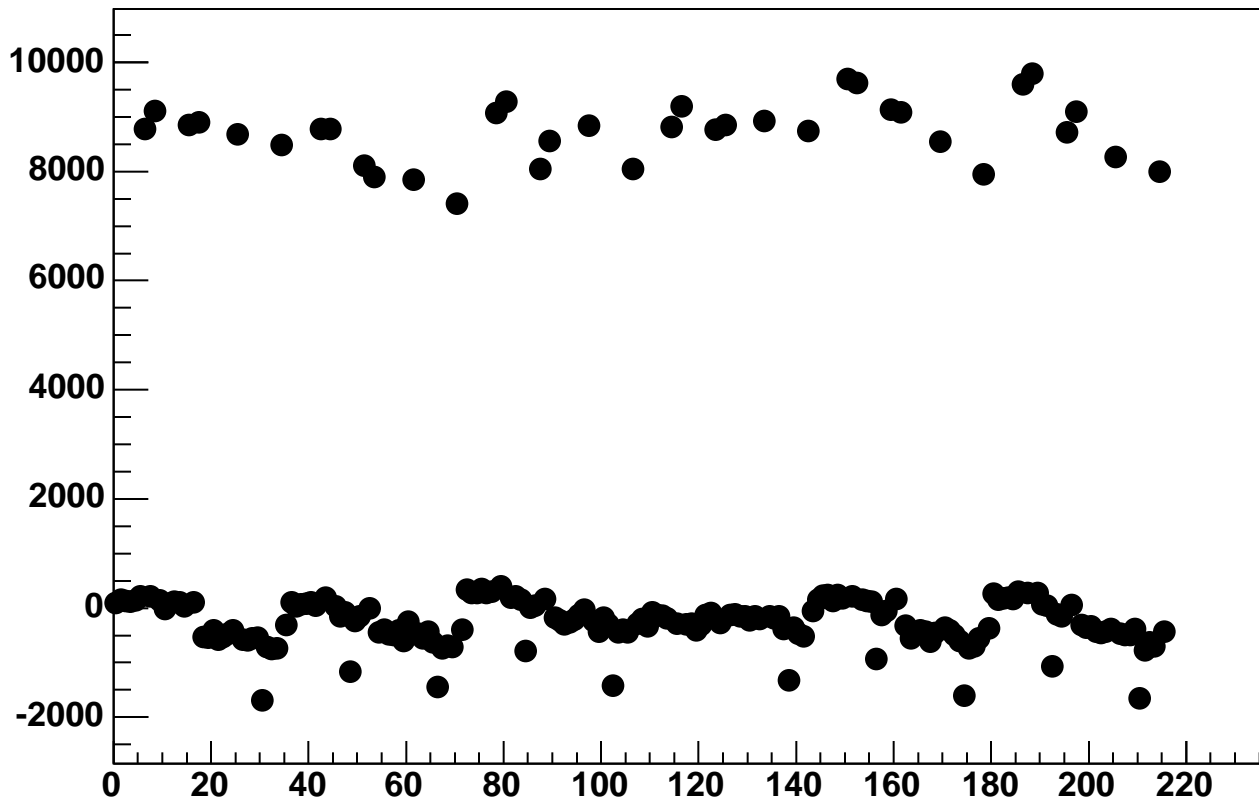
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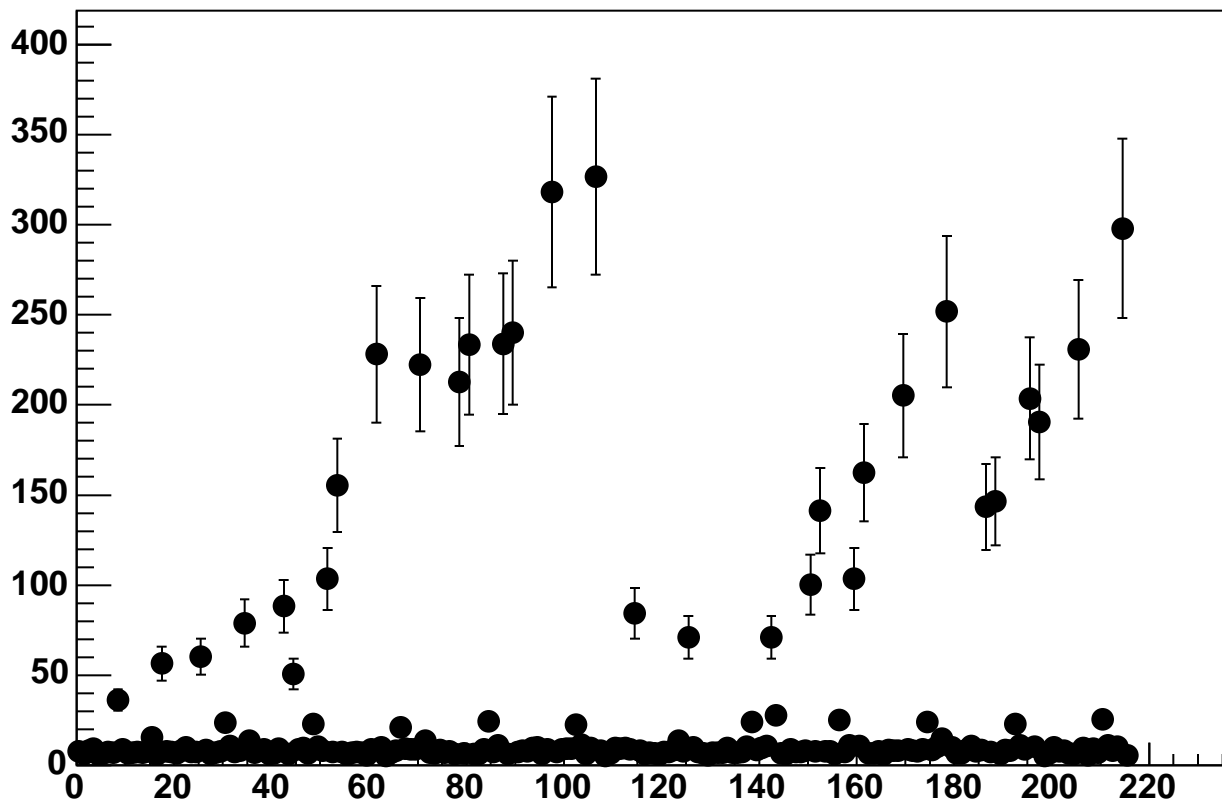
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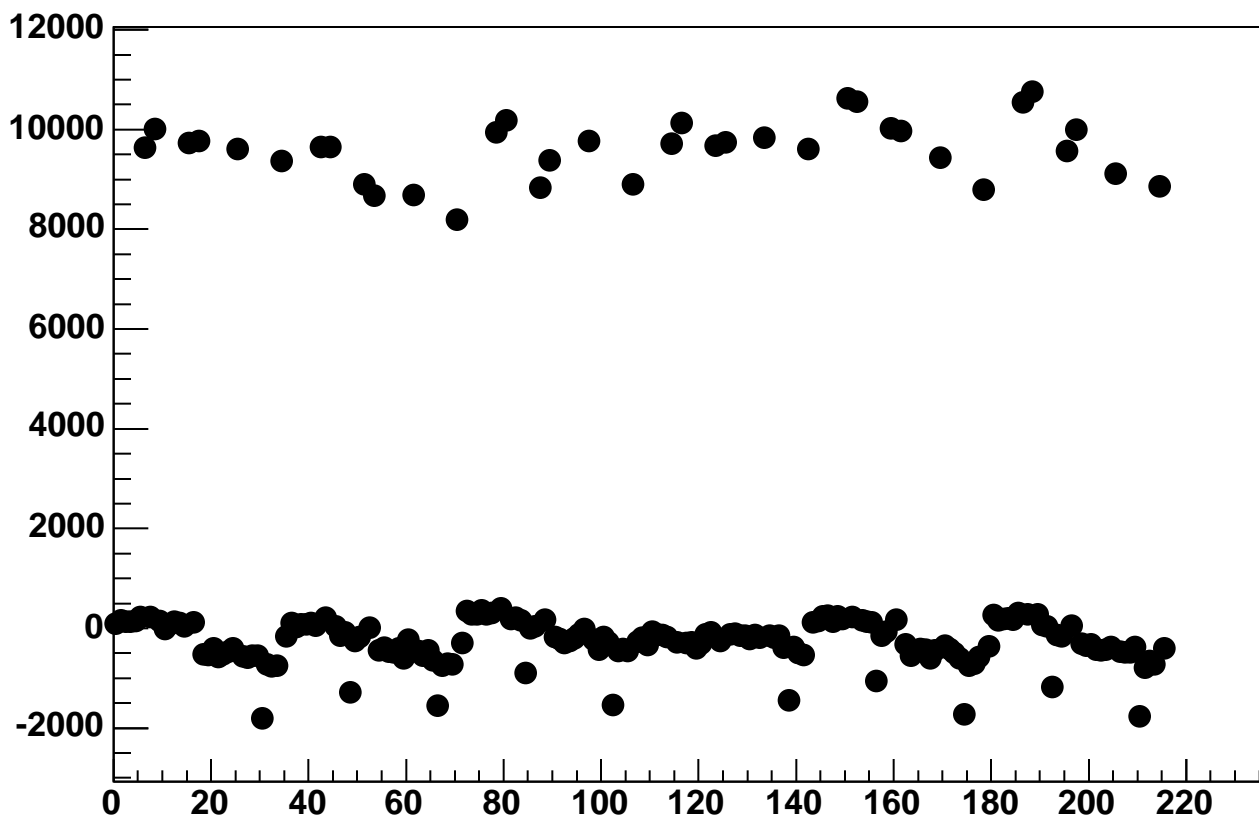
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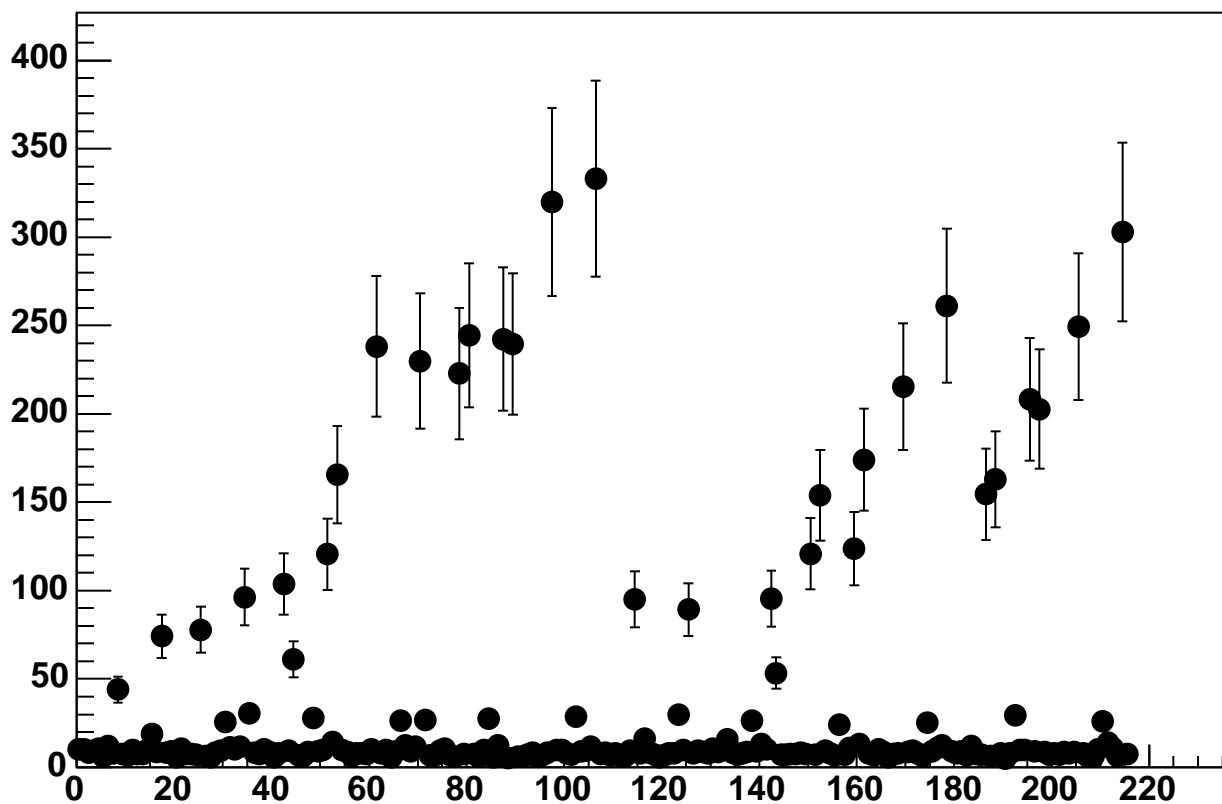
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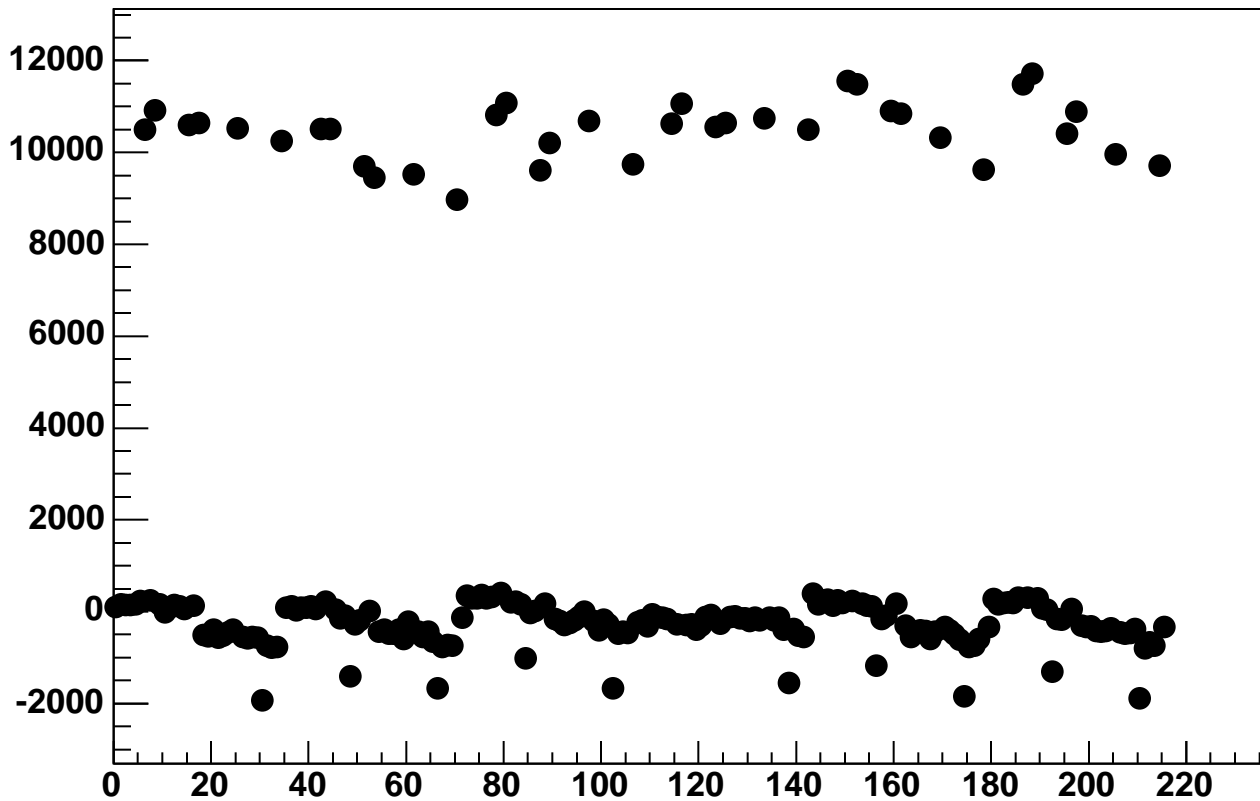
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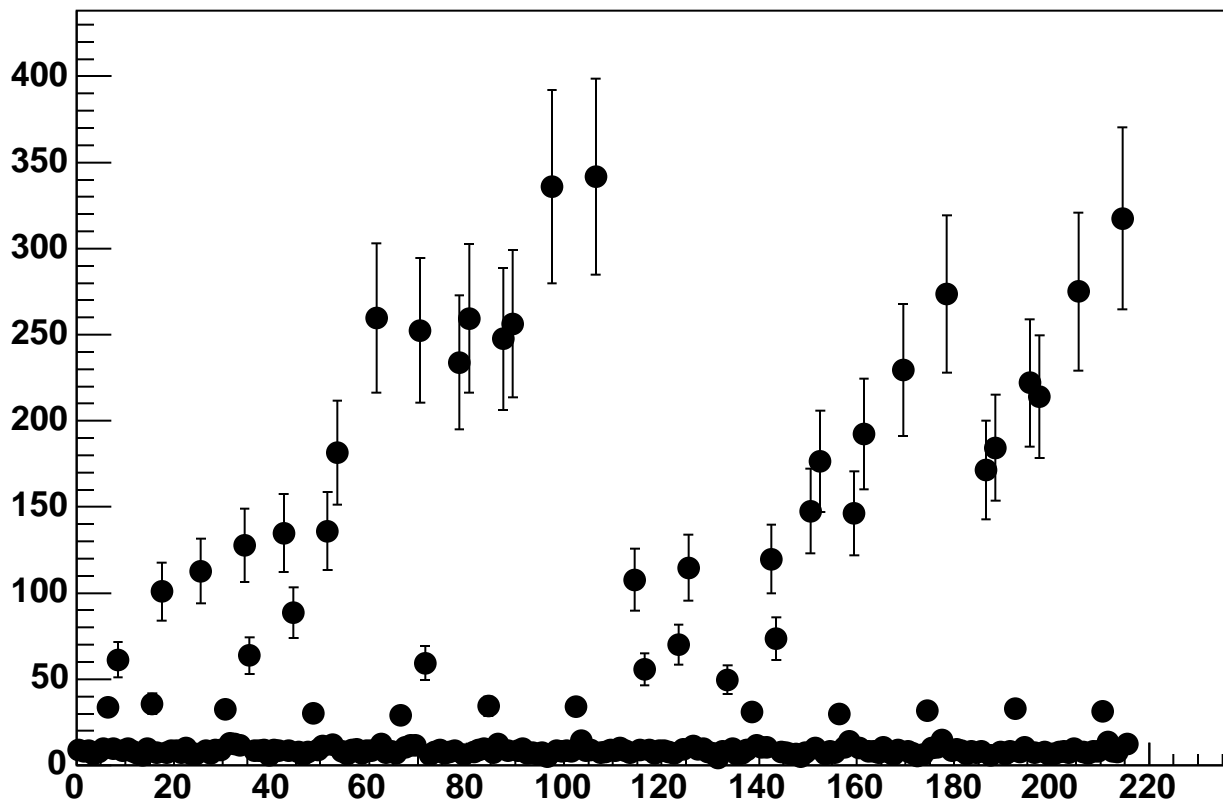
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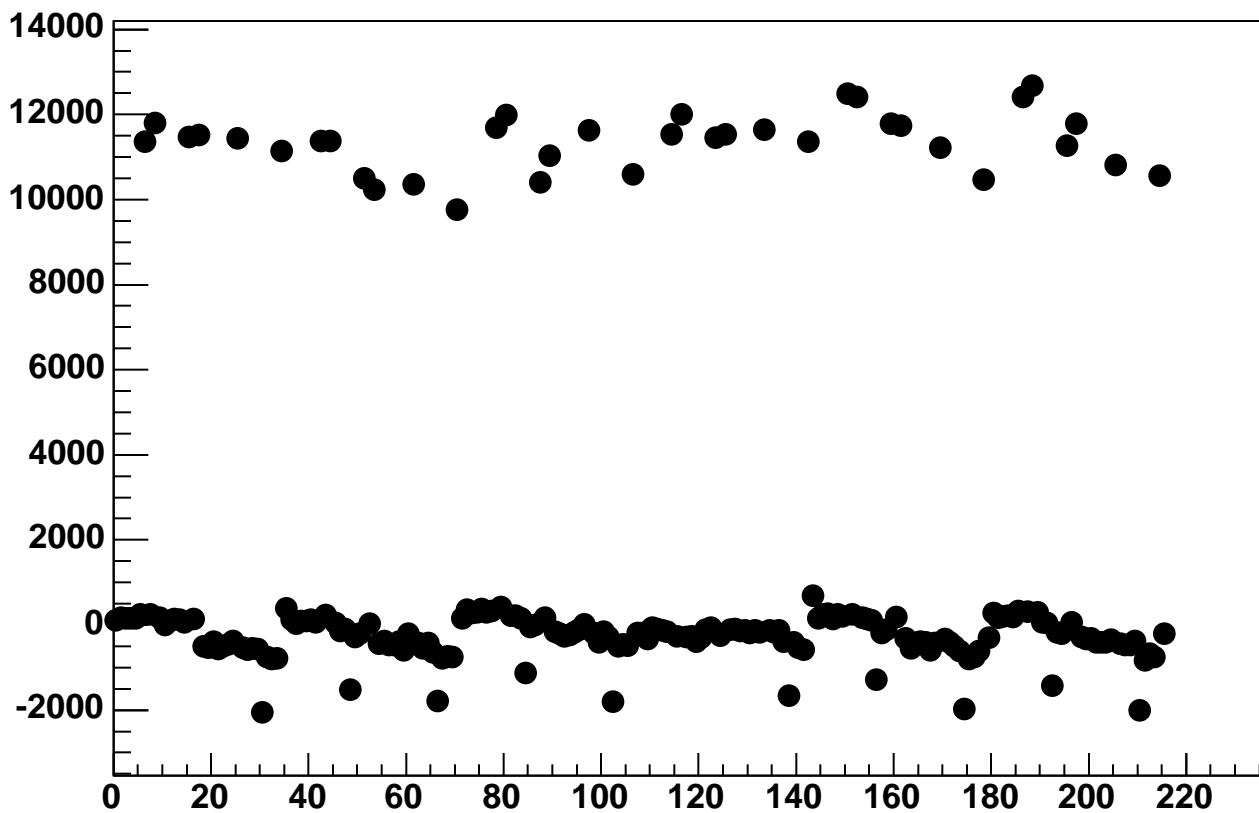
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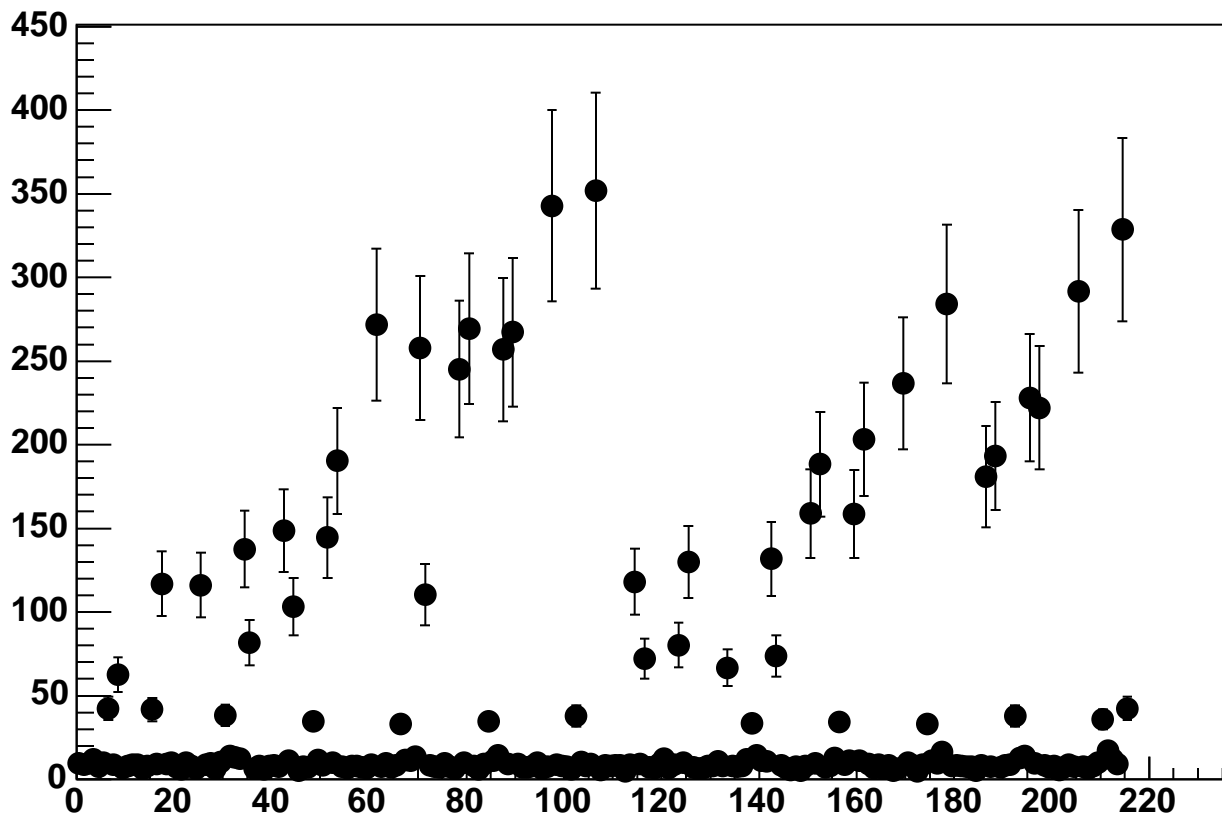
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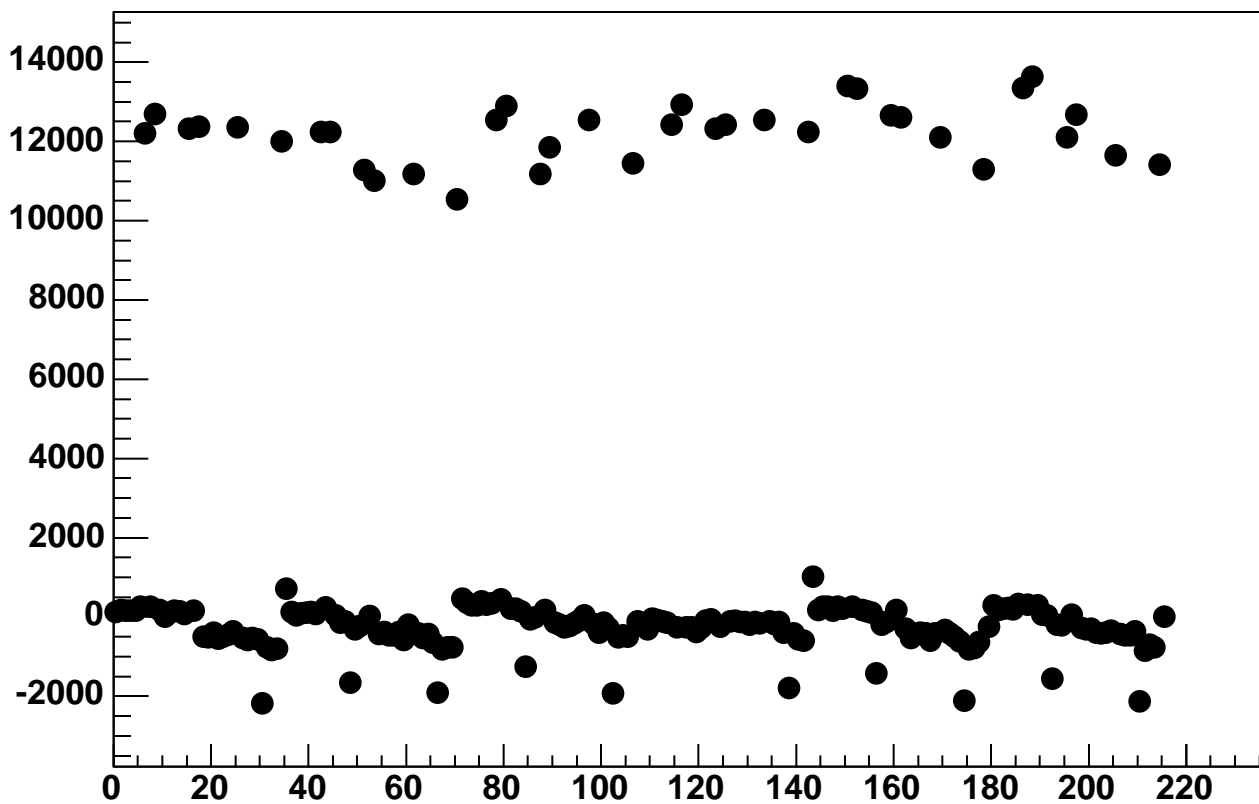
Enable 2, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



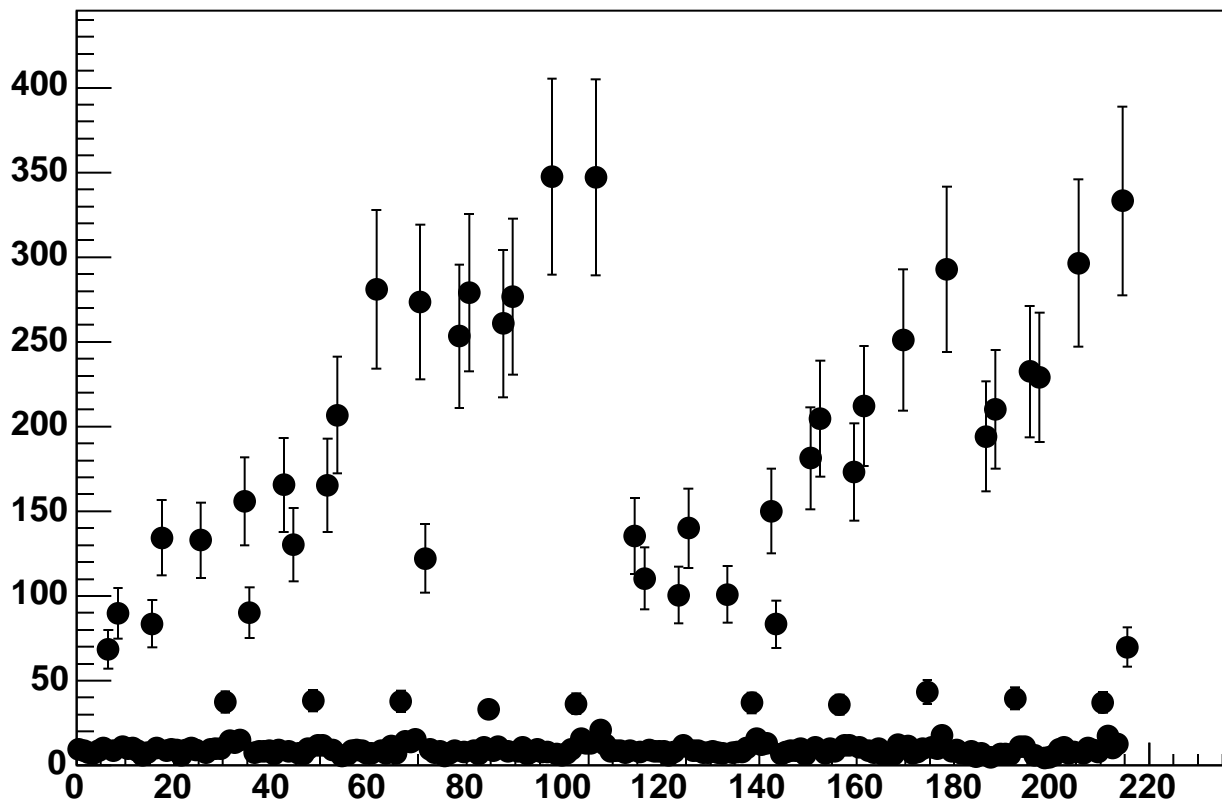
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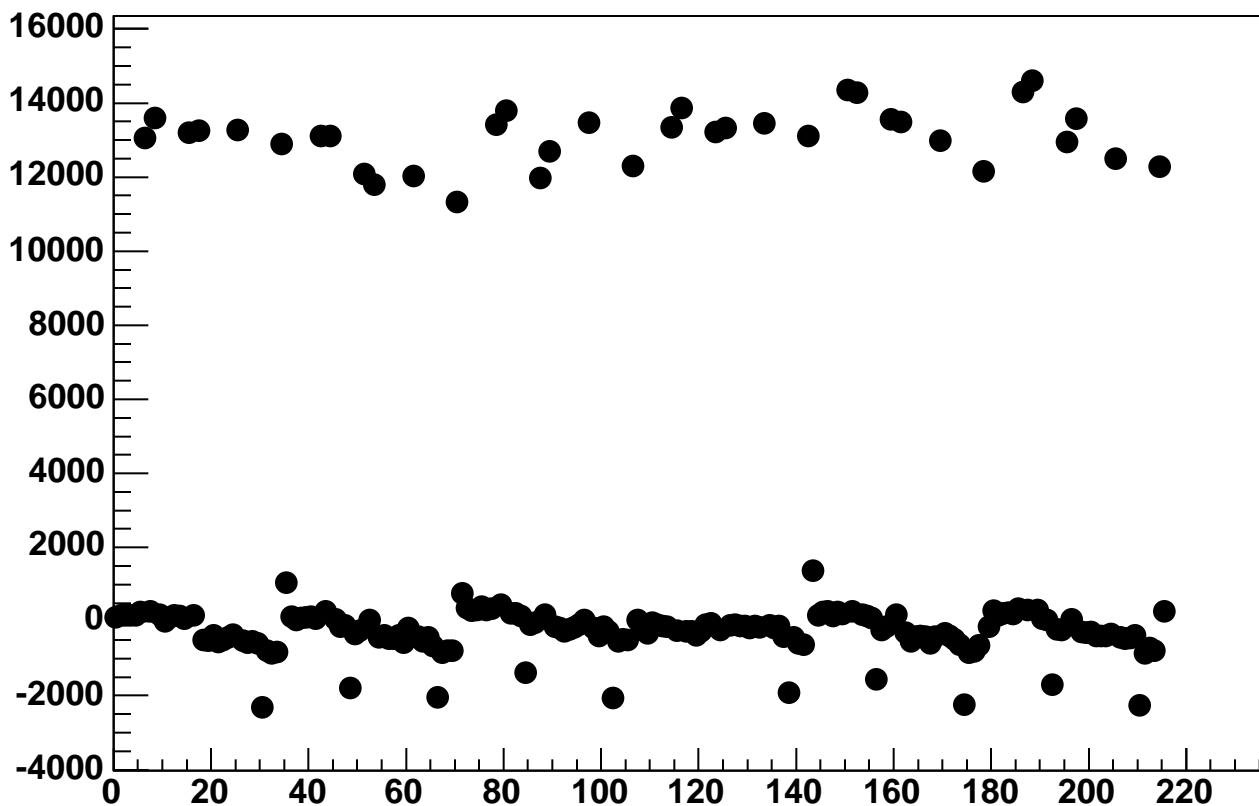
Enable 2, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



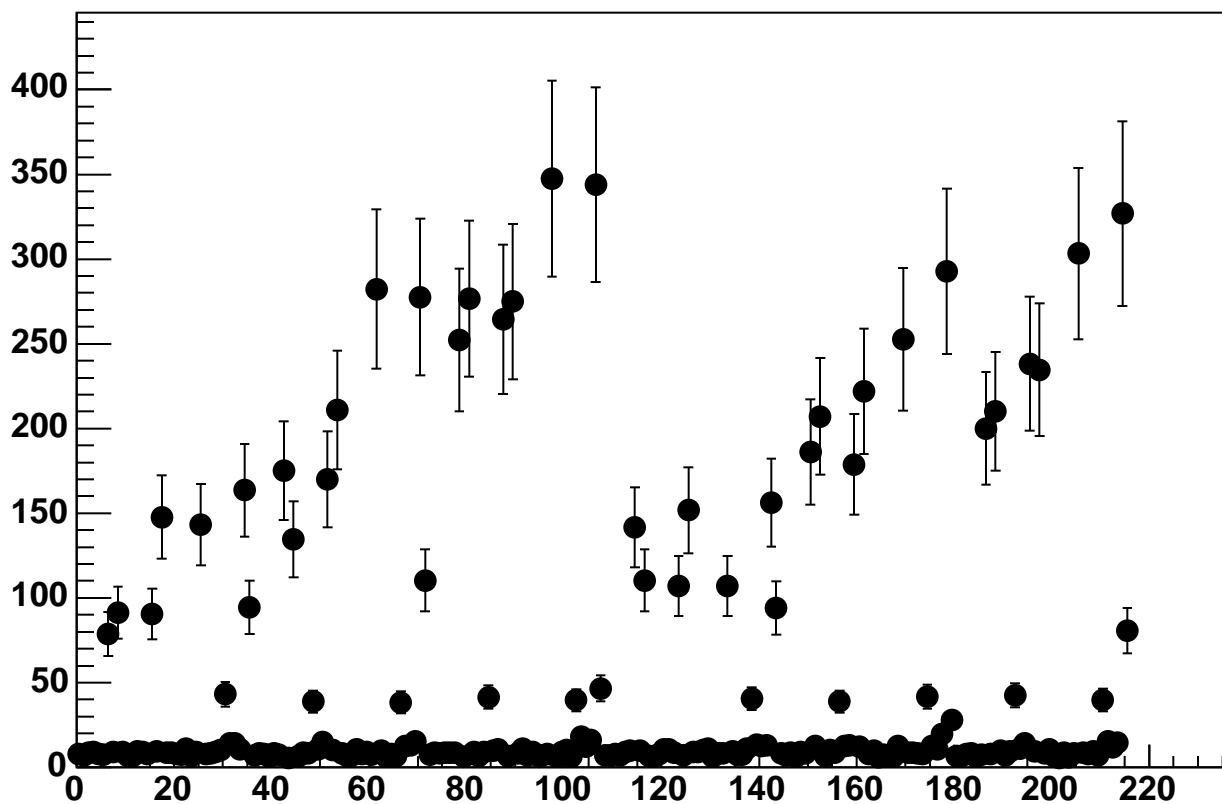
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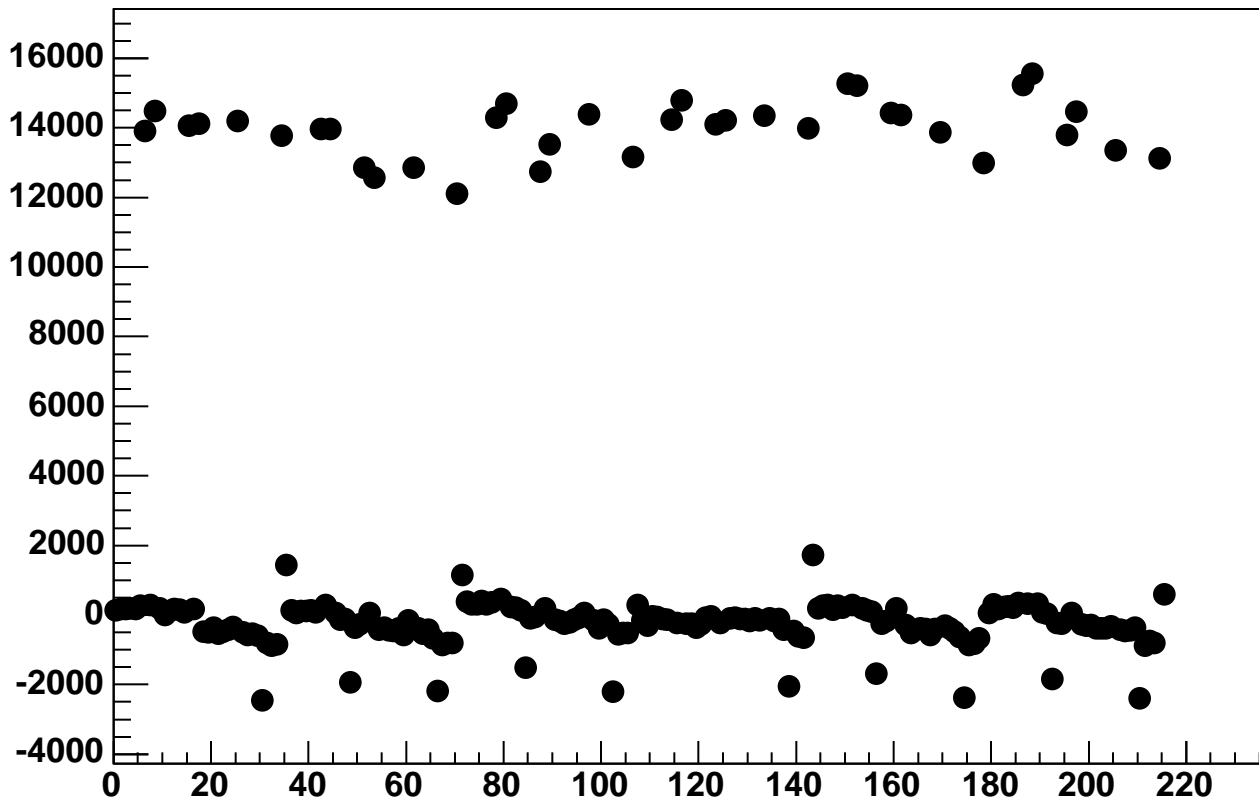
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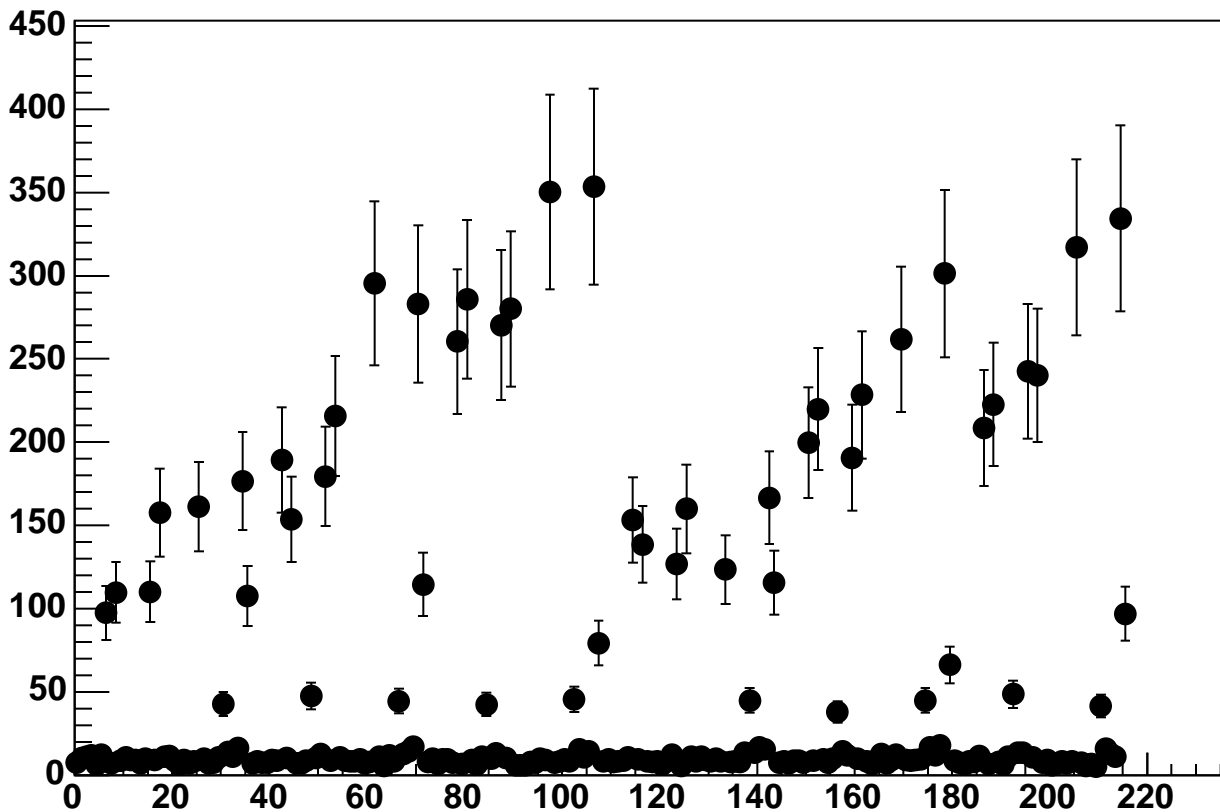
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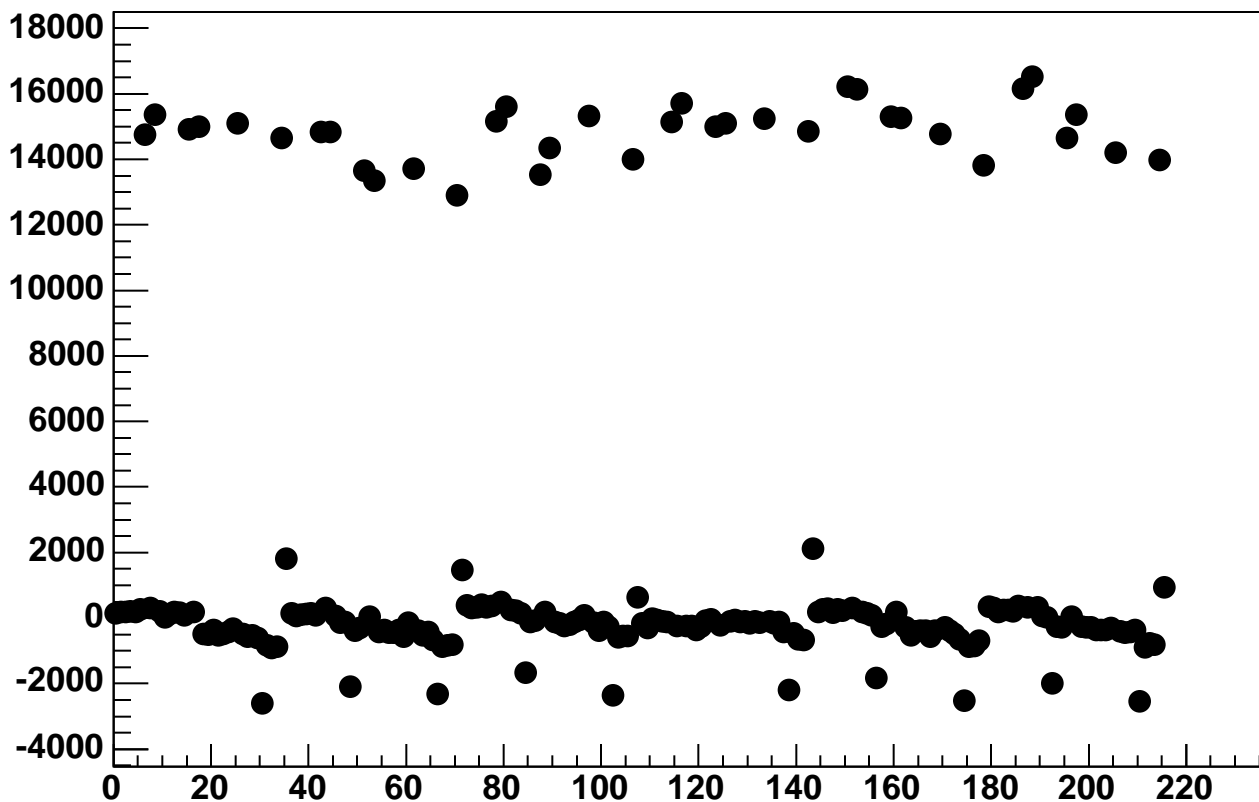
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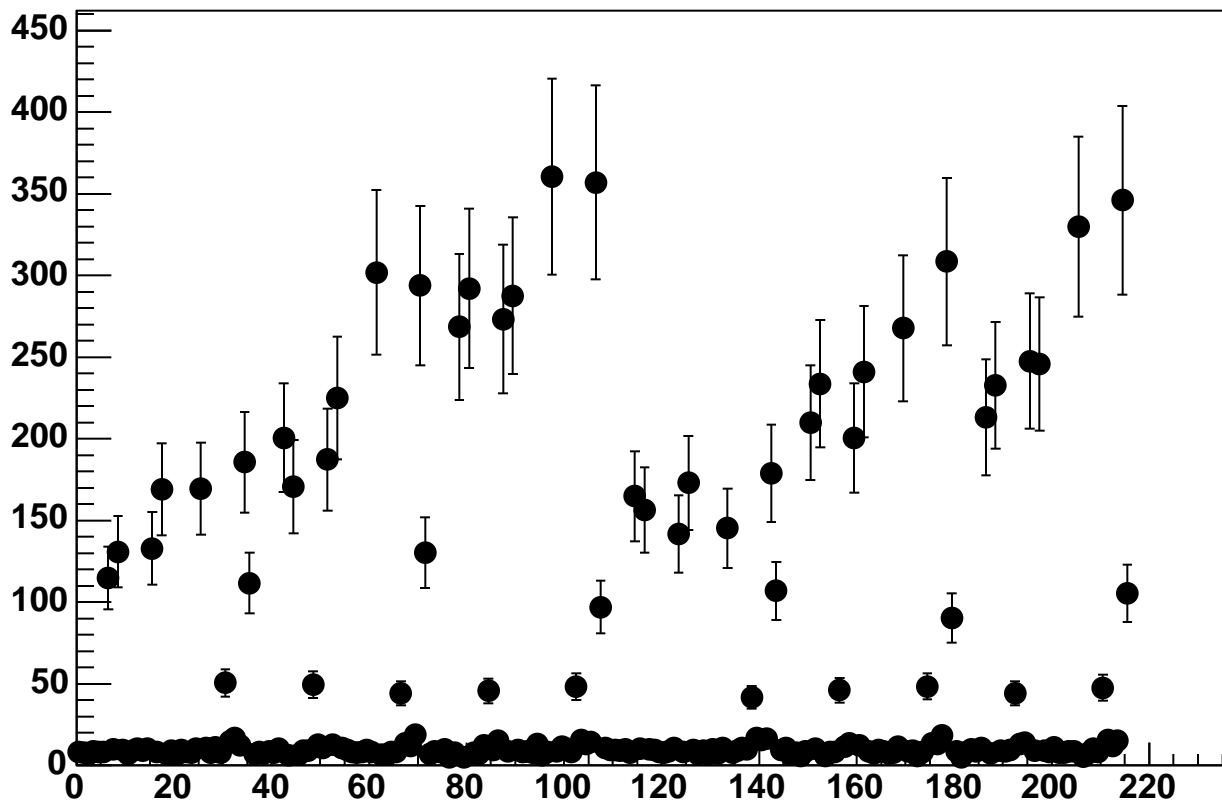
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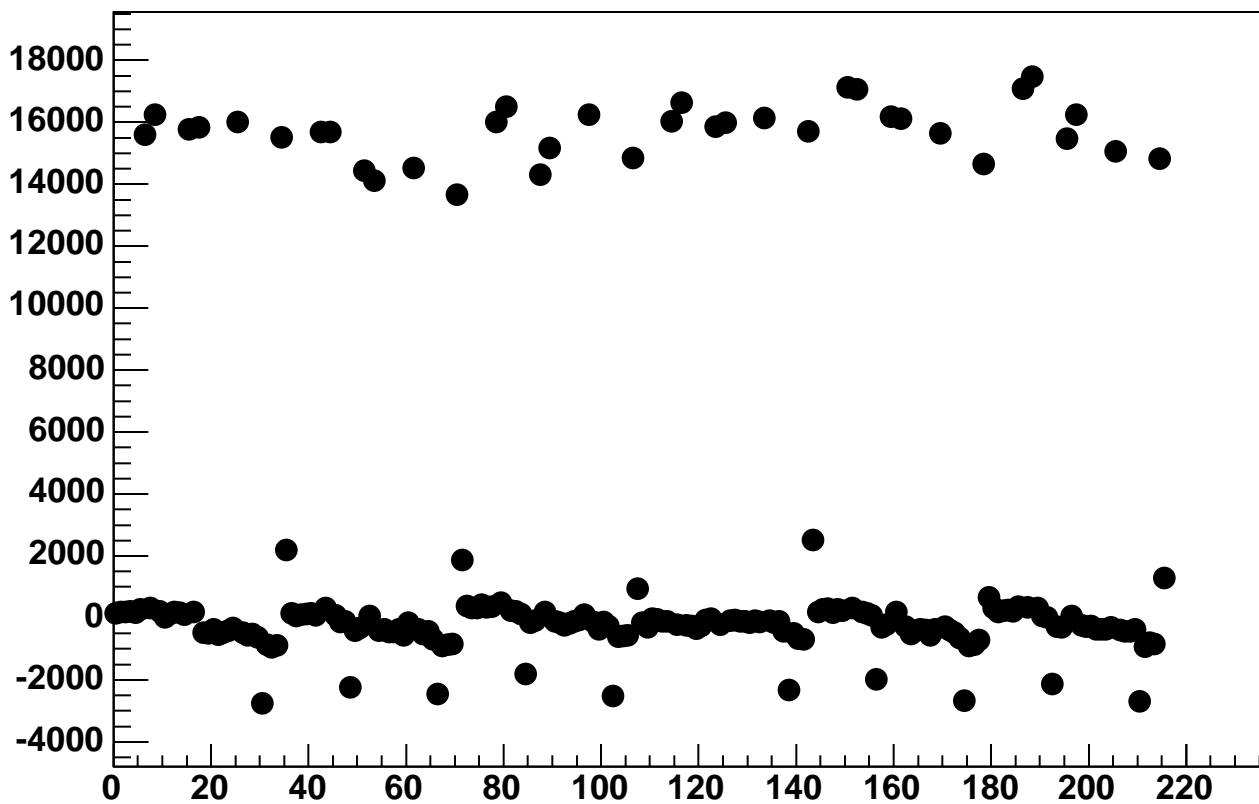
Enable 2, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



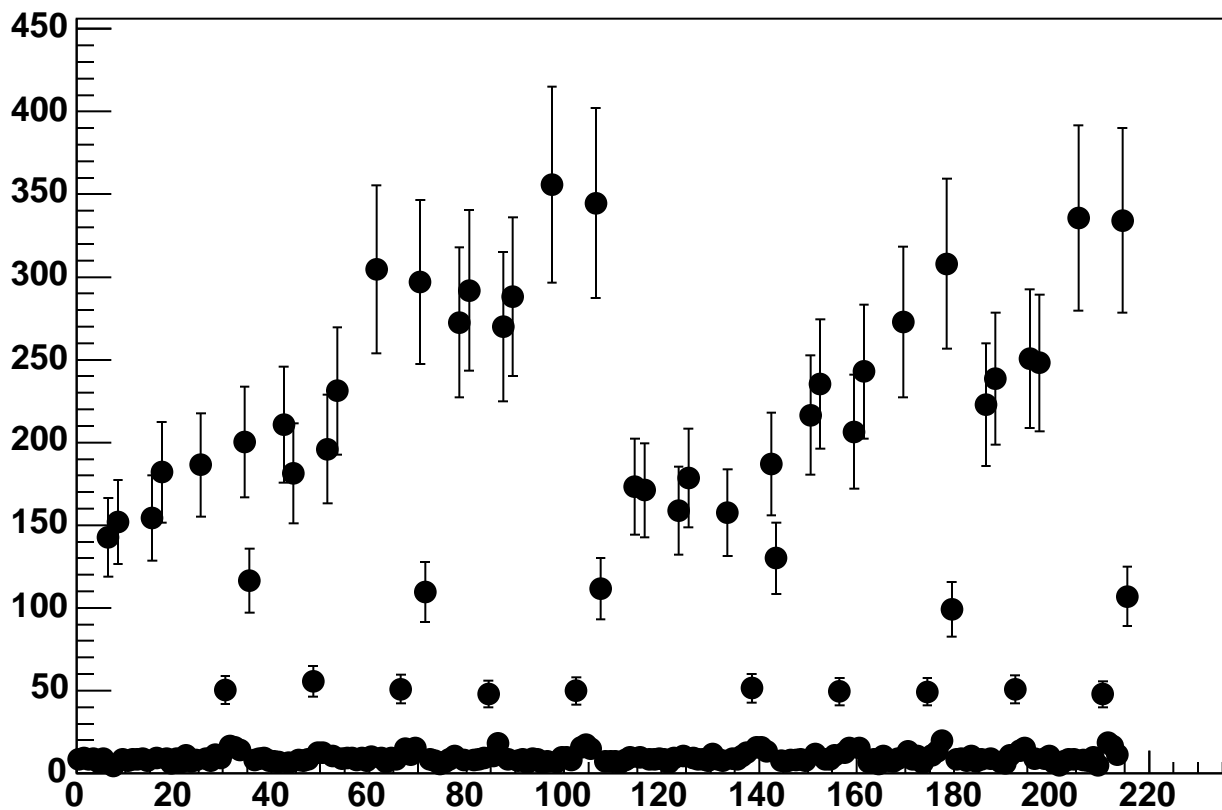
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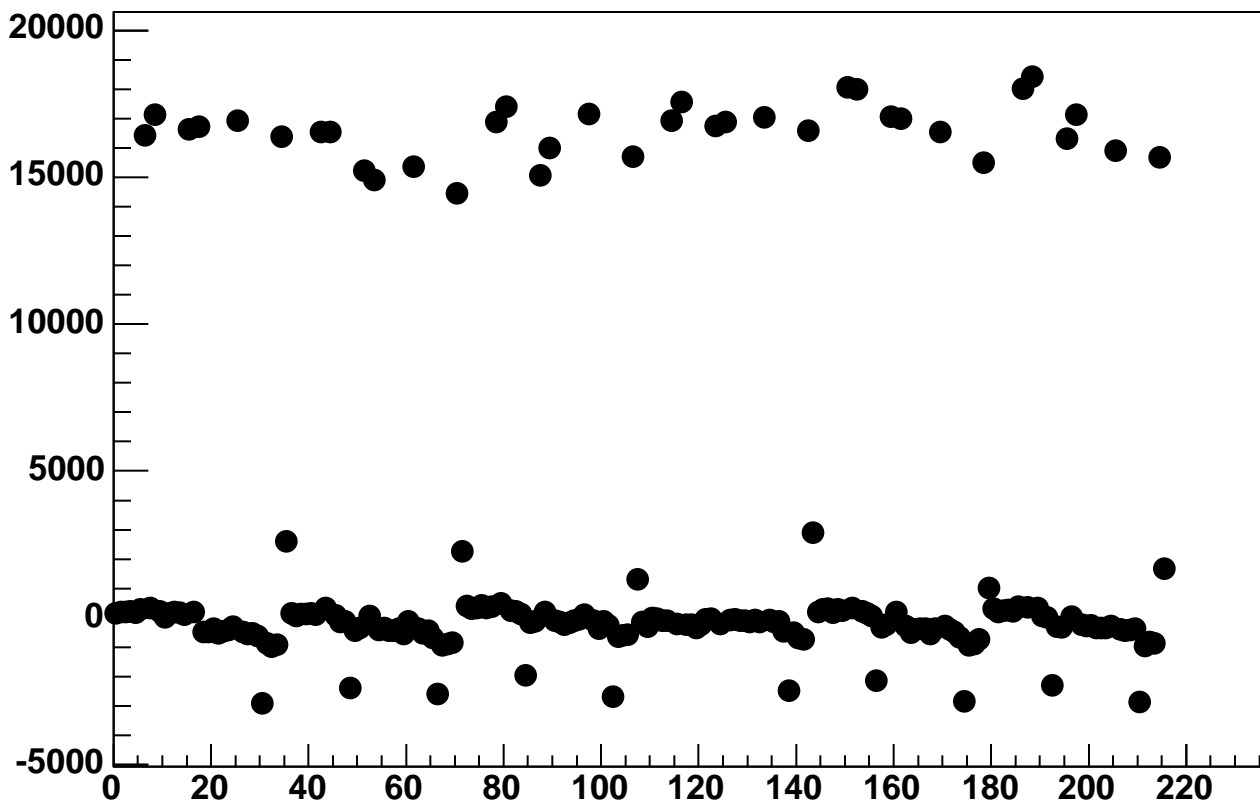
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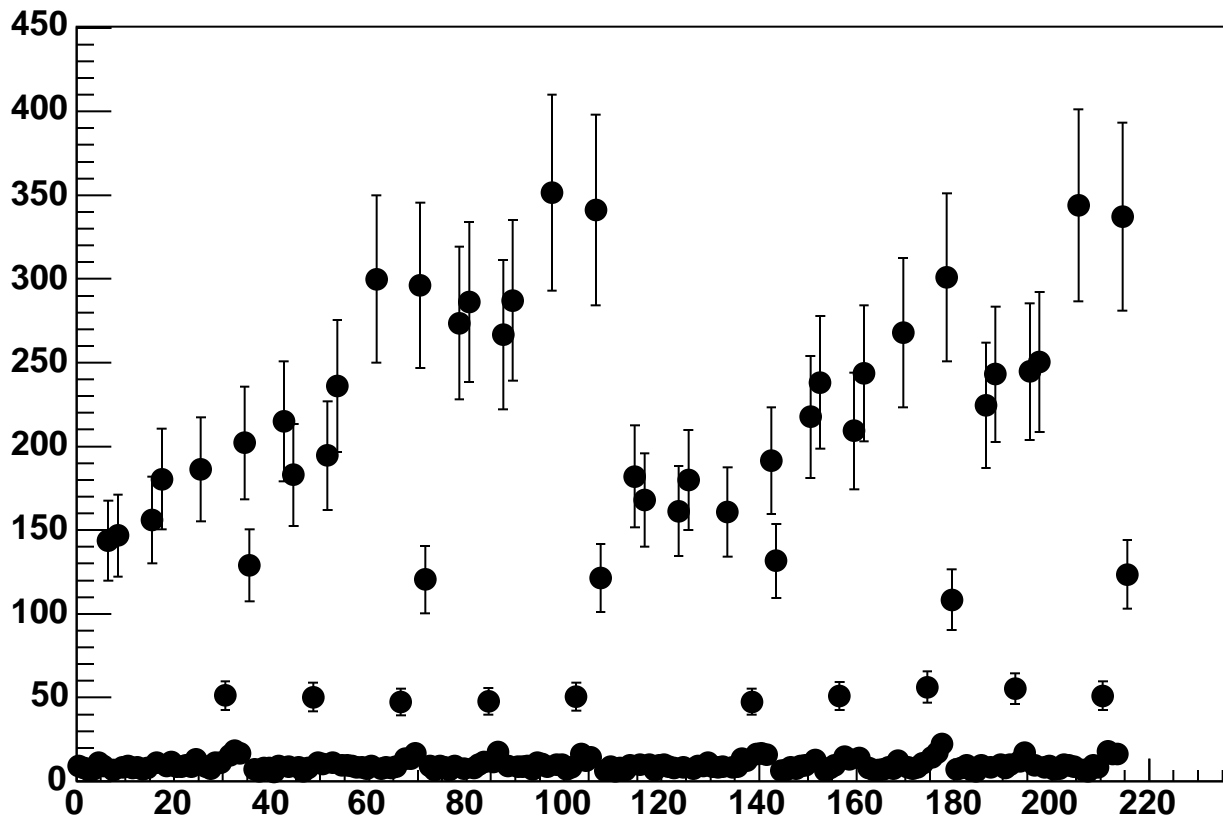
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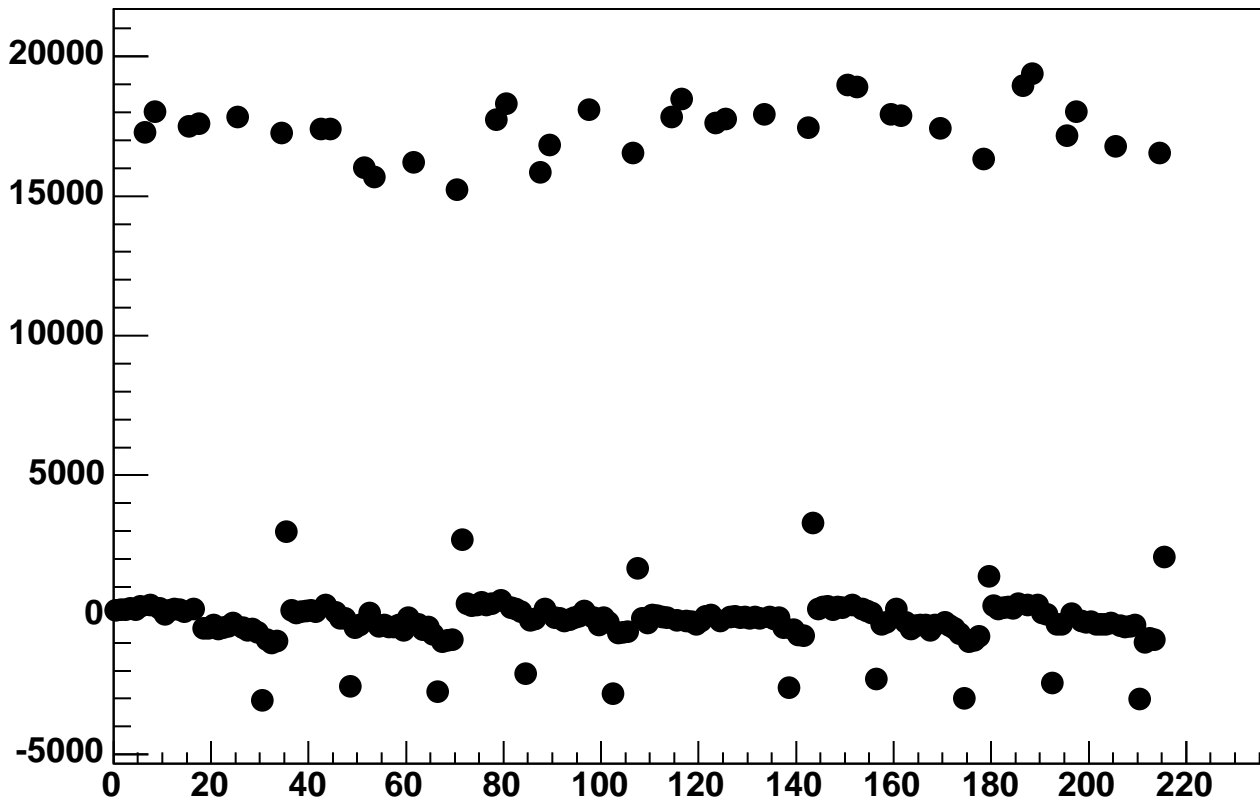
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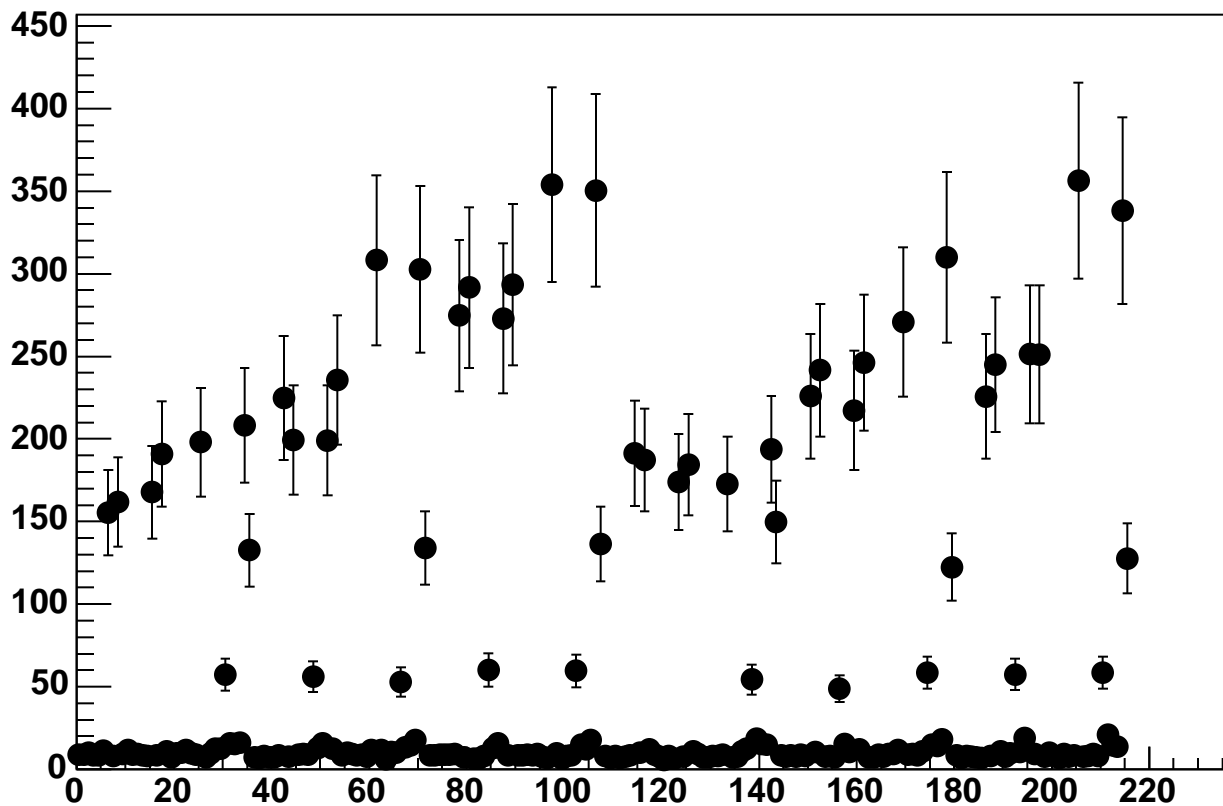
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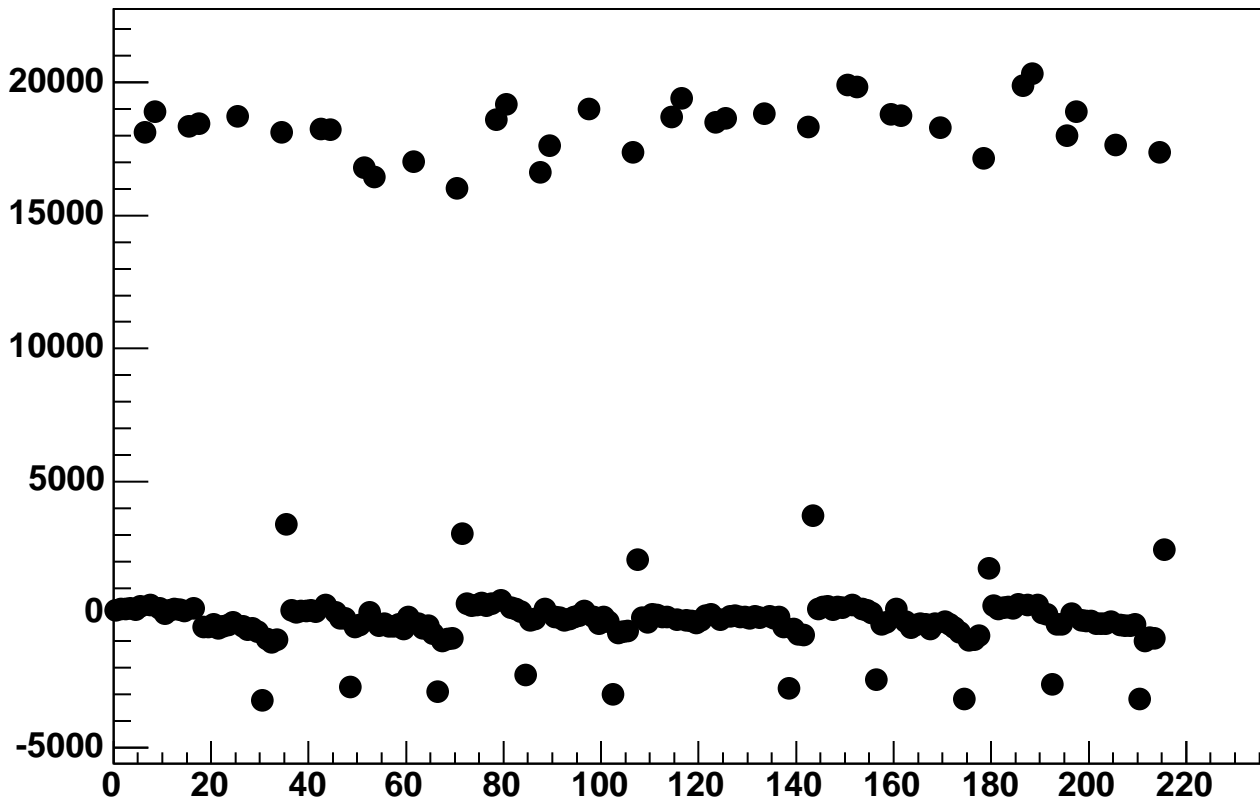
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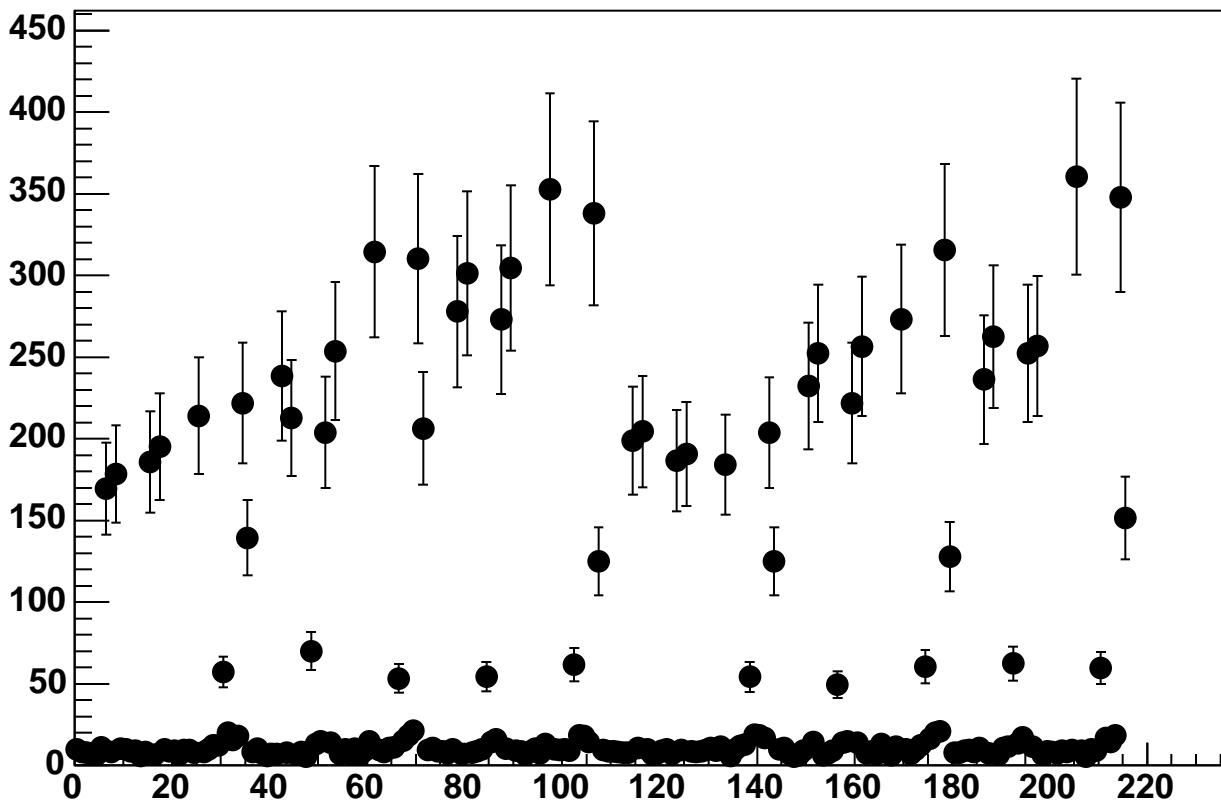
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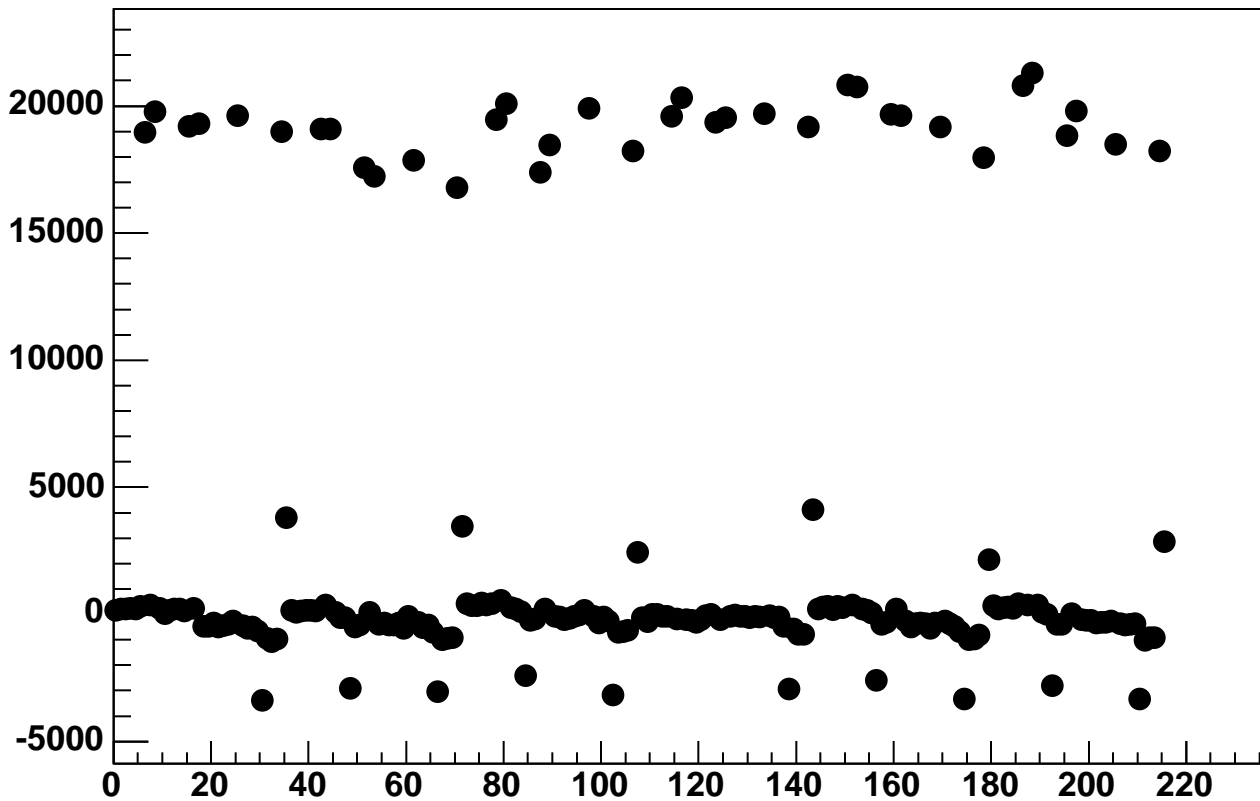
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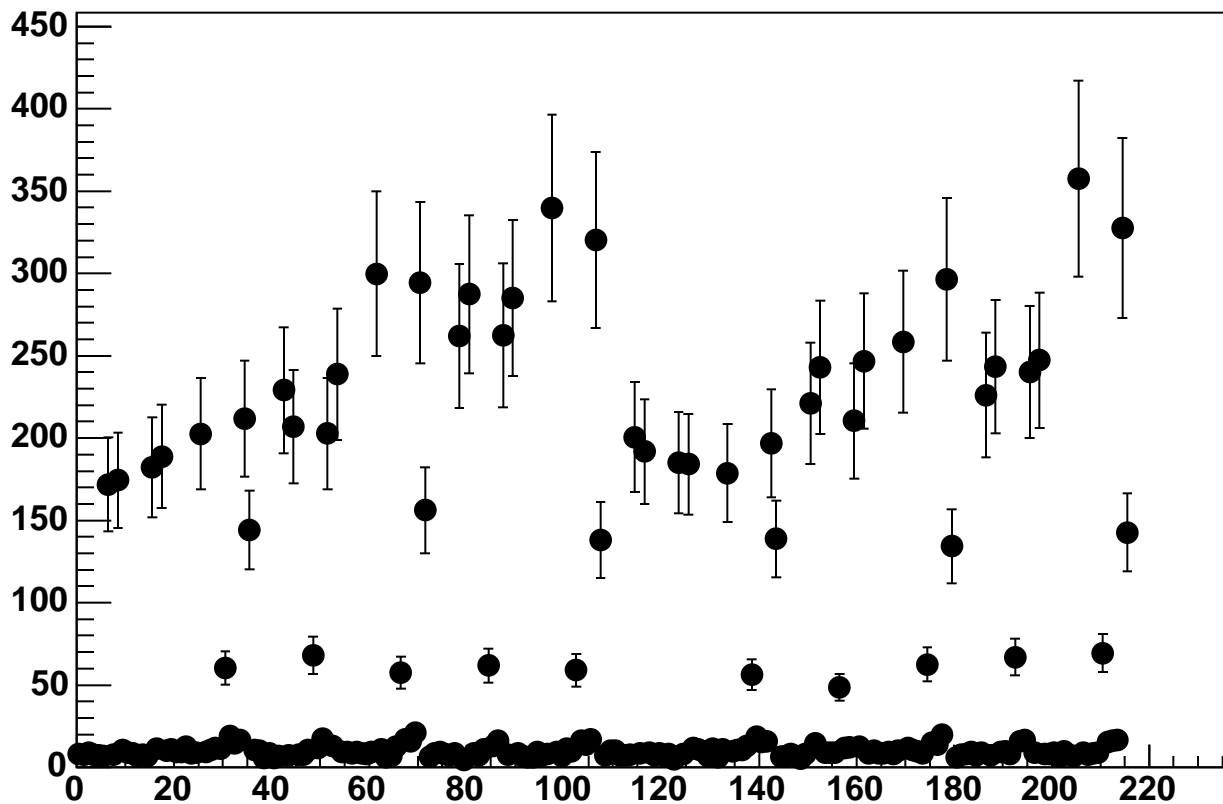
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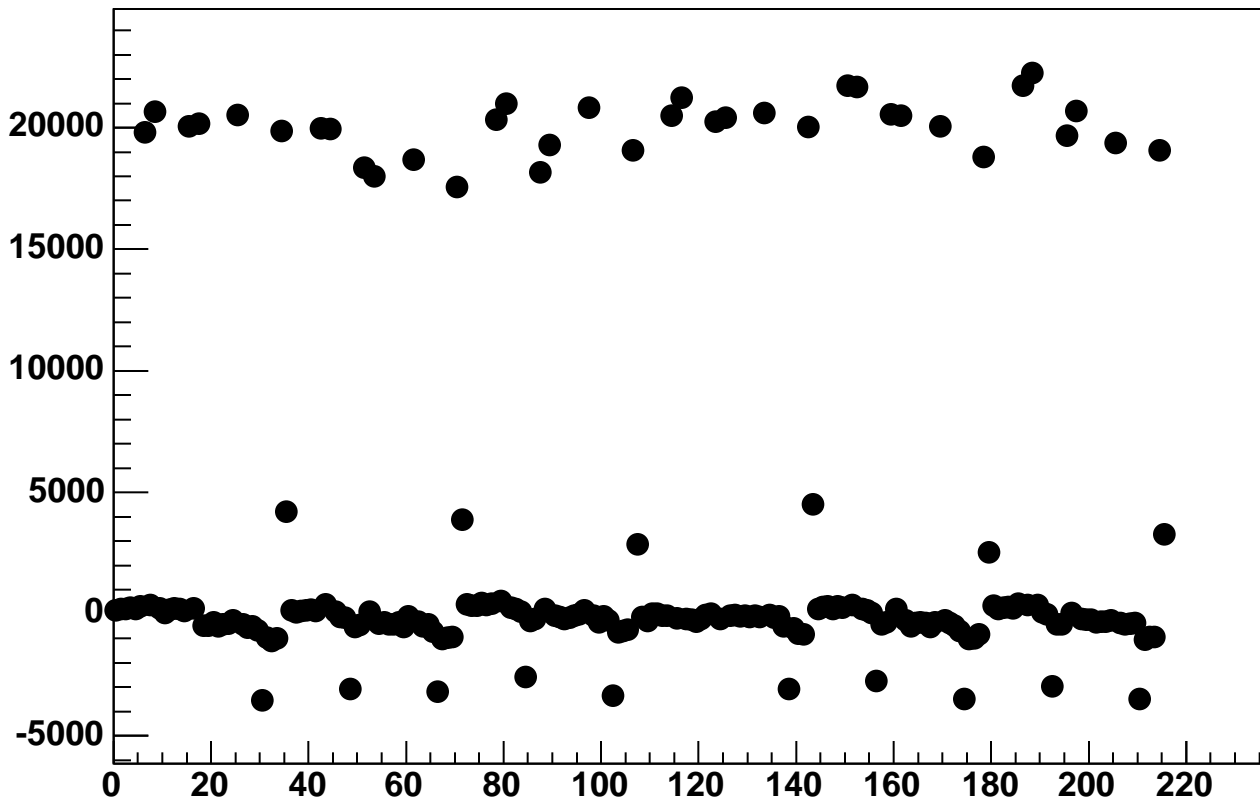
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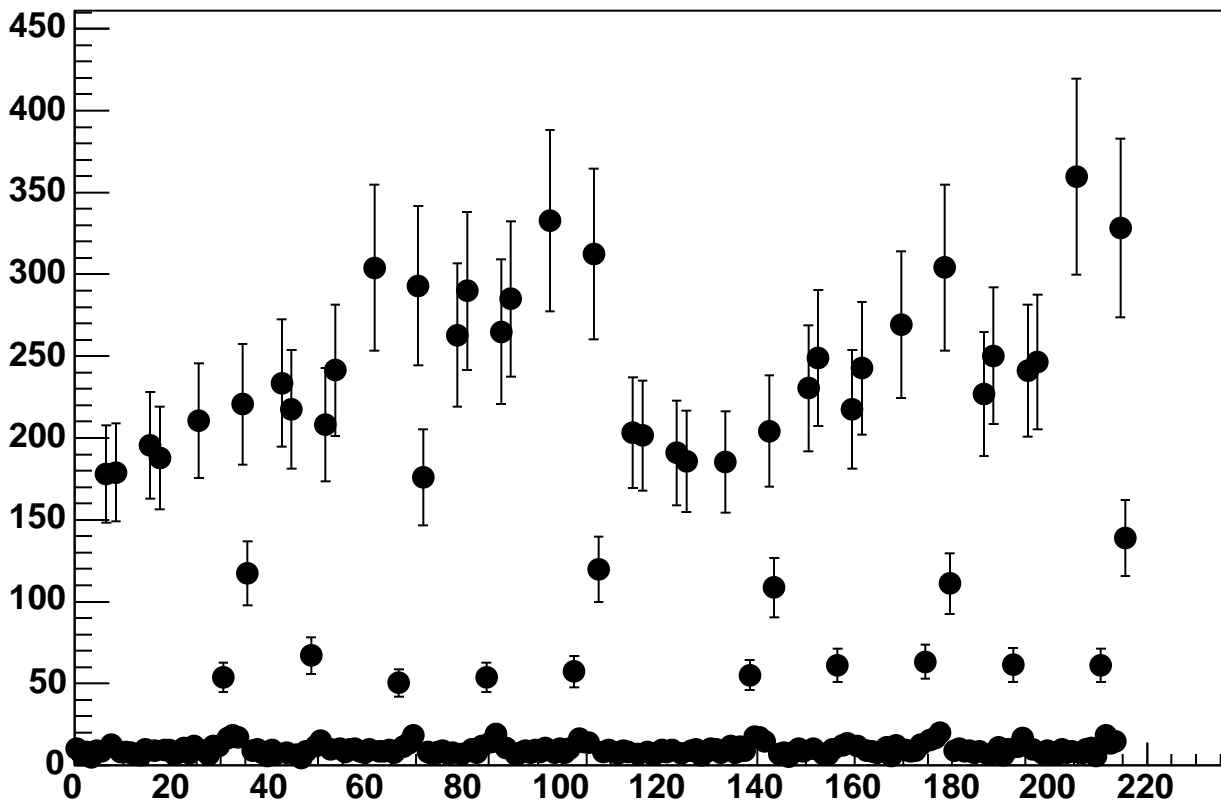
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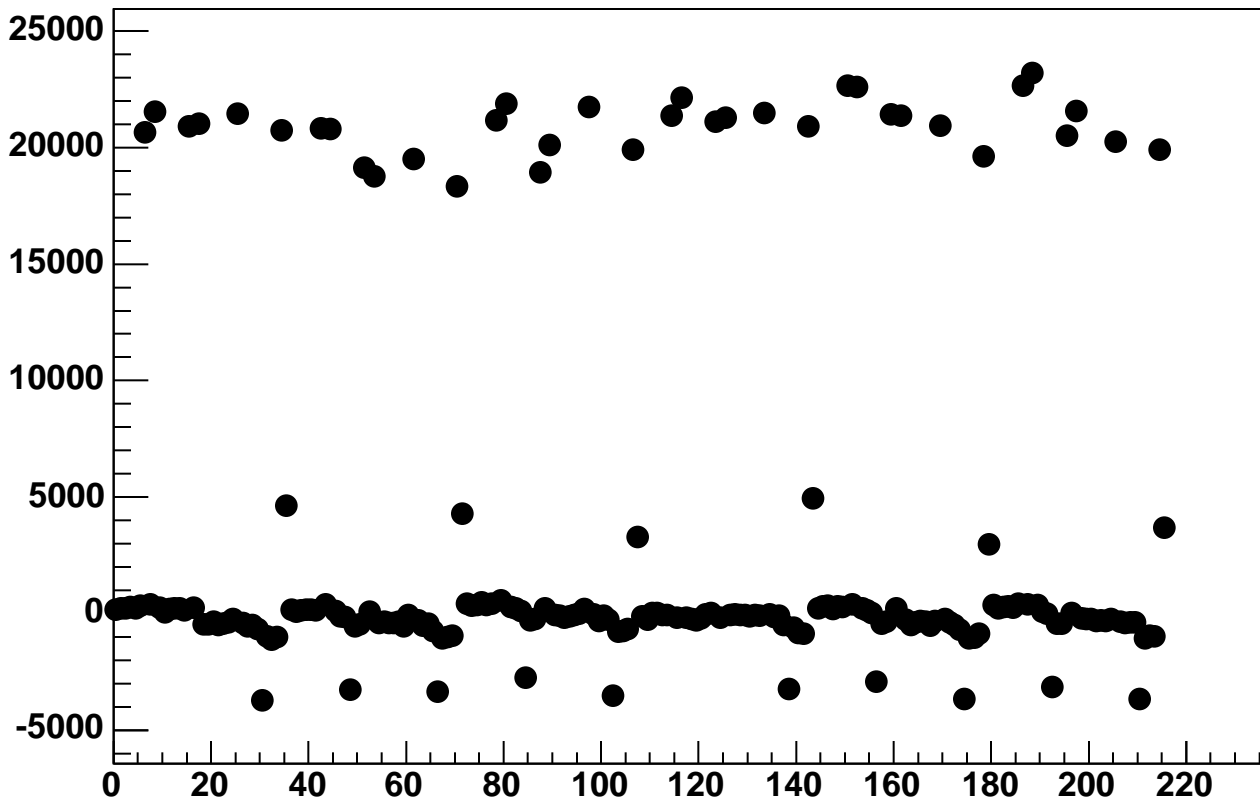
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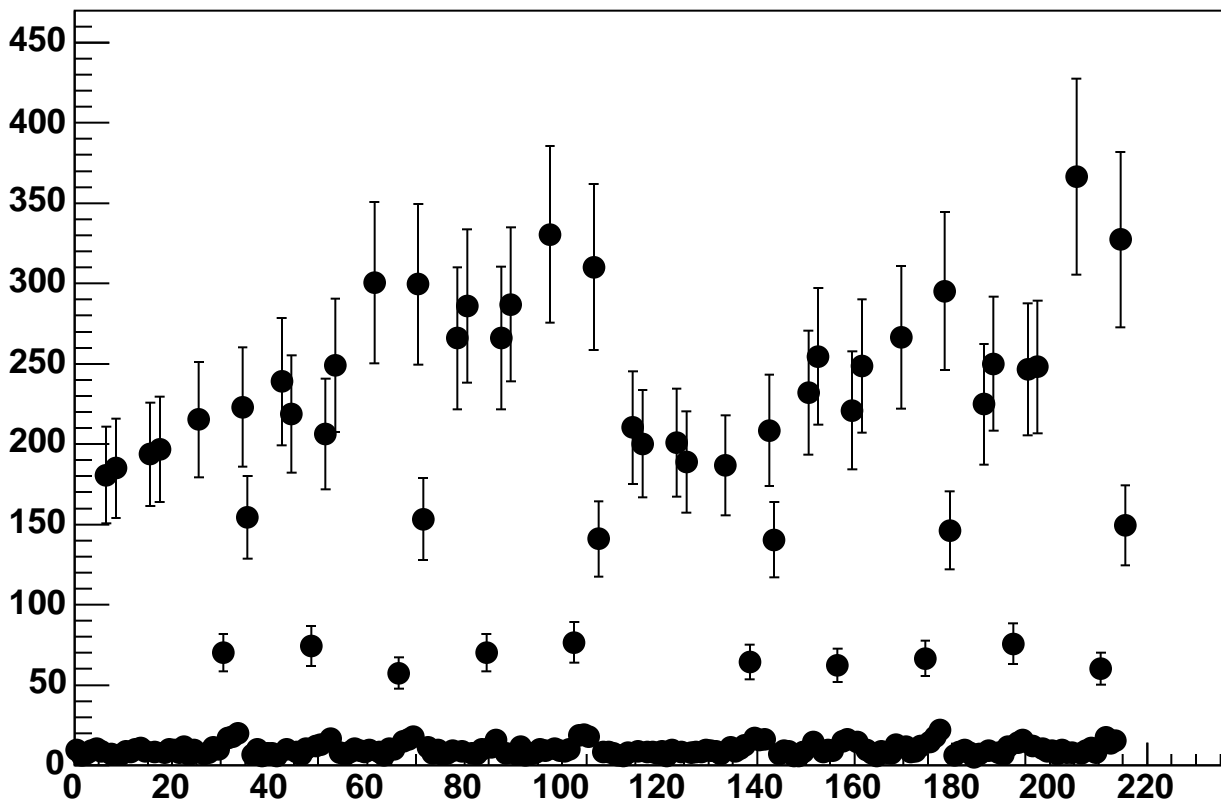
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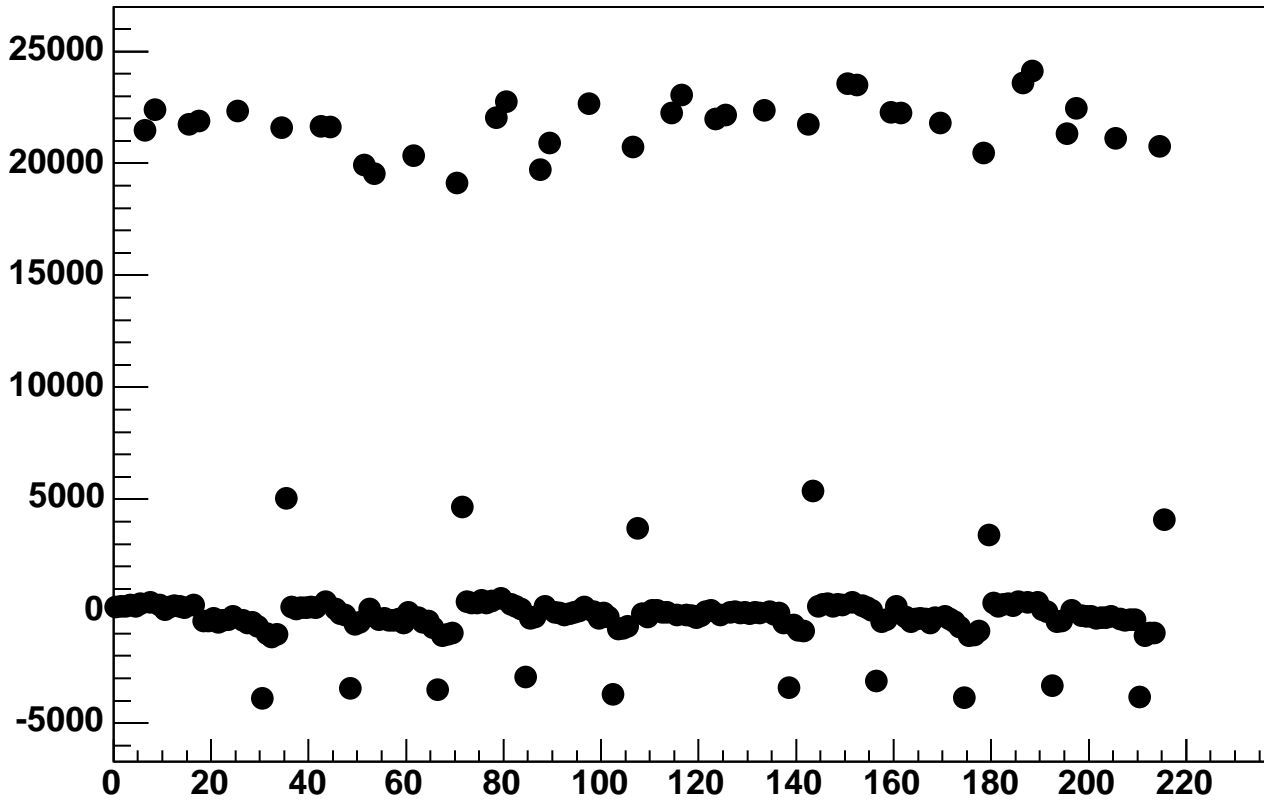
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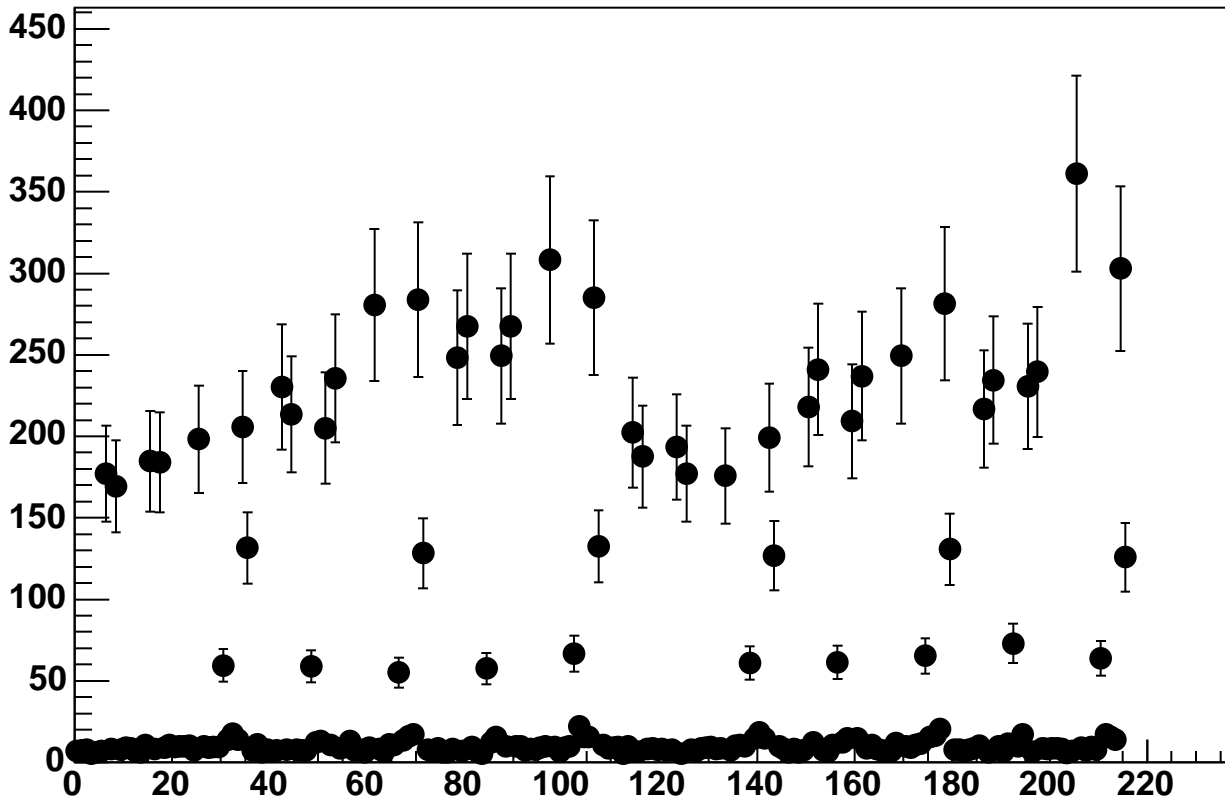
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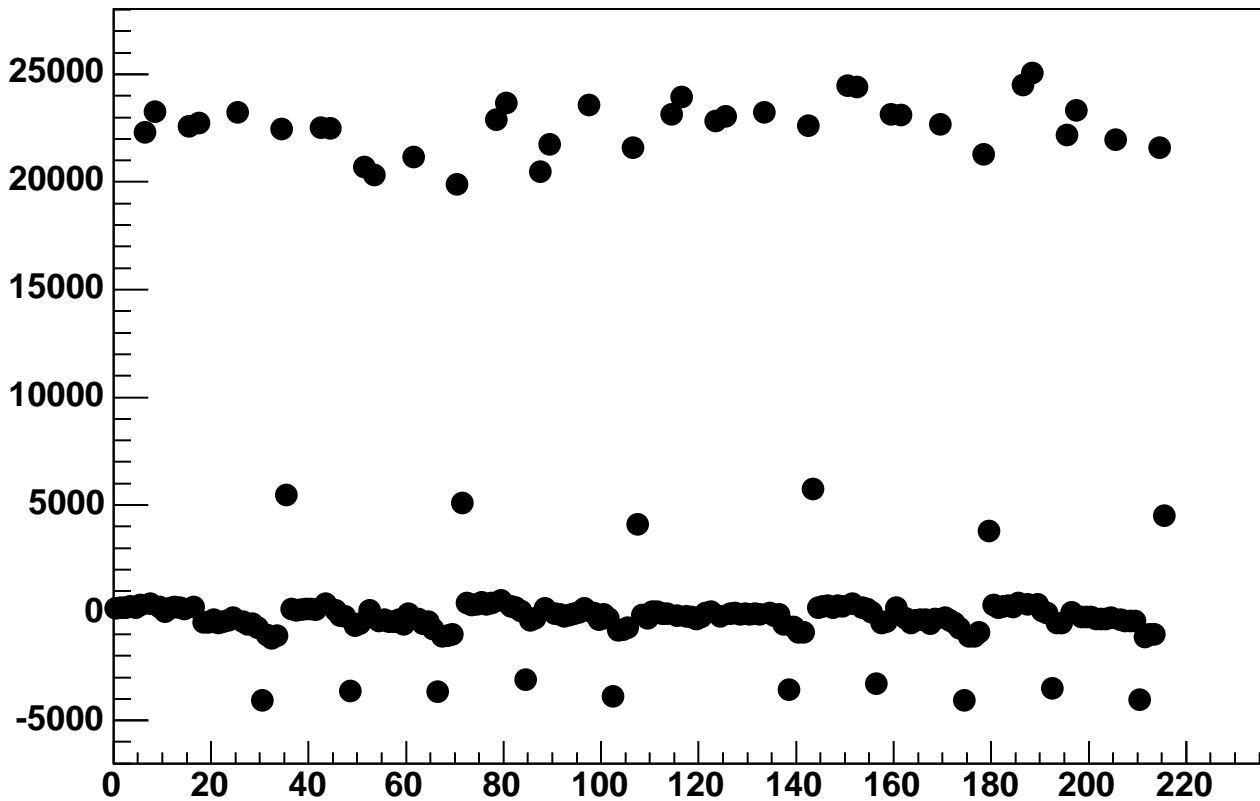
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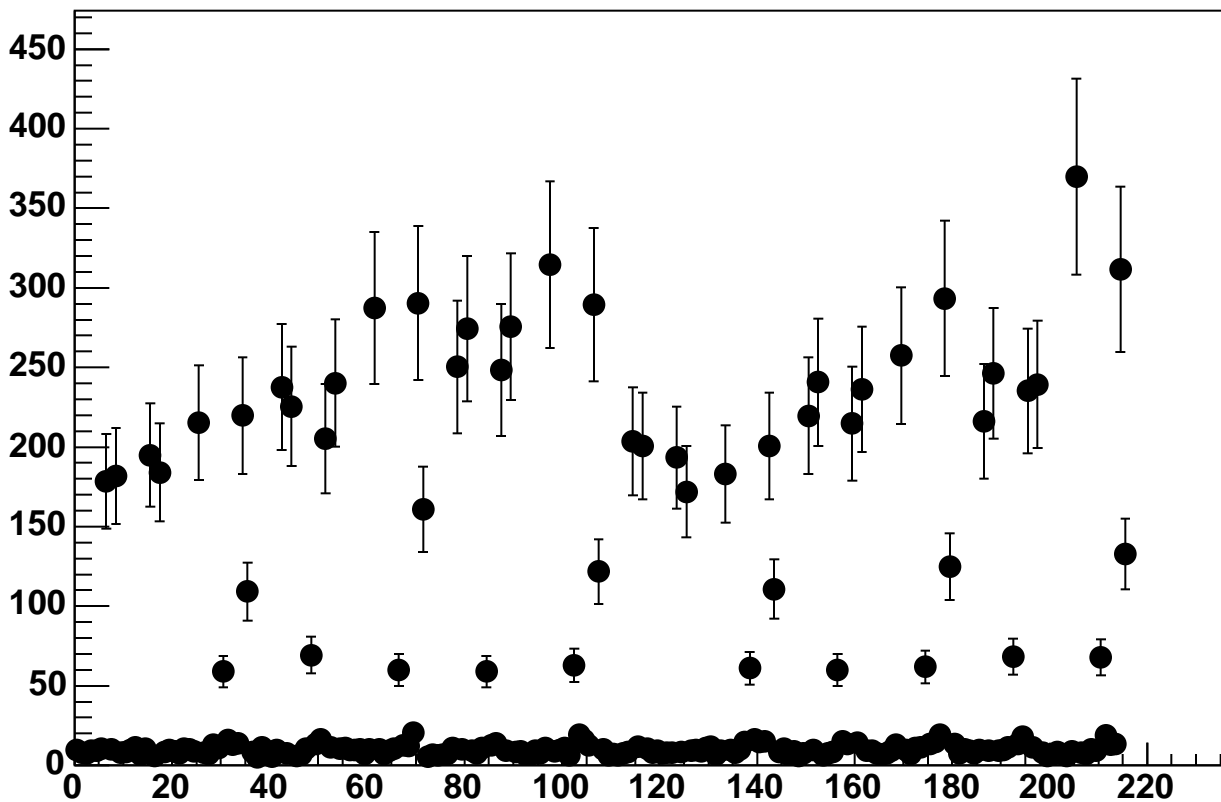
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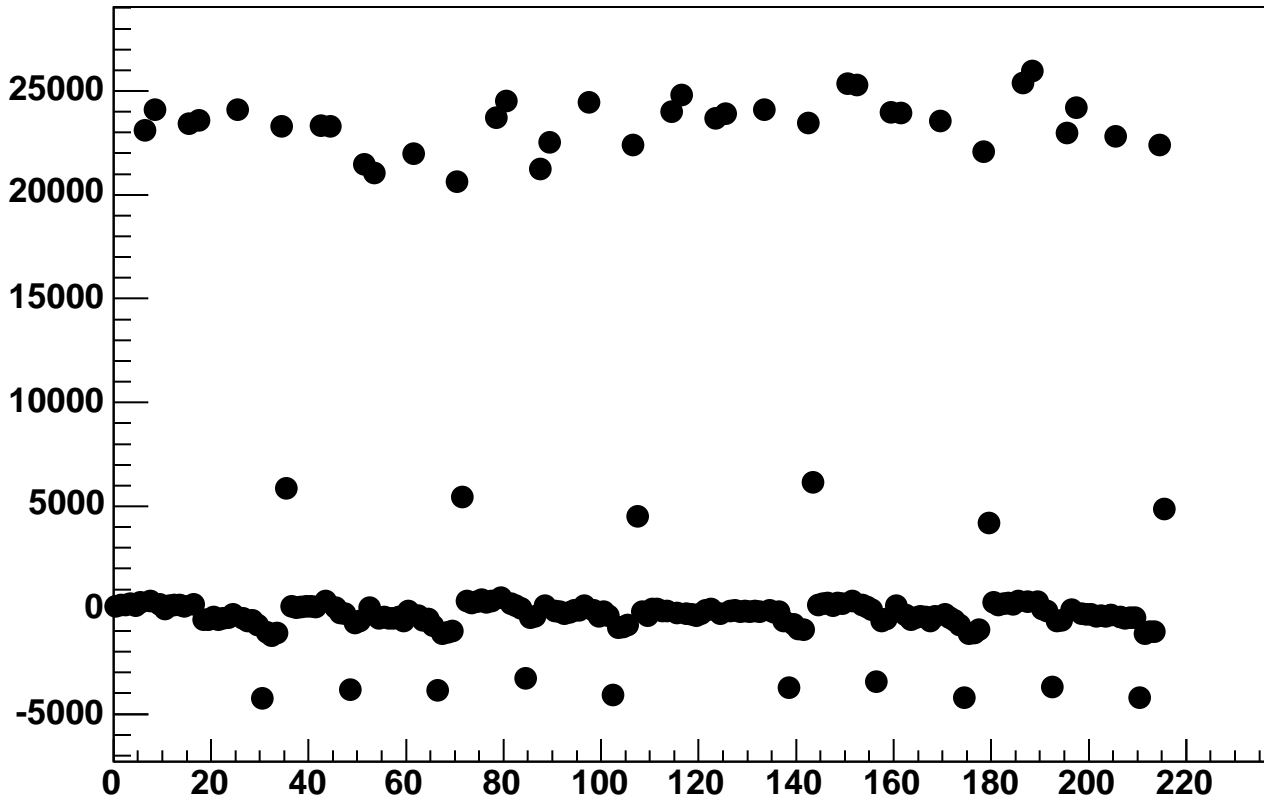
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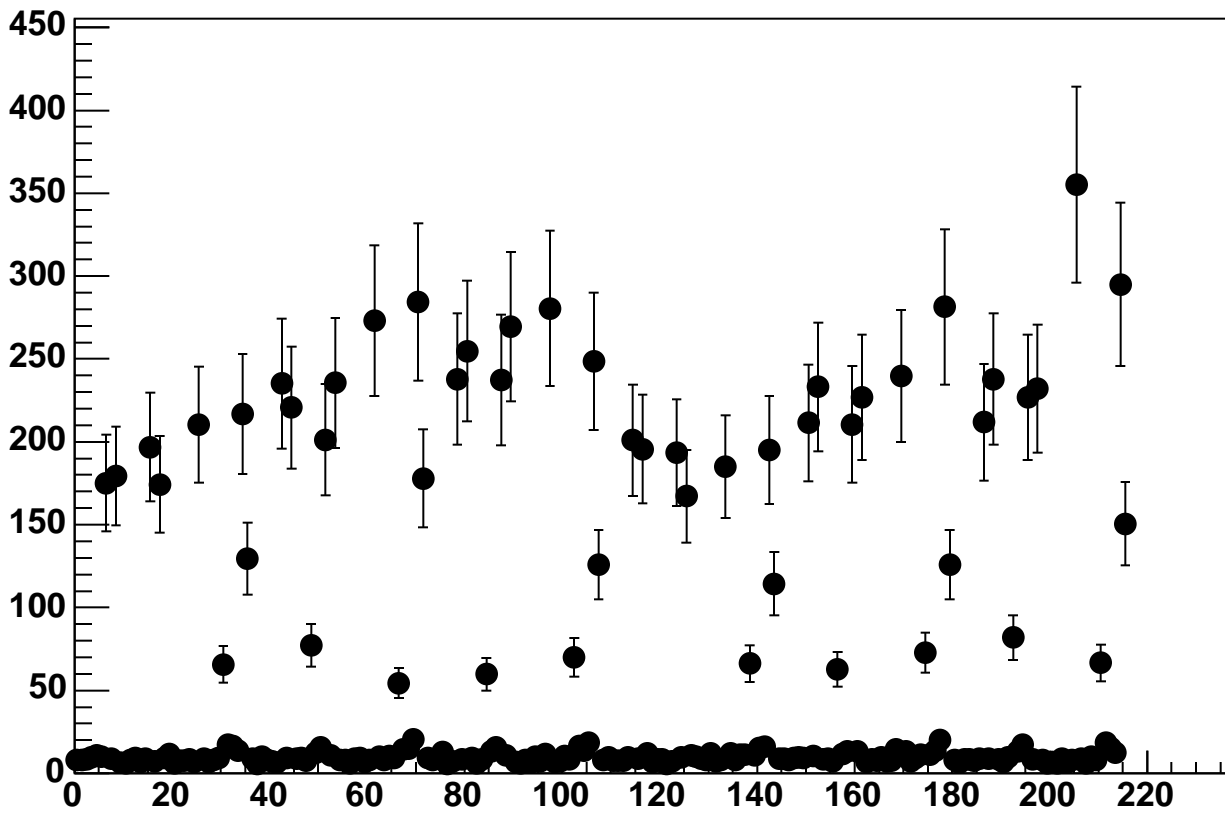
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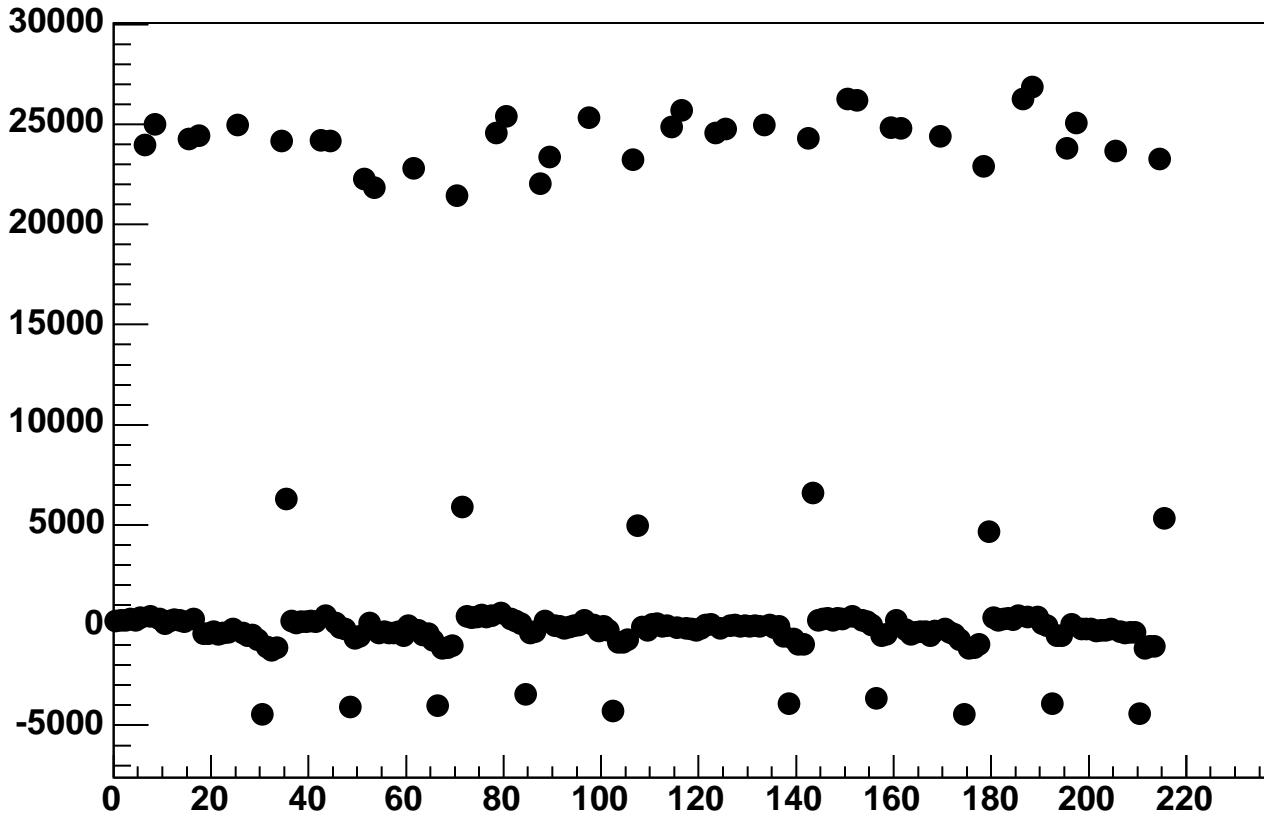
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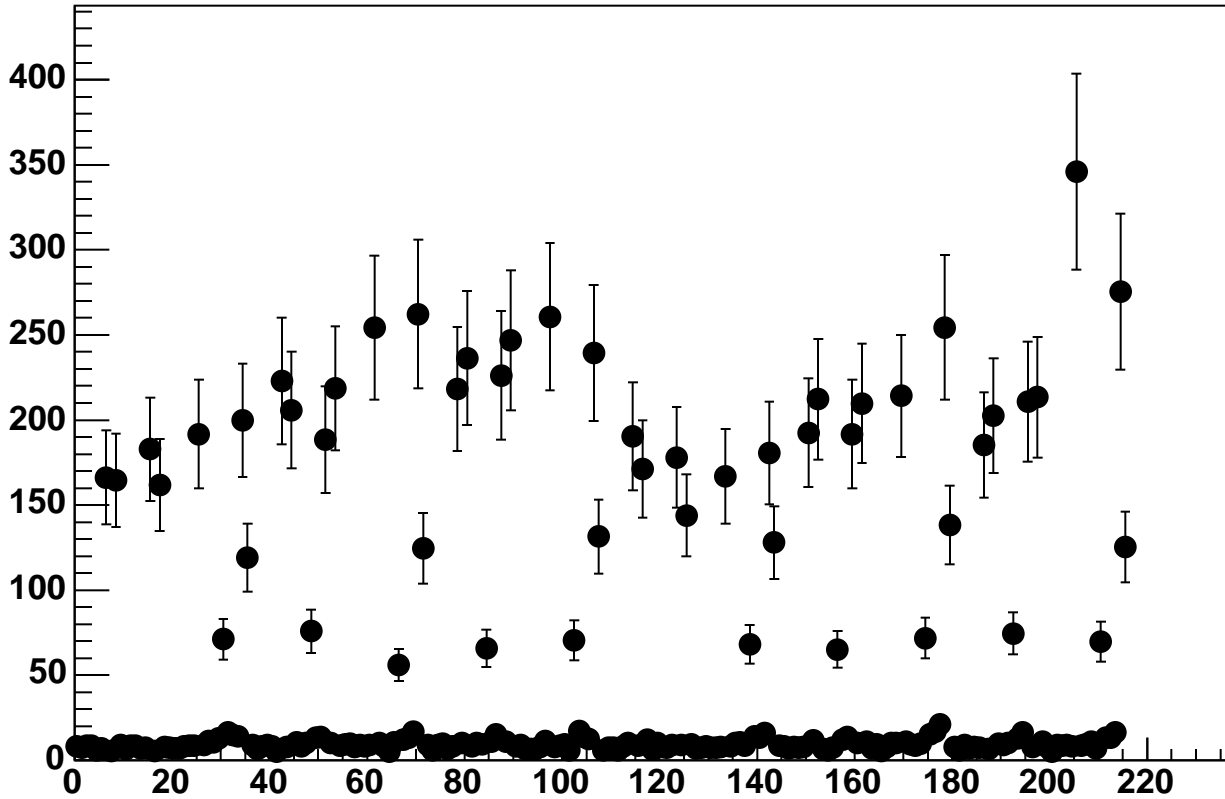
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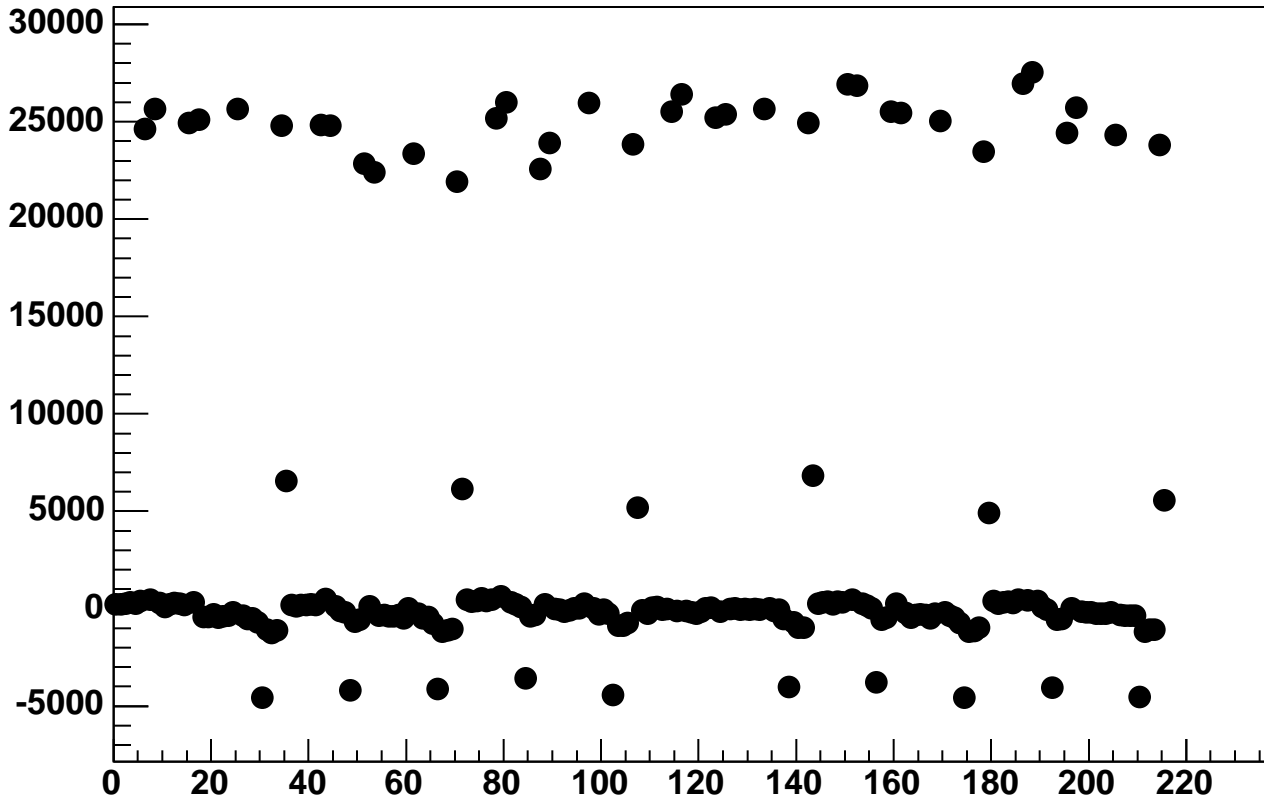
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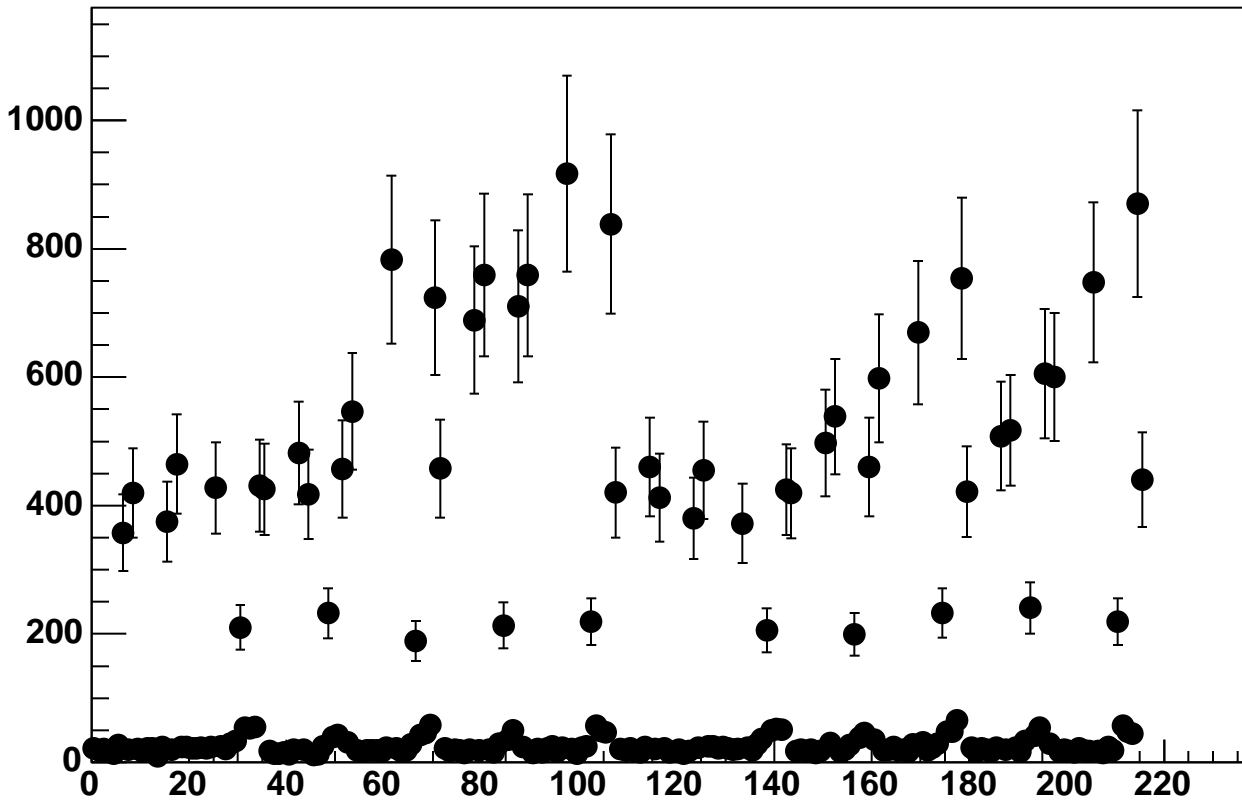
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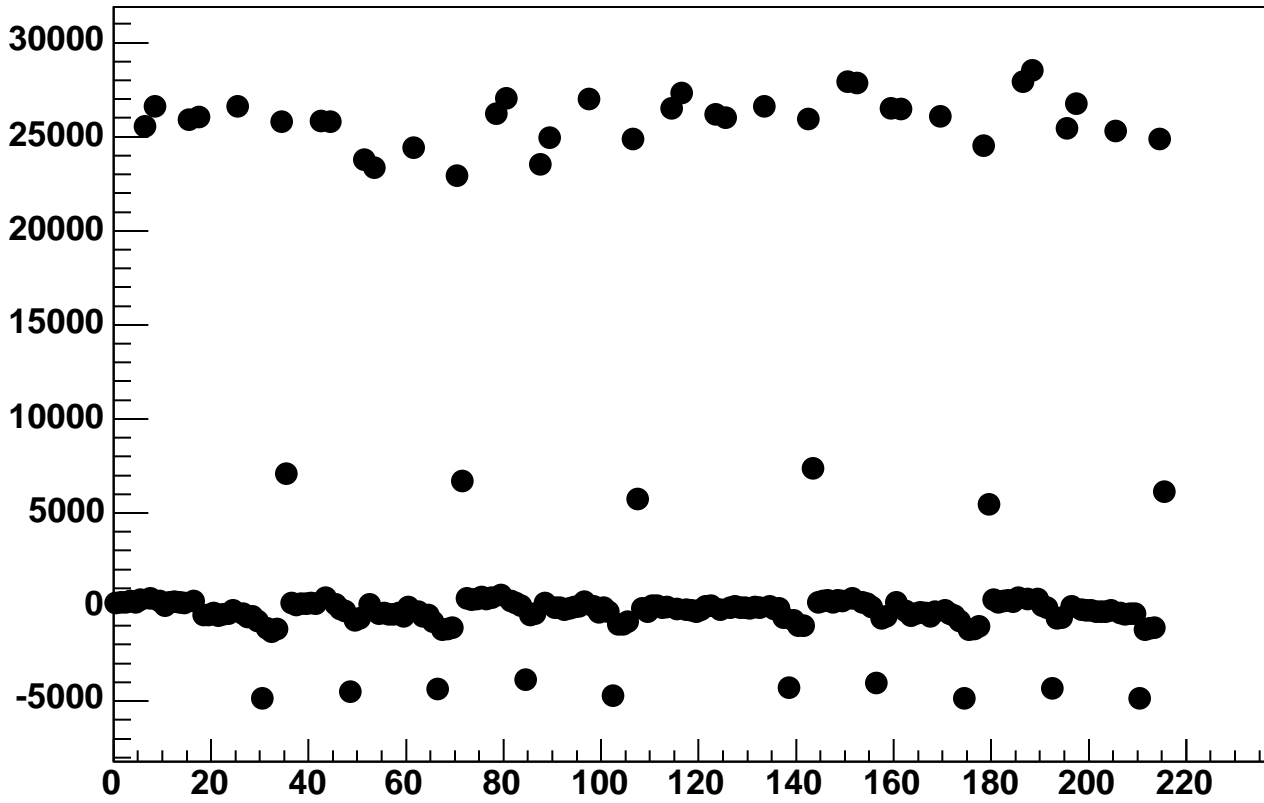
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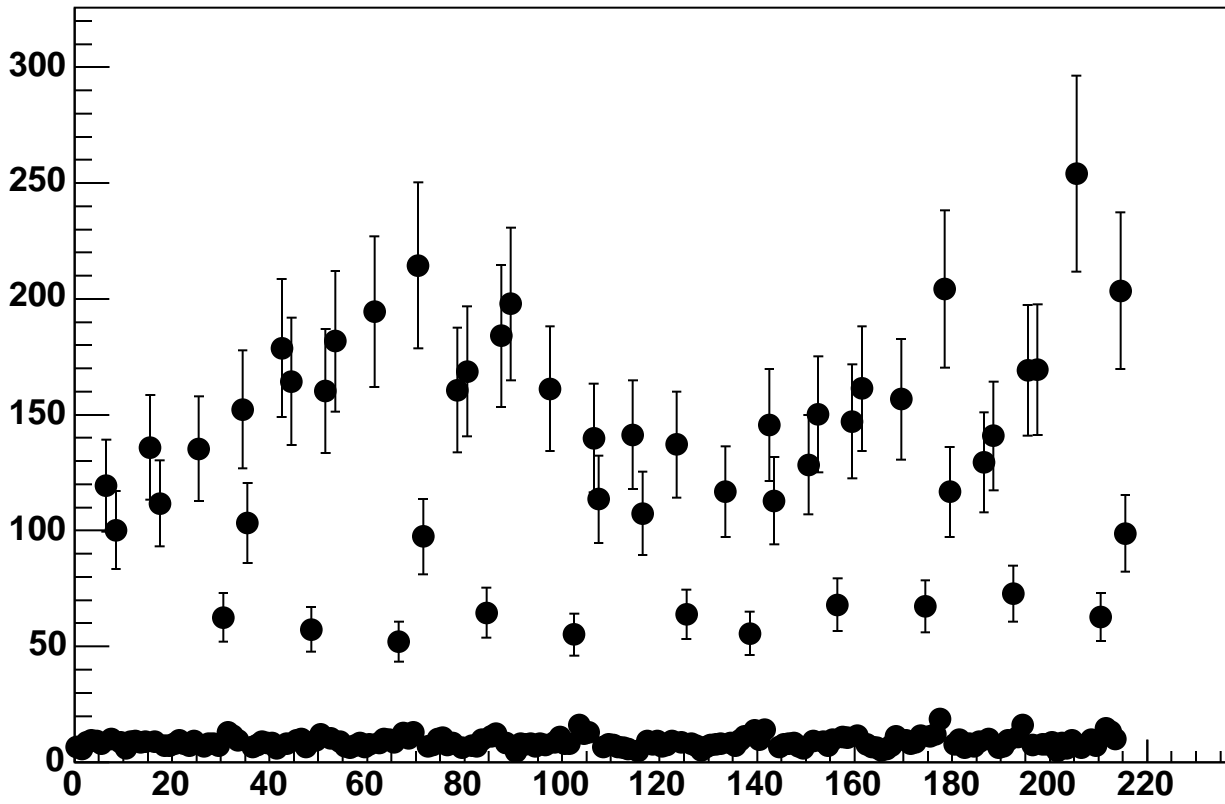
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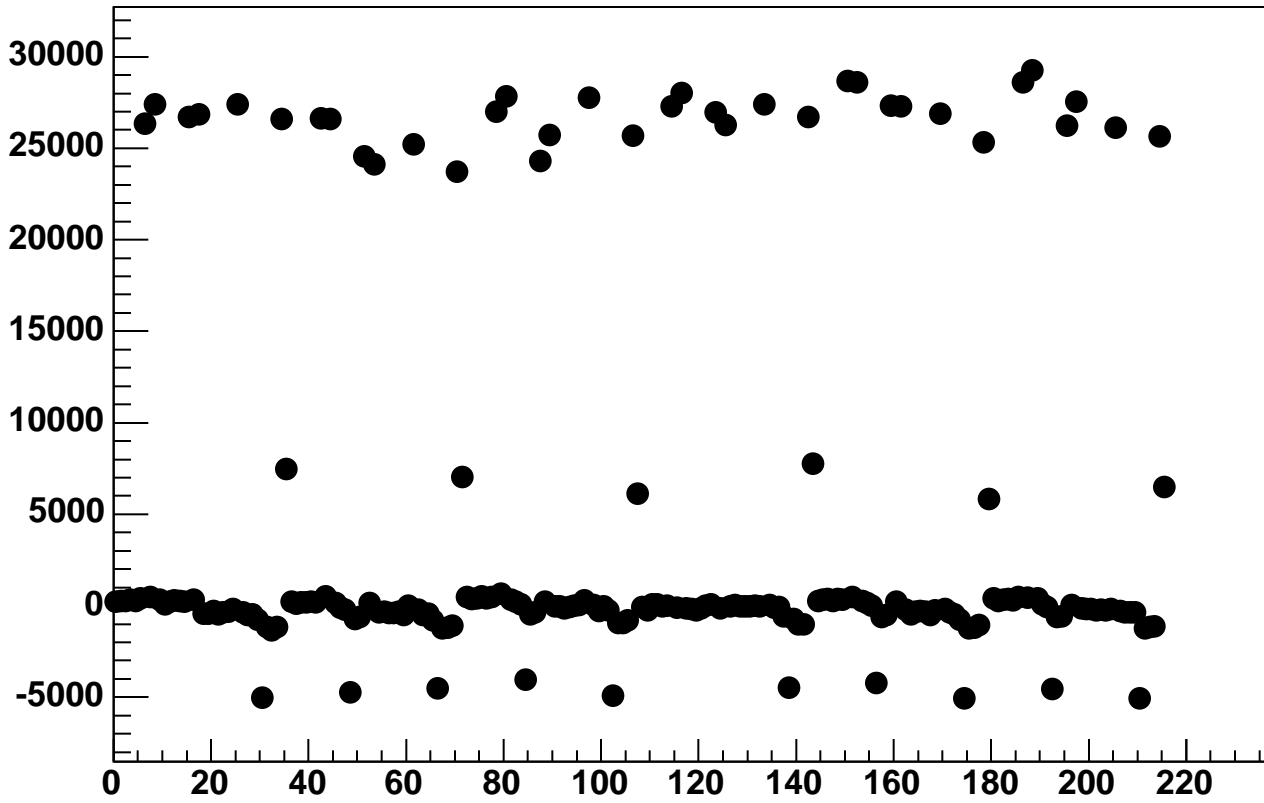
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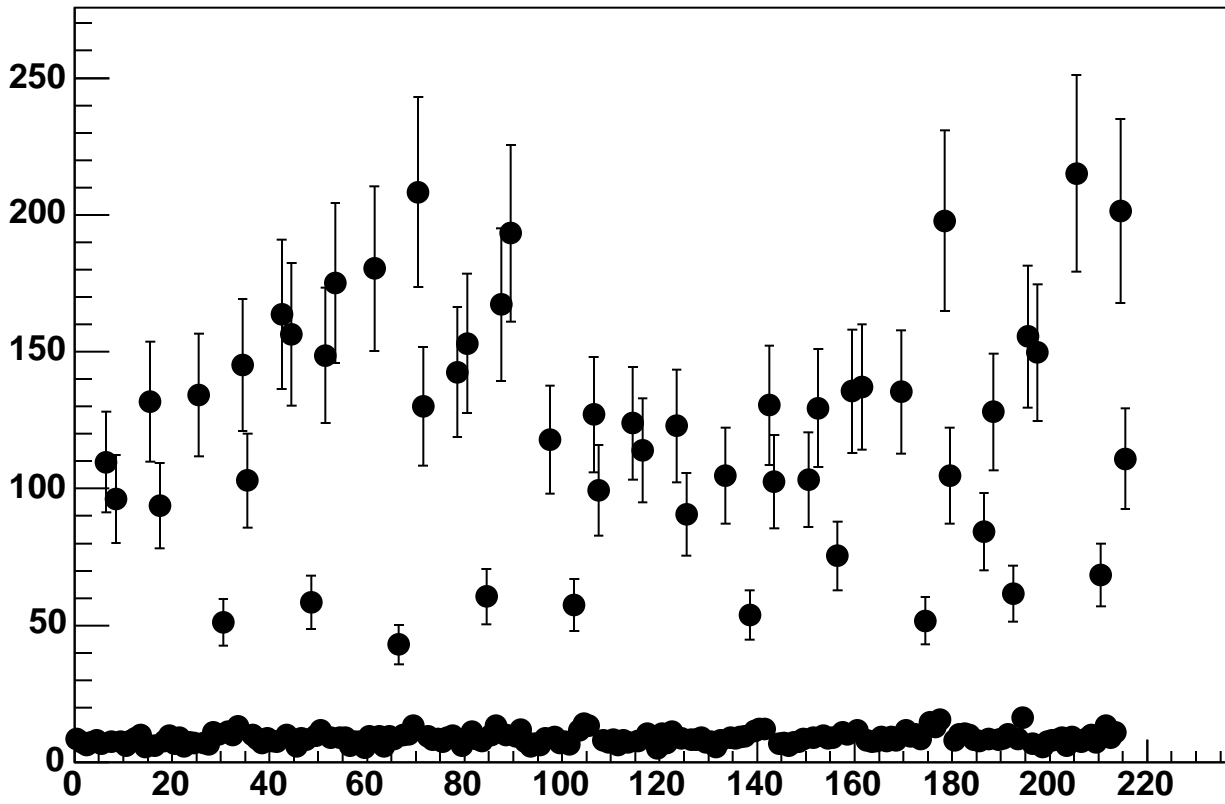
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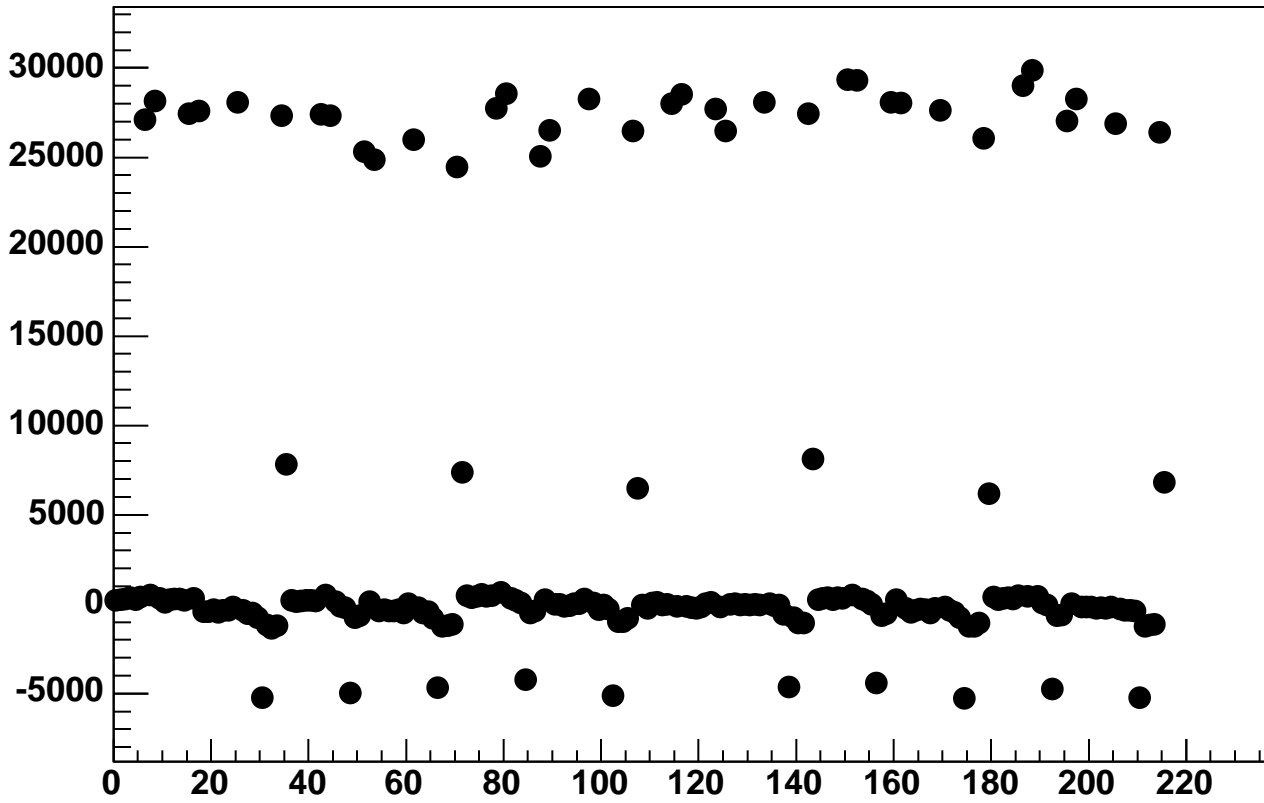
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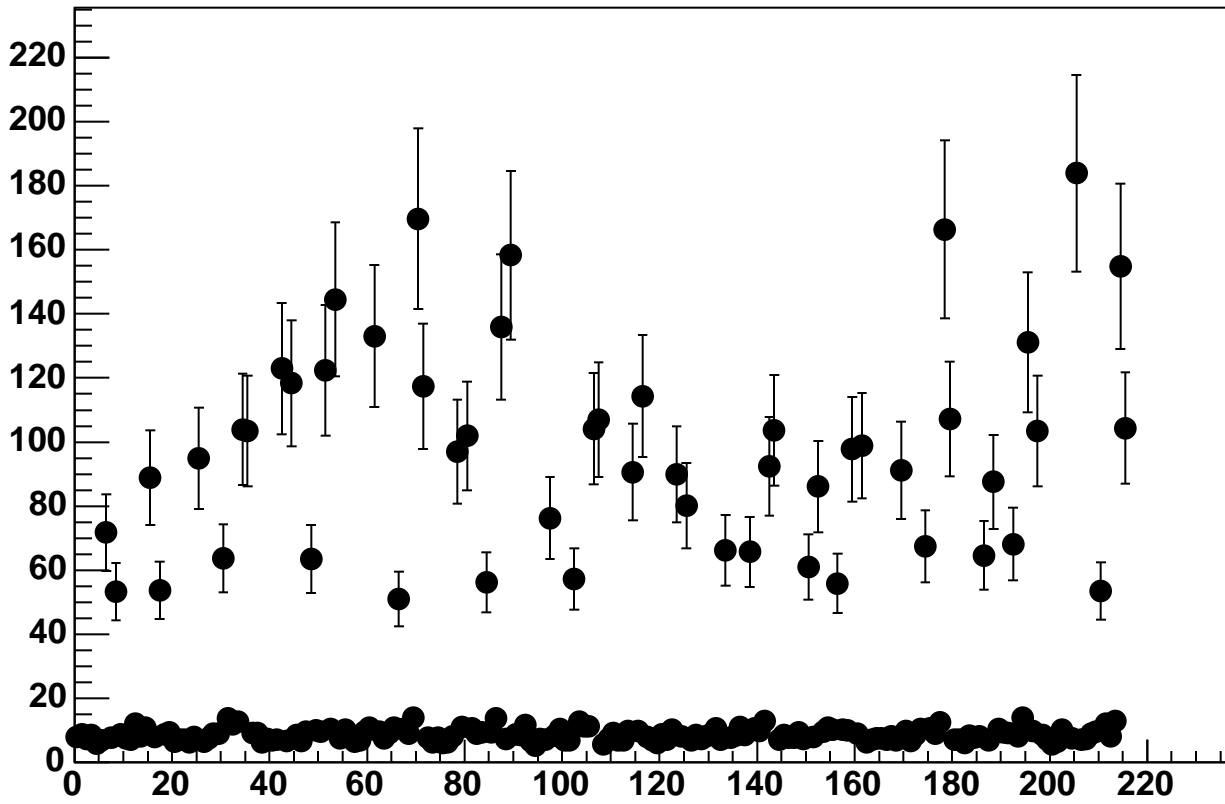
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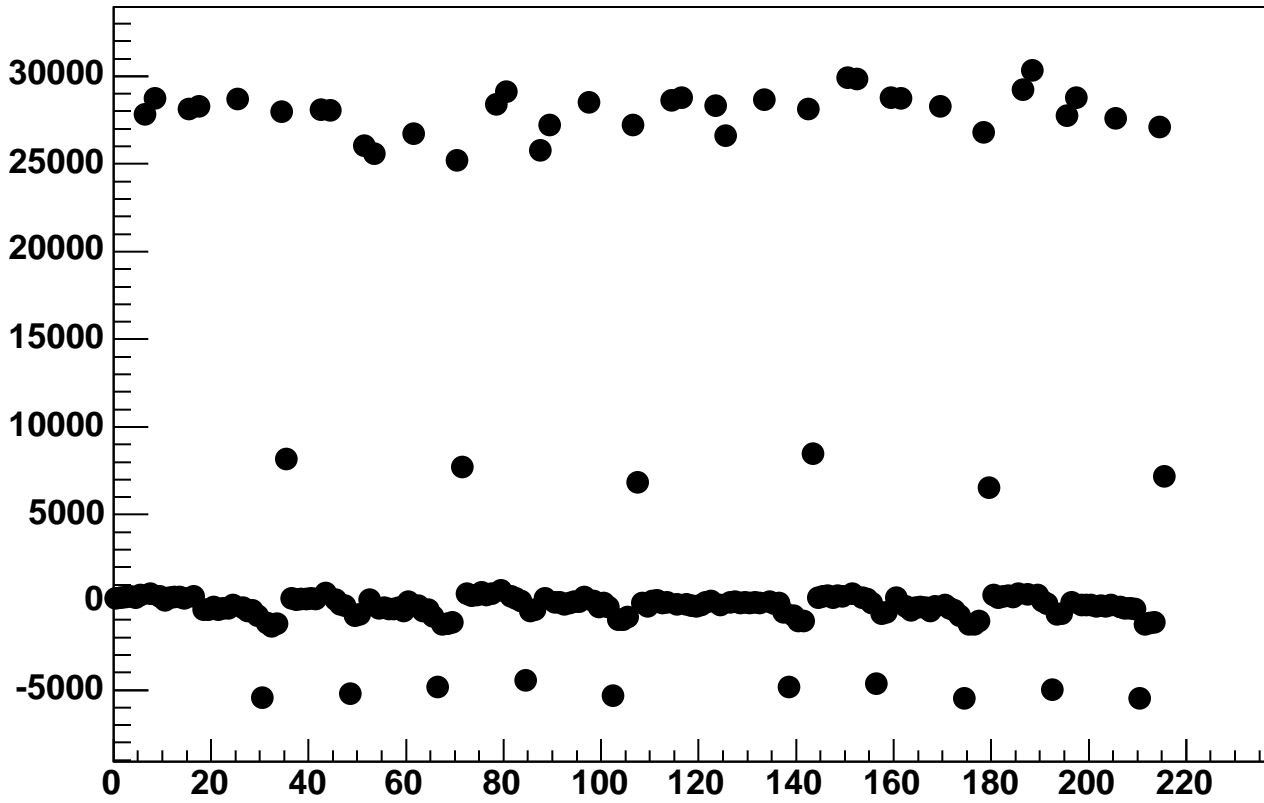
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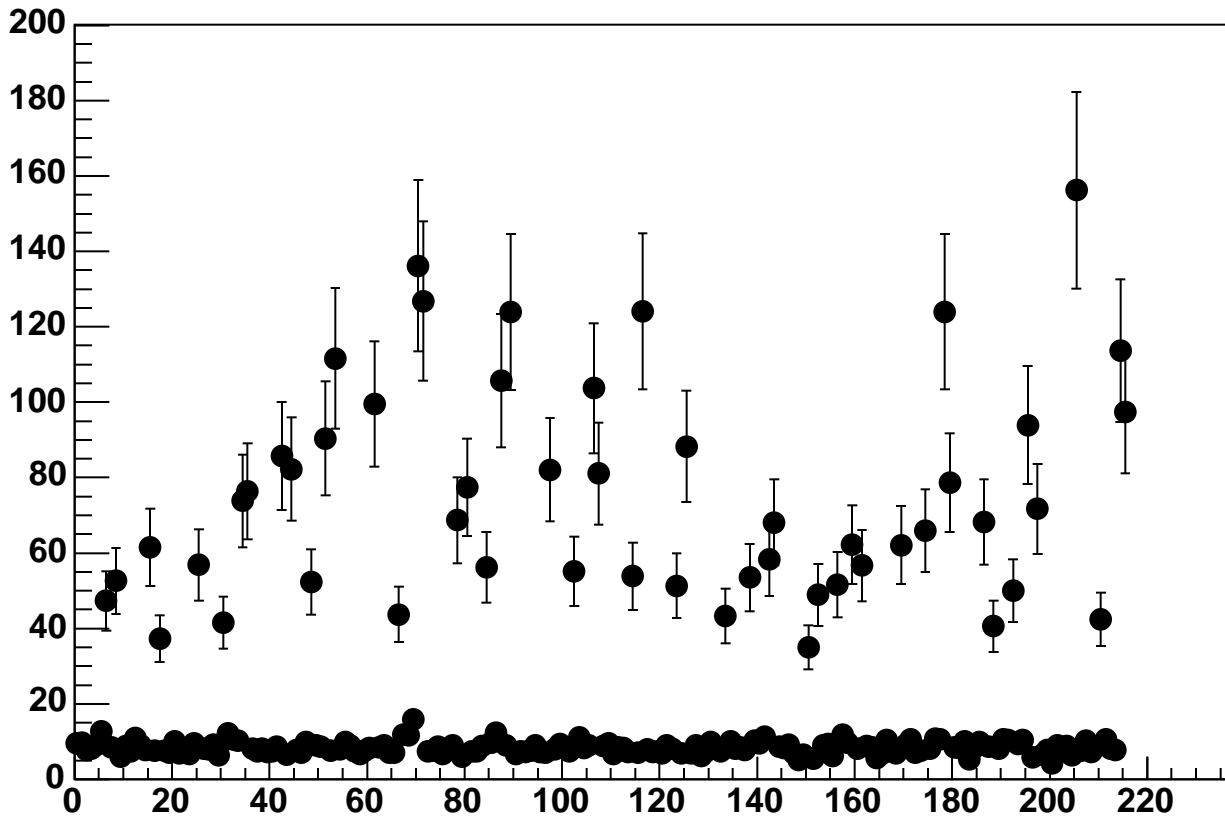
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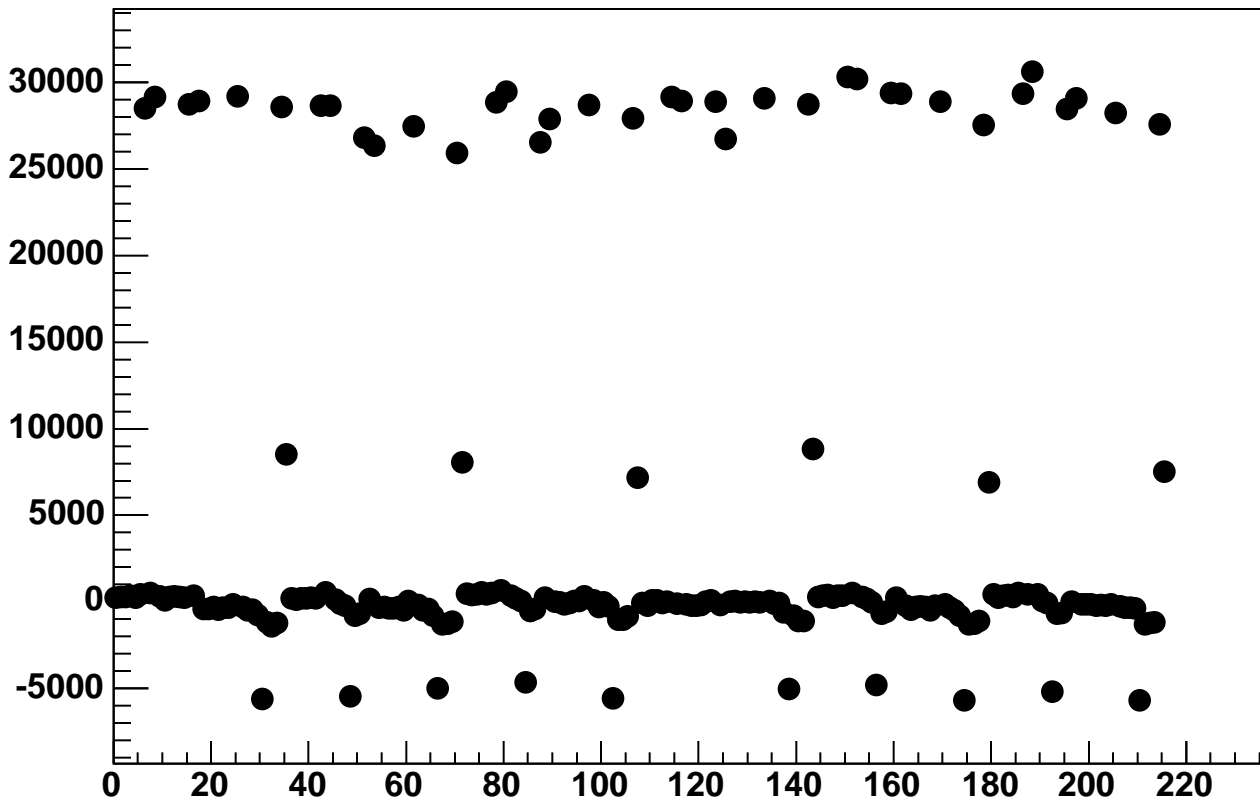
Enable 2, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



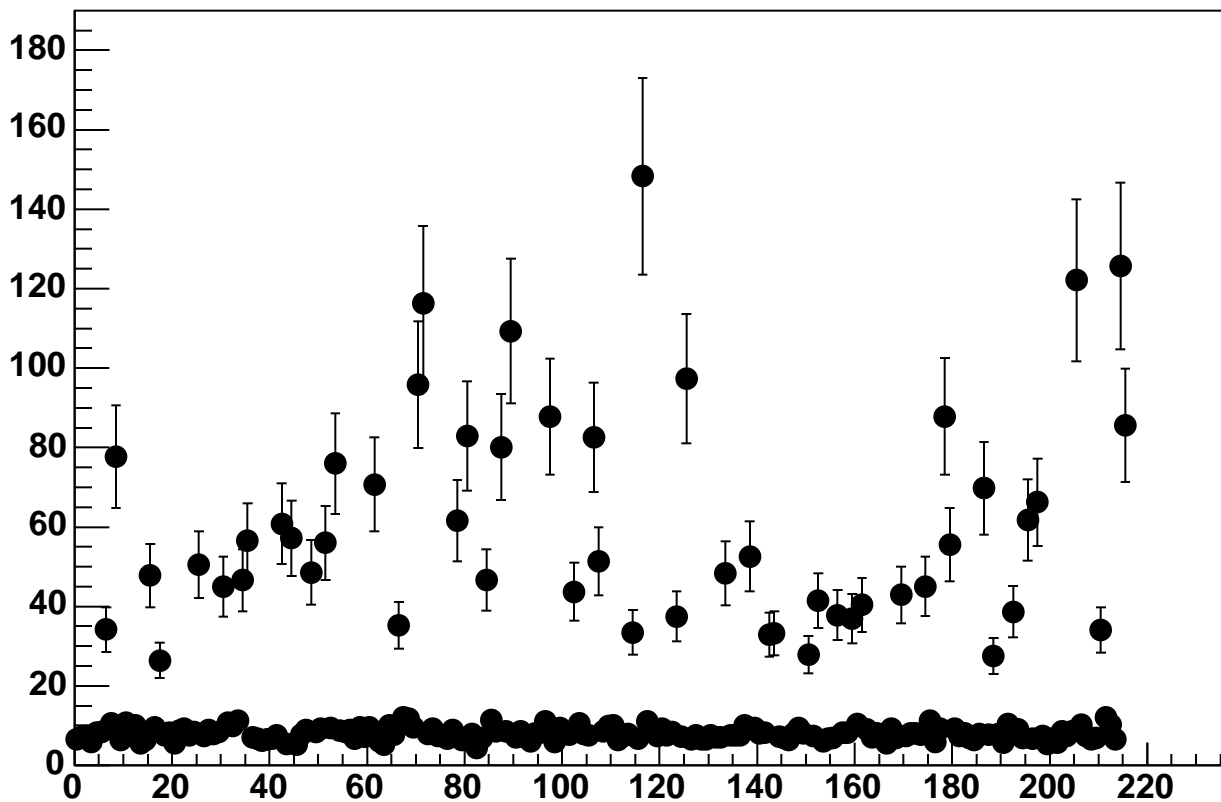
Enable 2, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



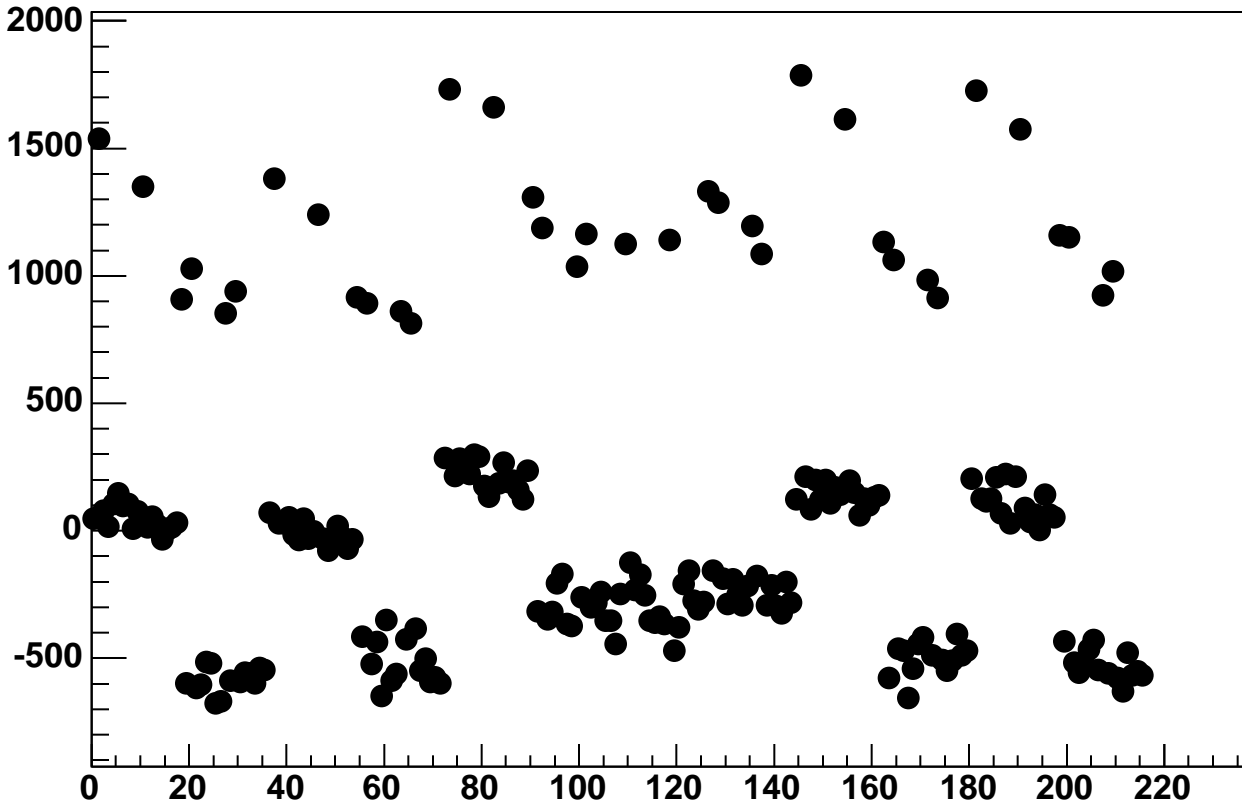
Enable 2, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



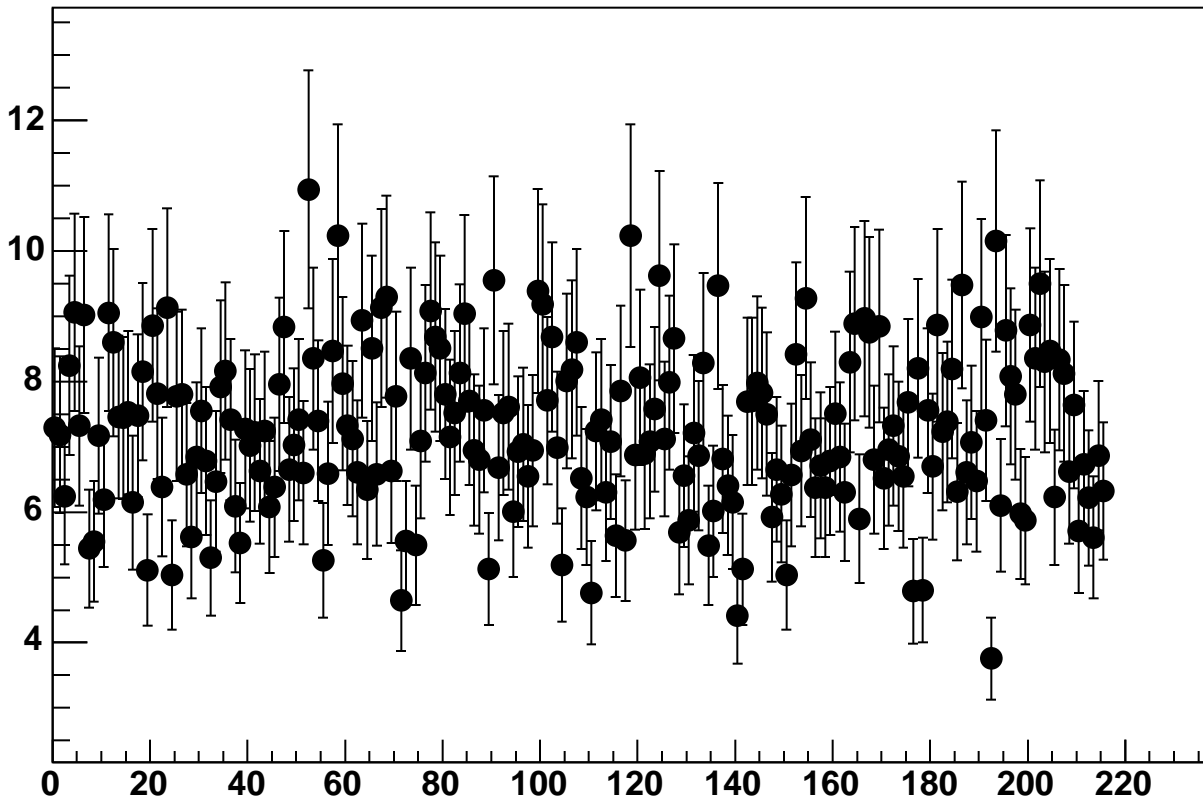
Enable 2, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



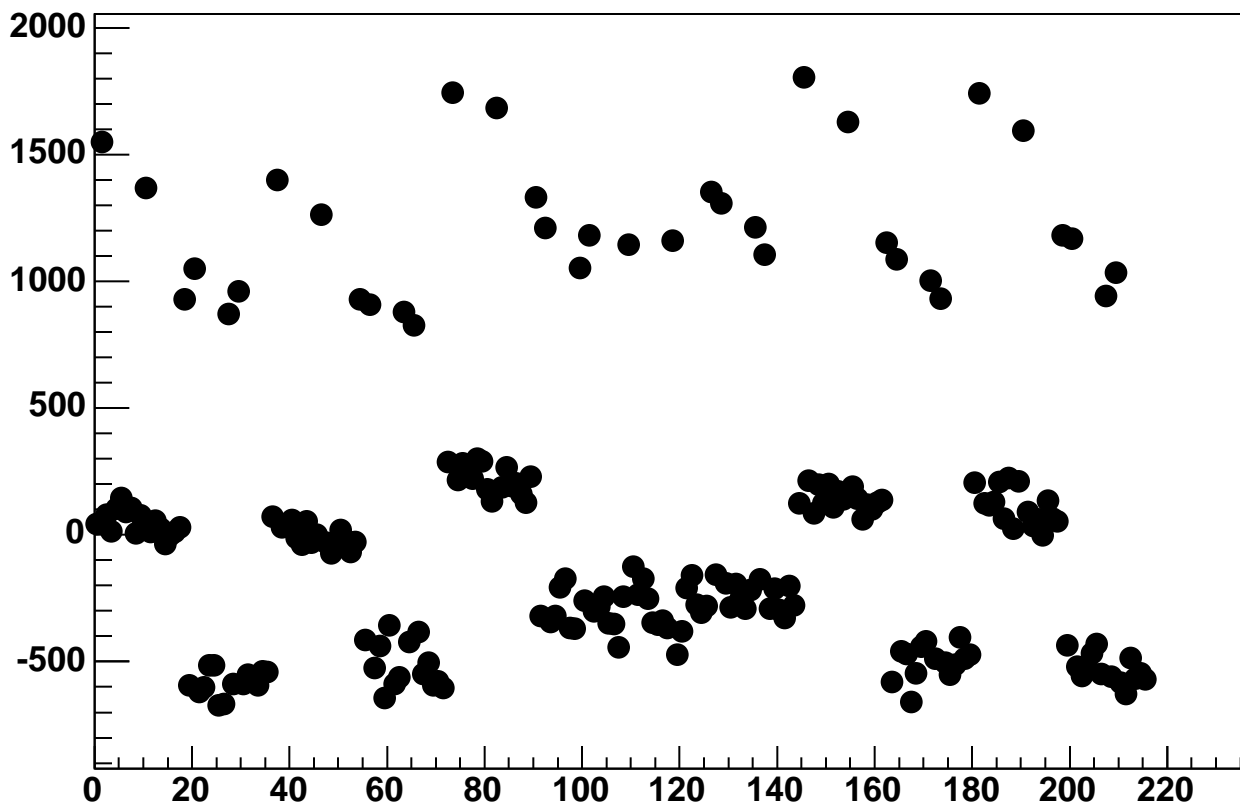
Enable 3, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



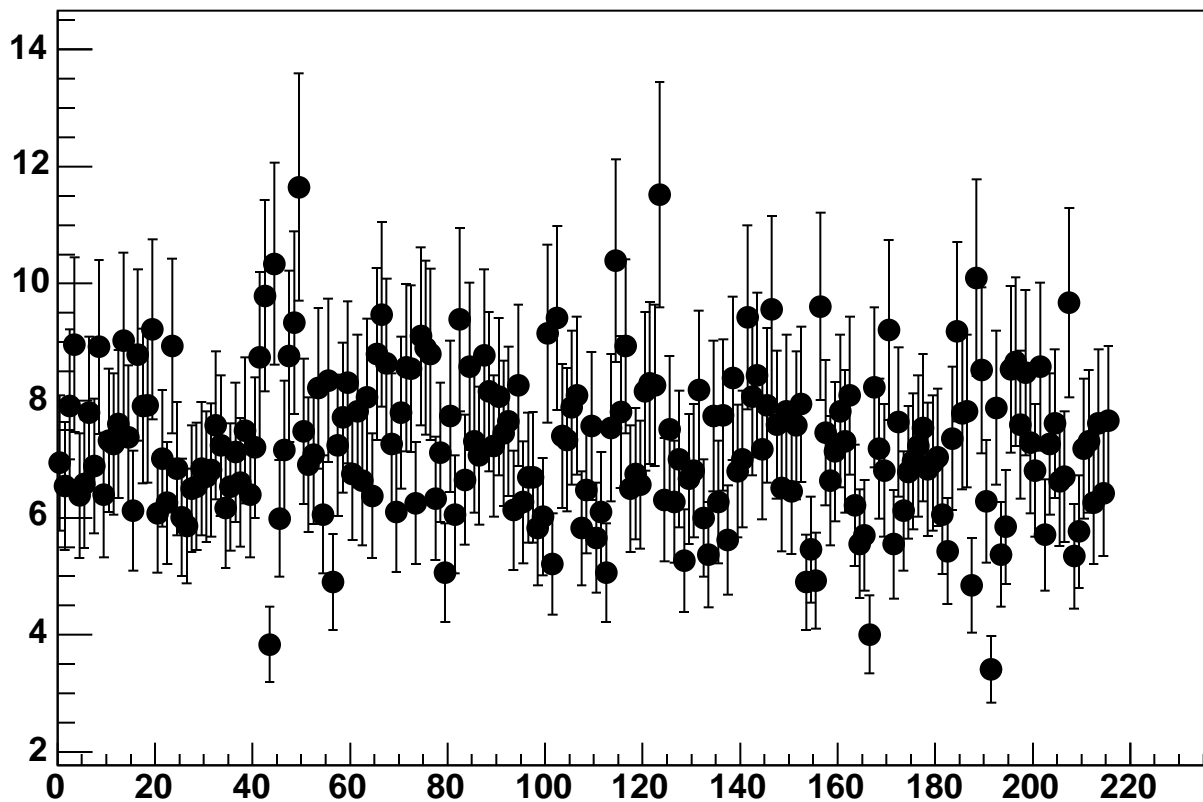
Enable 3, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



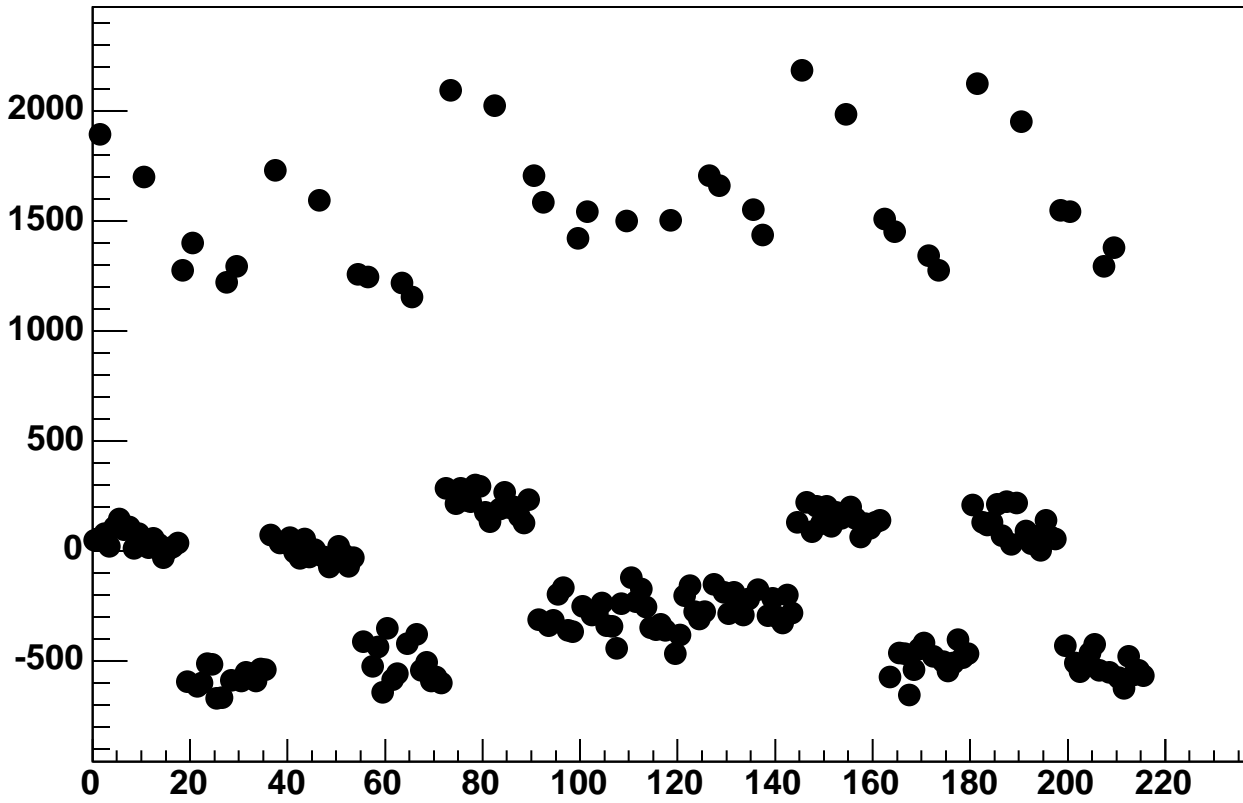
Enable 3, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



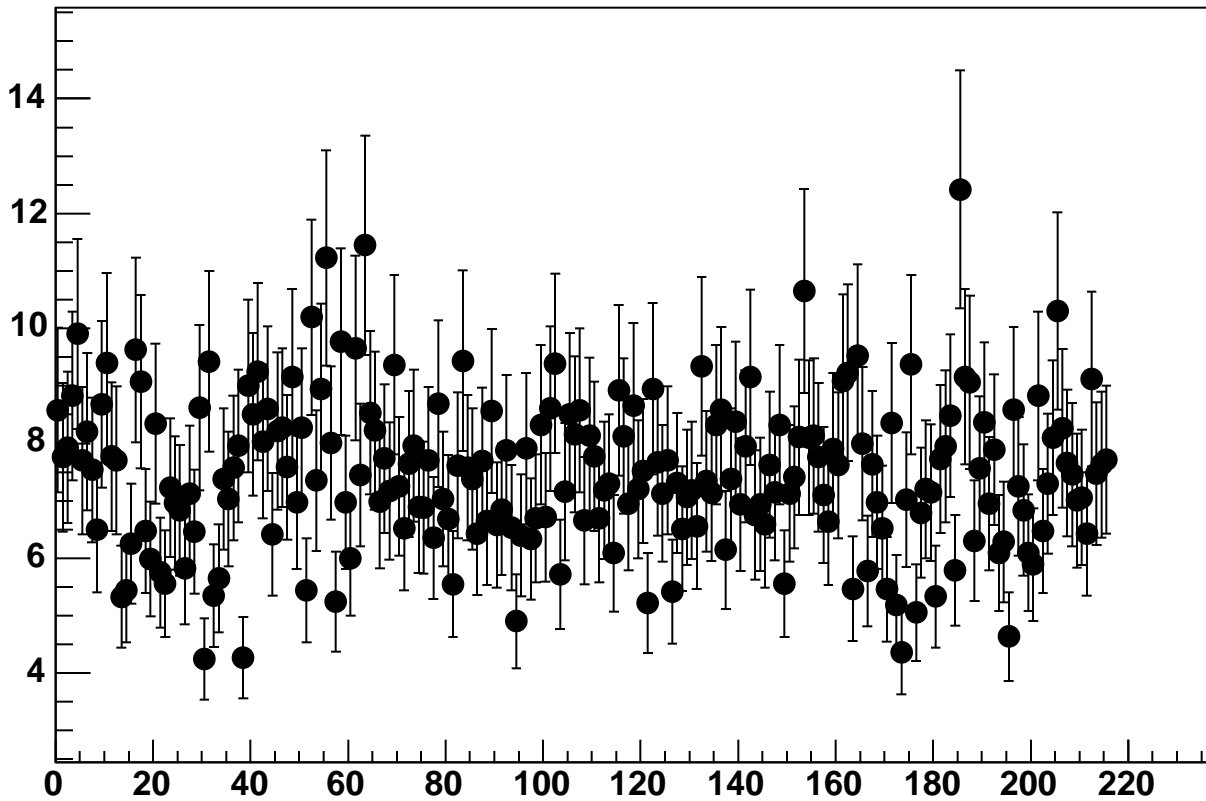
Enable 3, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



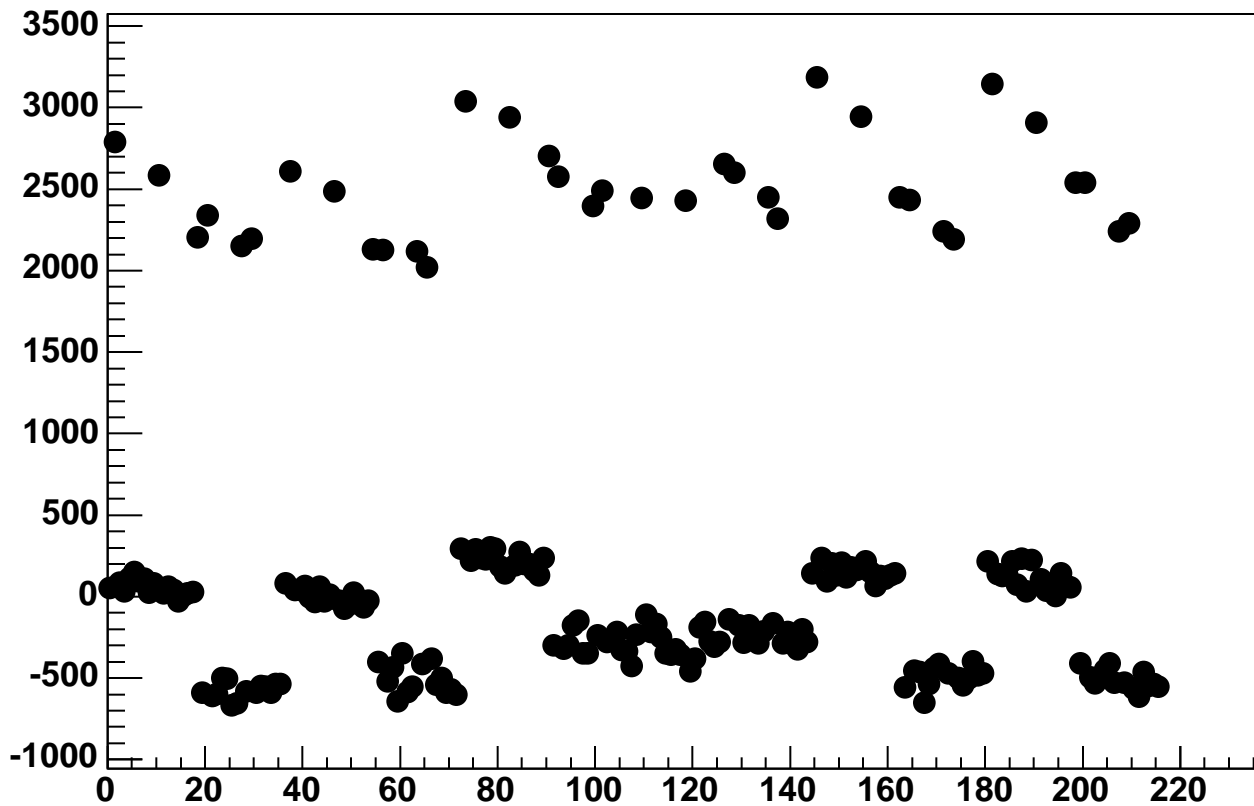
Enable 3, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



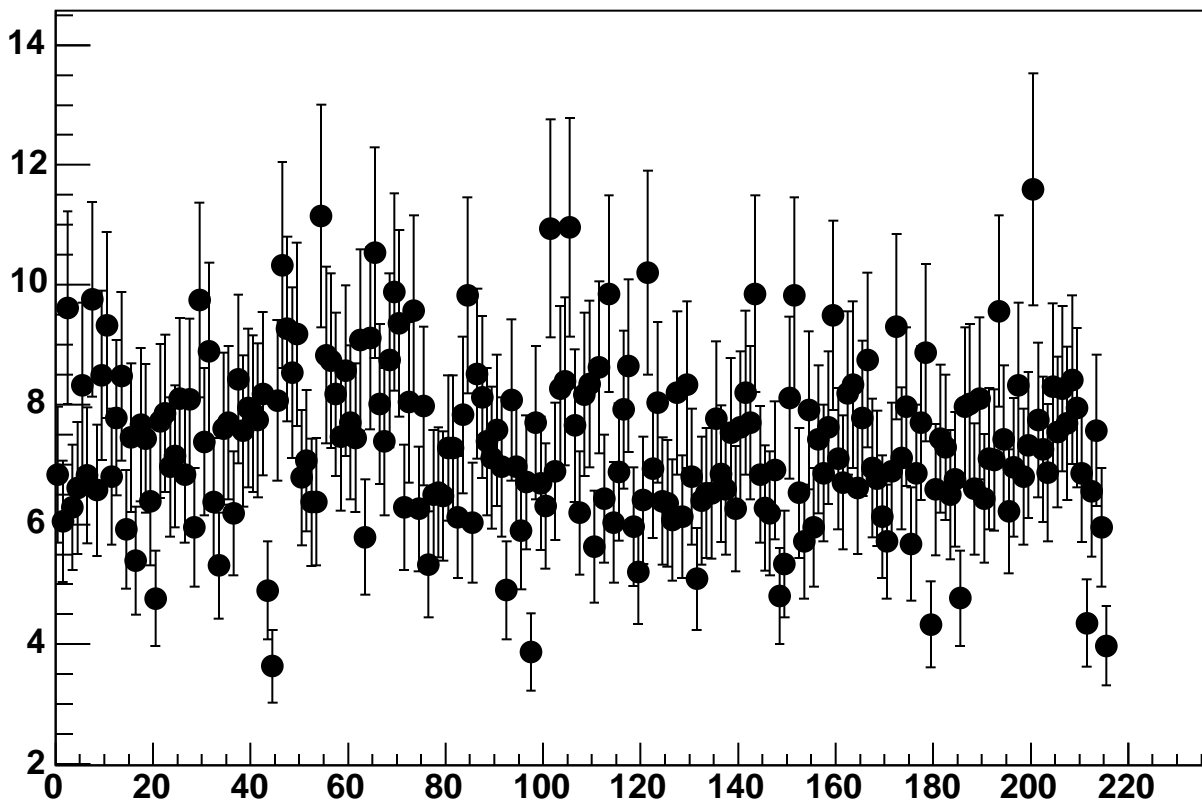
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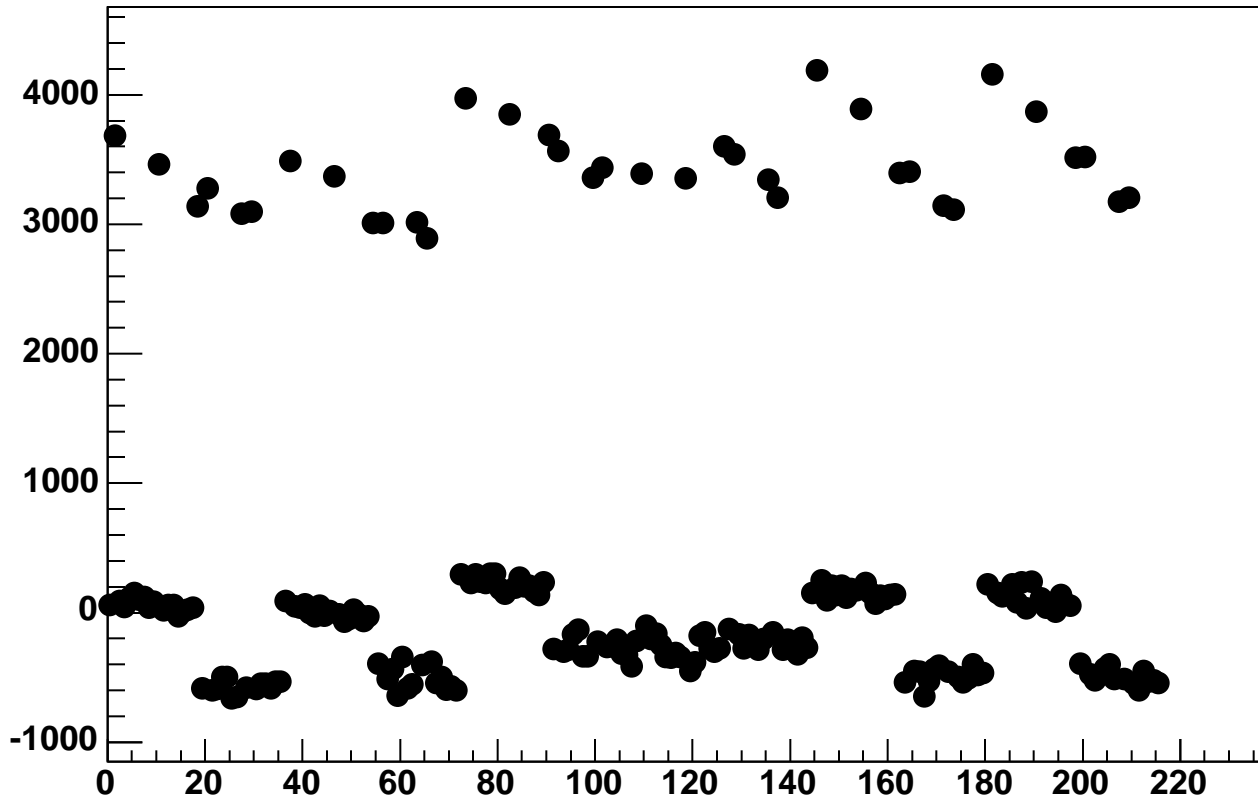
Enable 3, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



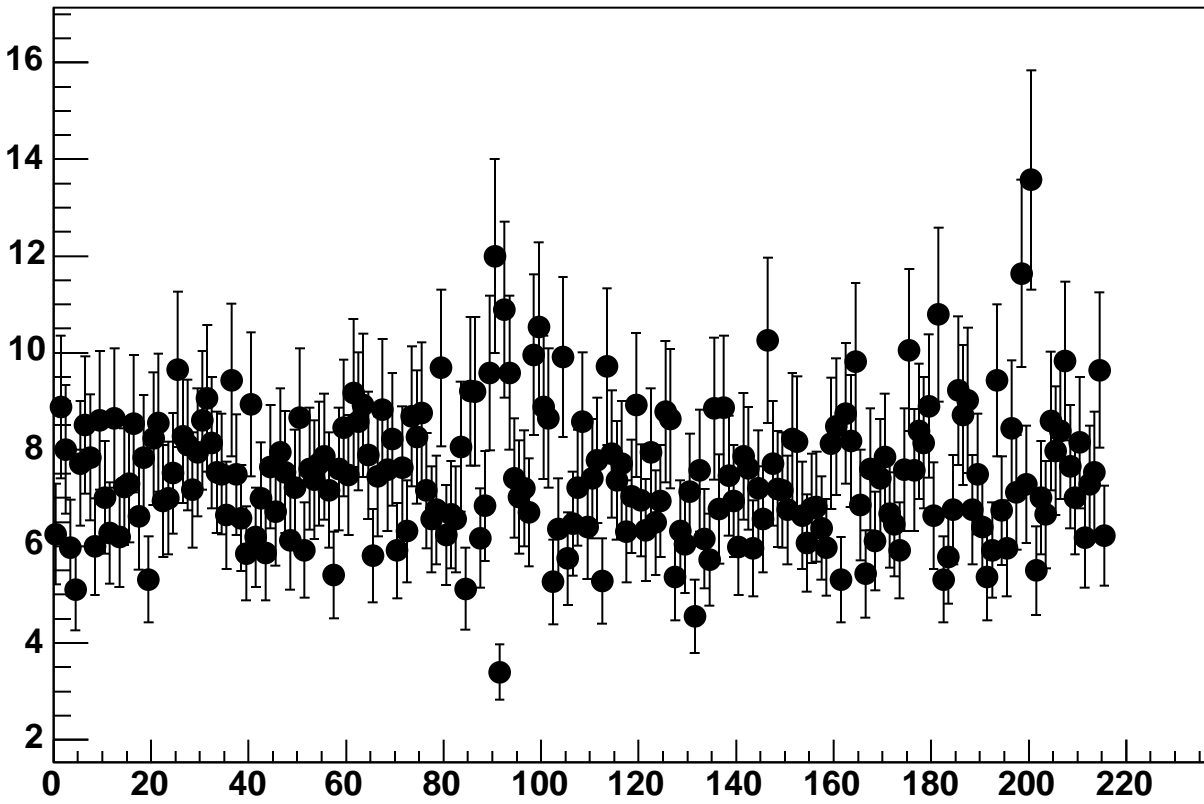
Enable 3, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



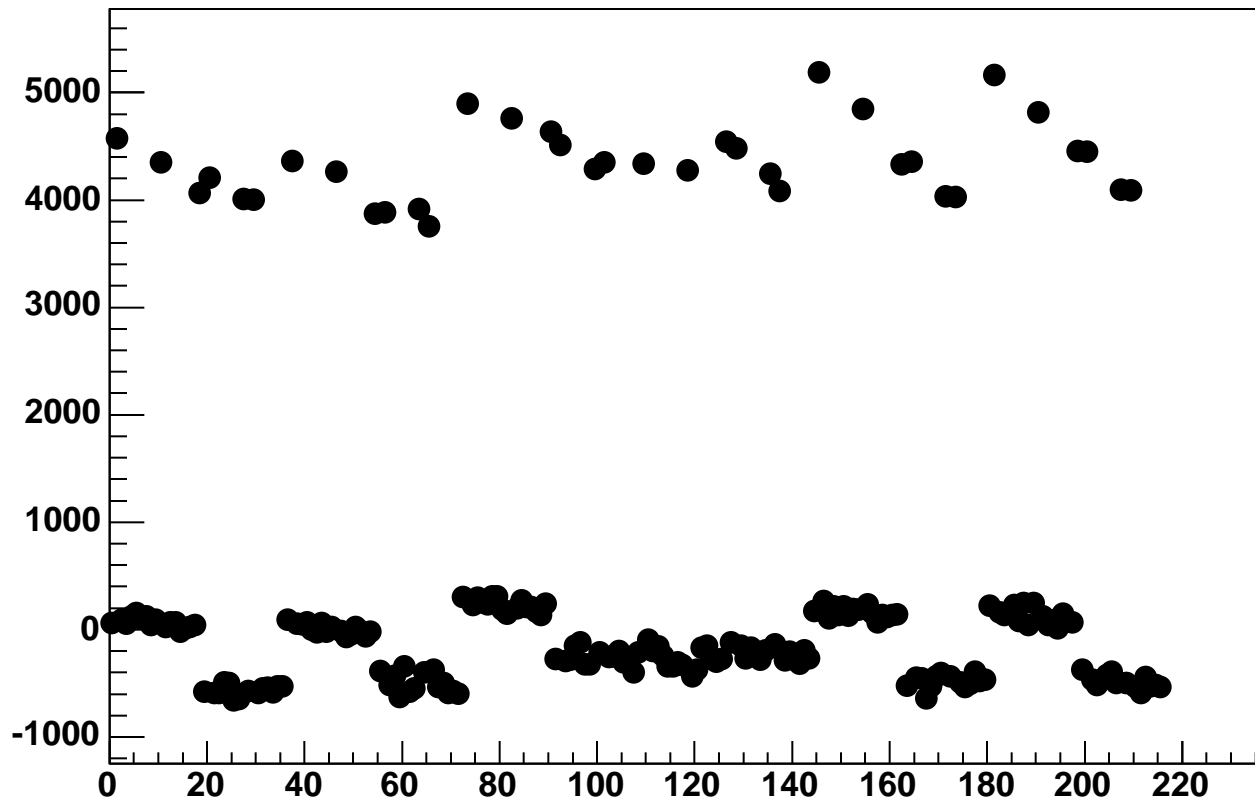
Enable 3, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



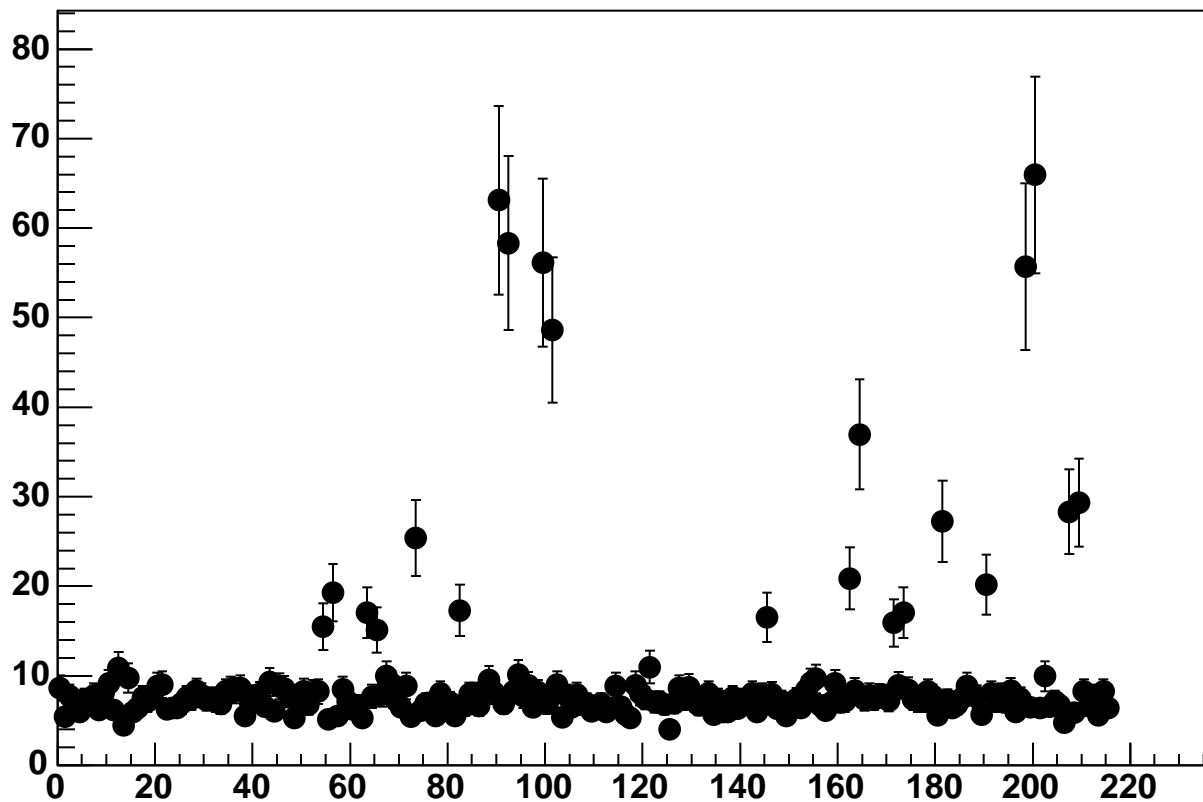
Enable 3, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



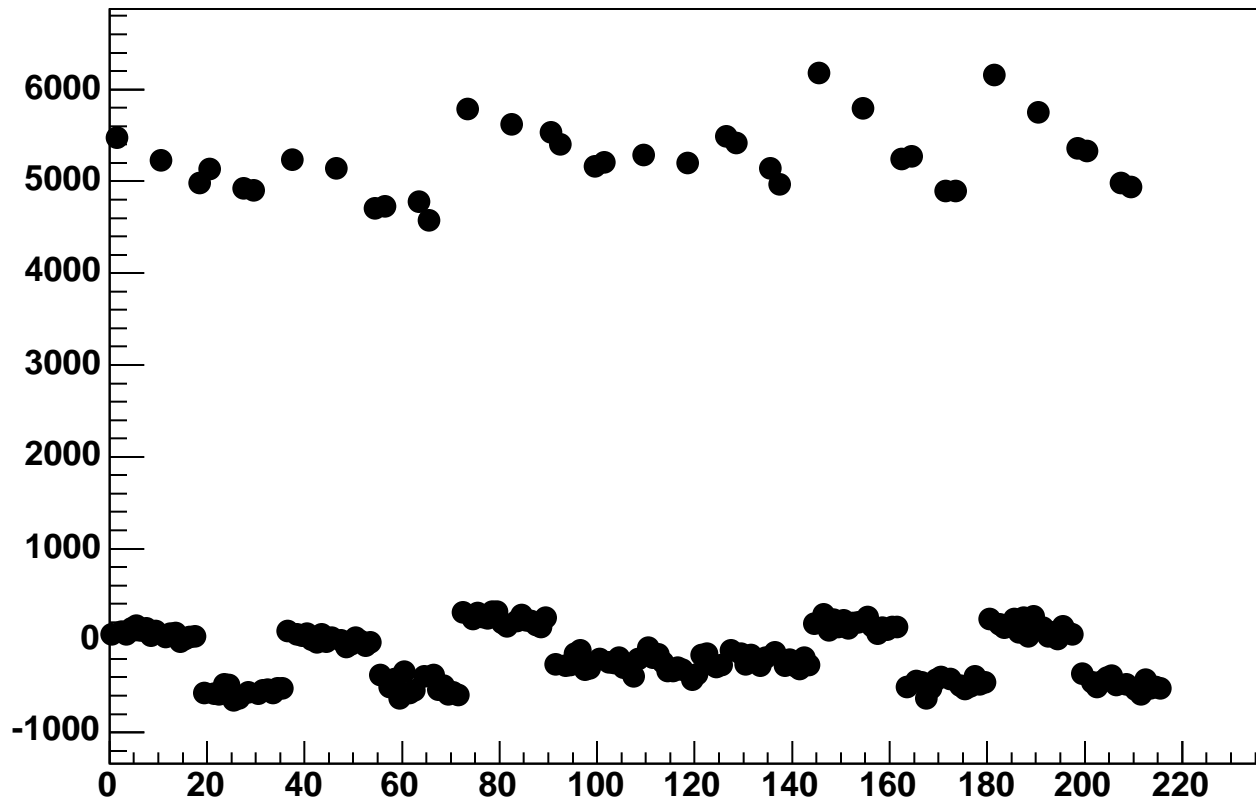
Enable 3, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



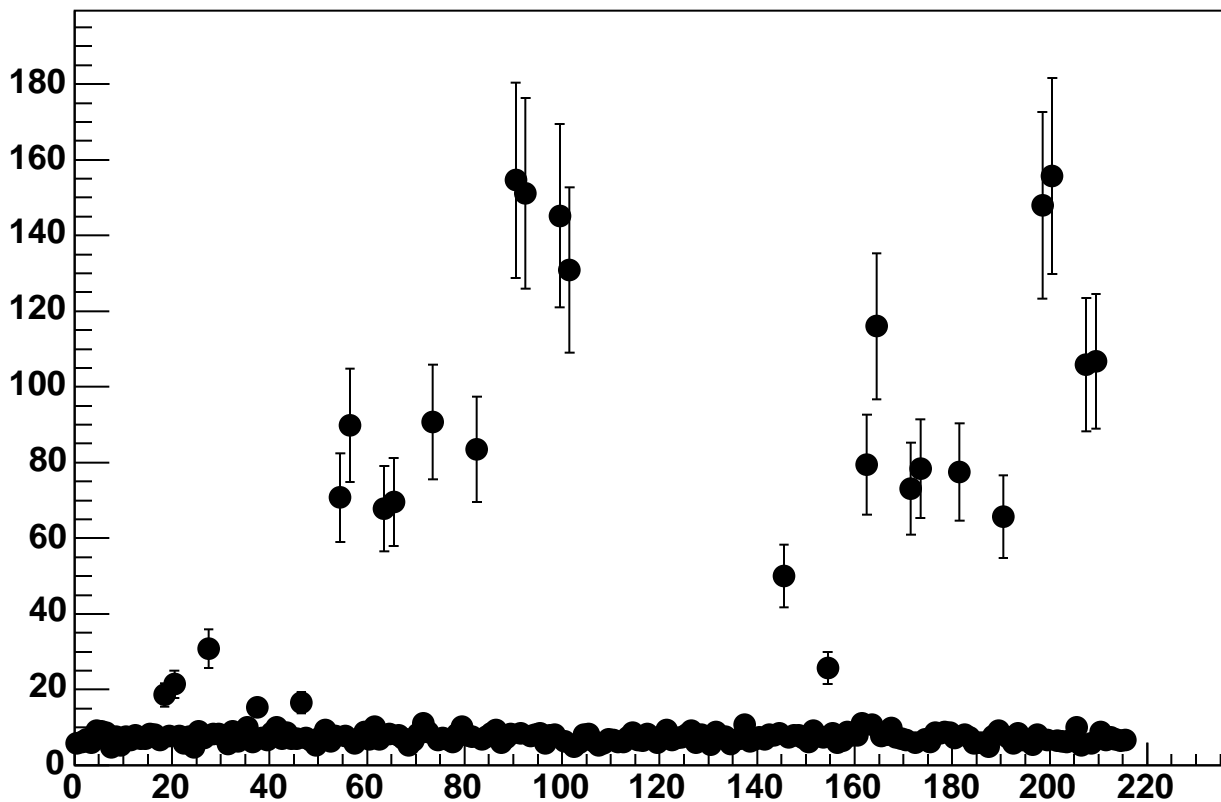
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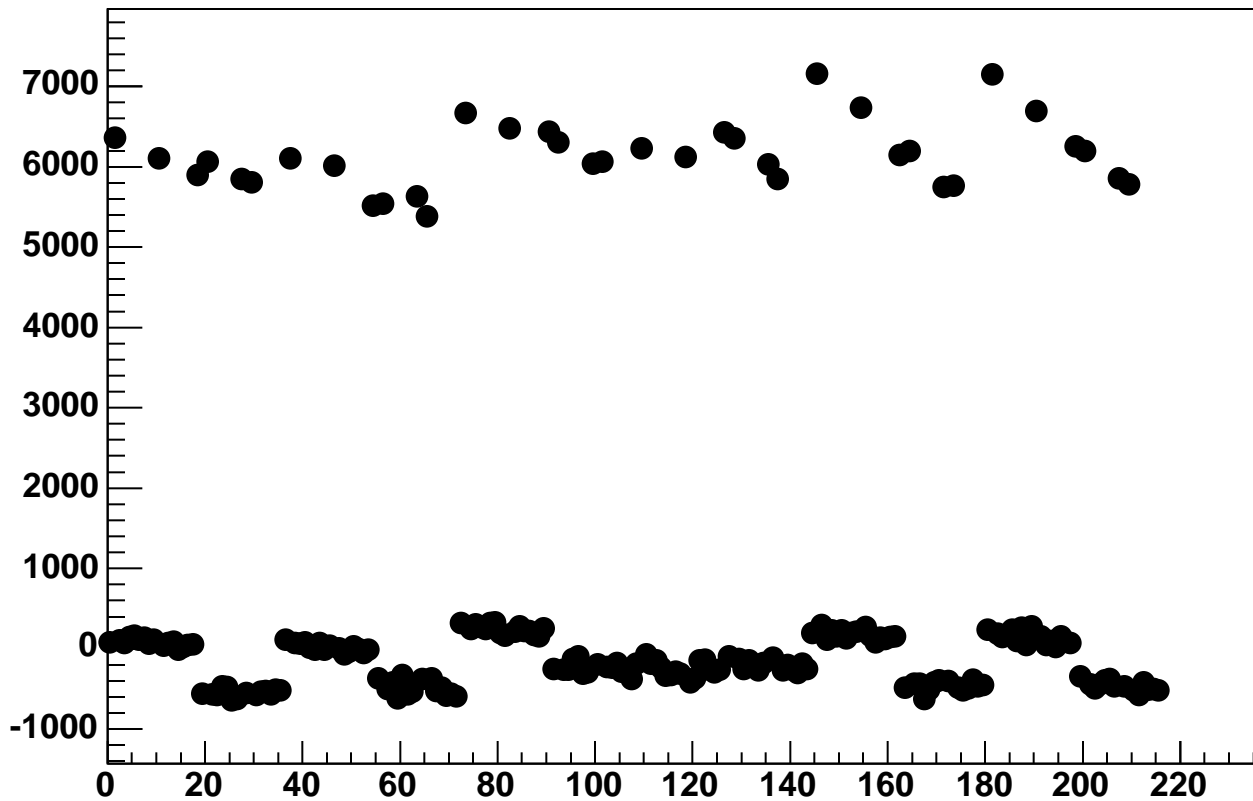
Enable 3, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



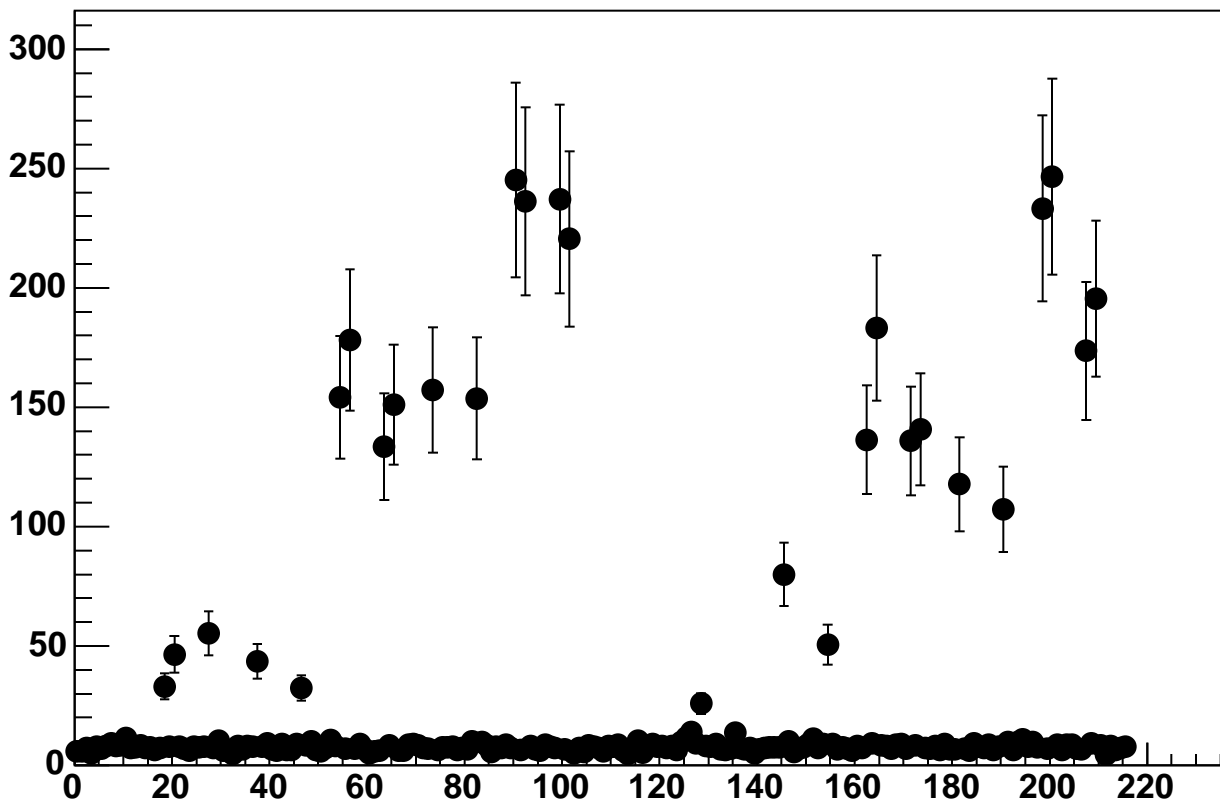
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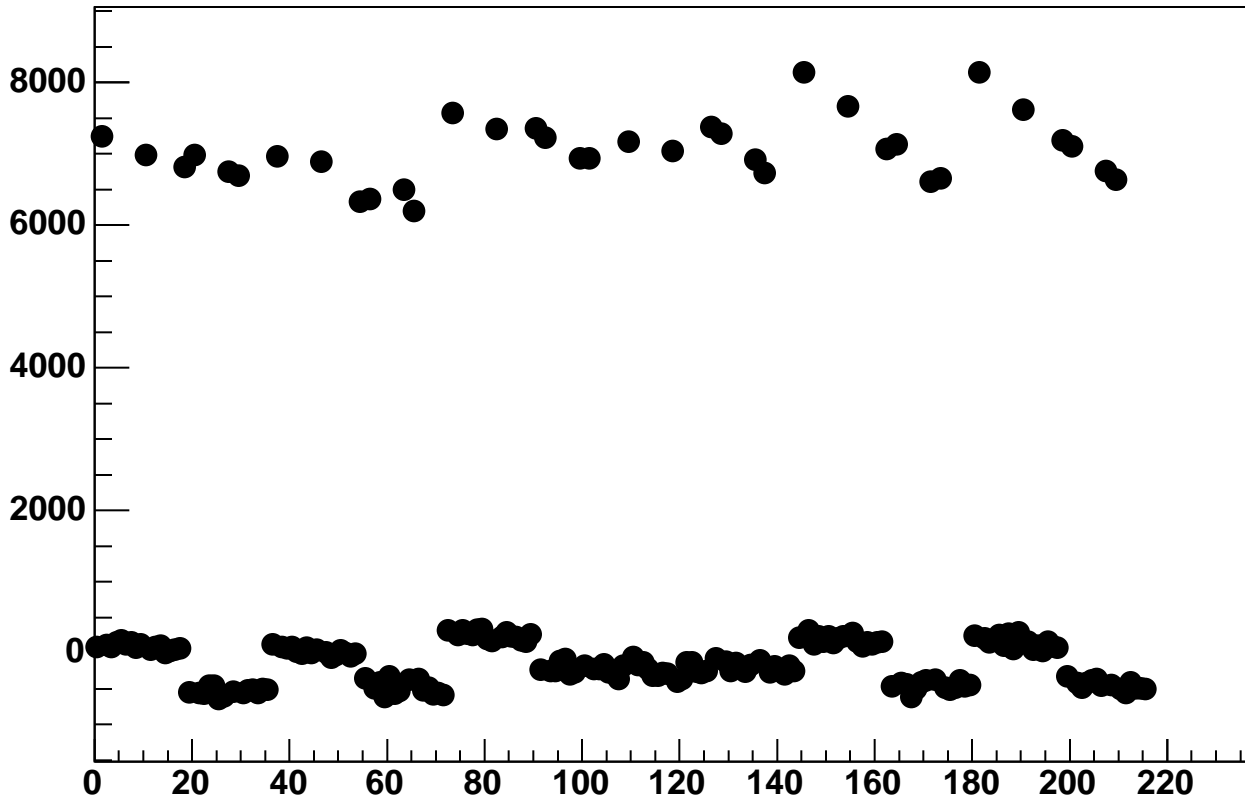
Enable 3, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



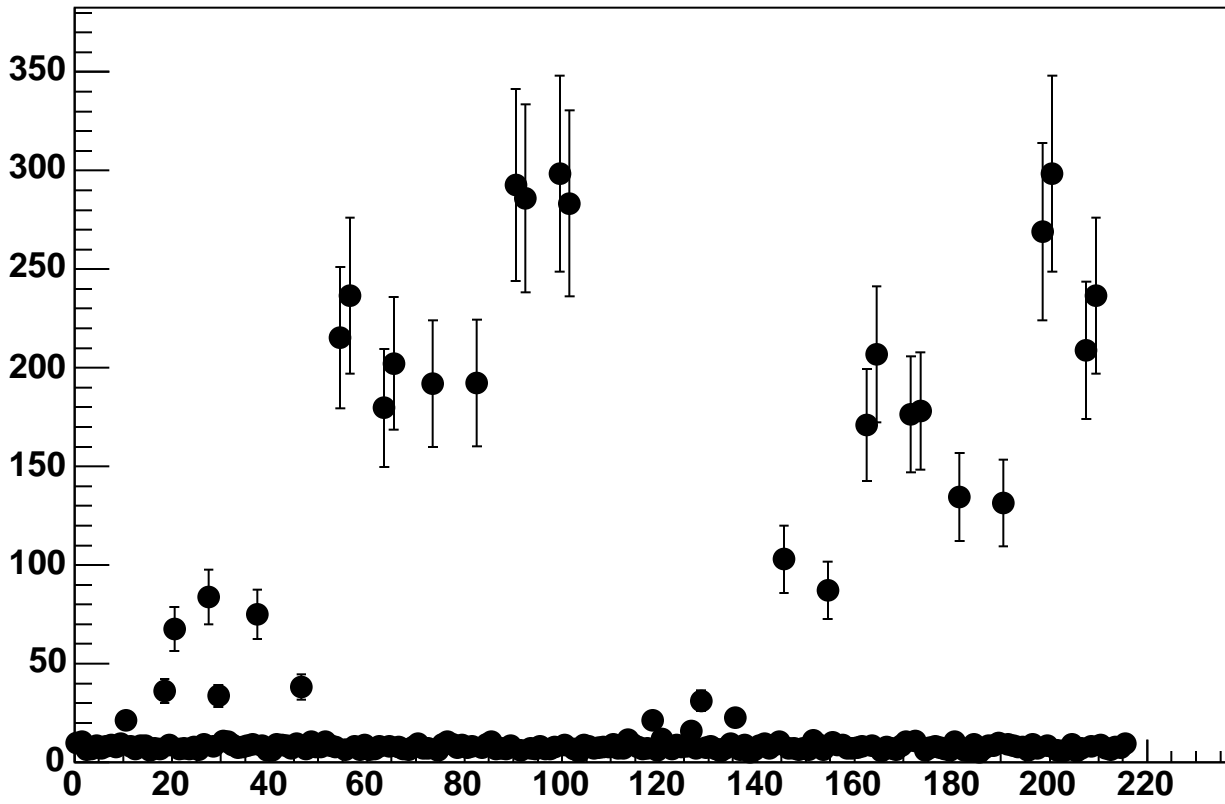
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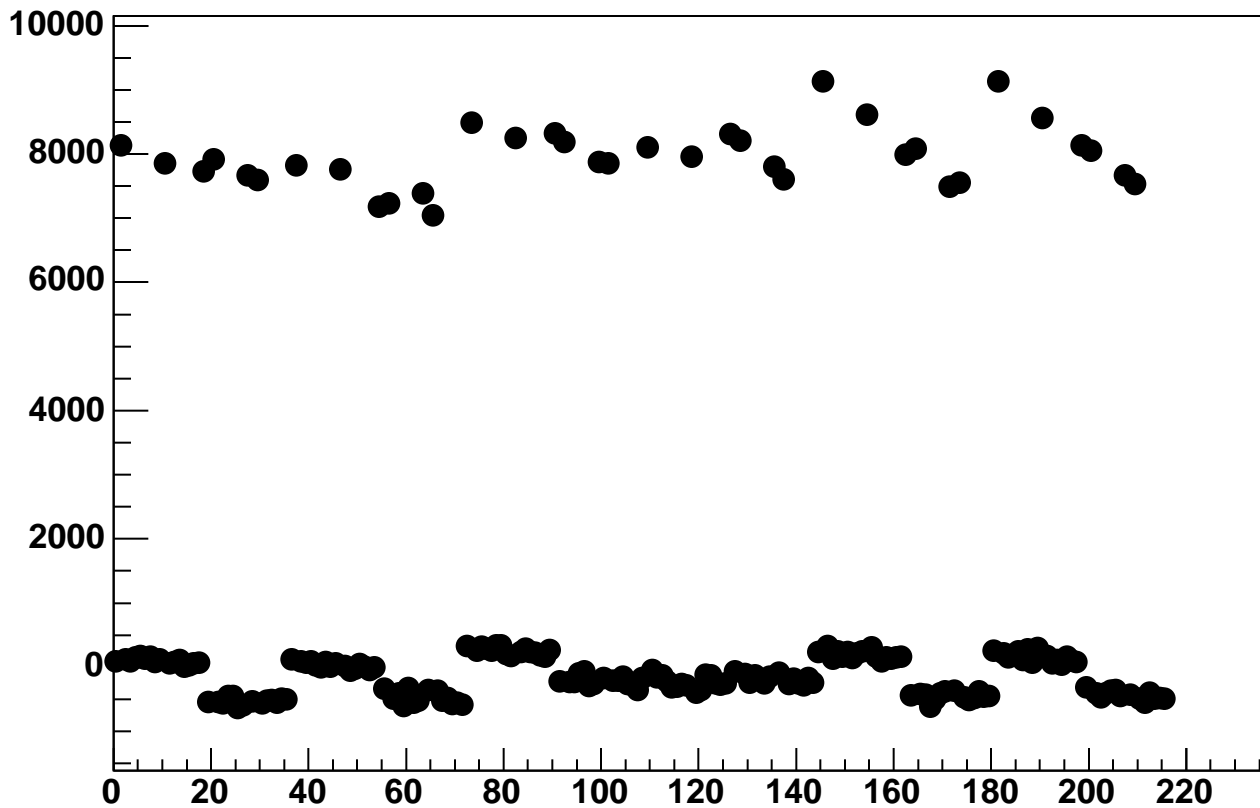
Enable 3, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



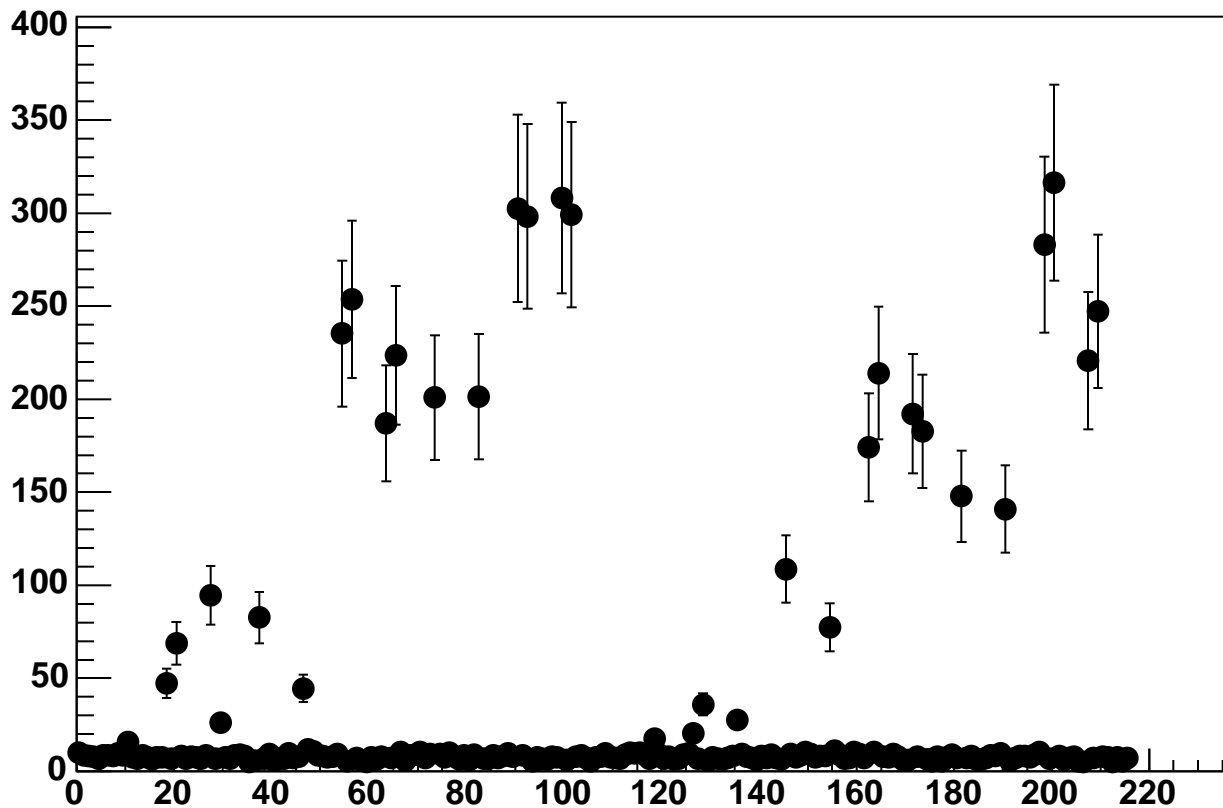
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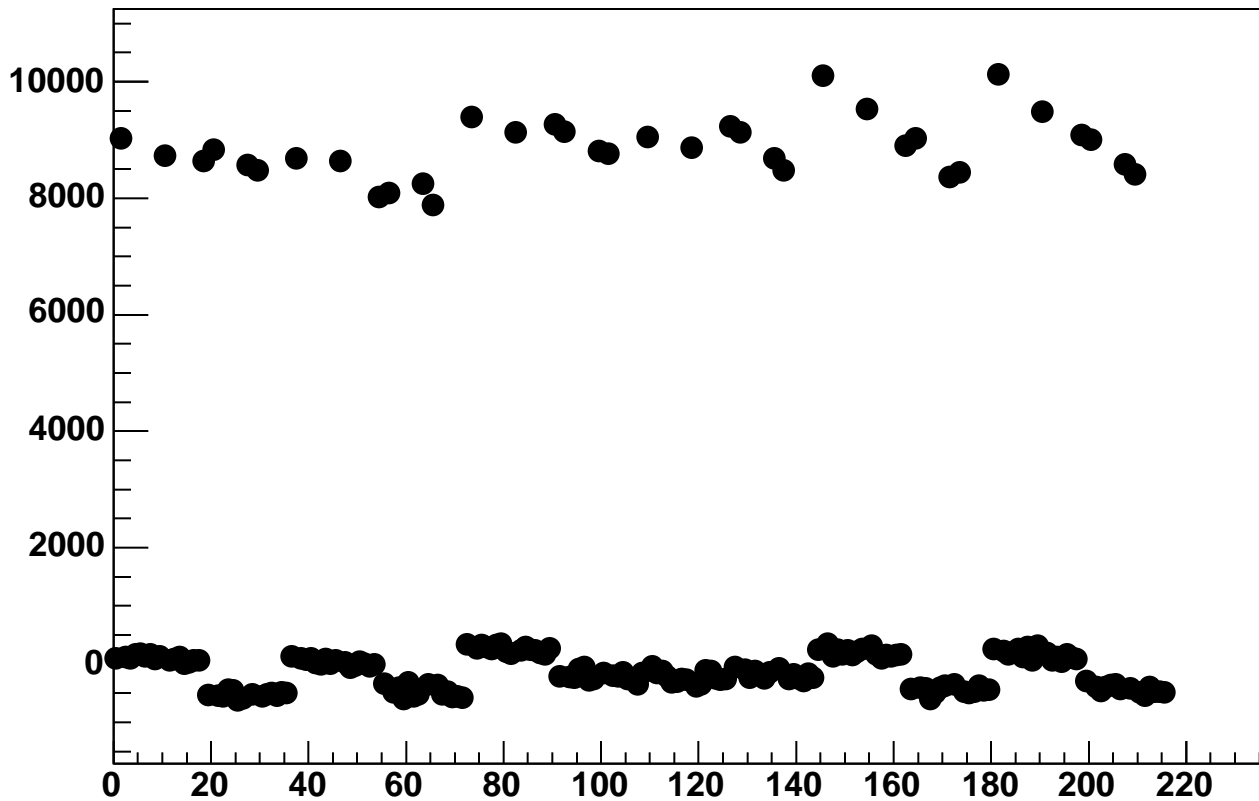
Enable 3, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



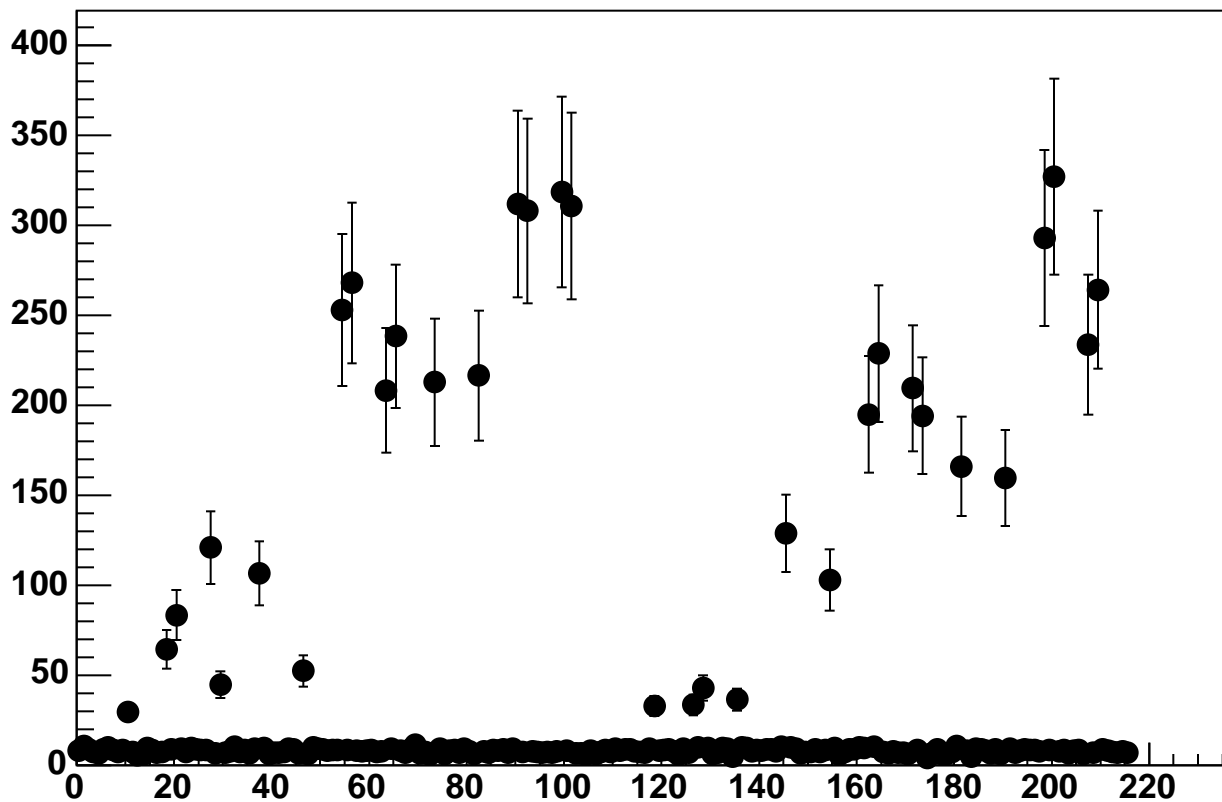
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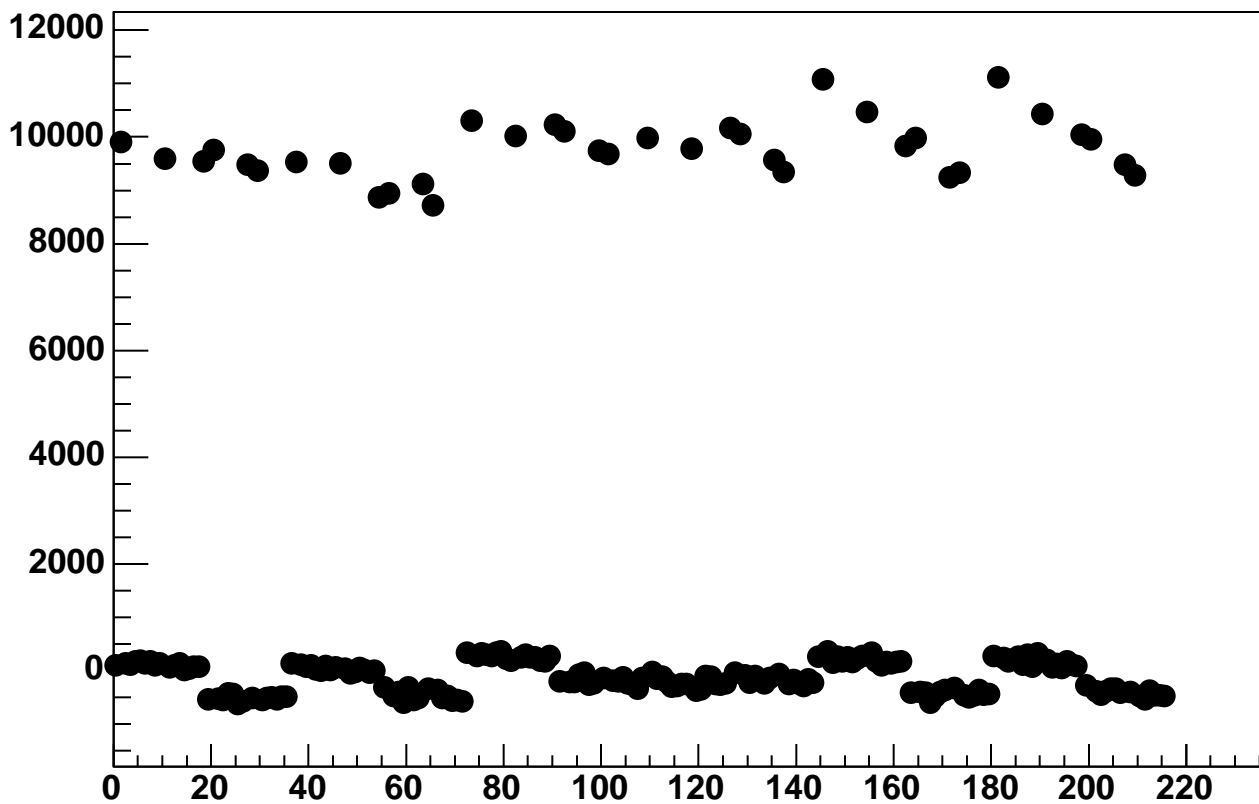
Enable 3, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



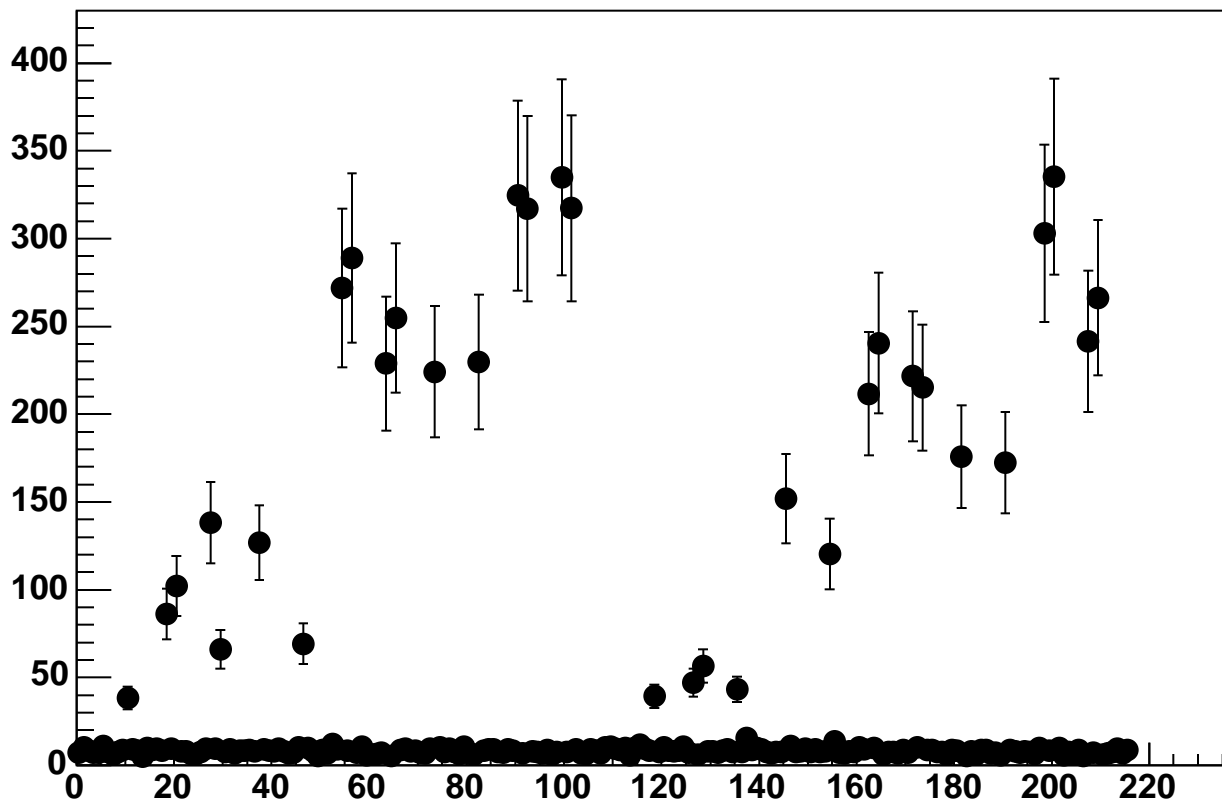
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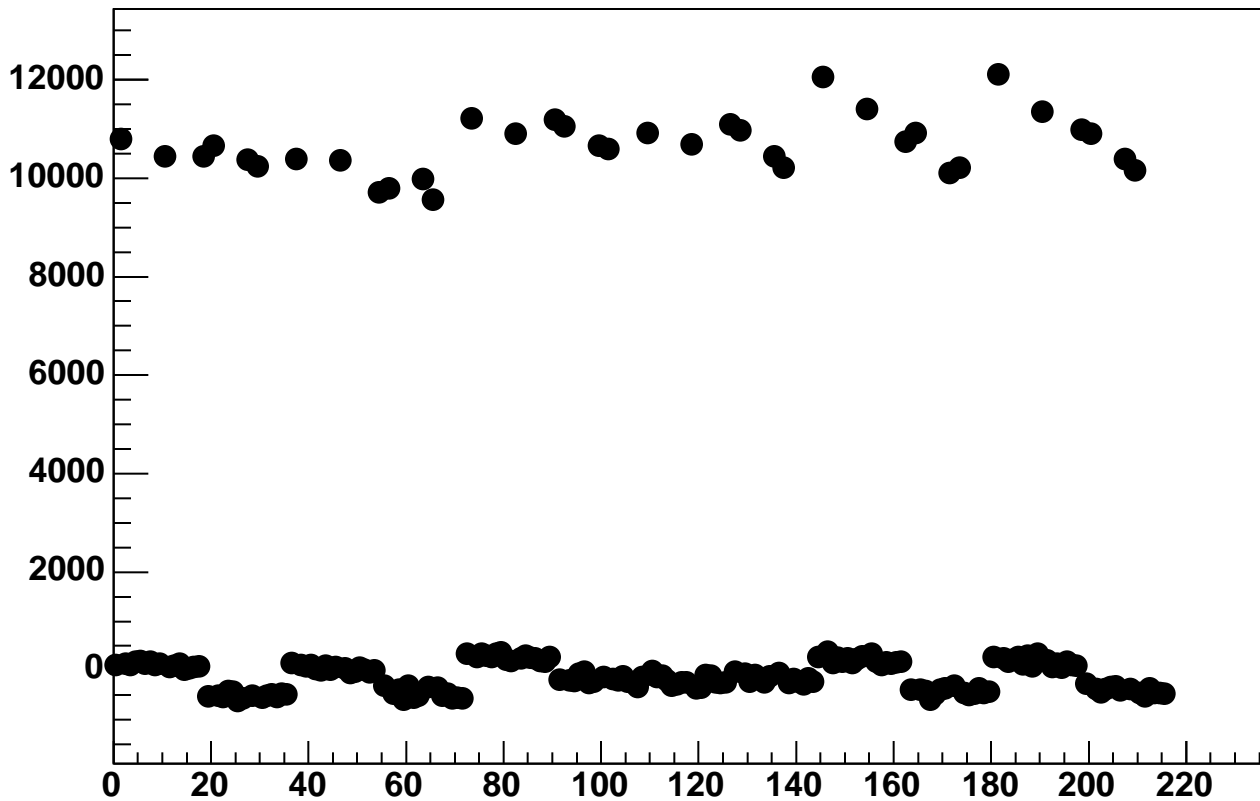
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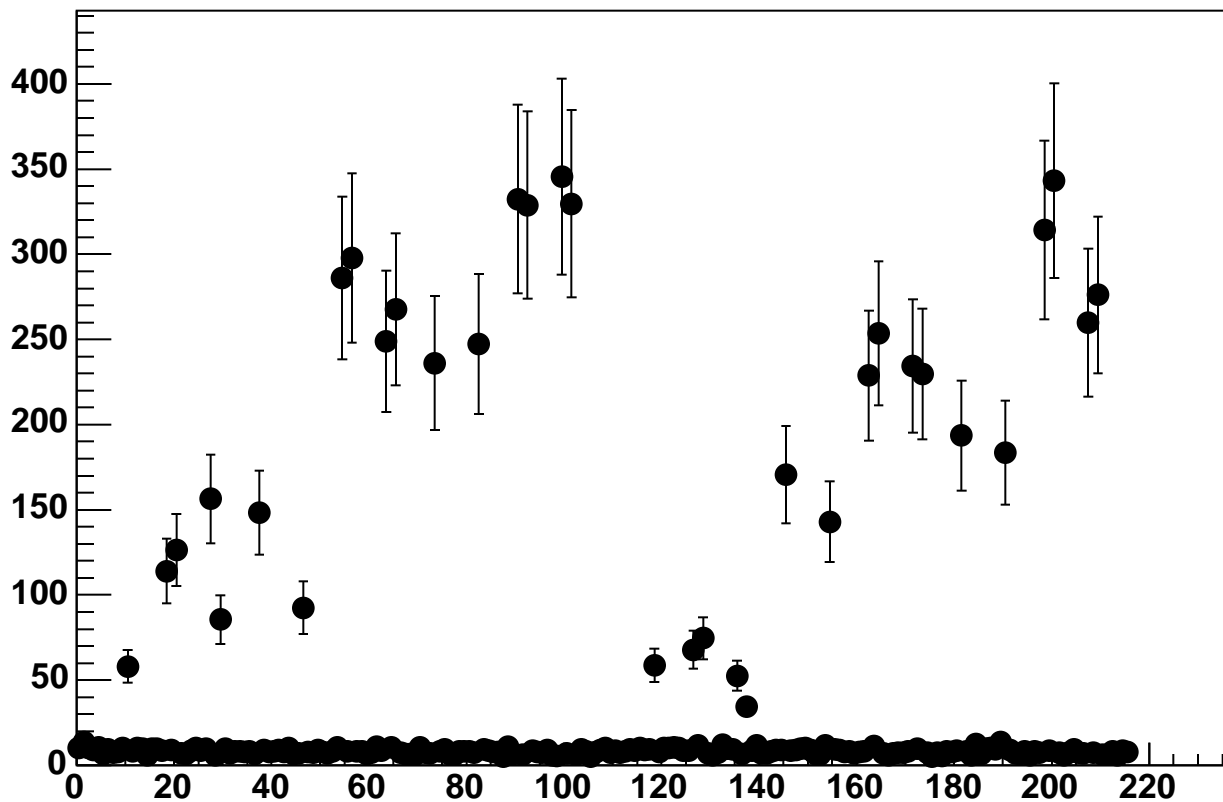
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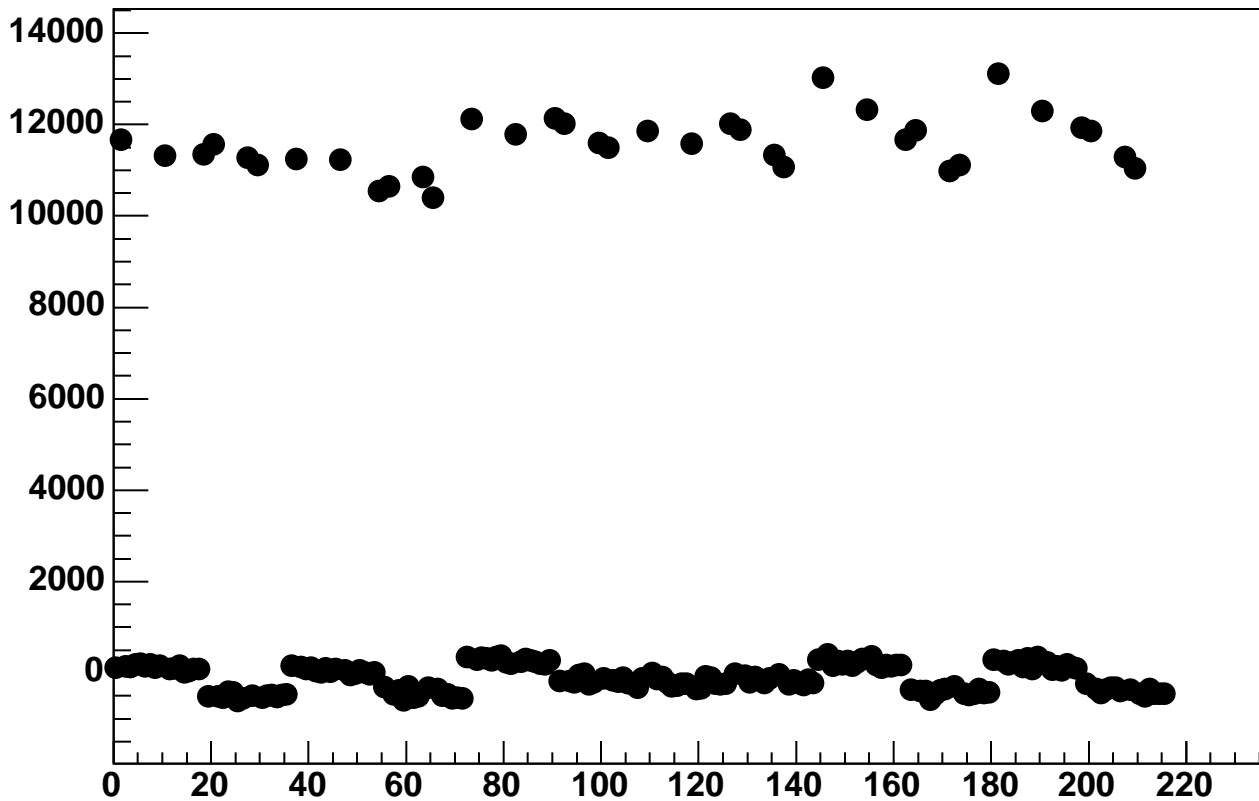
Enable 3, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



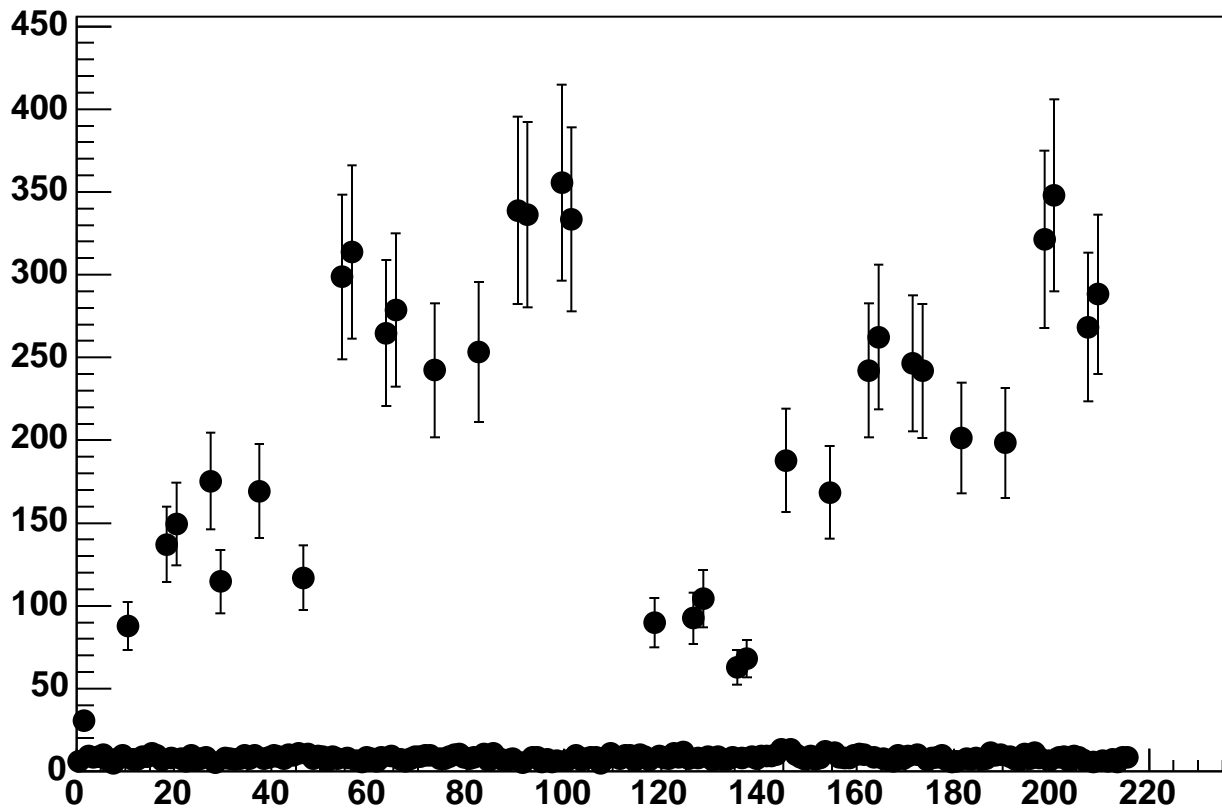
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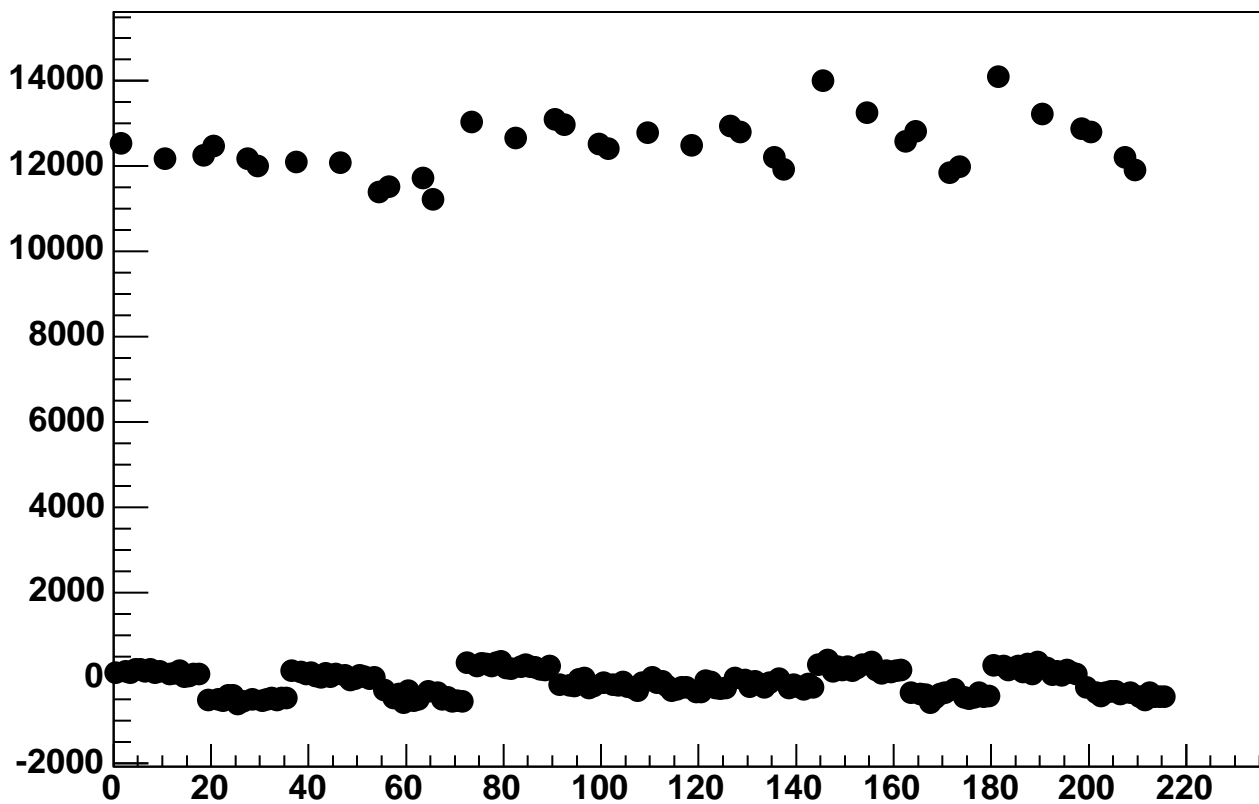
Enable 3, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



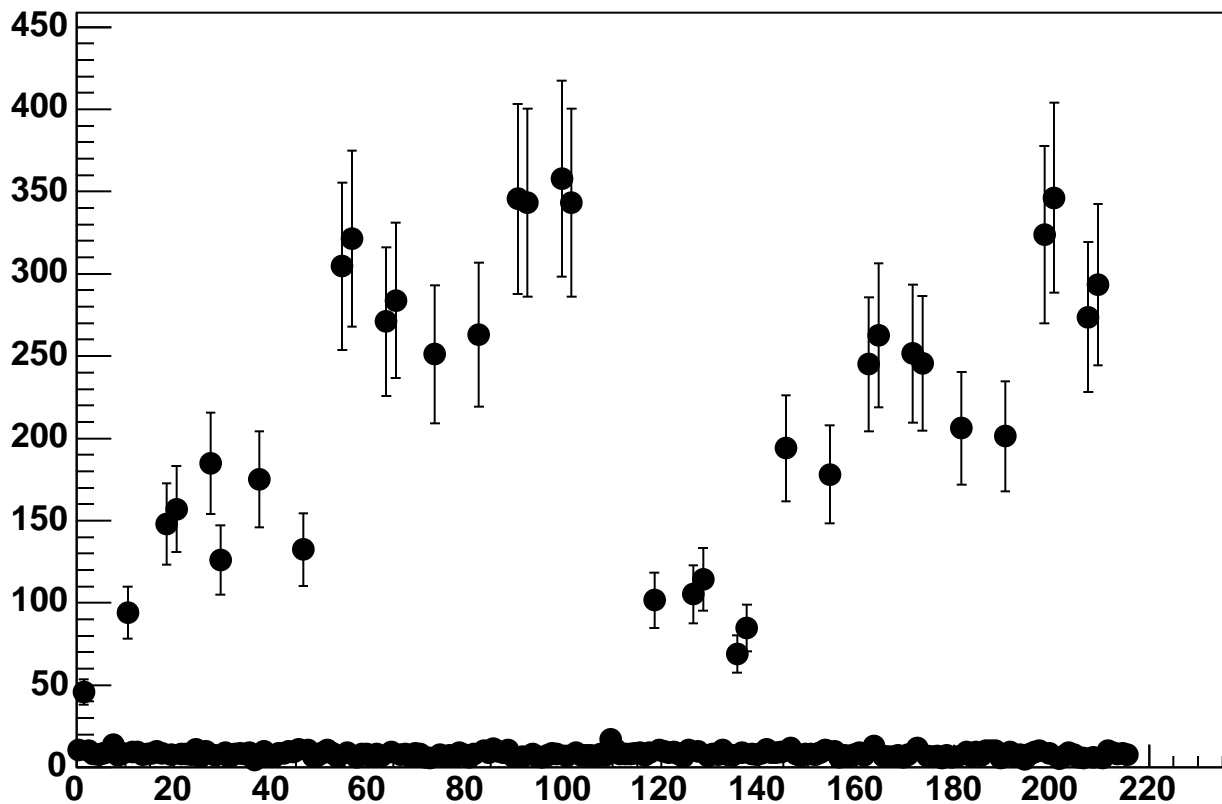
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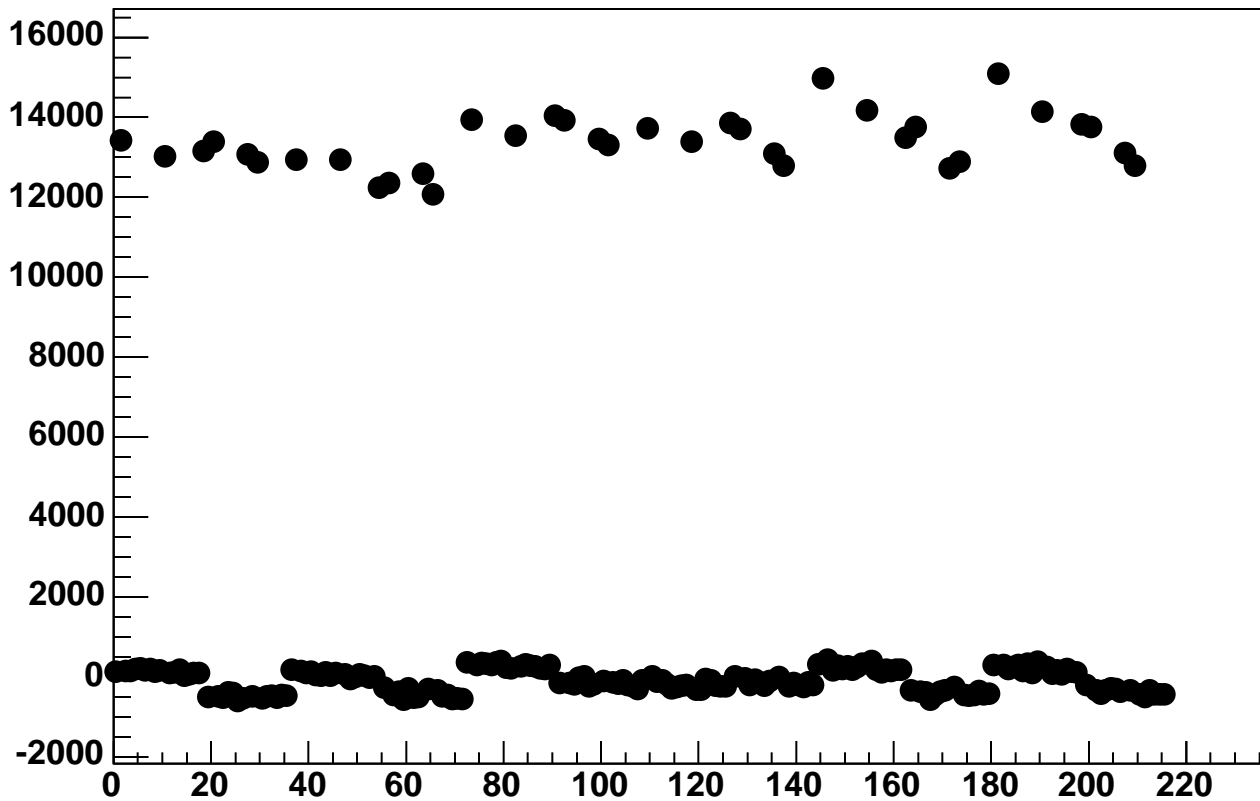
Enable 3, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



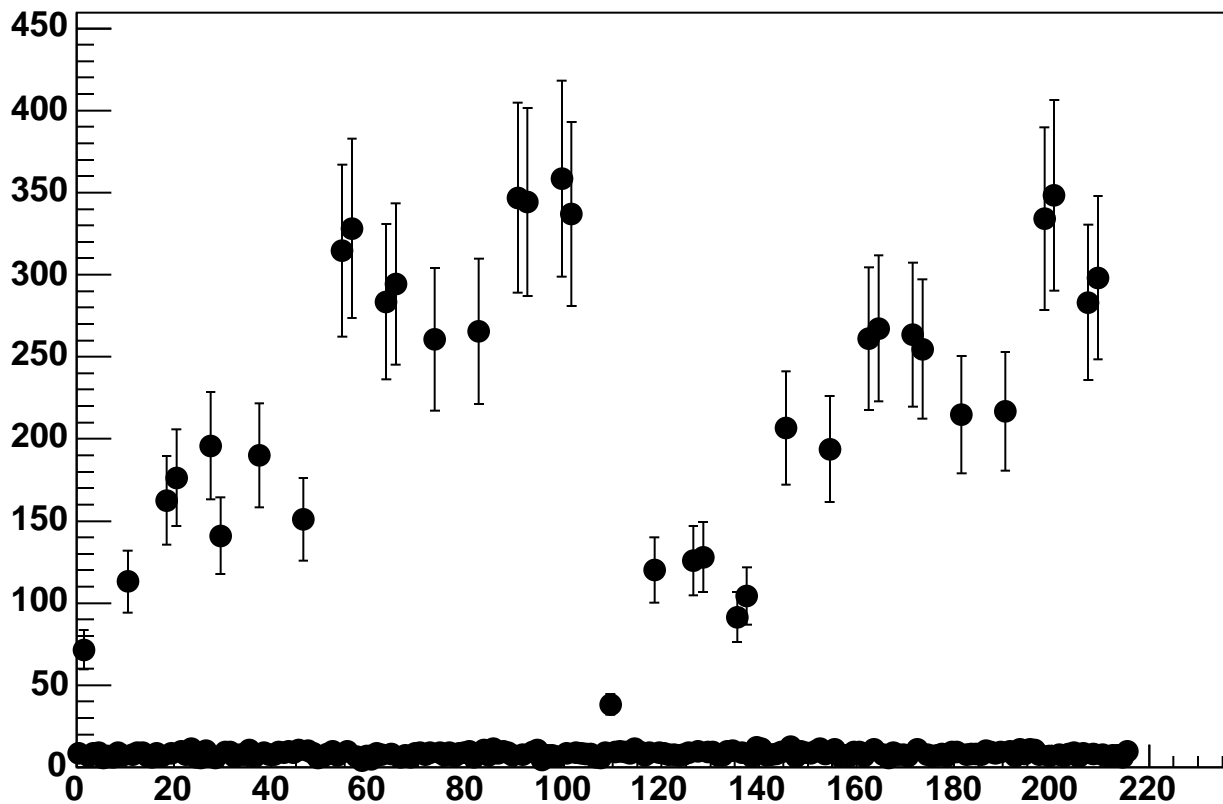
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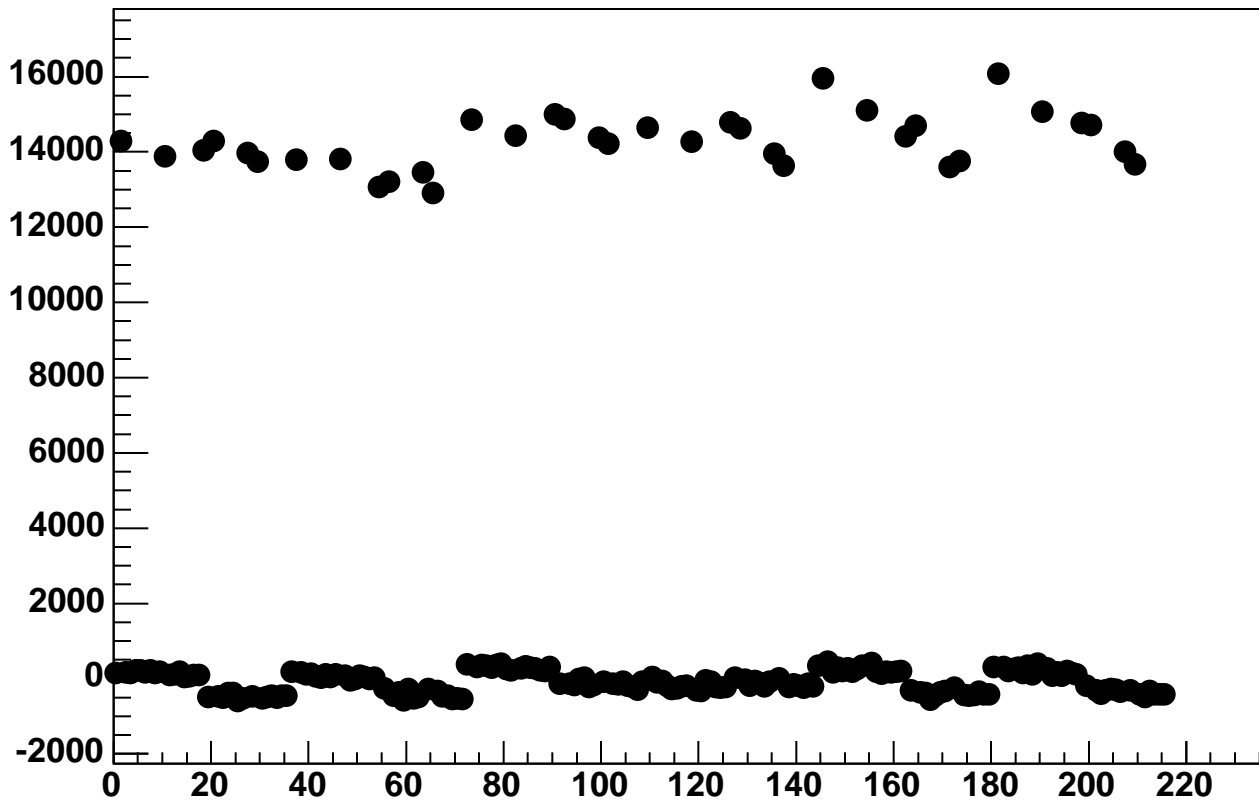
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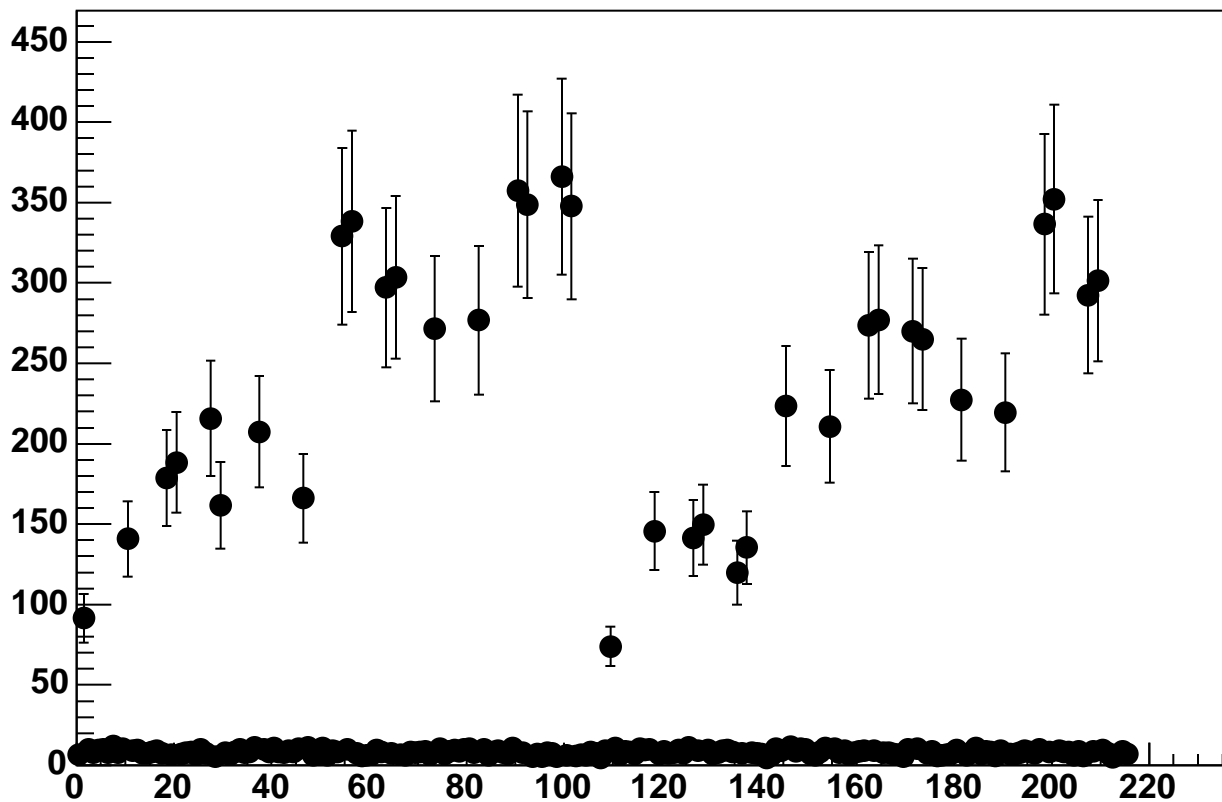
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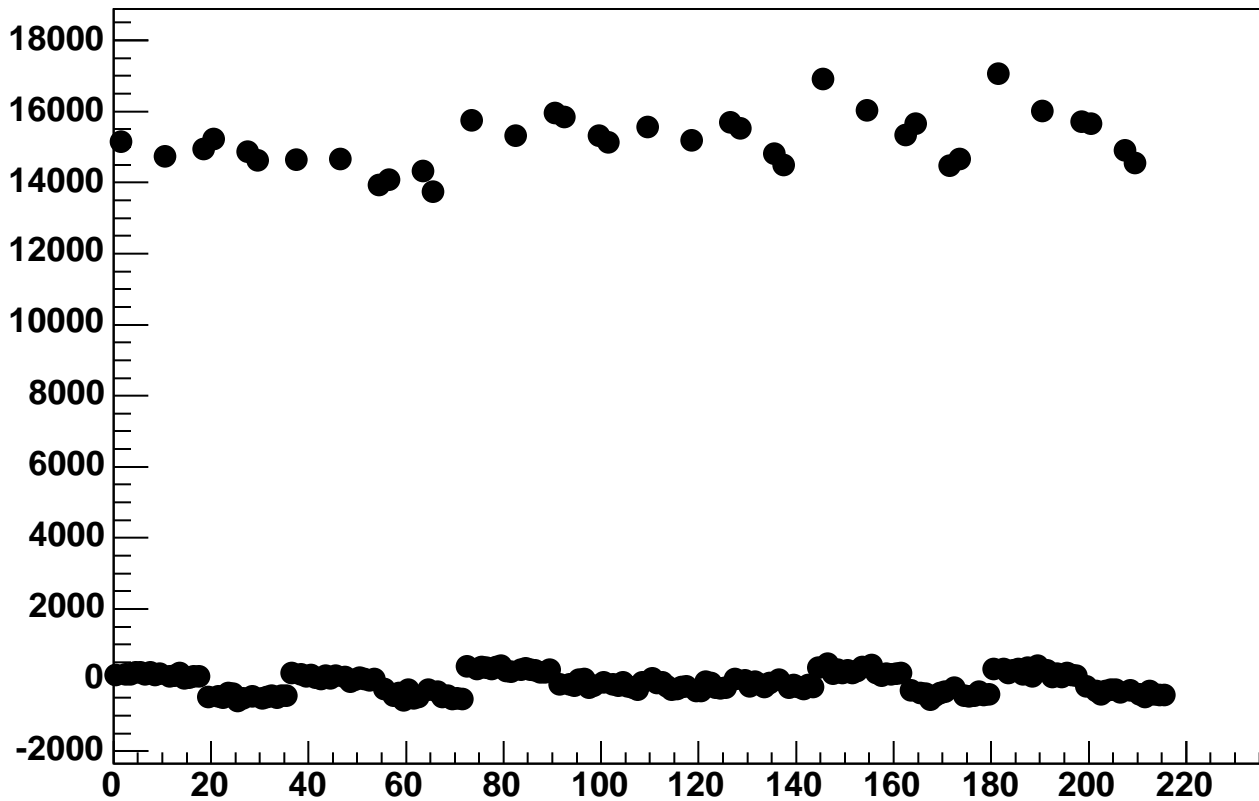
Enable 3, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



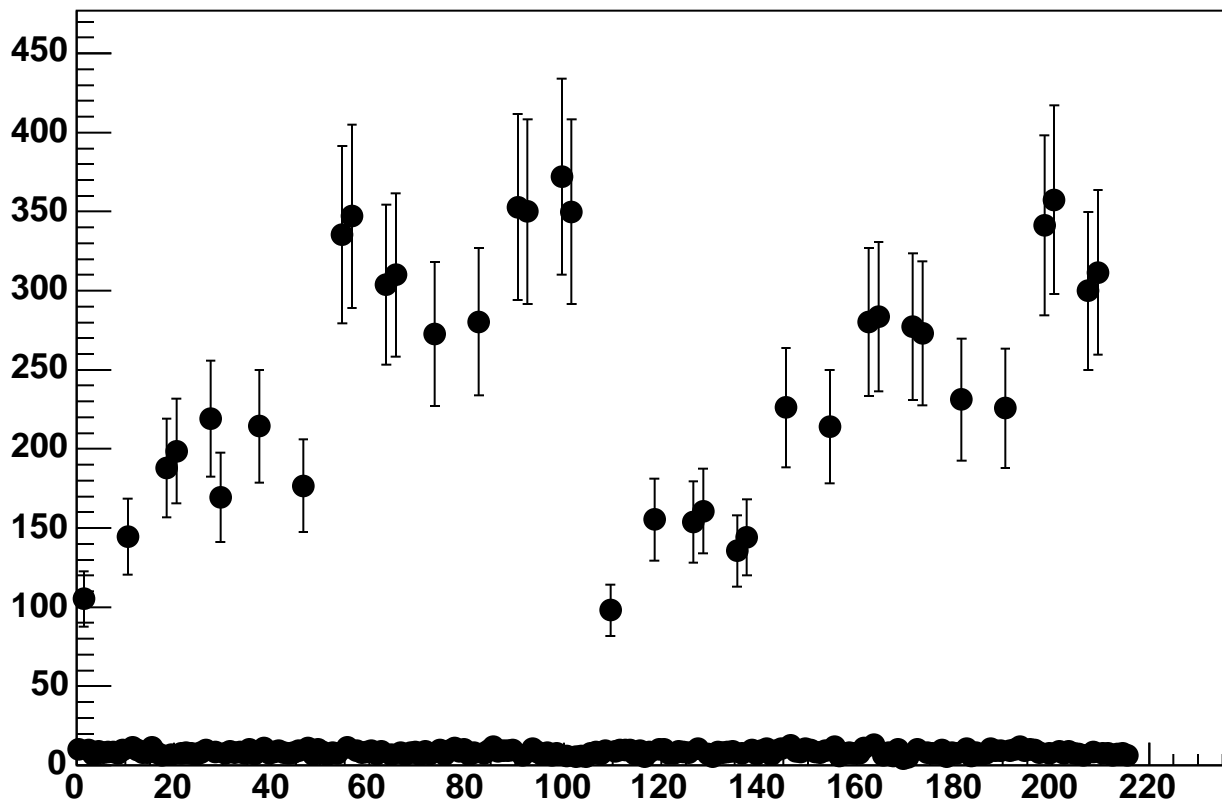
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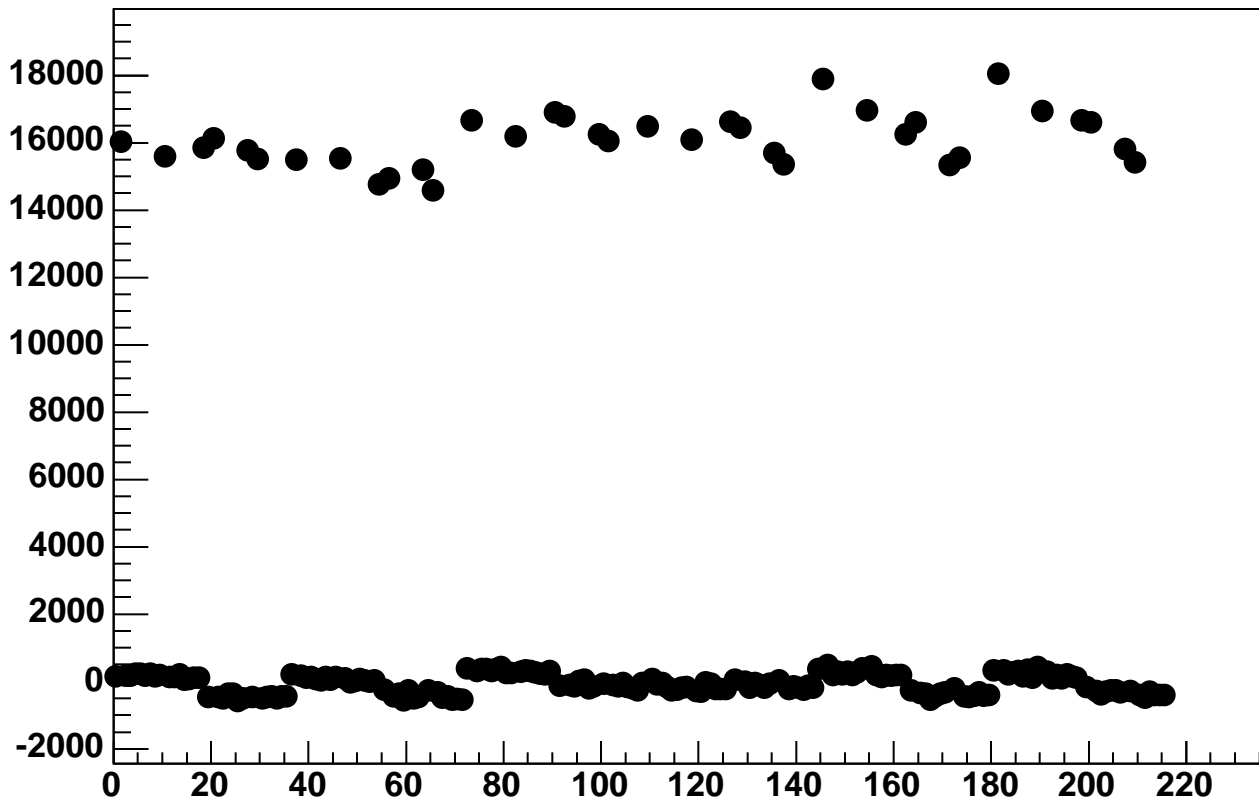
Enable 3, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



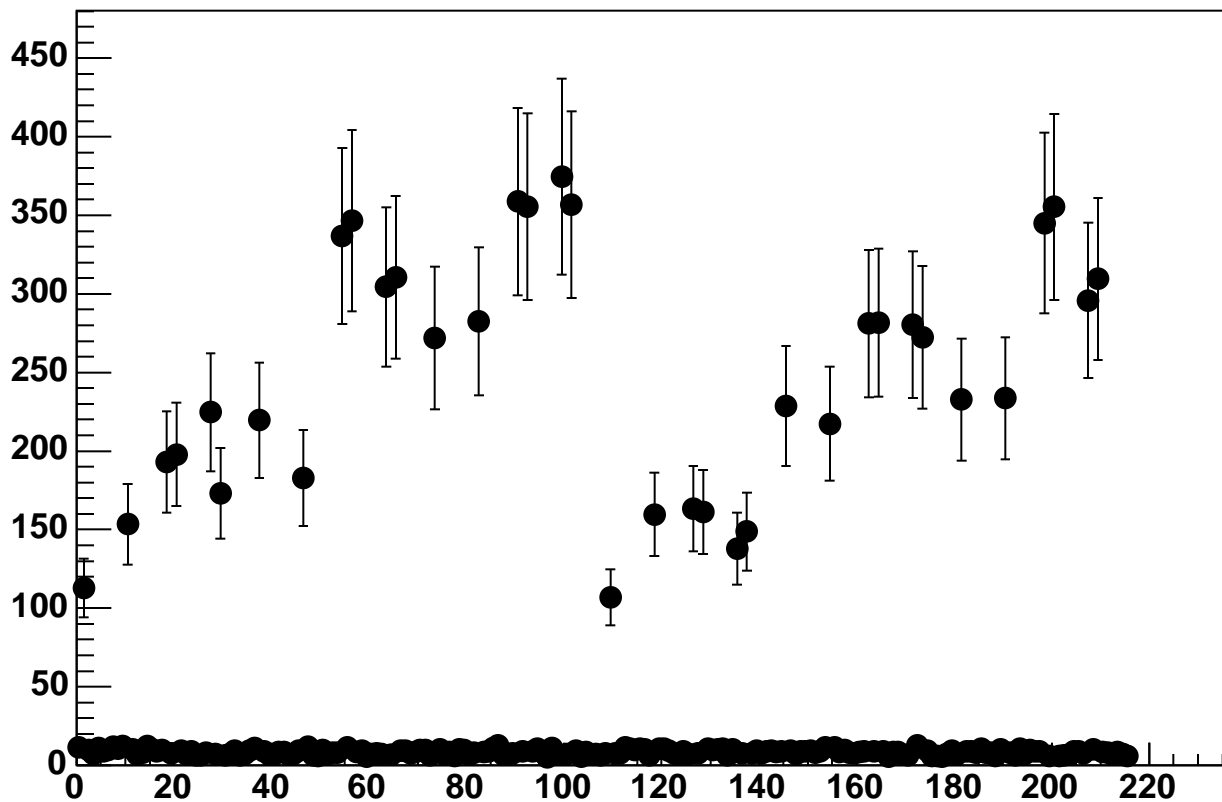
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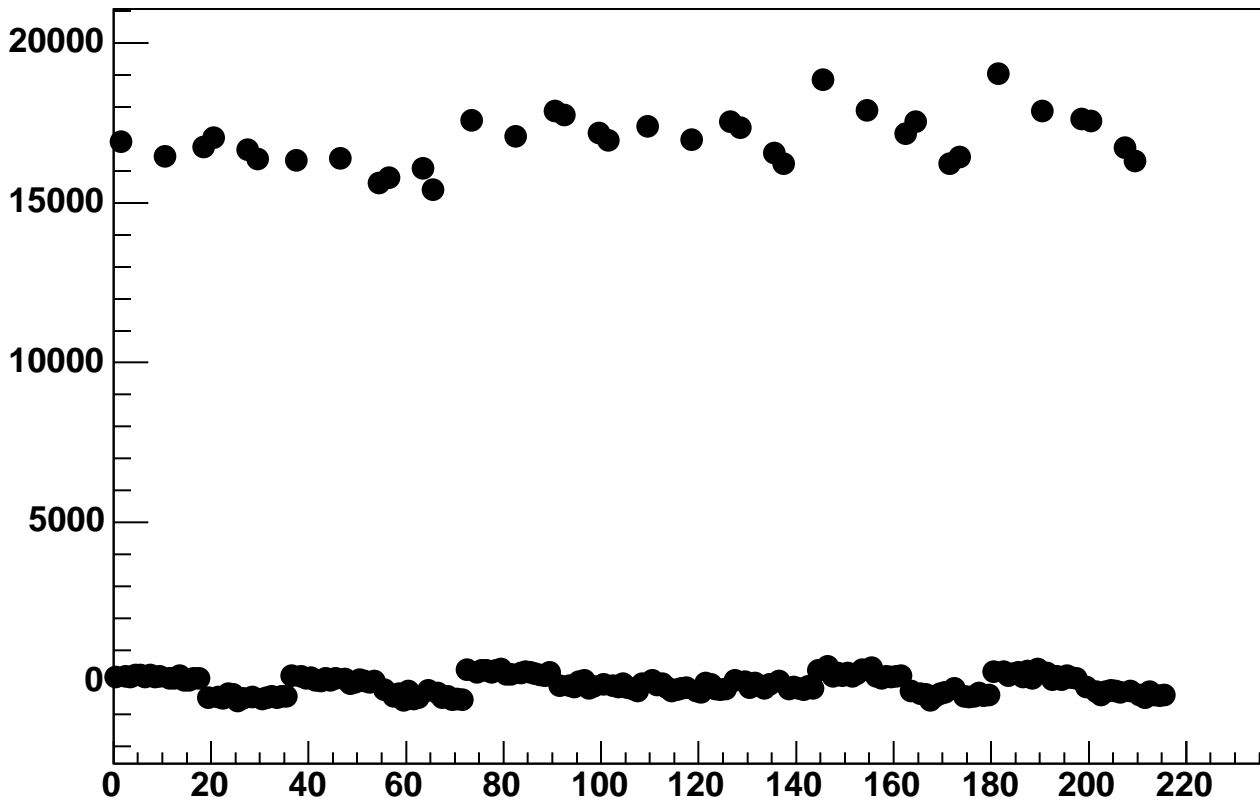
Enable 3, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



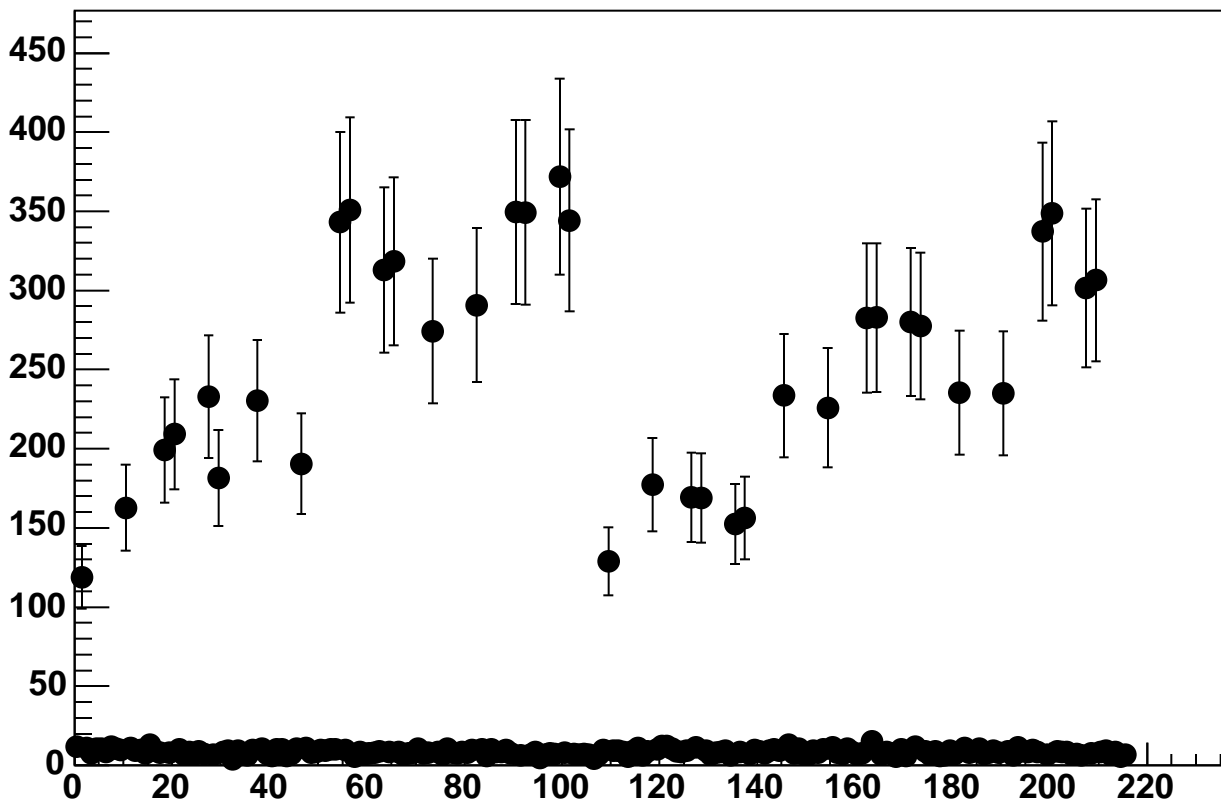
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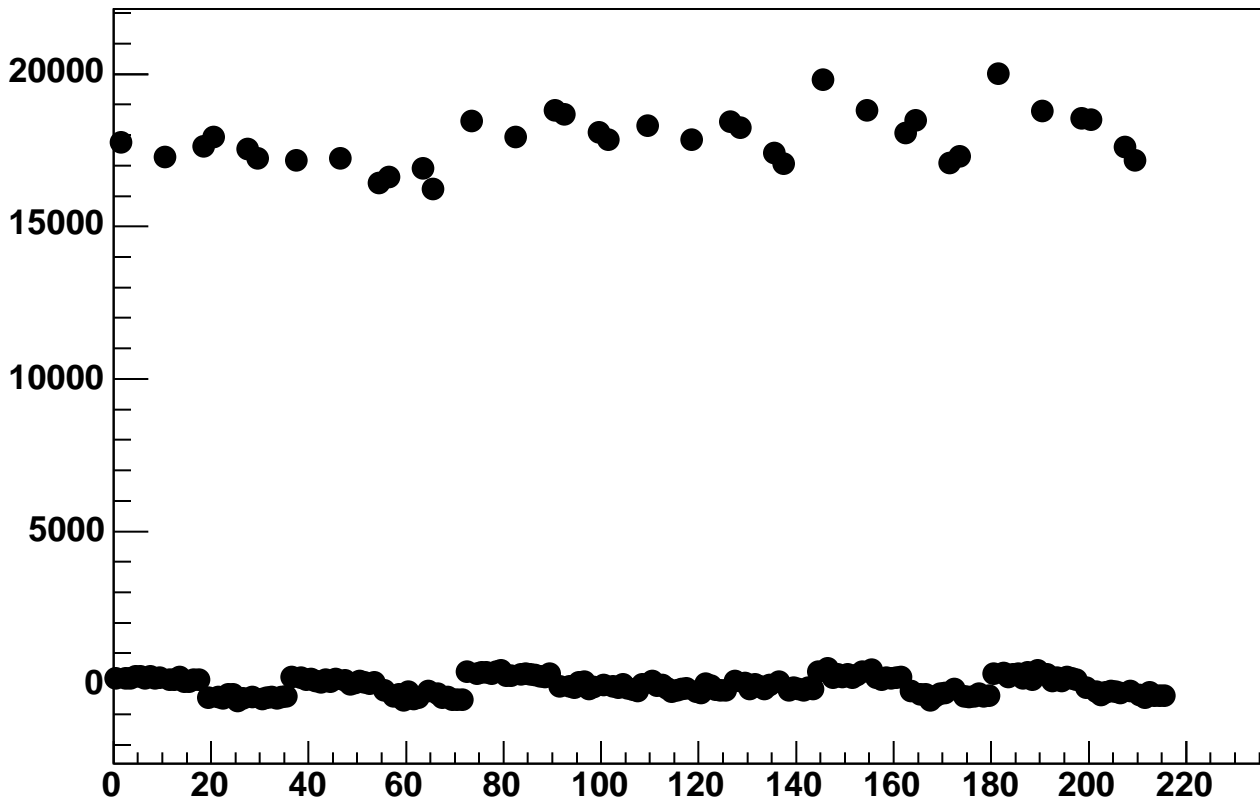
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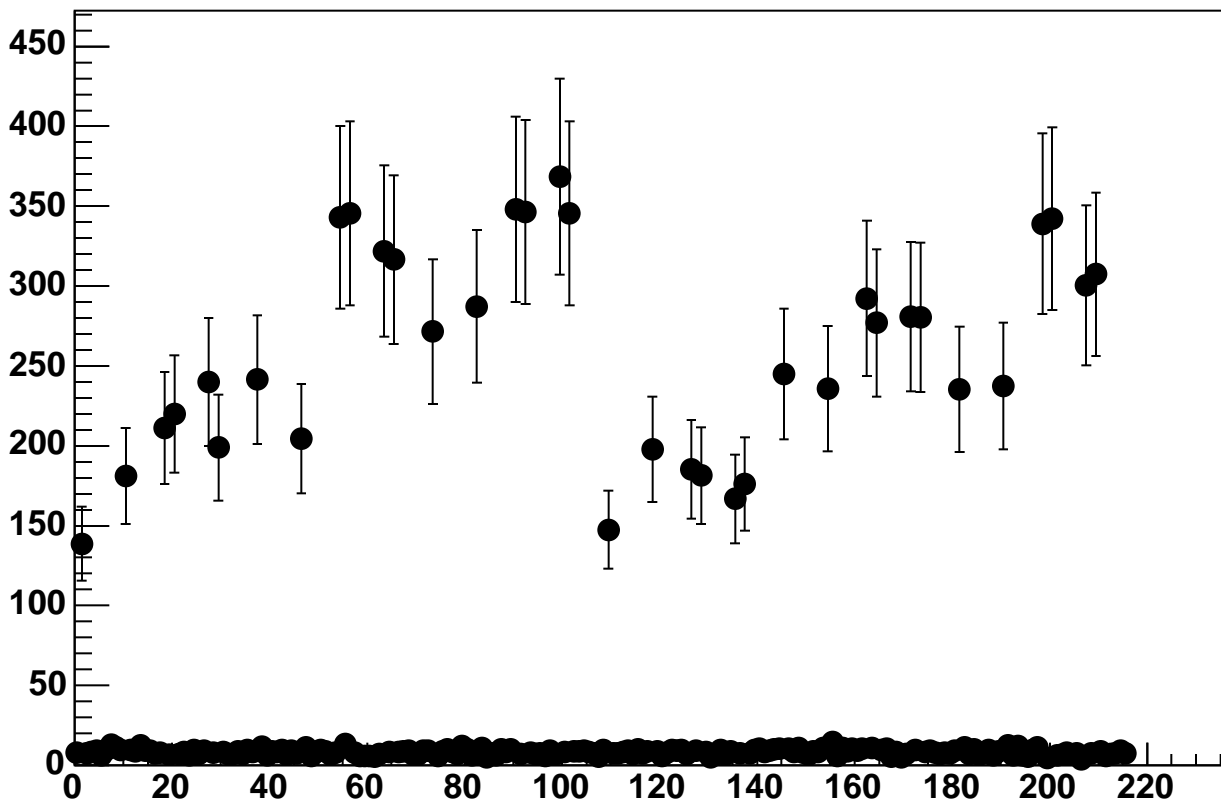
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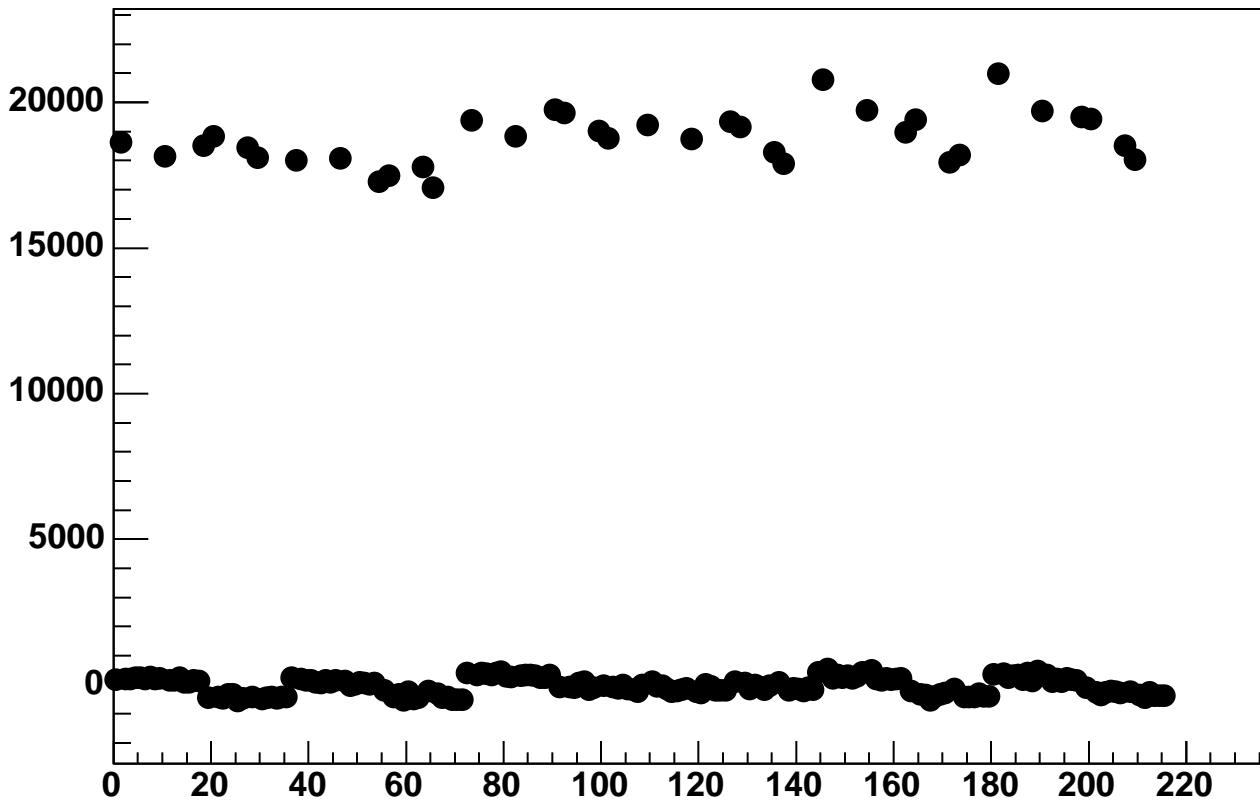
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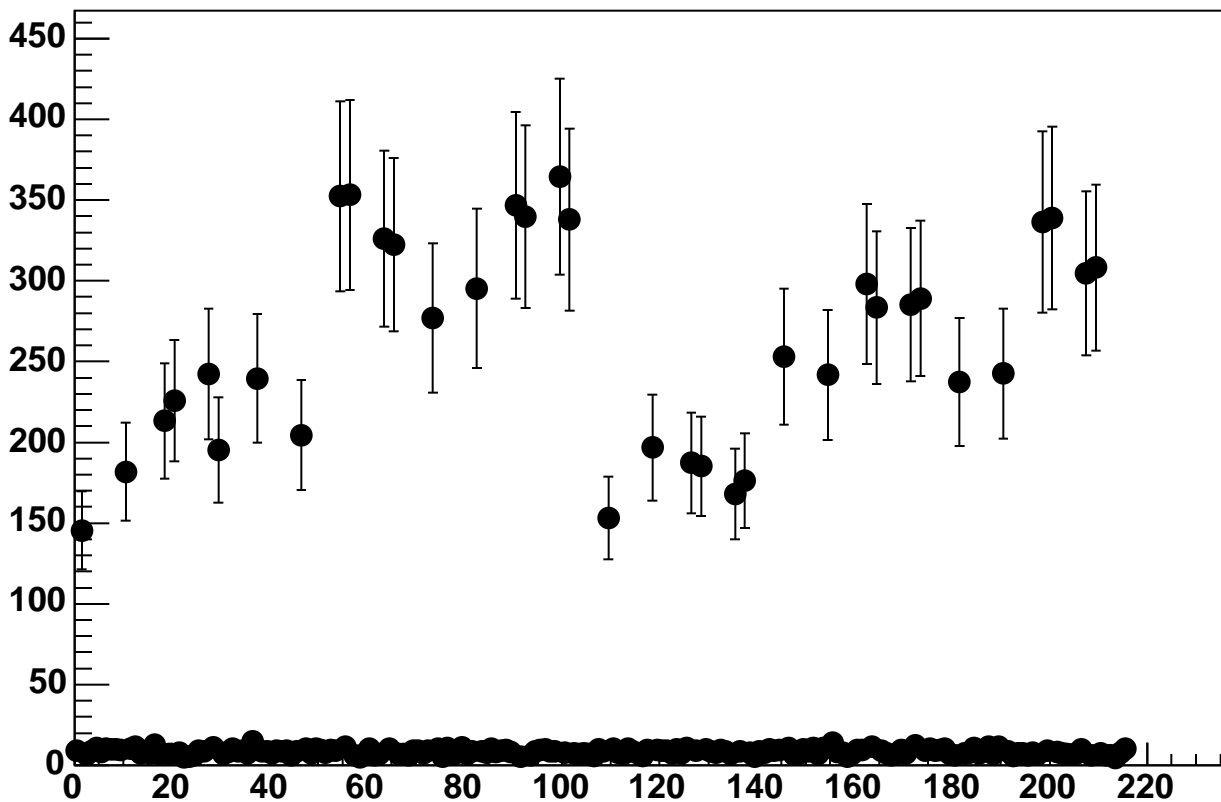
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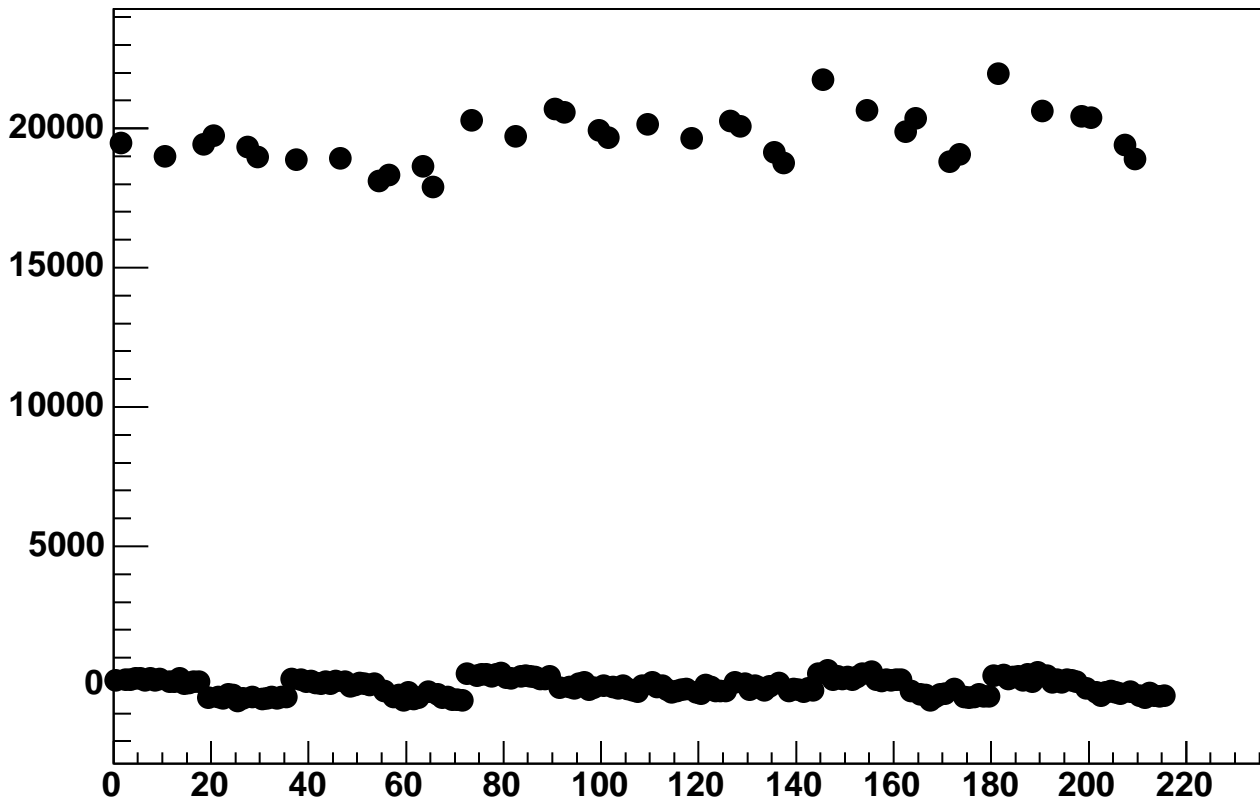
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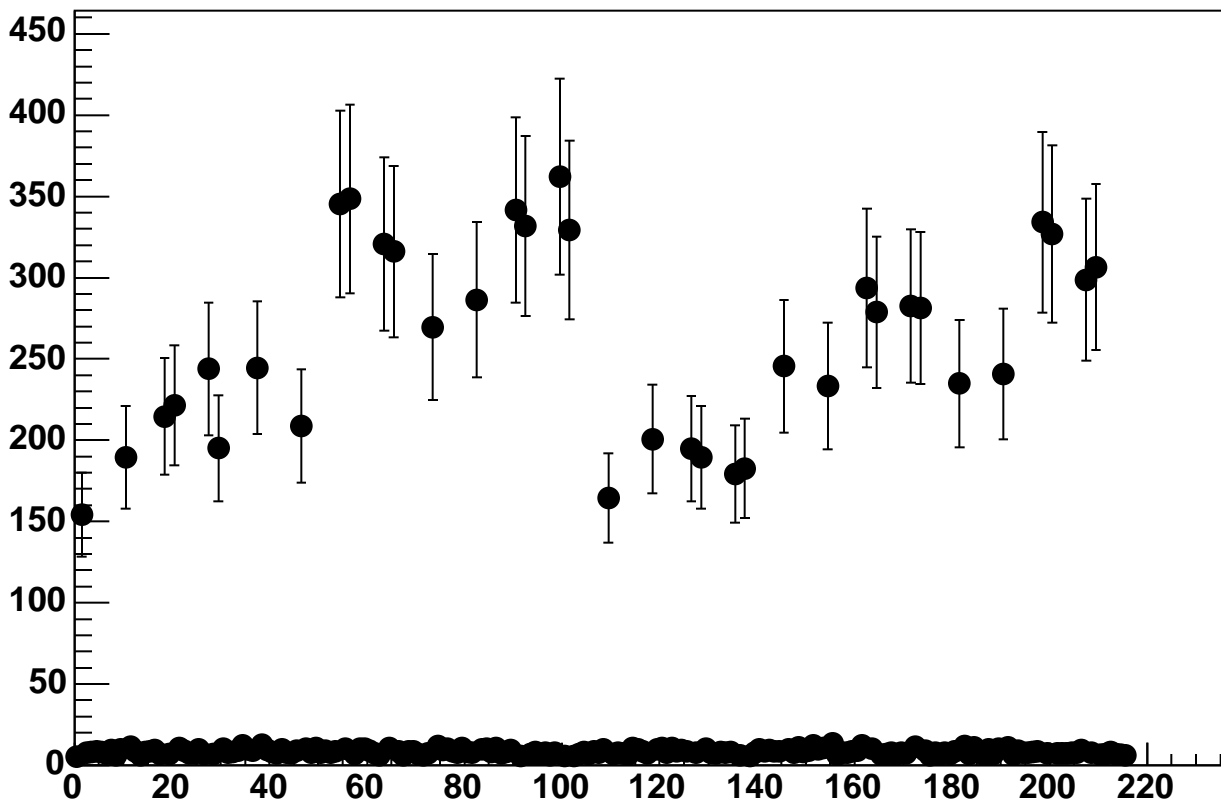
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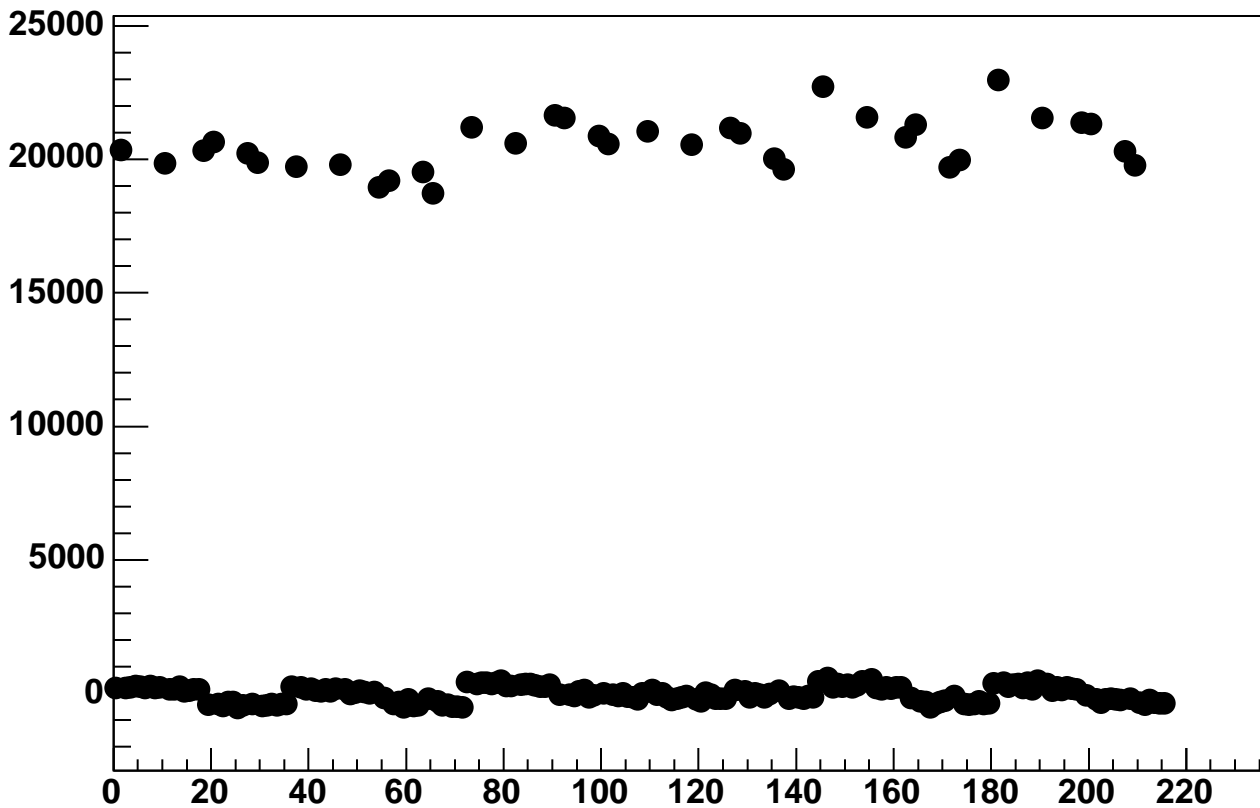
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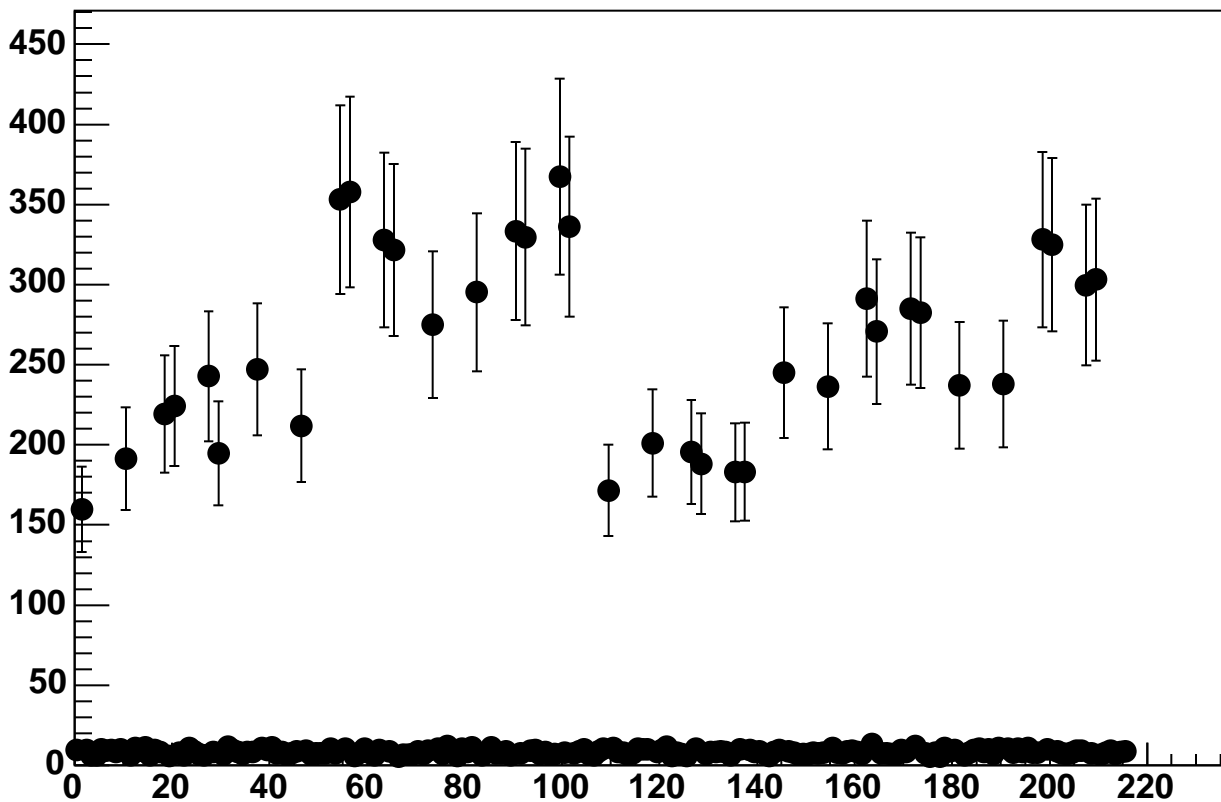
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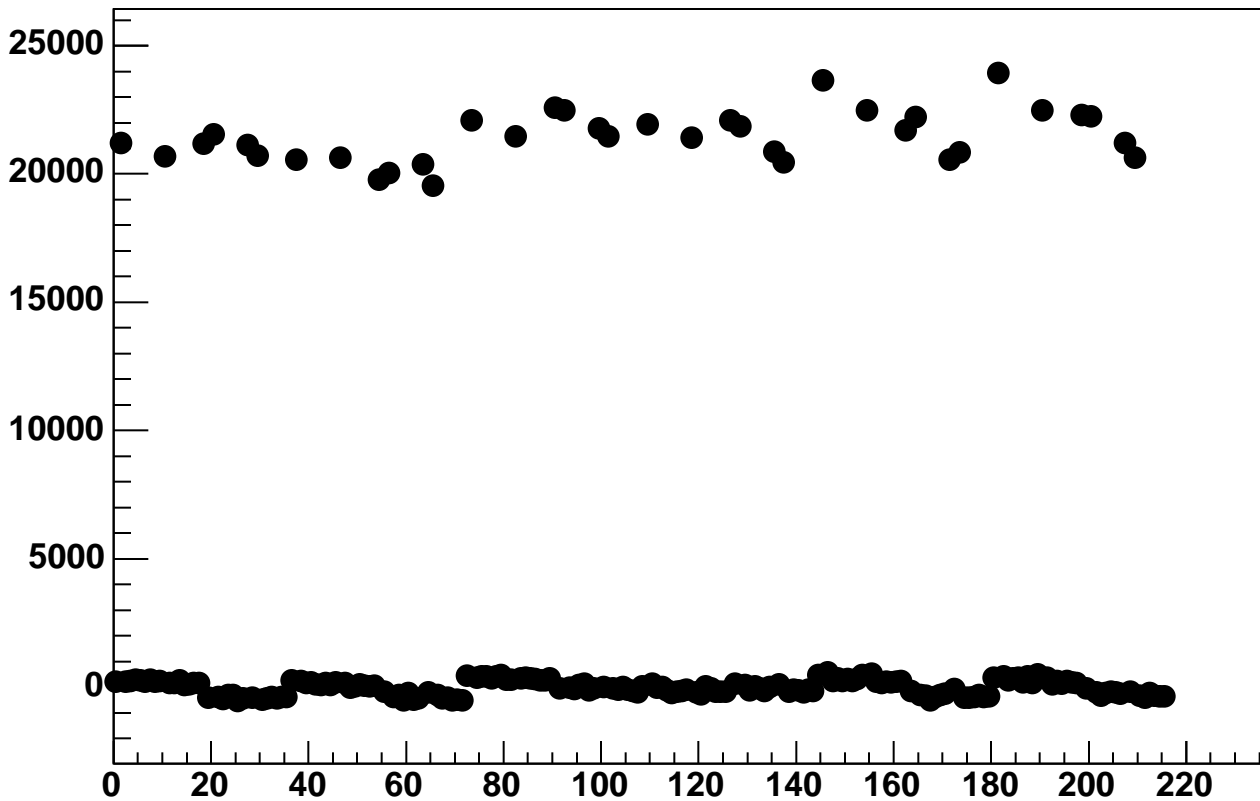
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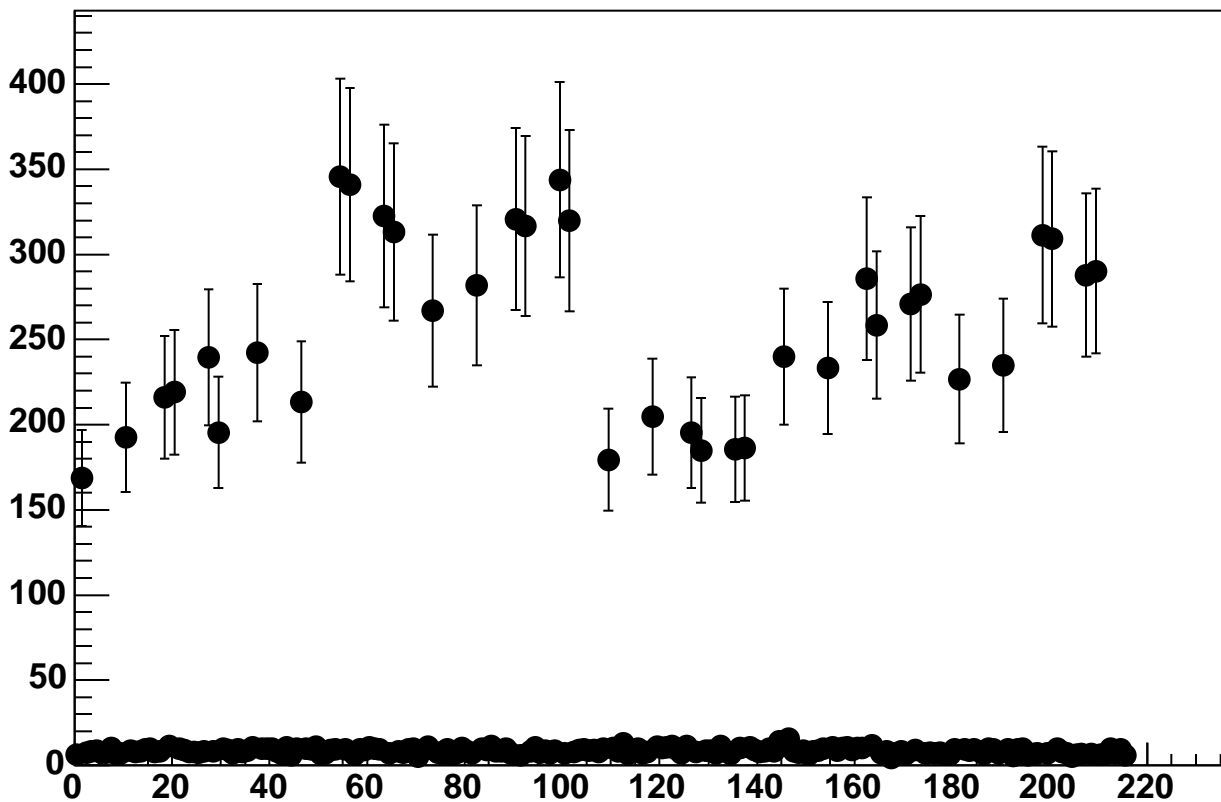
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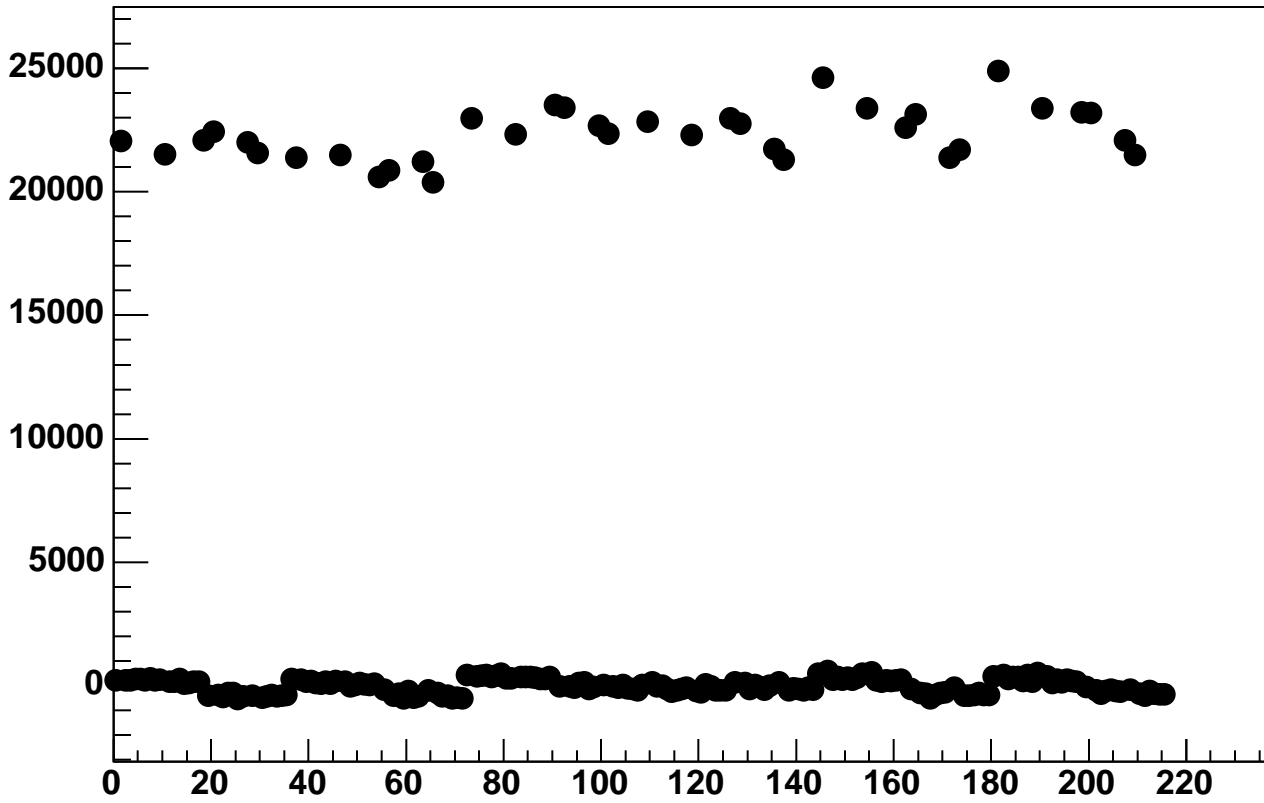
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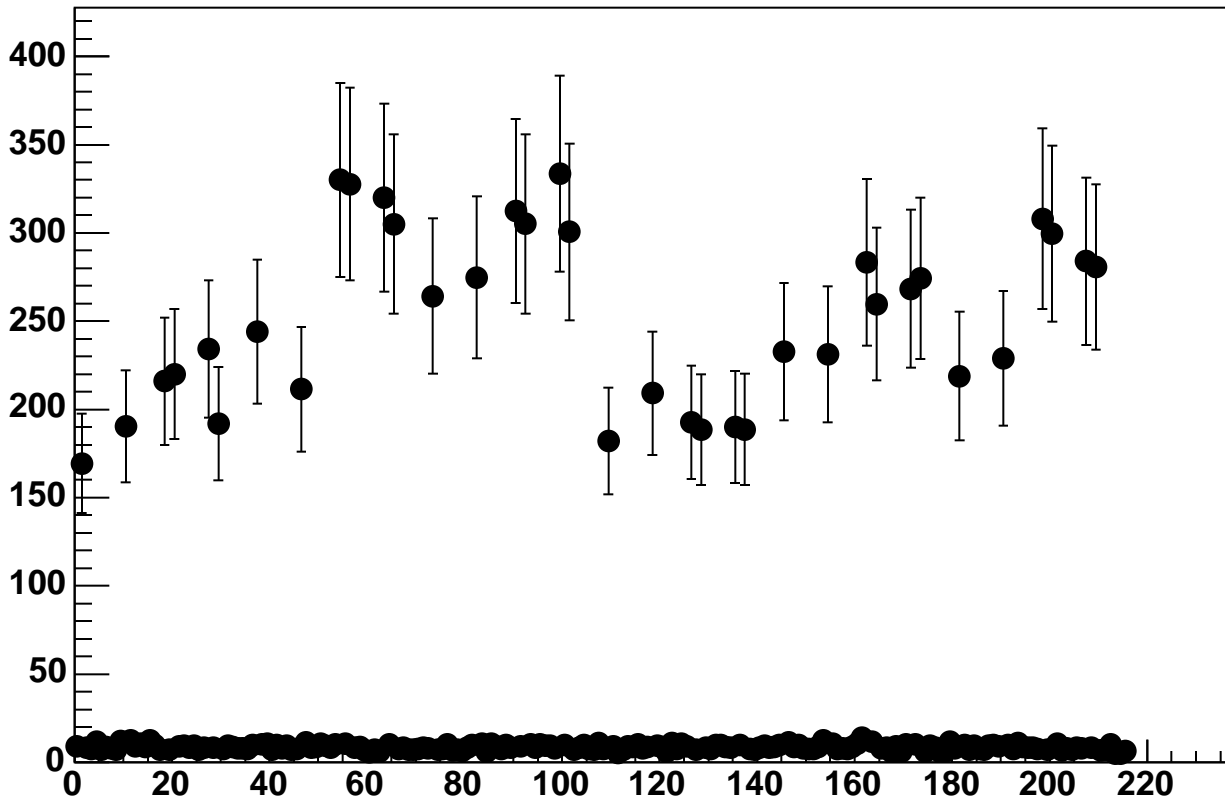
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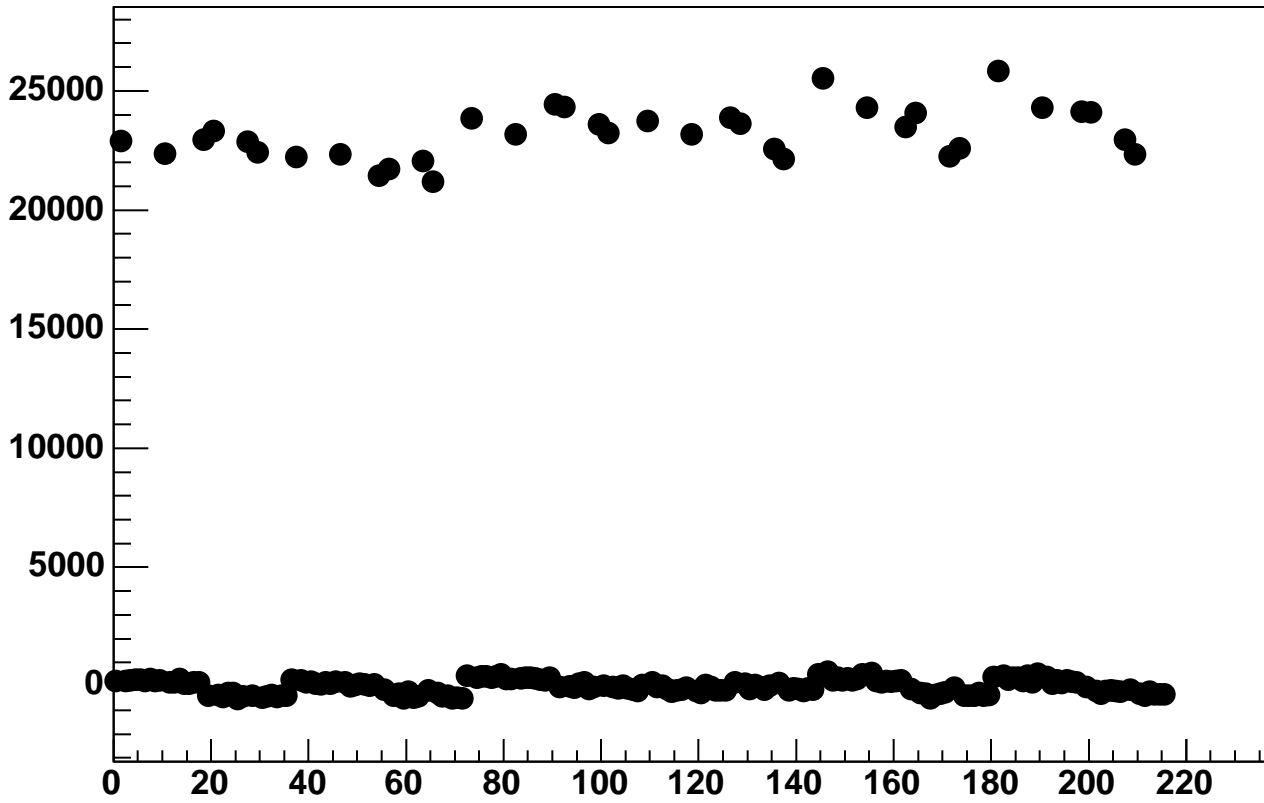
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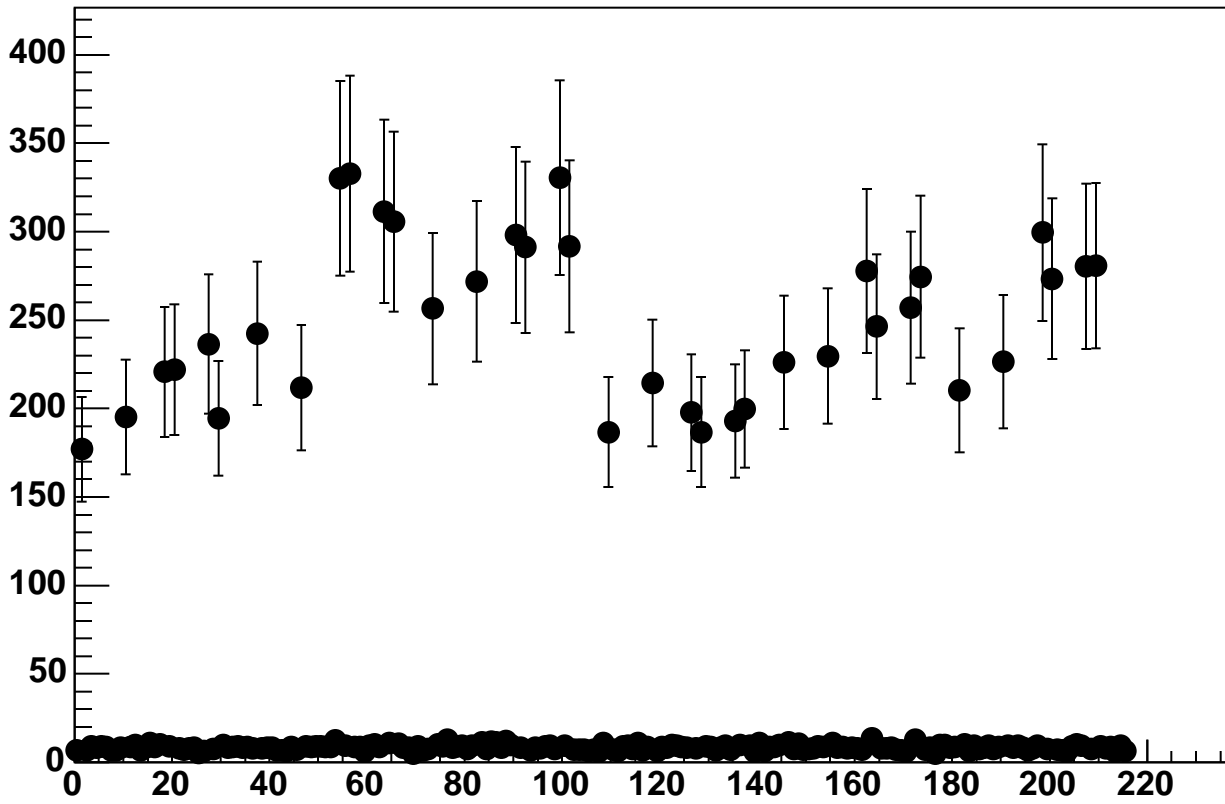
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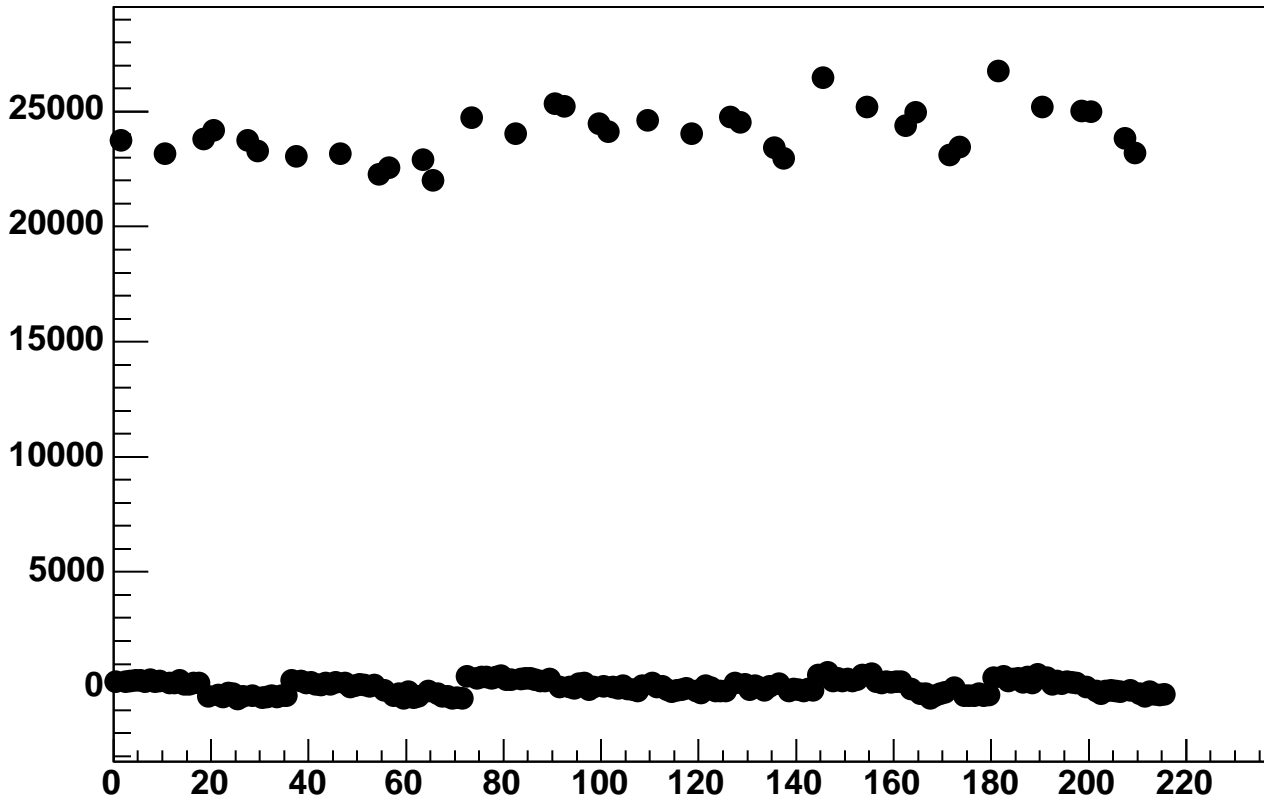
Enable 3, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



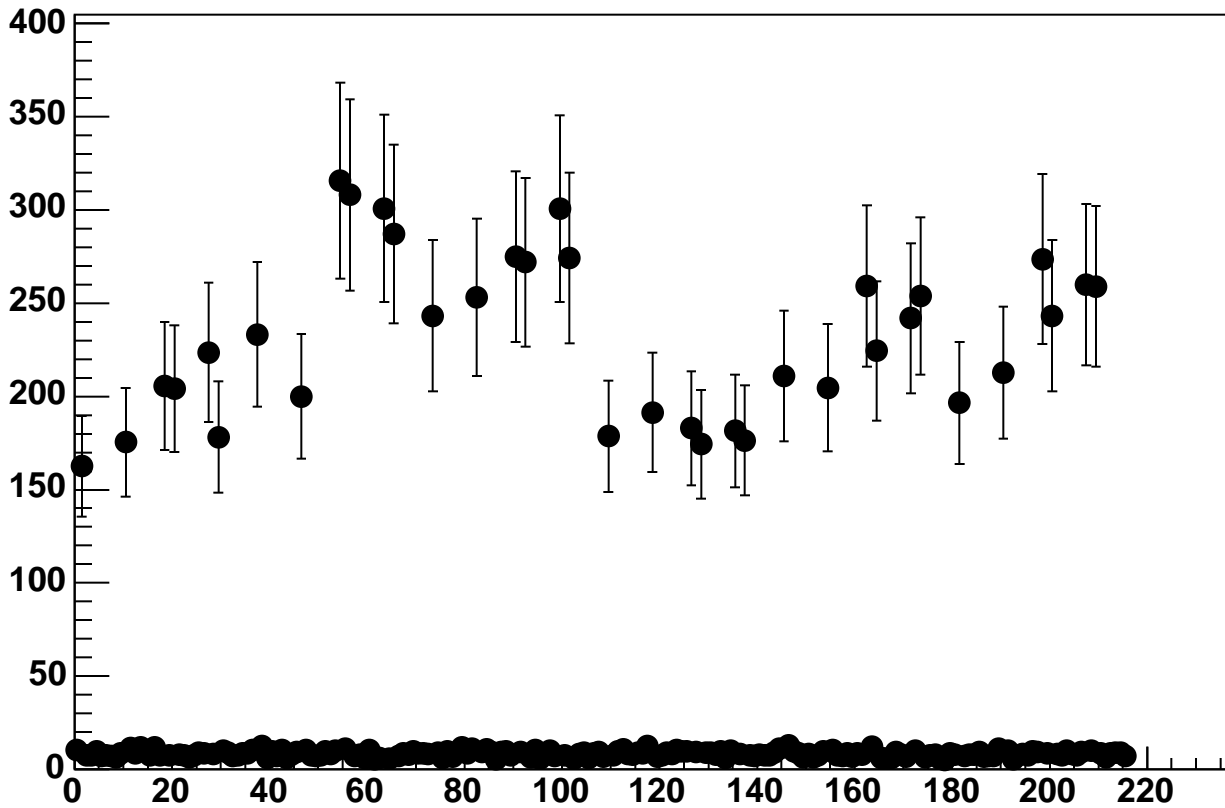
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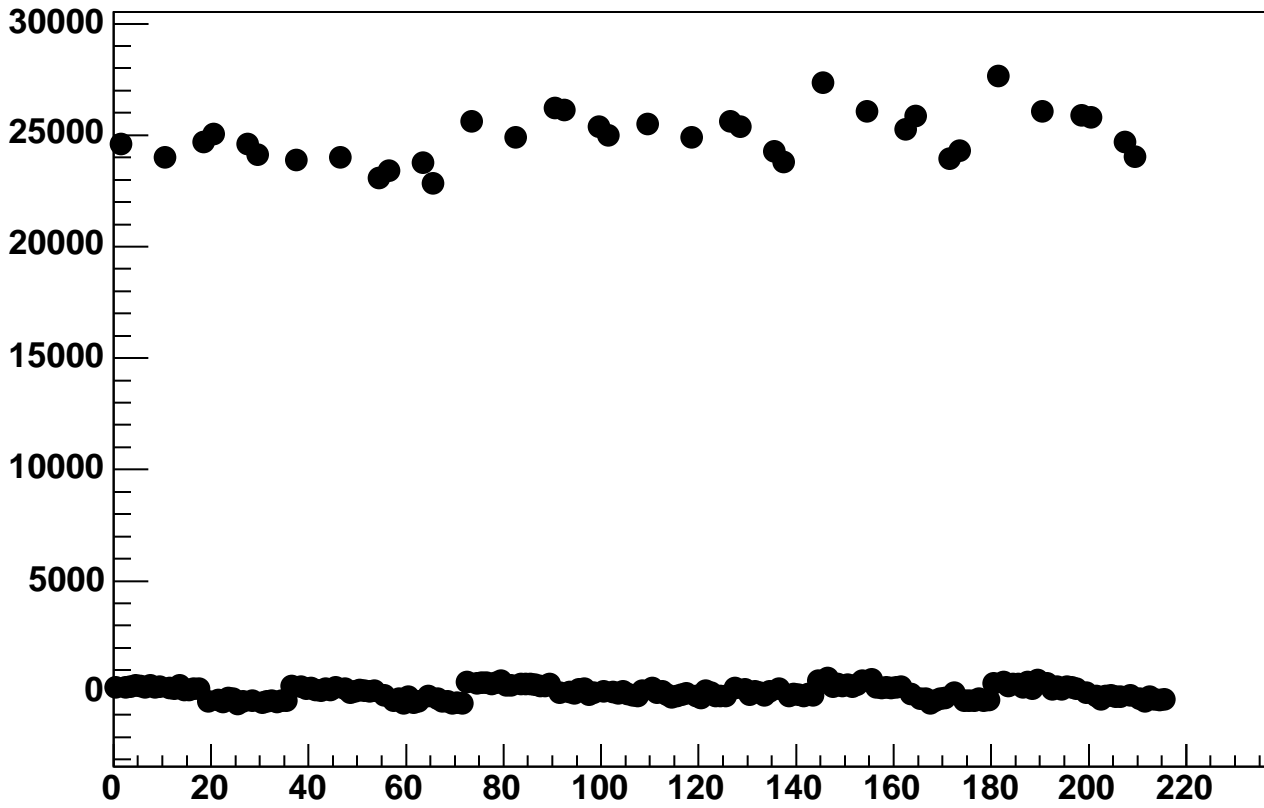
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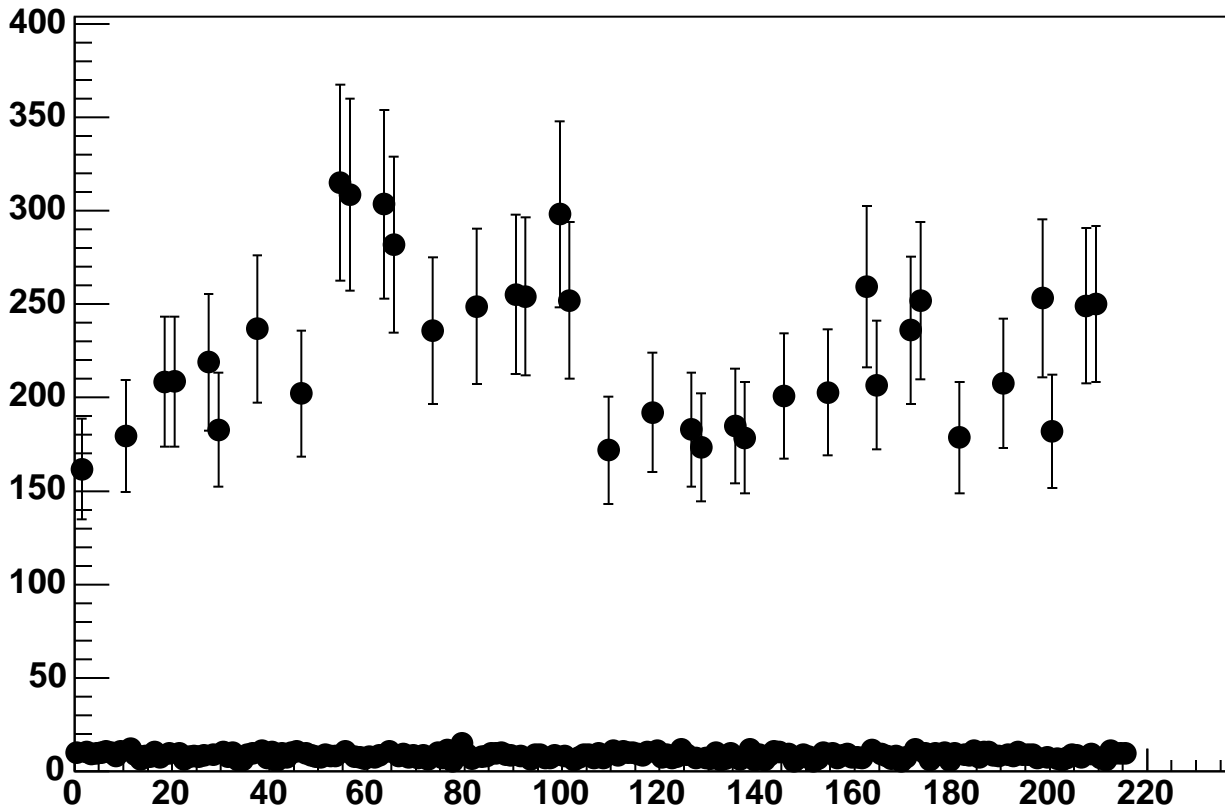
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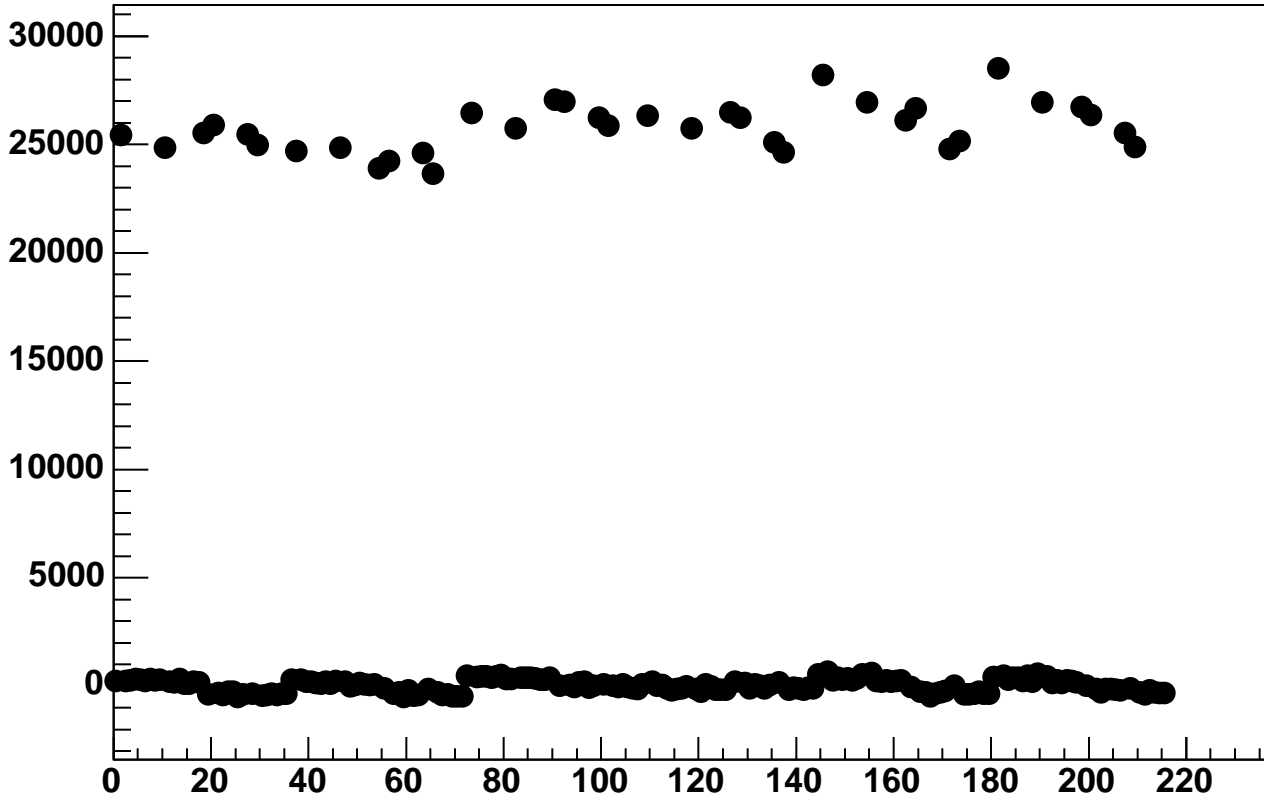
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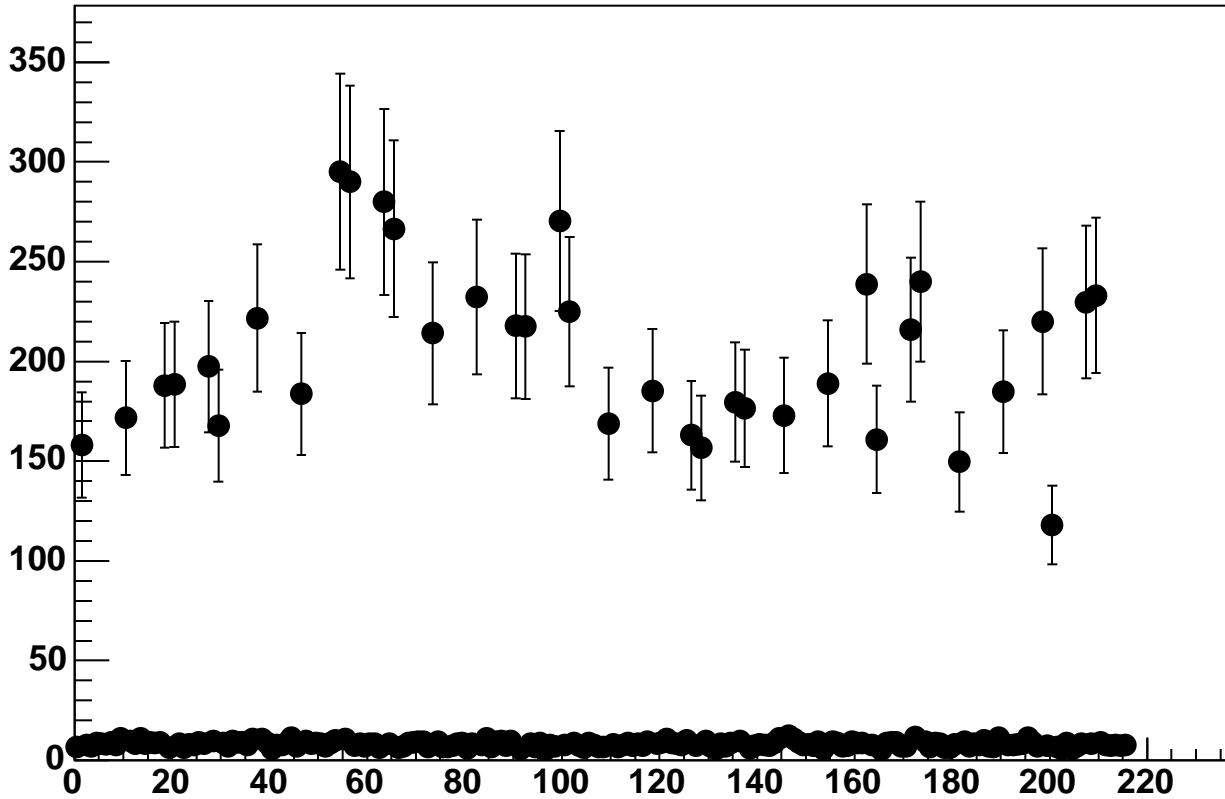
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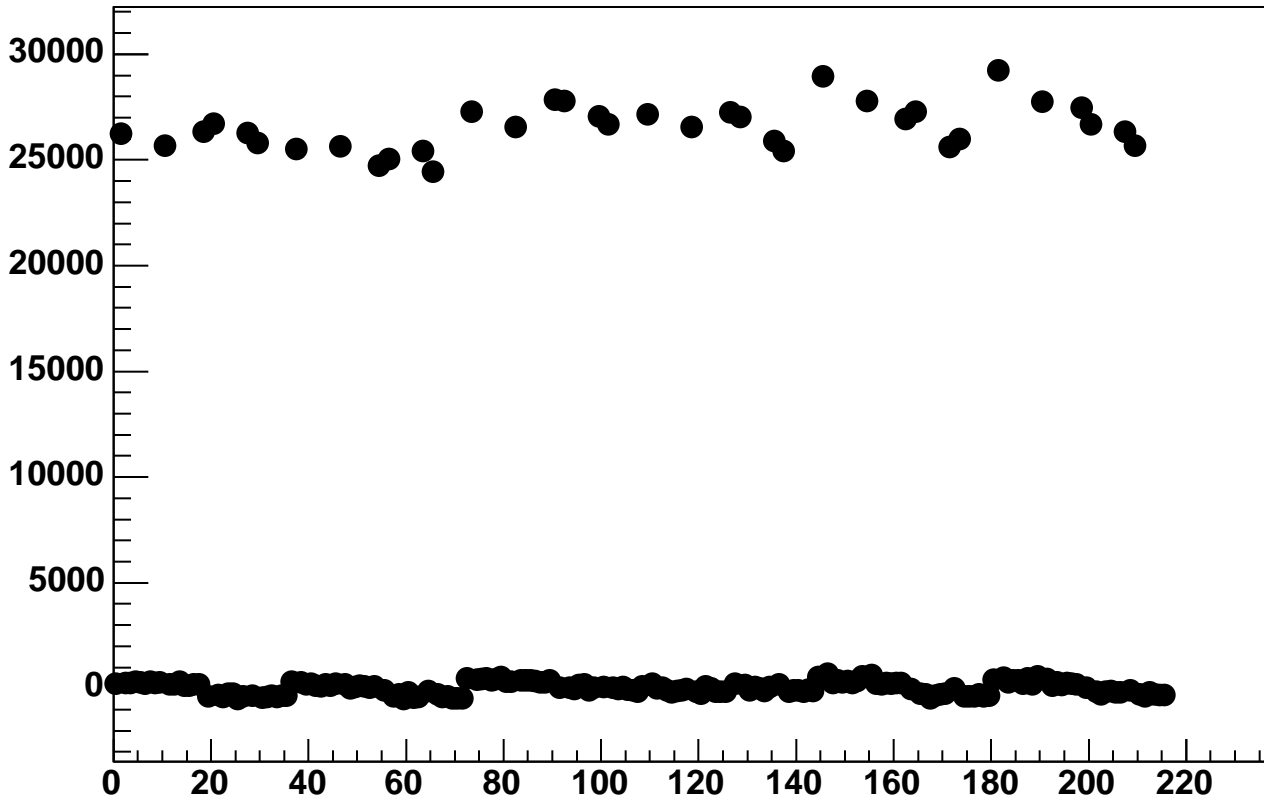
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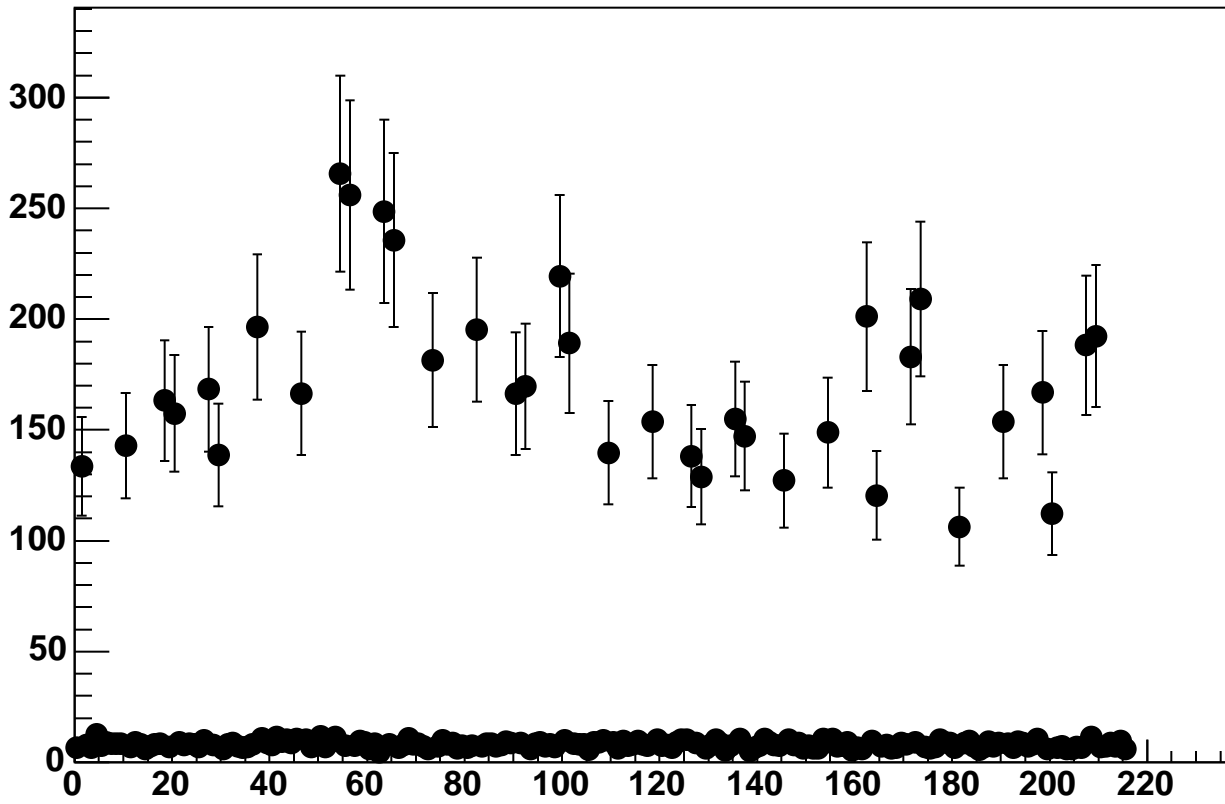
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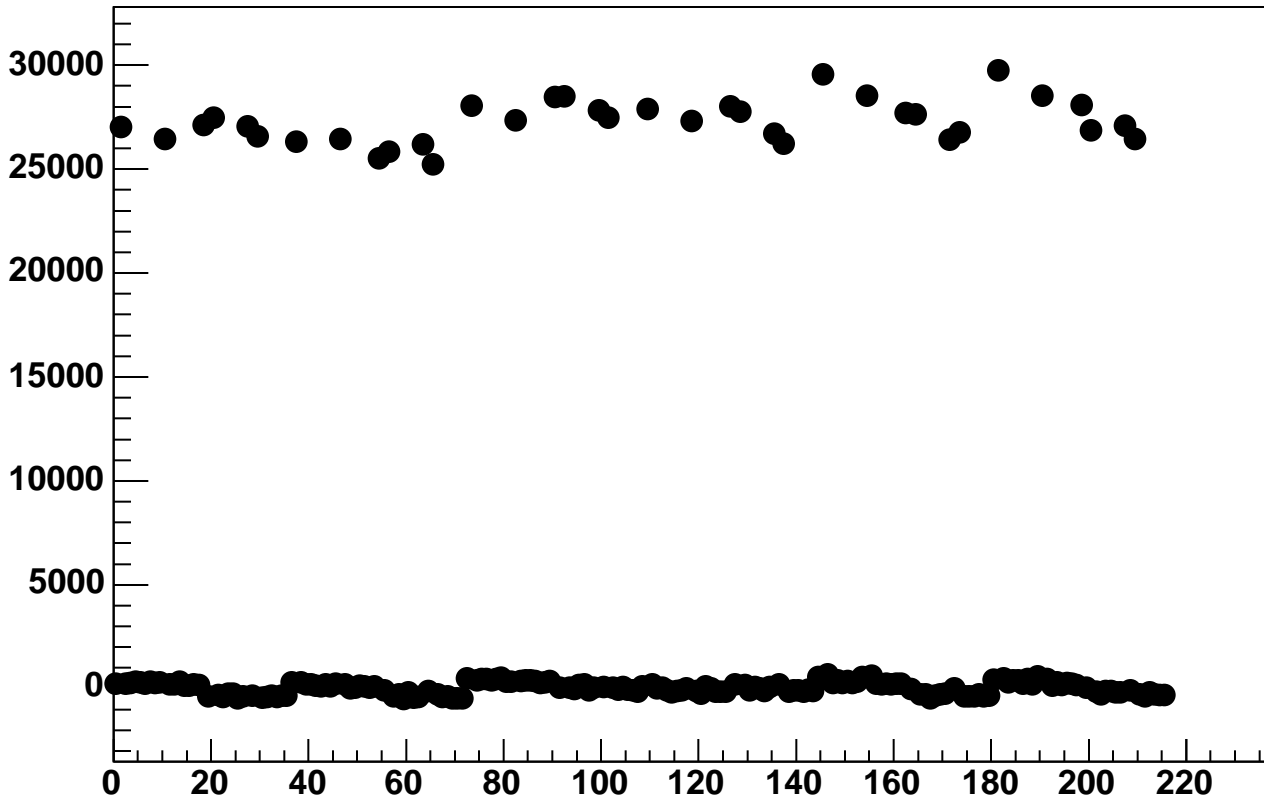
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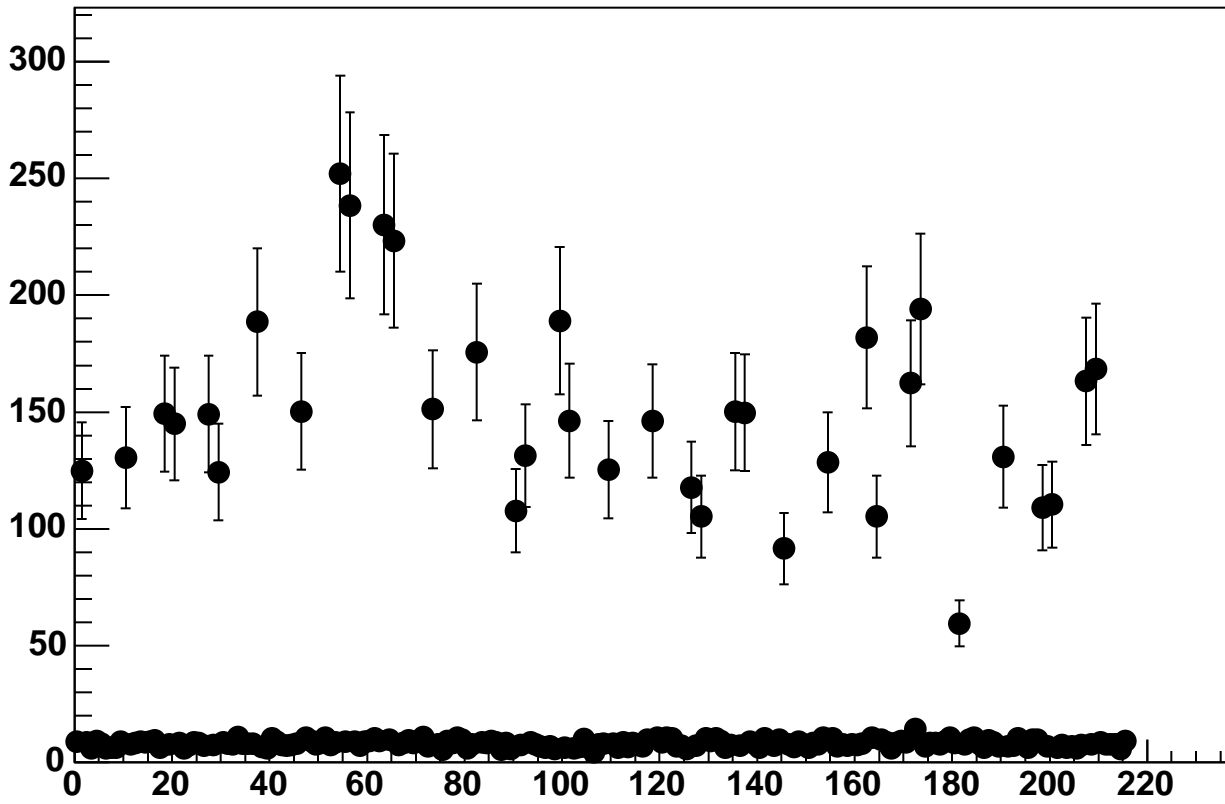
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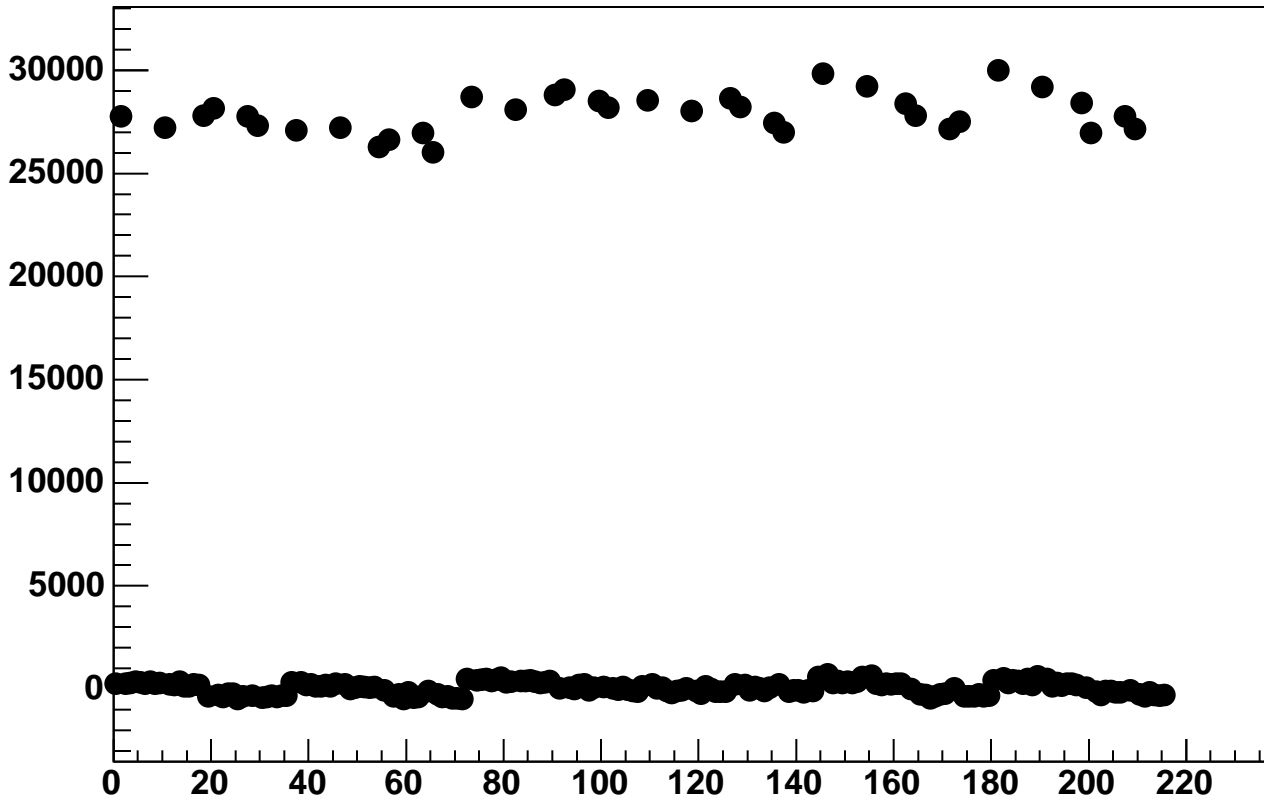
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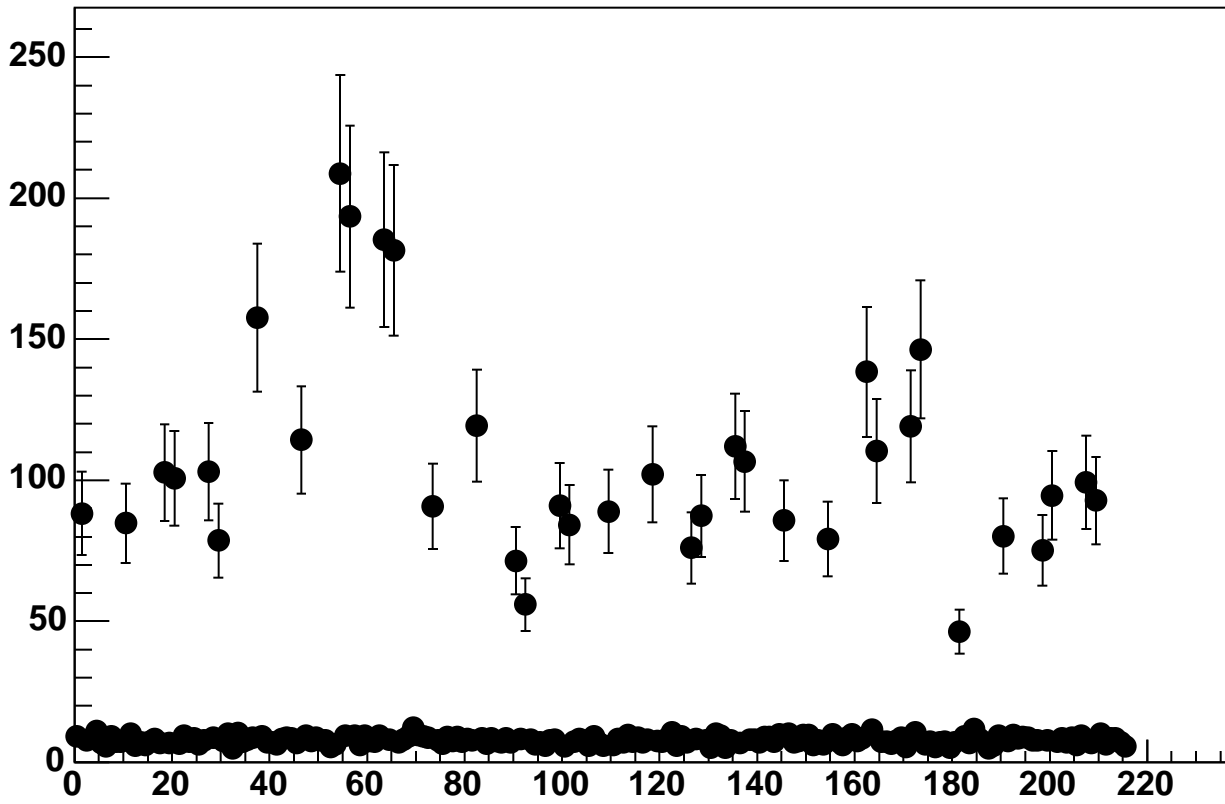
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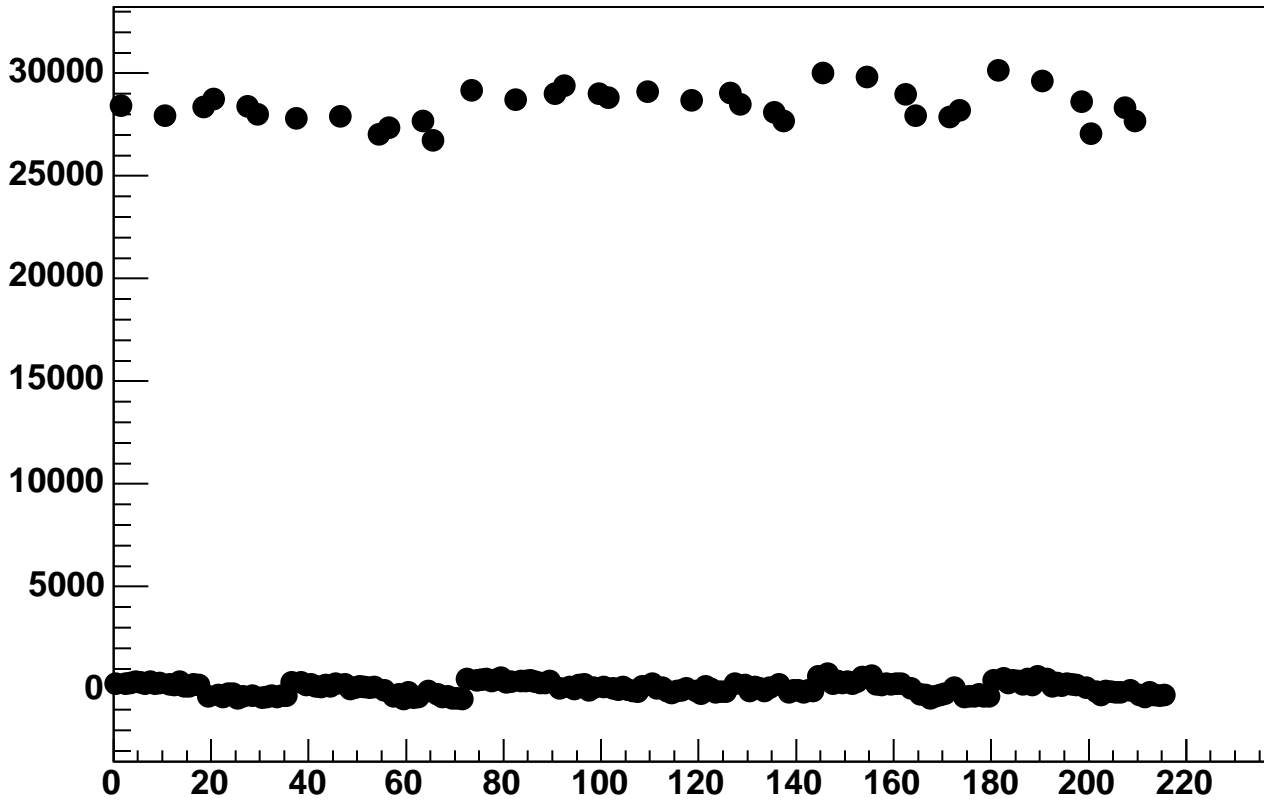
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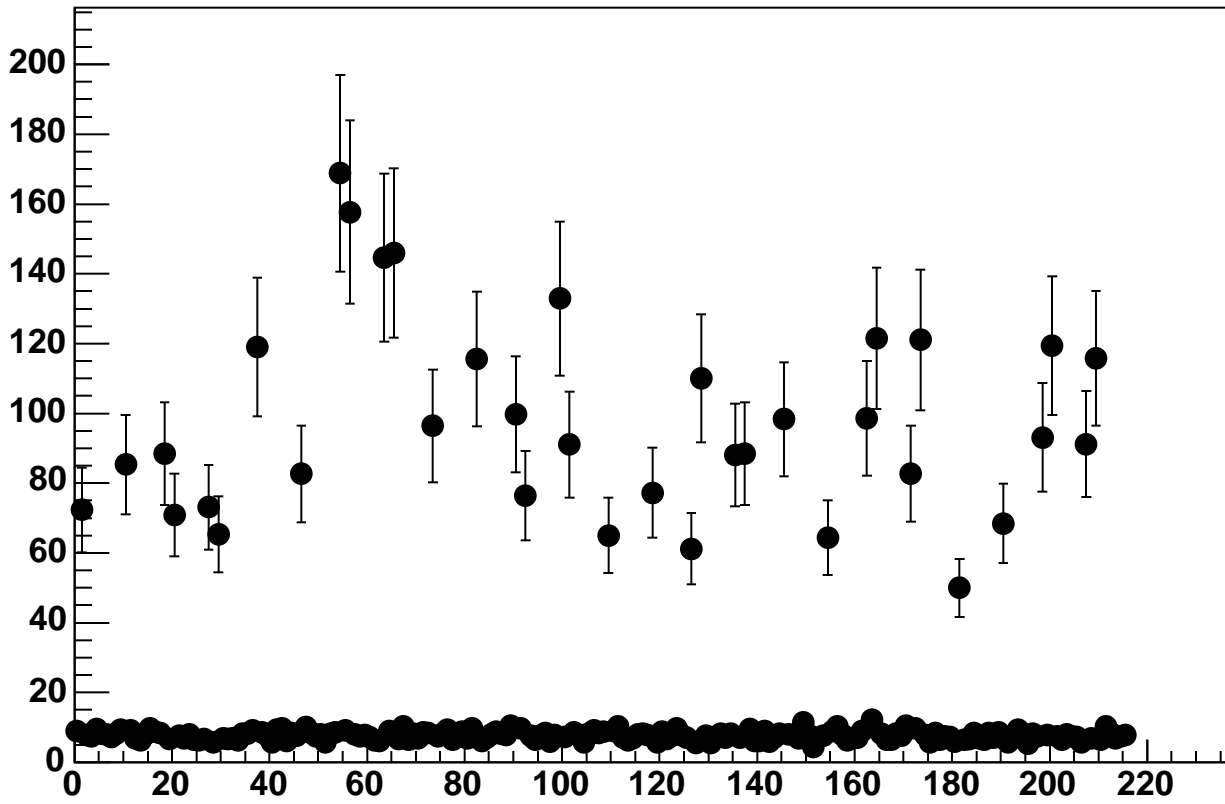
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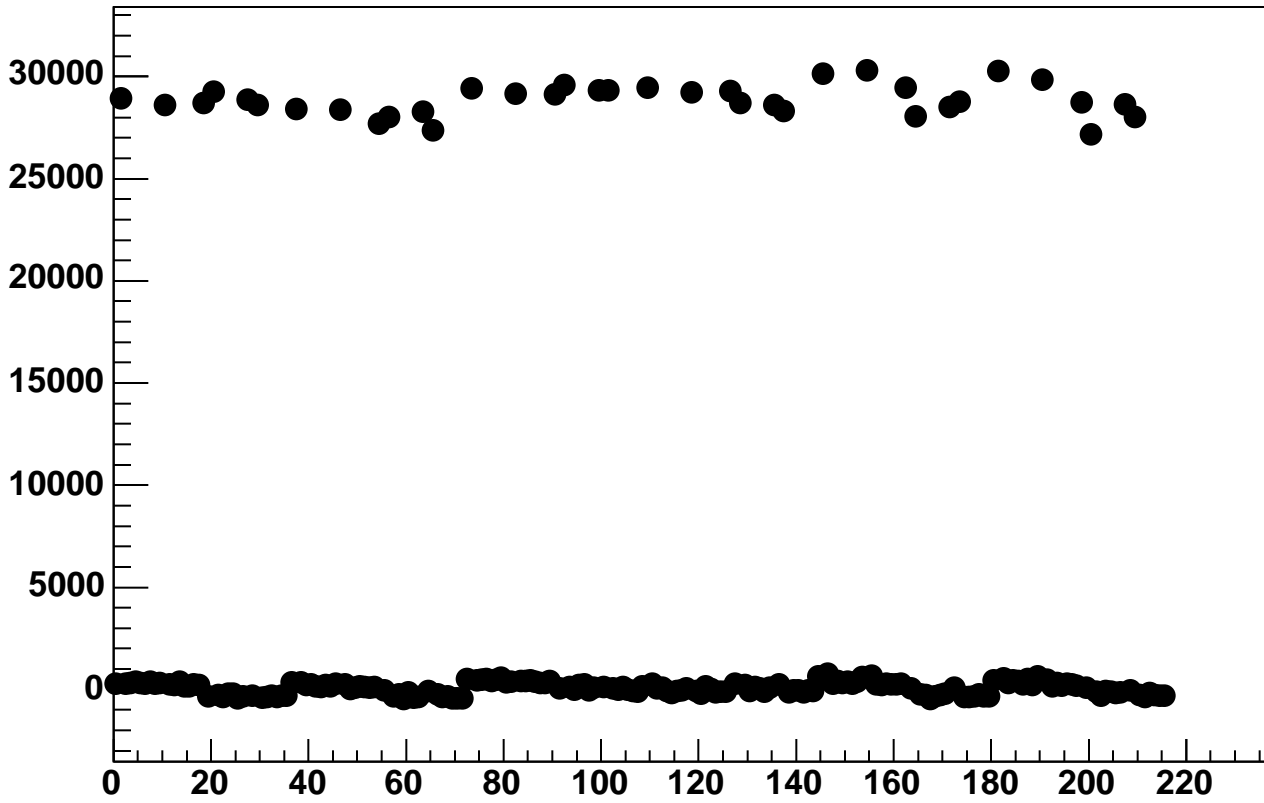
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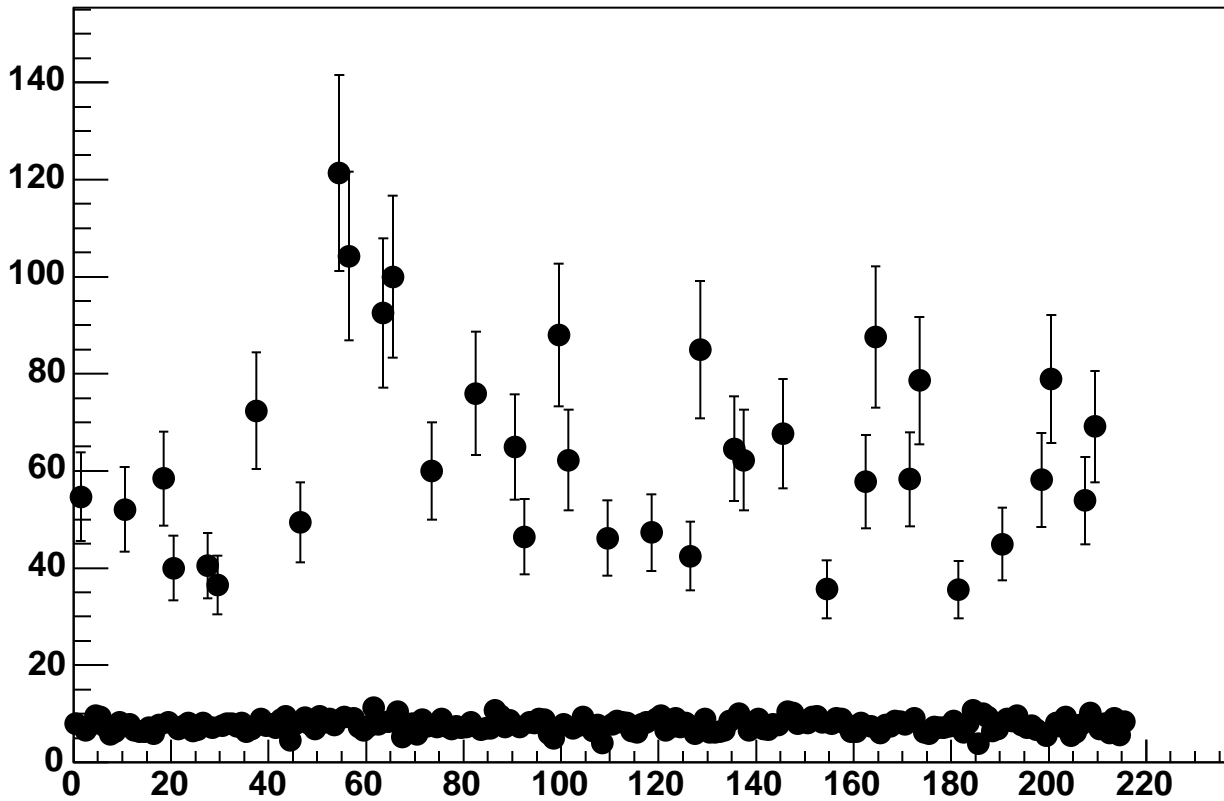
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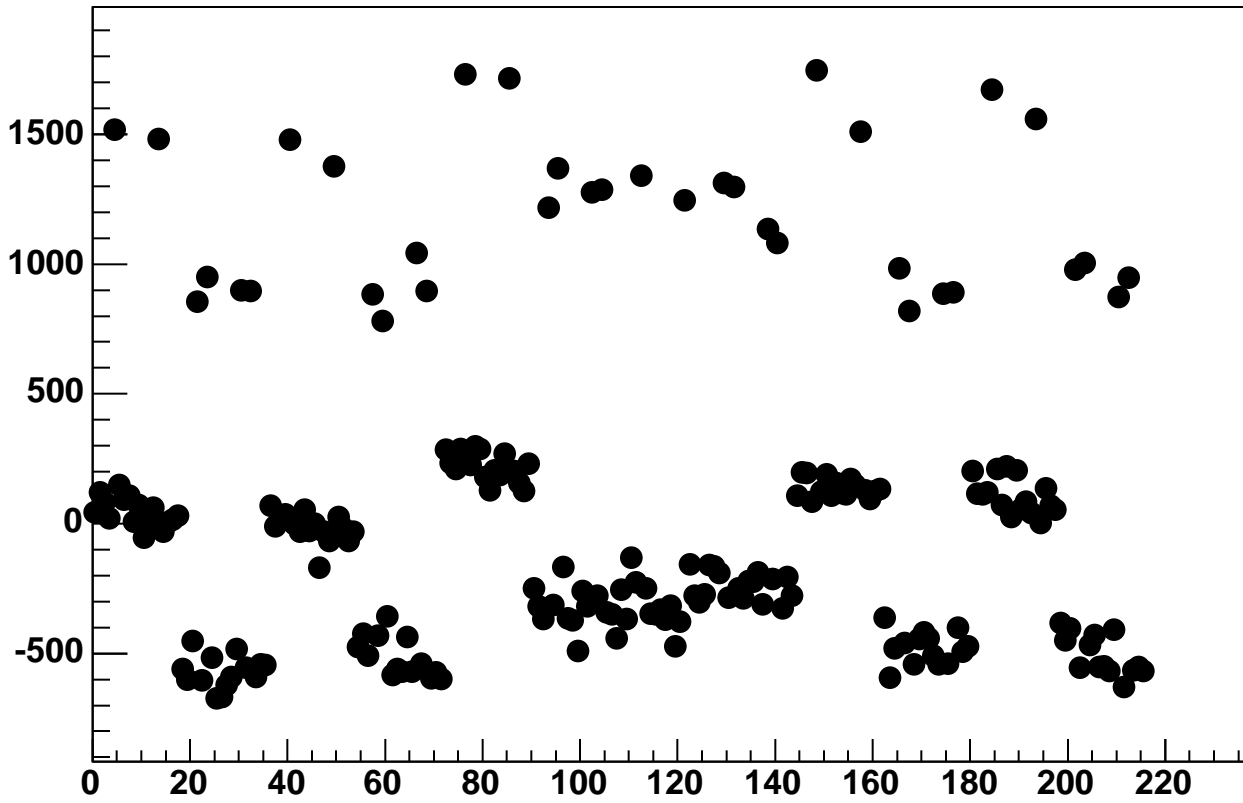
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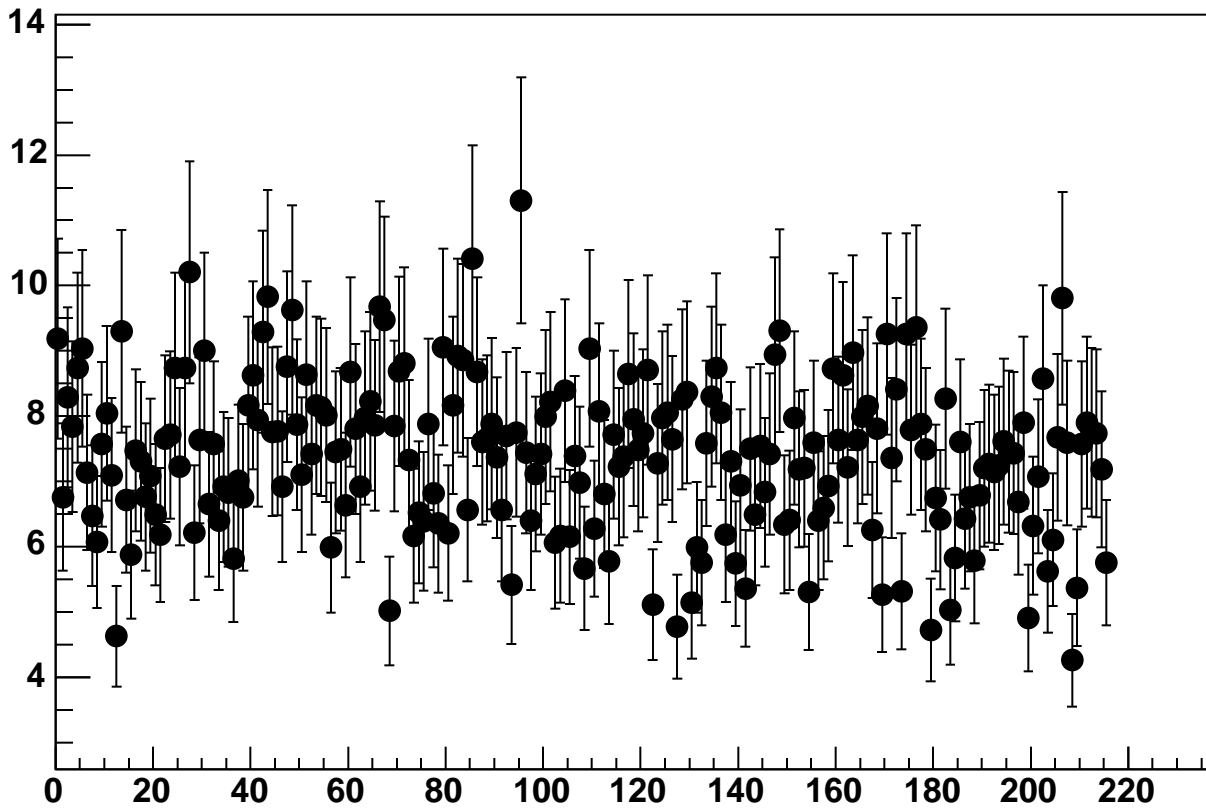
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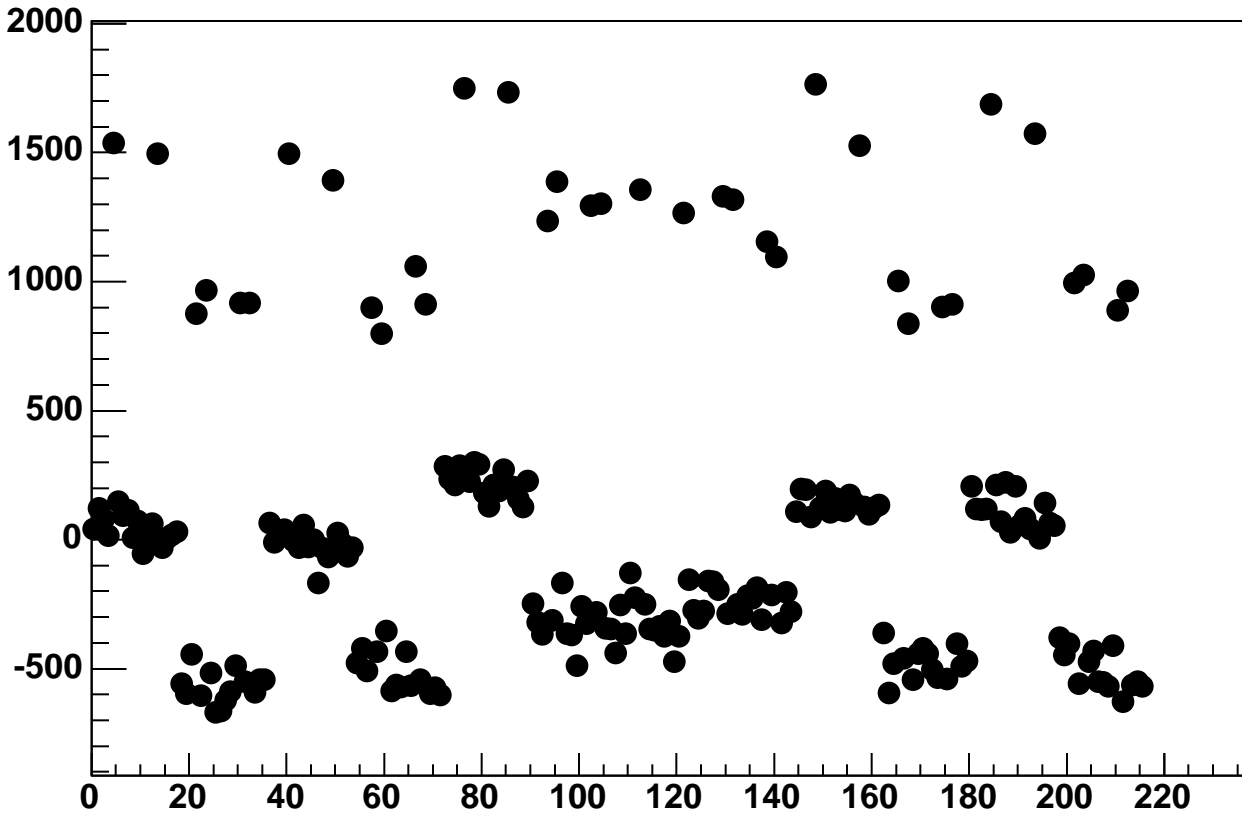
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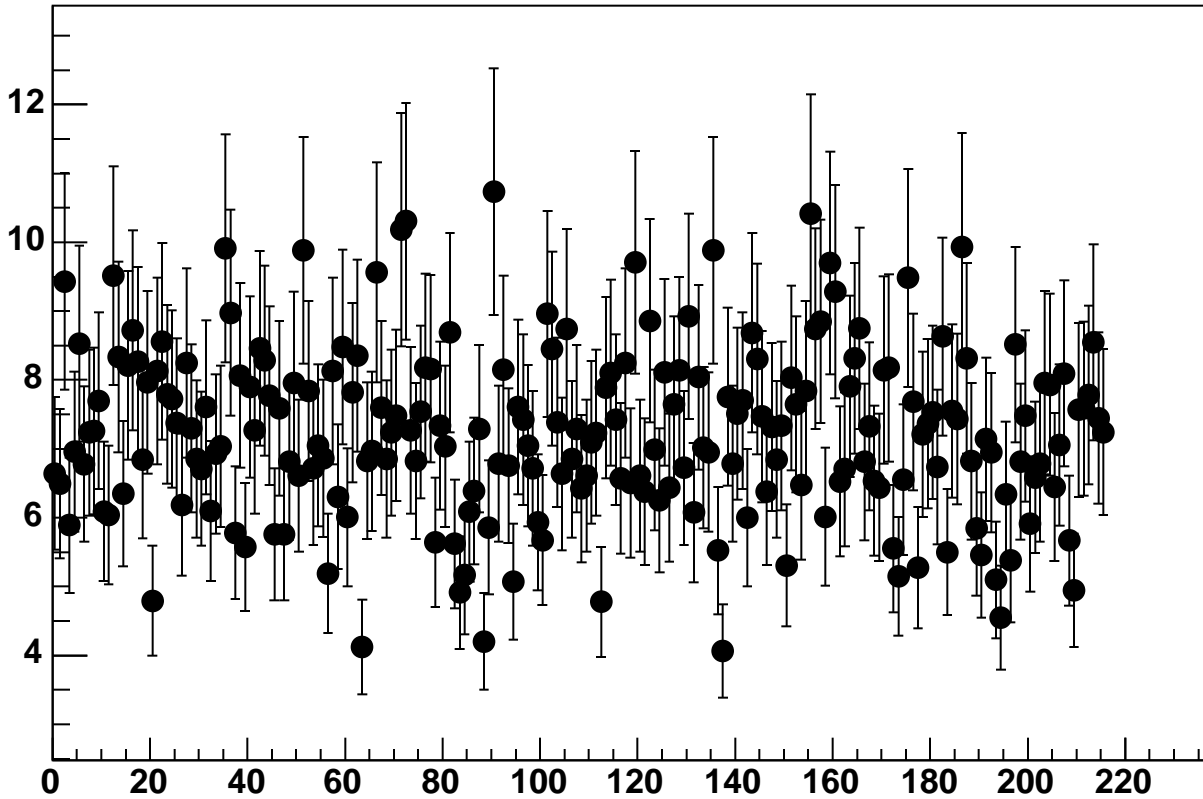
Enable 4, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



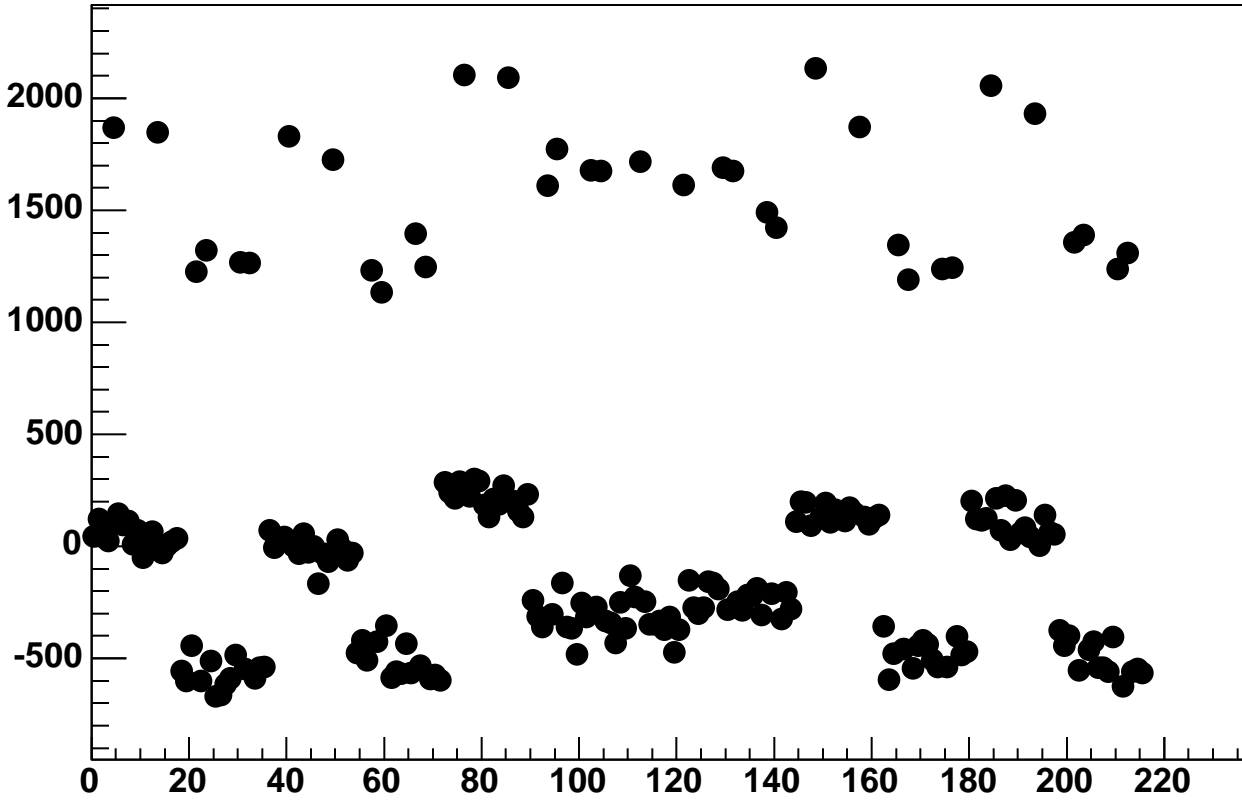
Enable 4, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



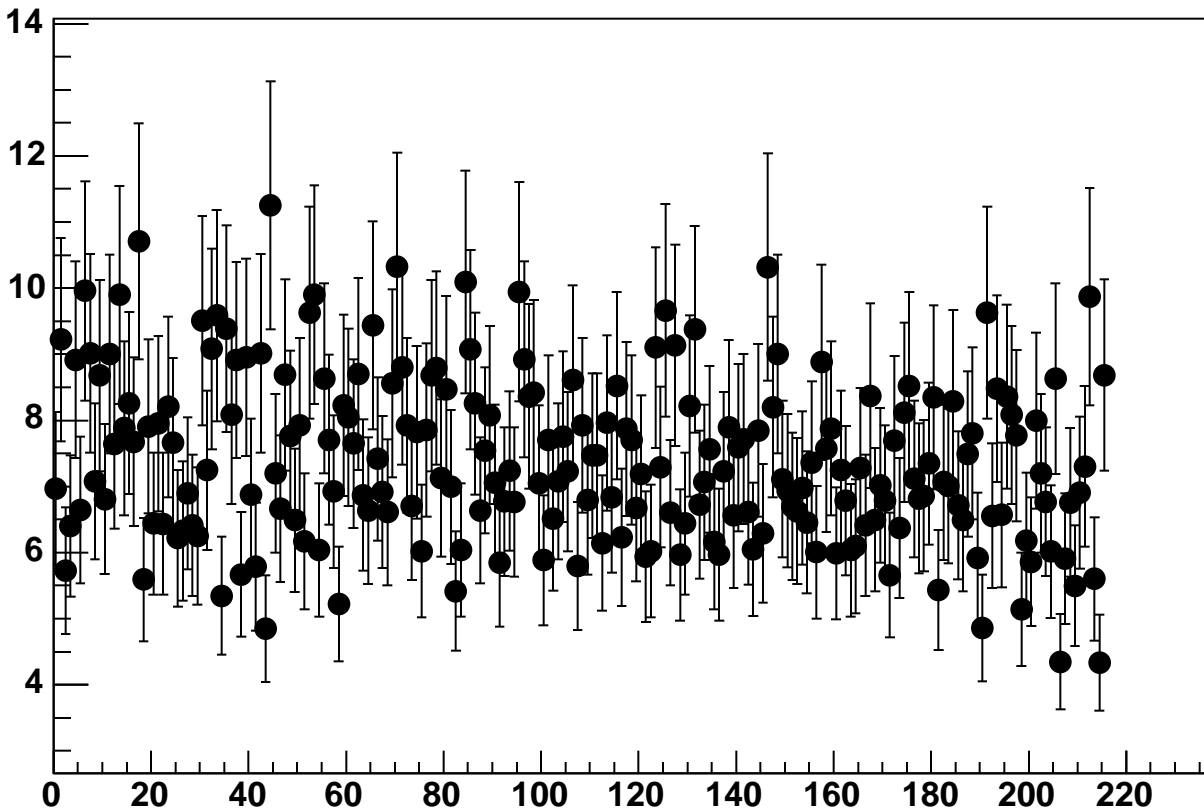
Enable 4, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



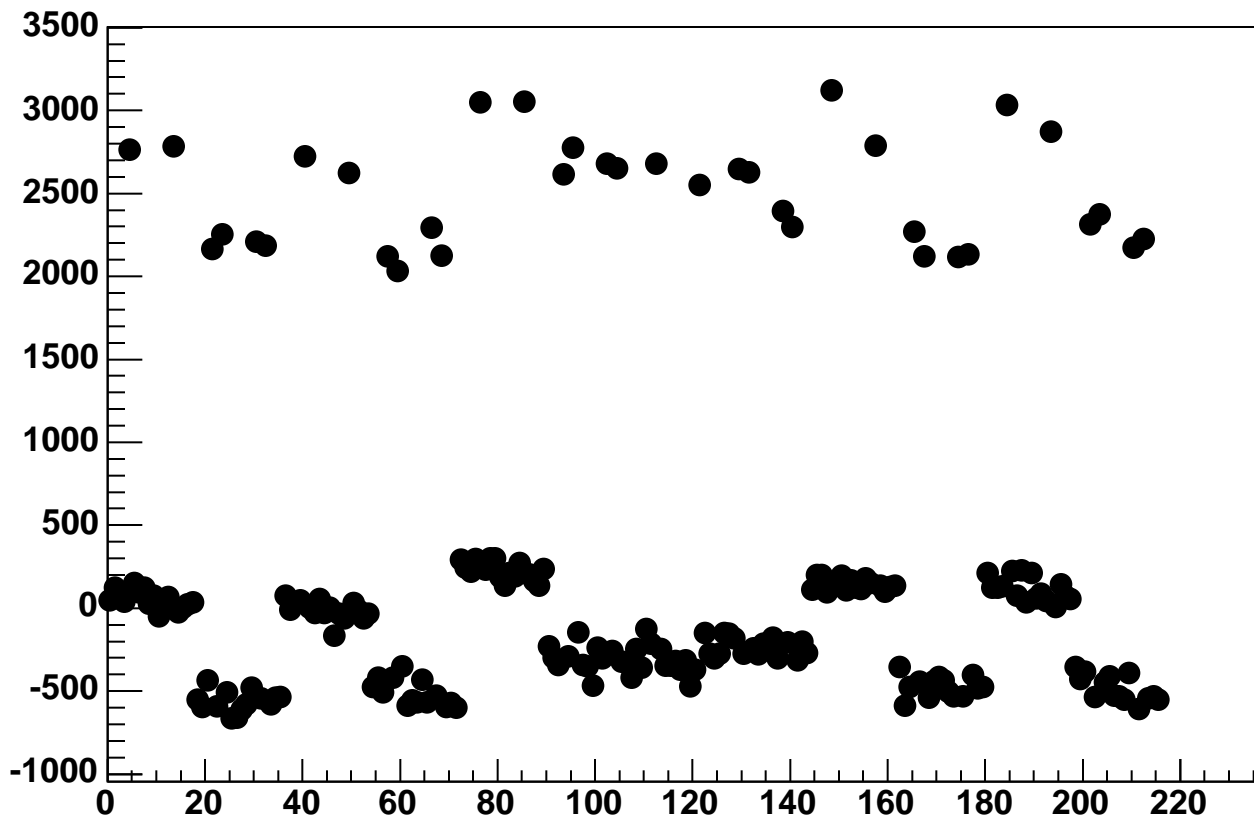
Enable 4, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



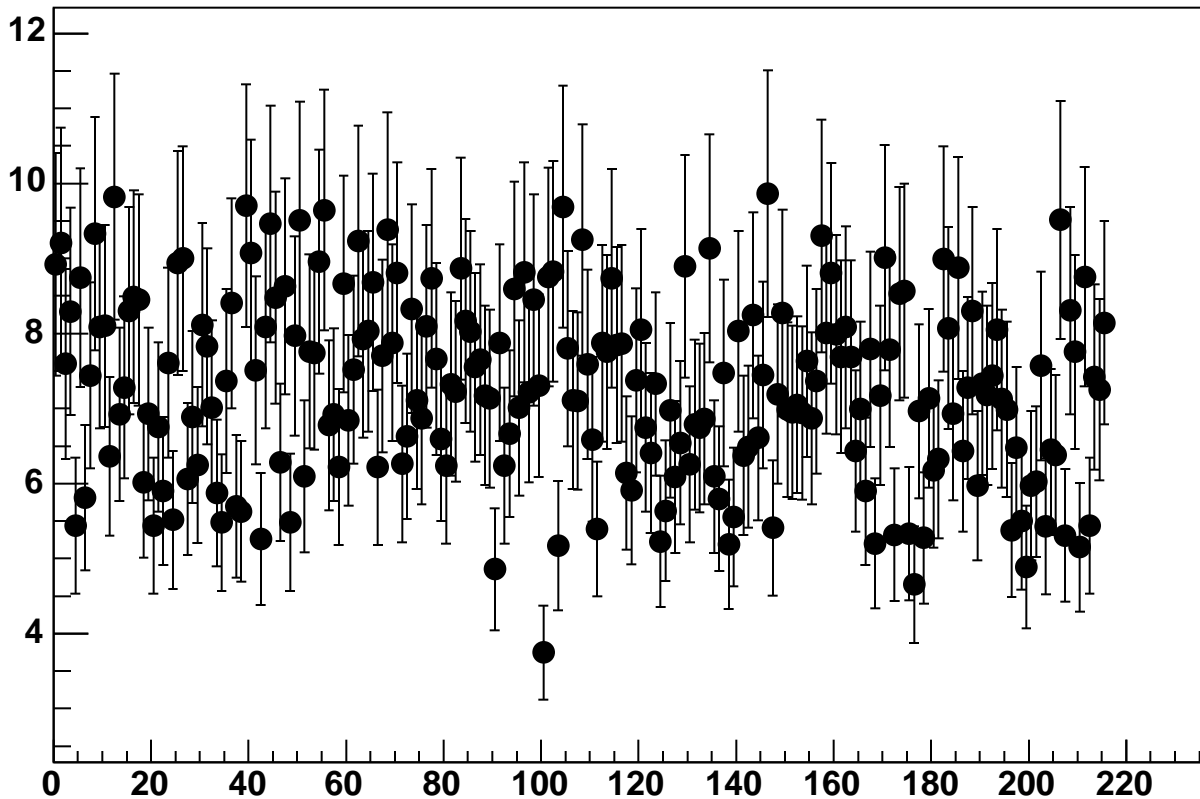
Enable 4, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



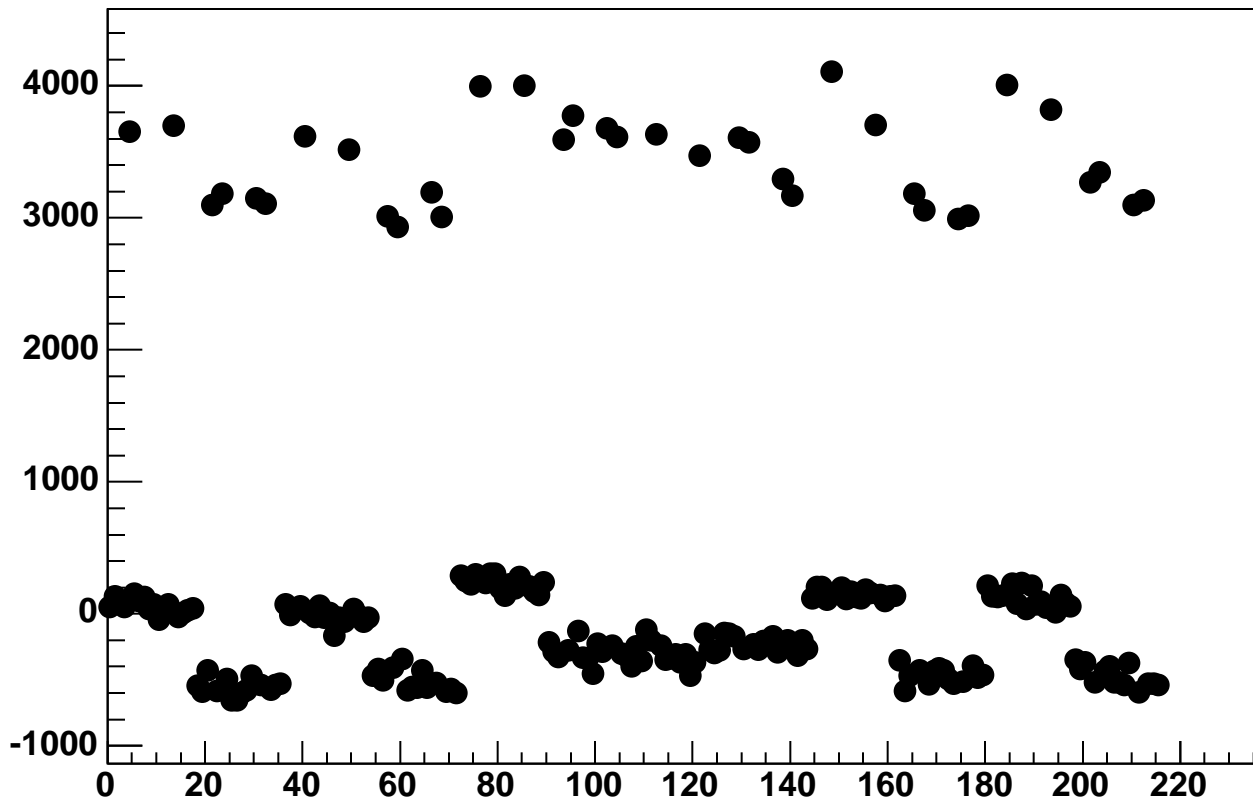
Enable 4, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



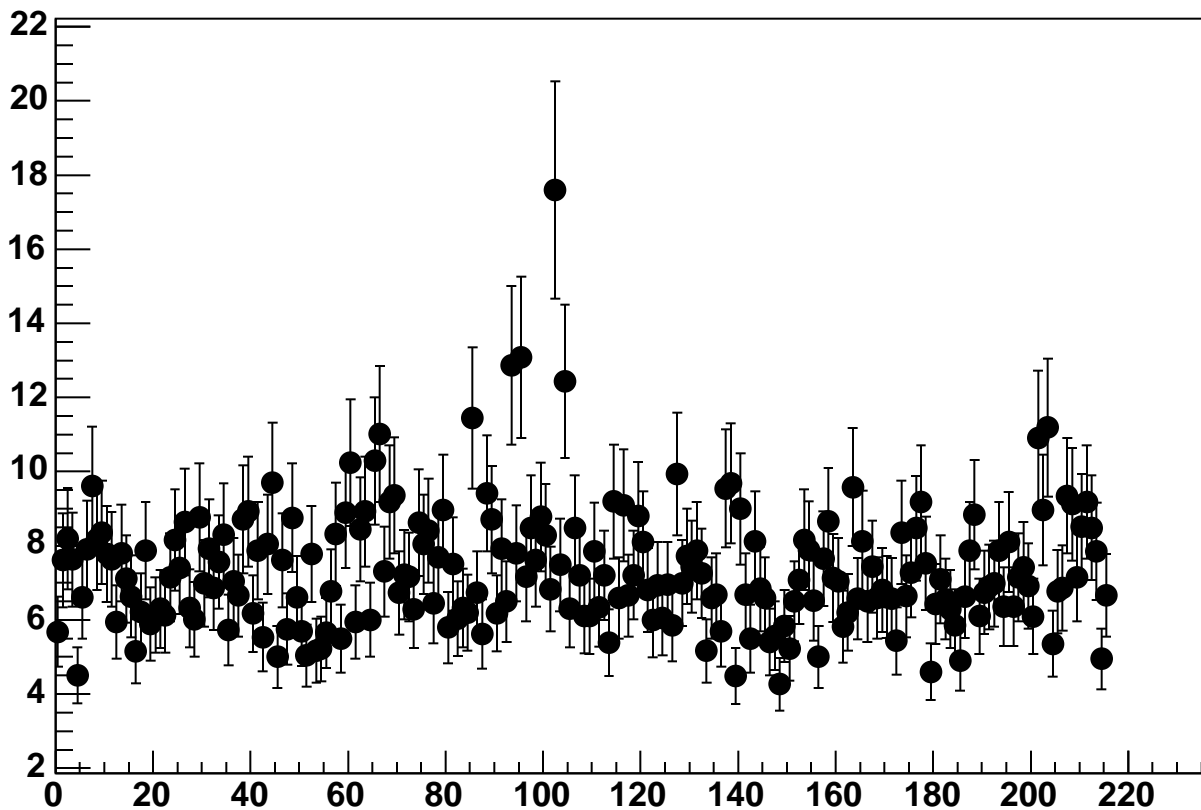
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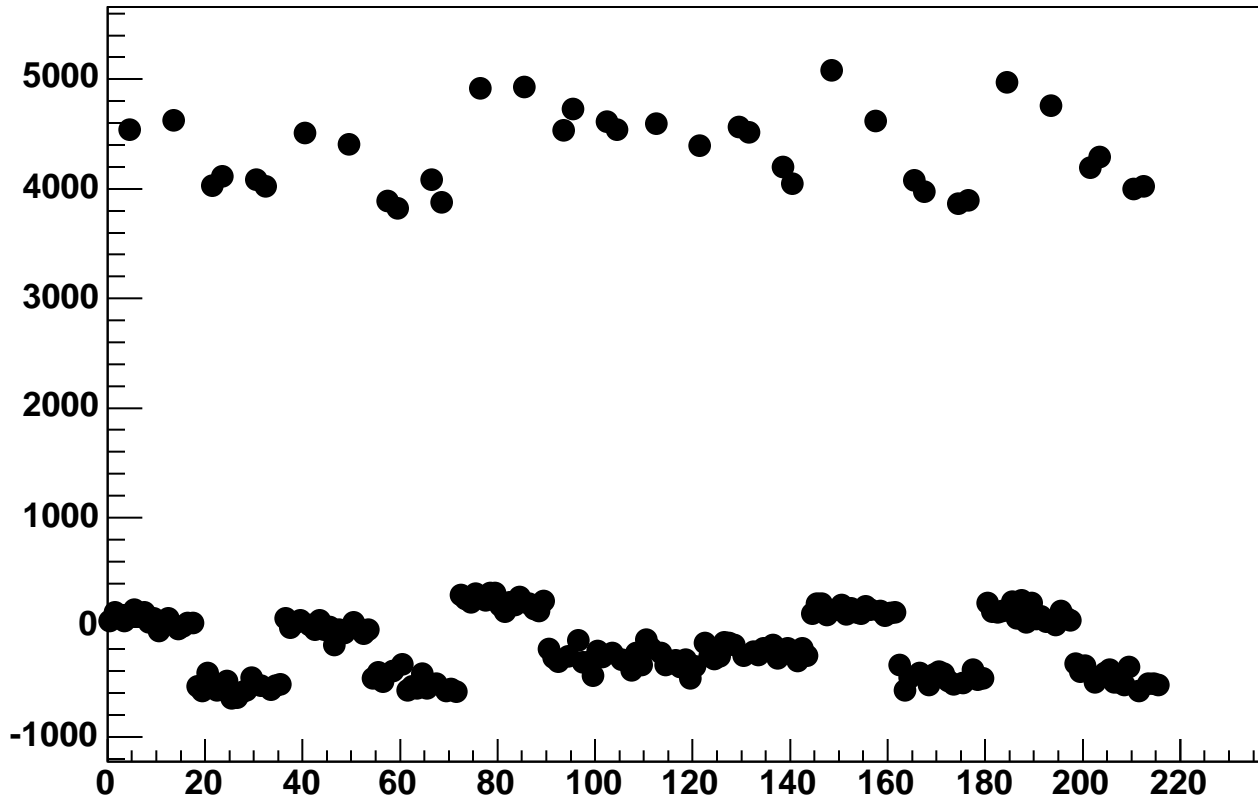
Enable 4, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



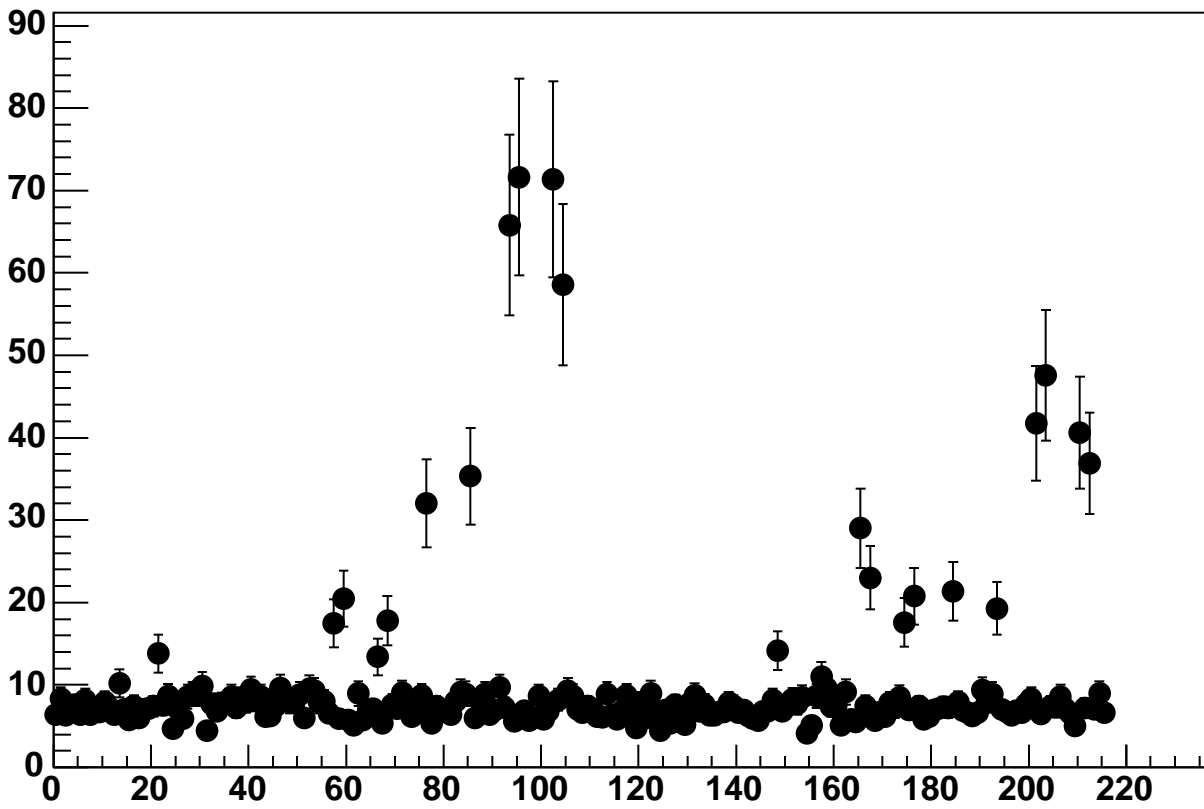
Enable 4, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



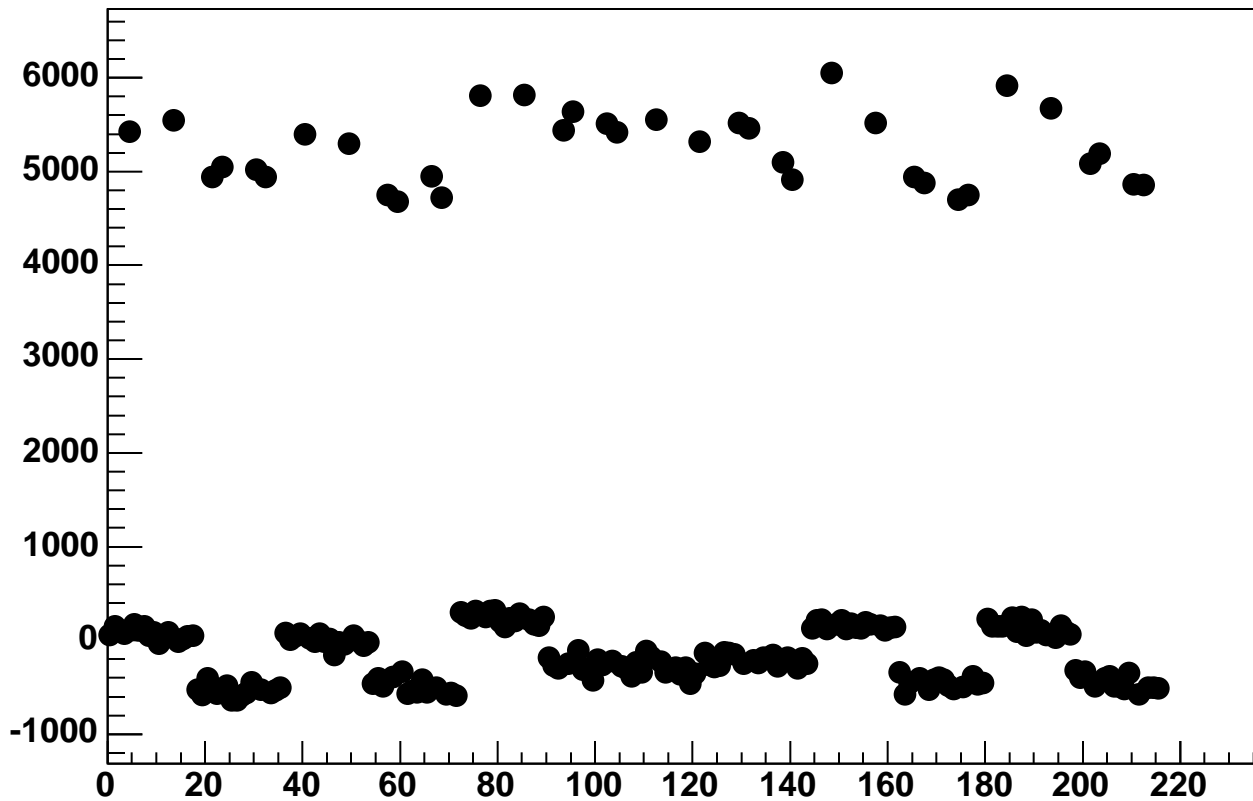
Enable 4, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



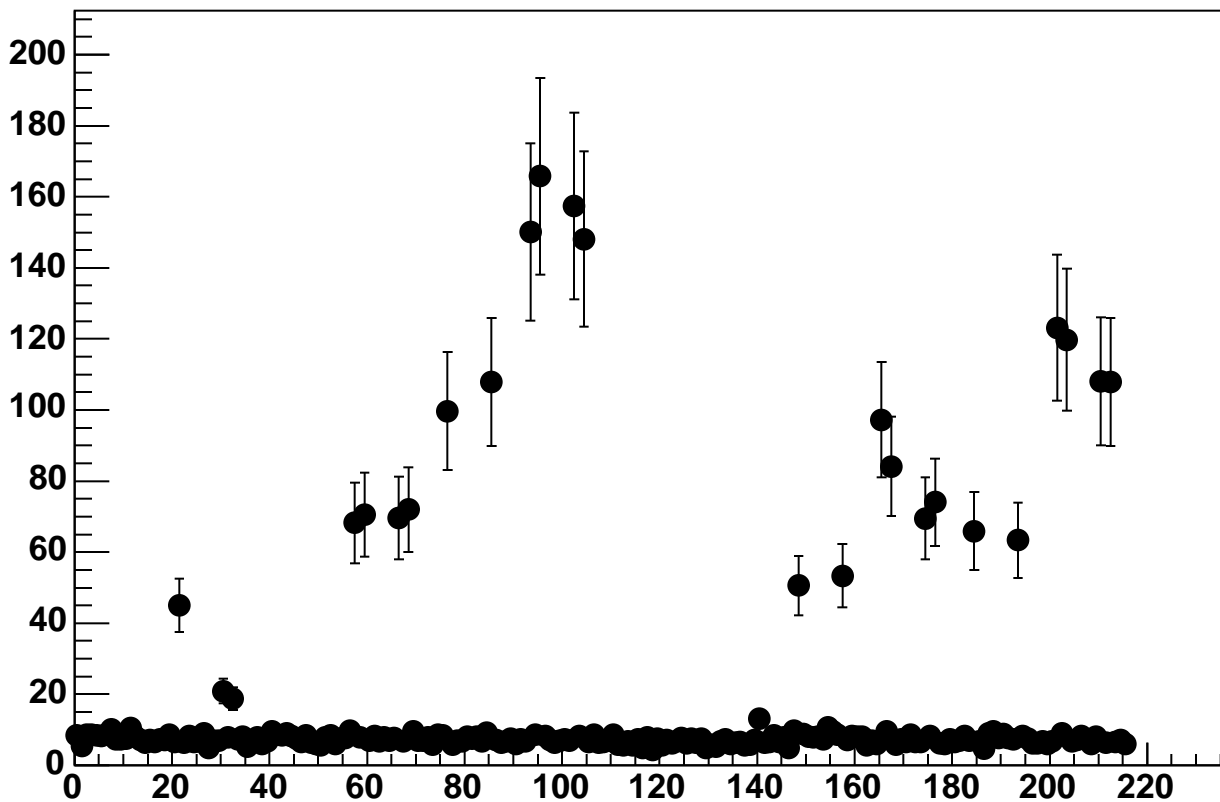
Enable 4, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



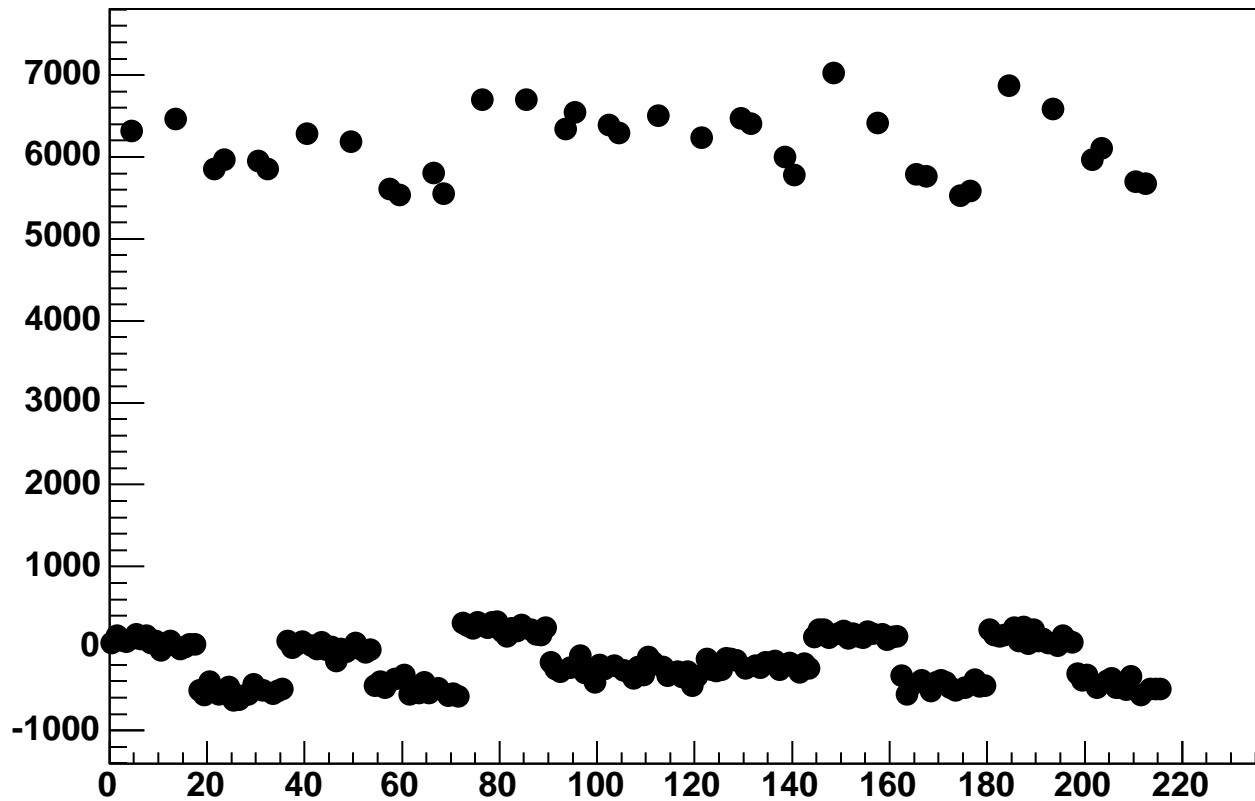
Enable 4, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



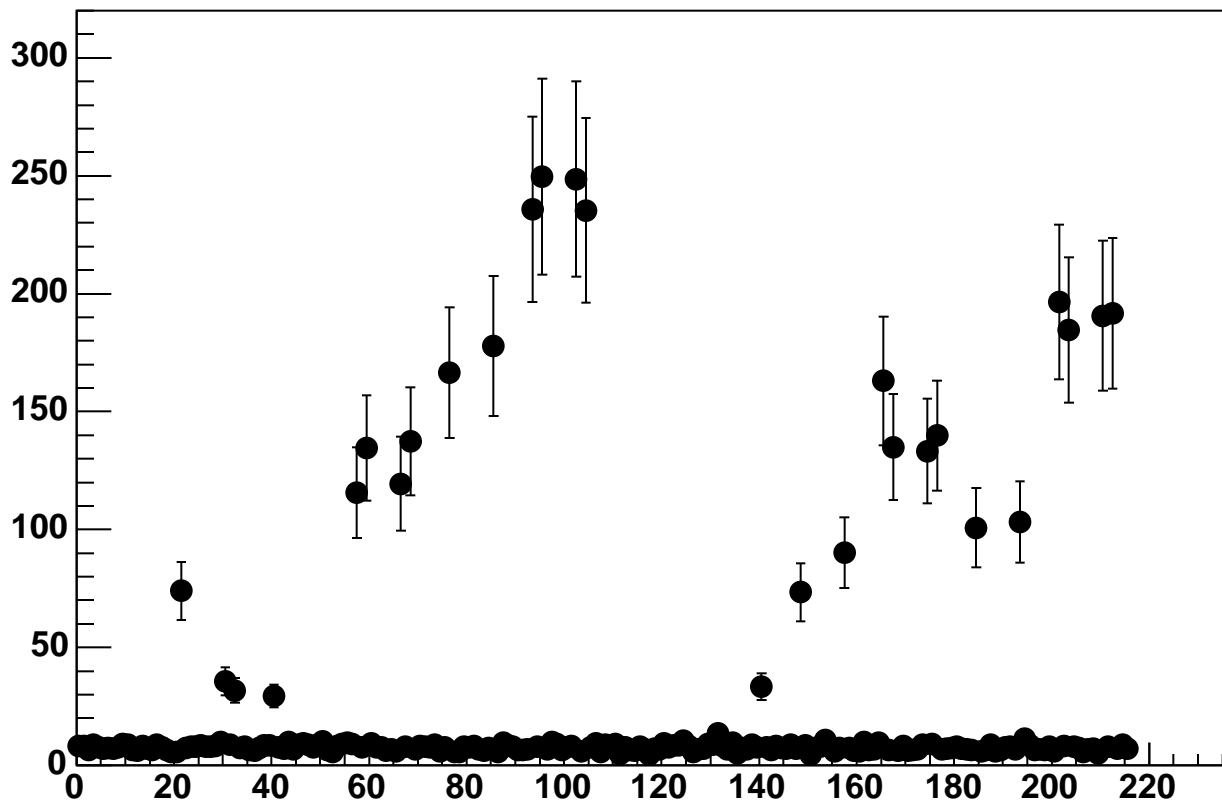
Enable 4, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



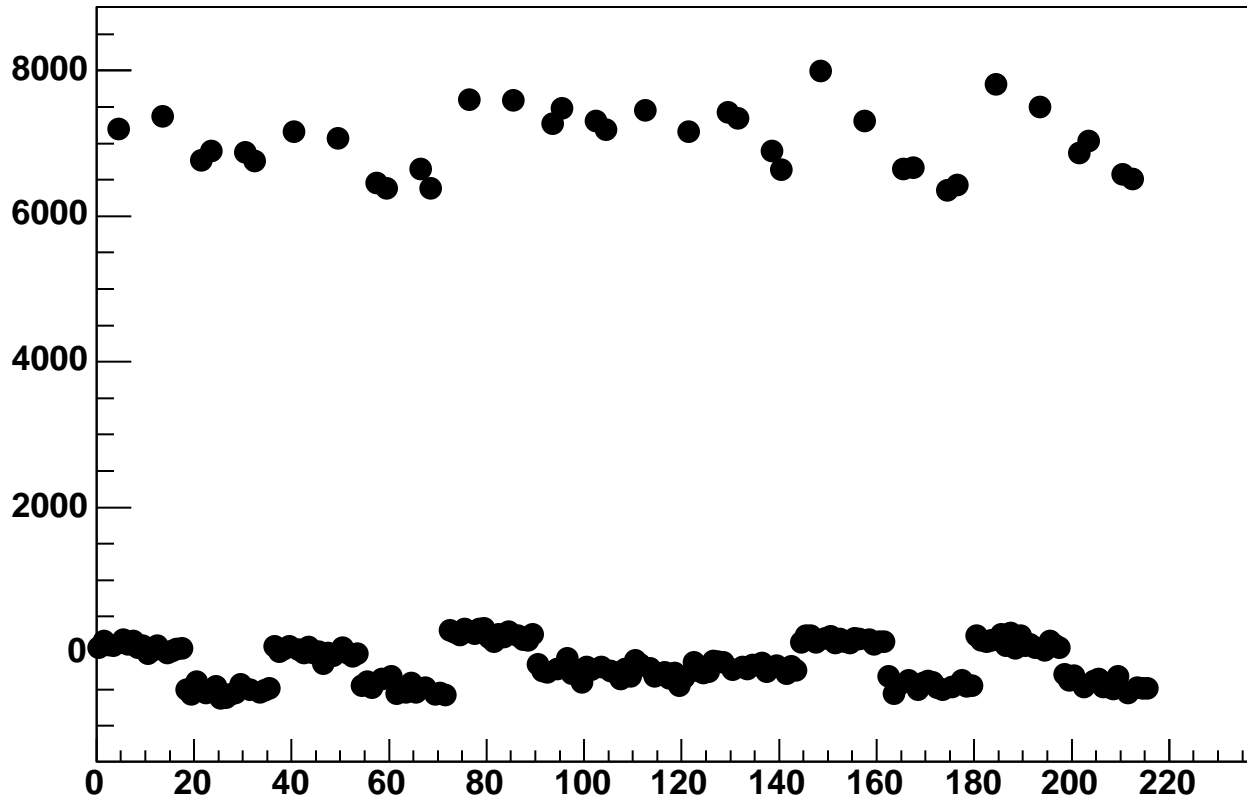
Enable 4, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



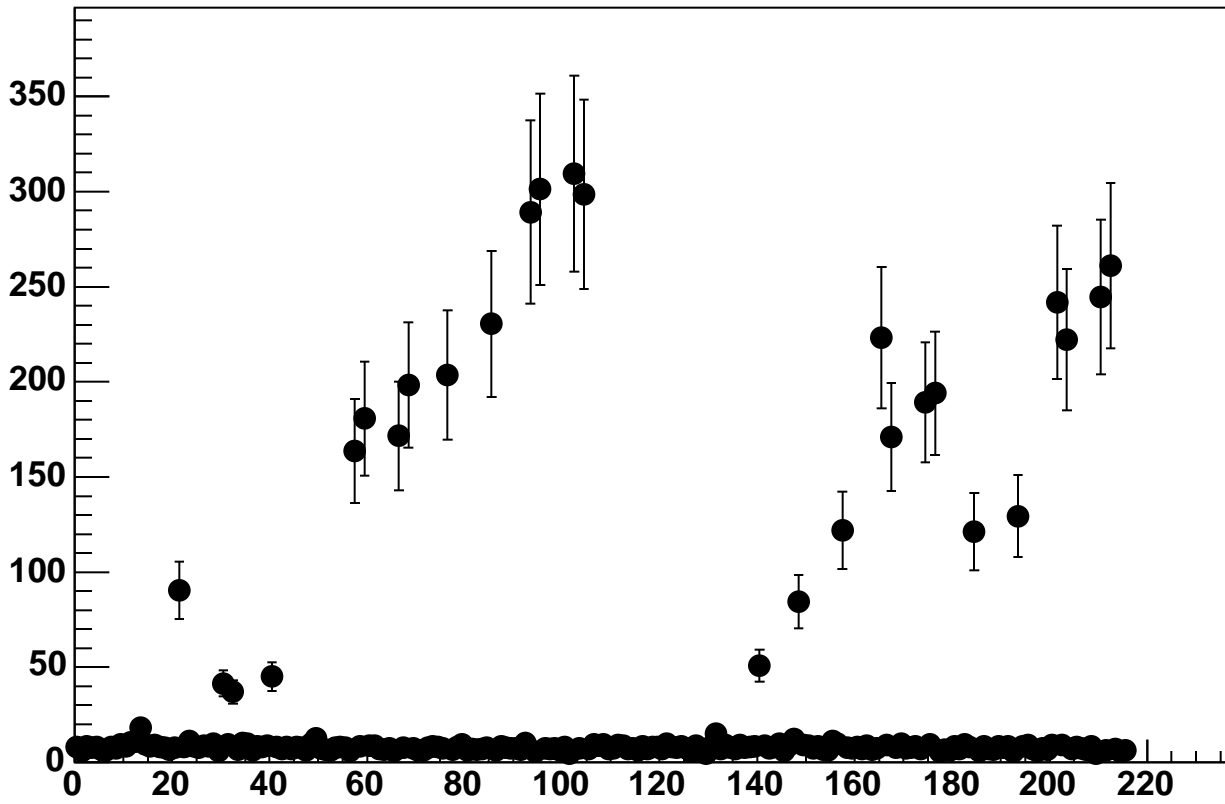
Enable 4, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



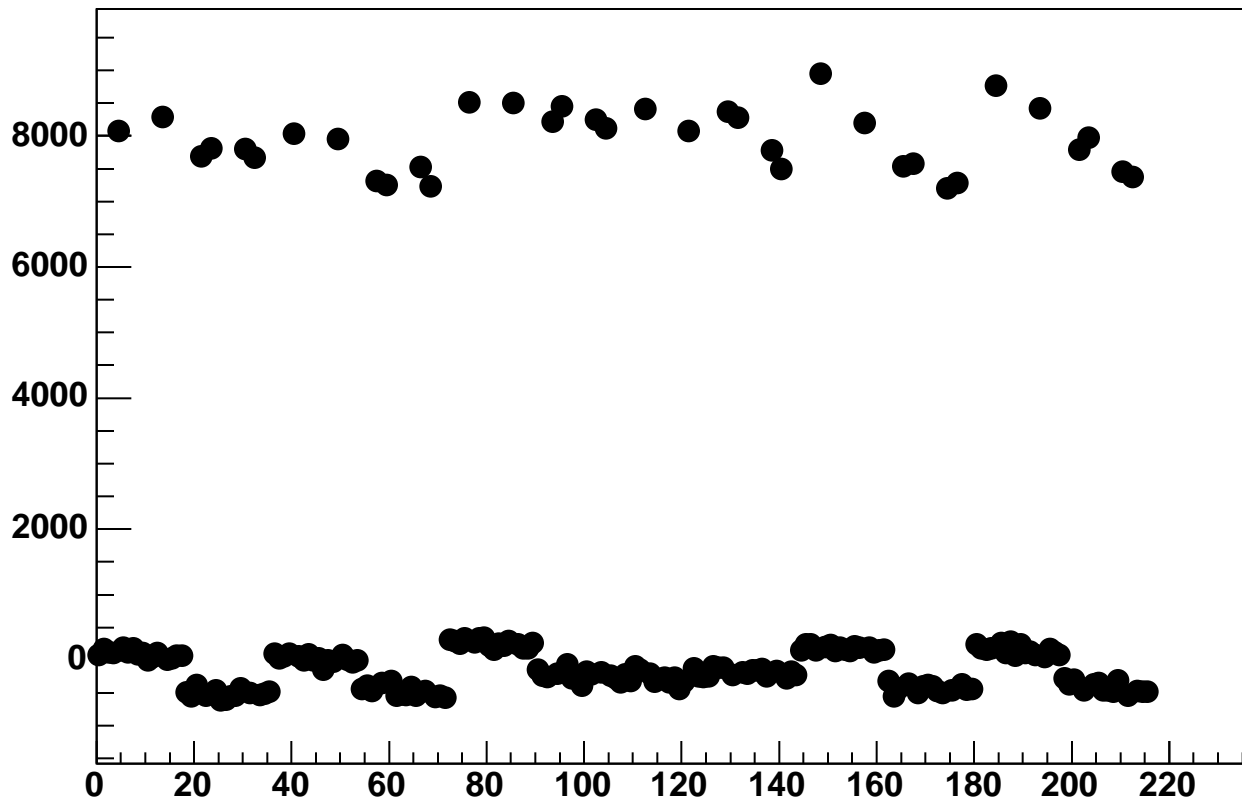
Enable 4, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



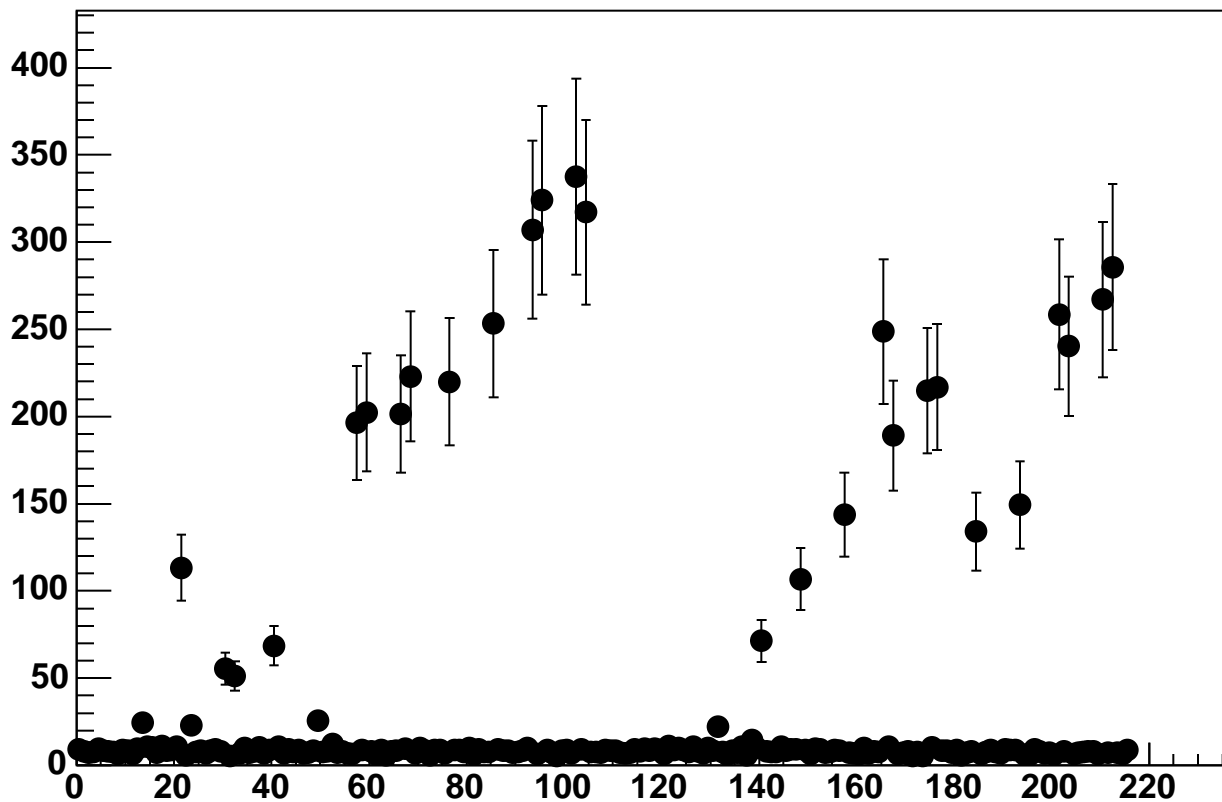
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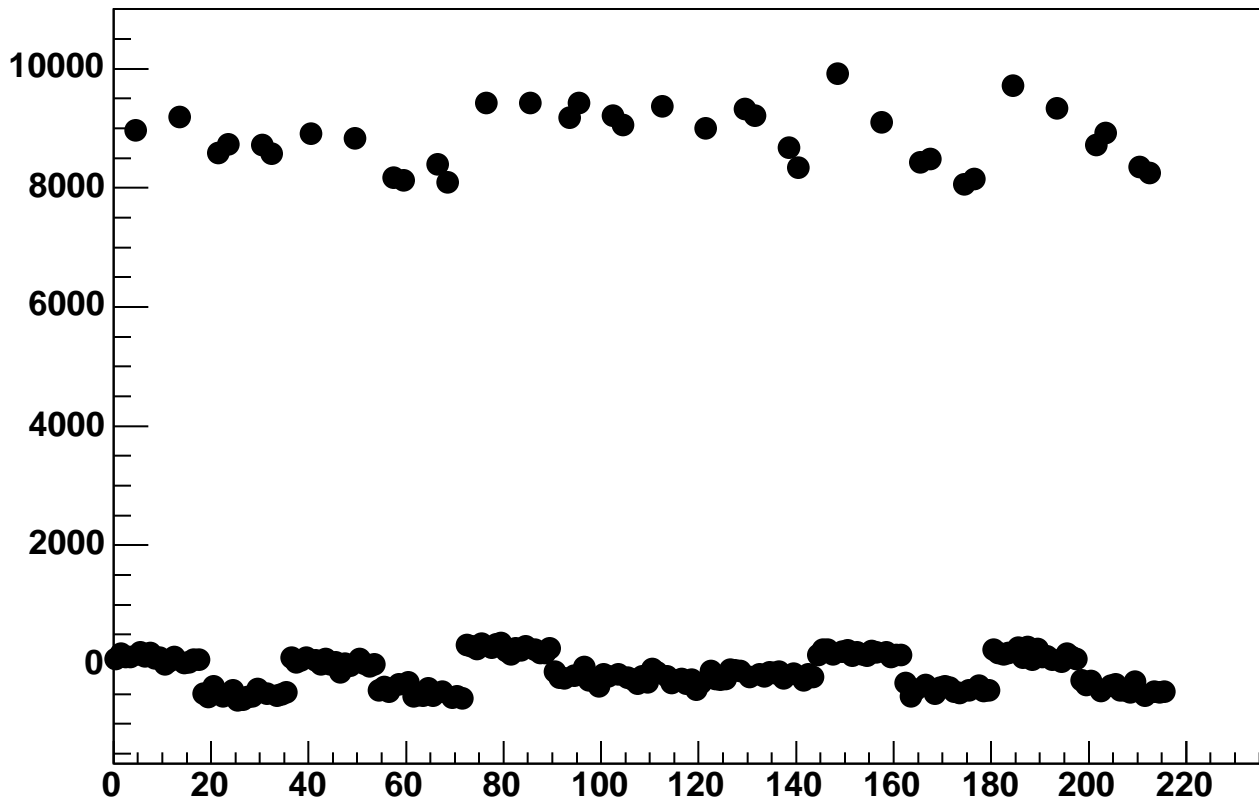
Enable 4, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



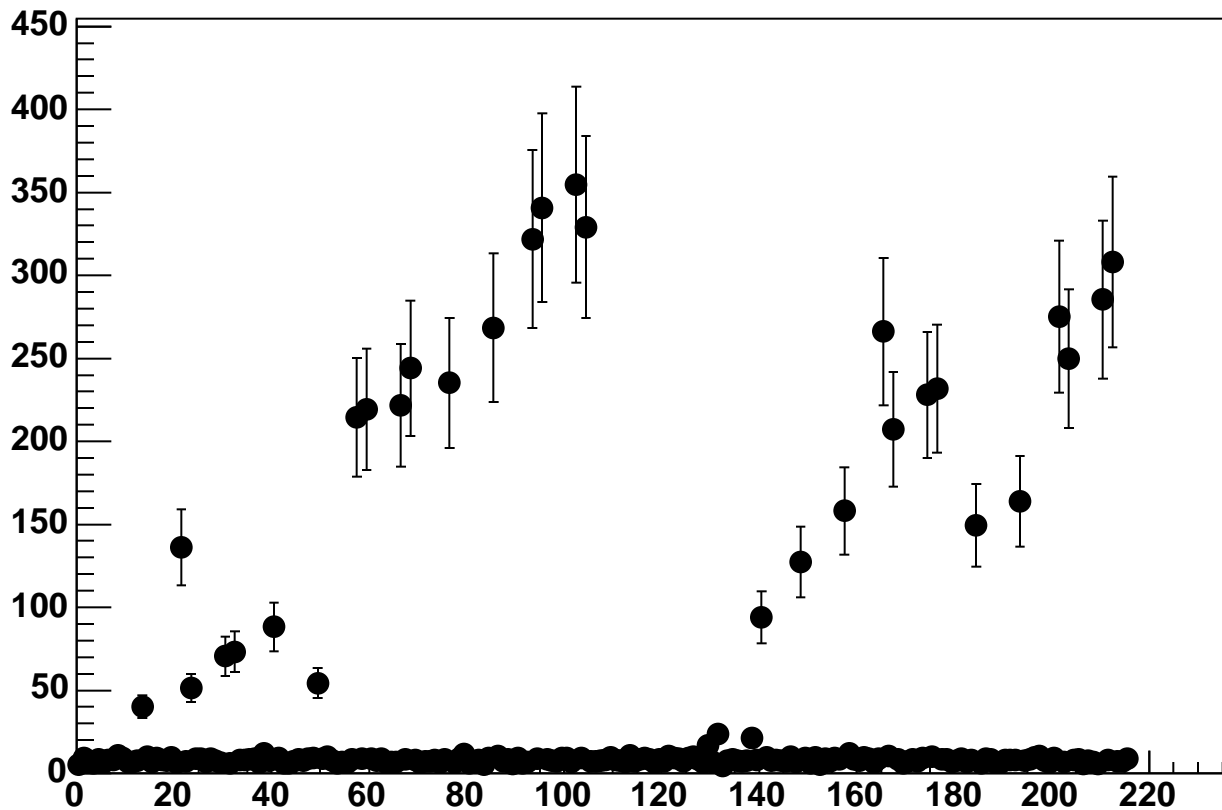
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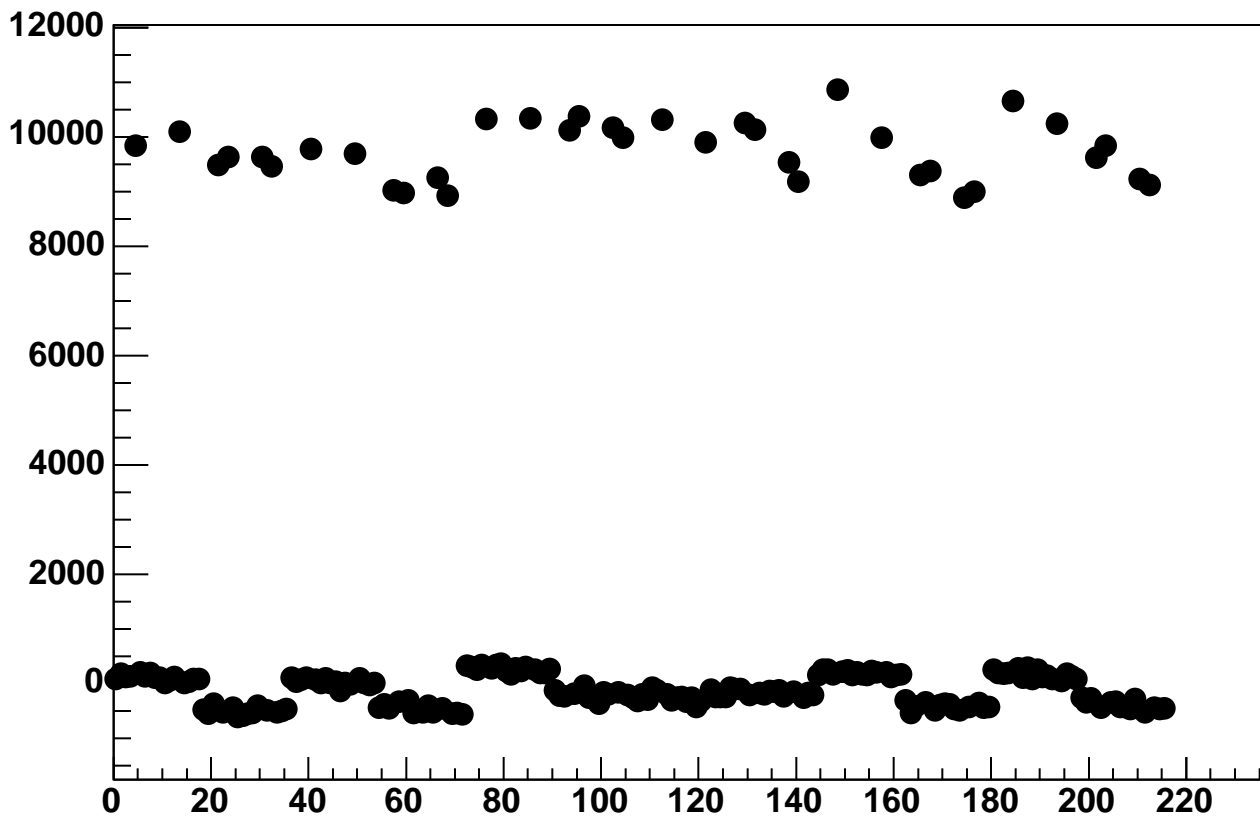
Enable 4, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



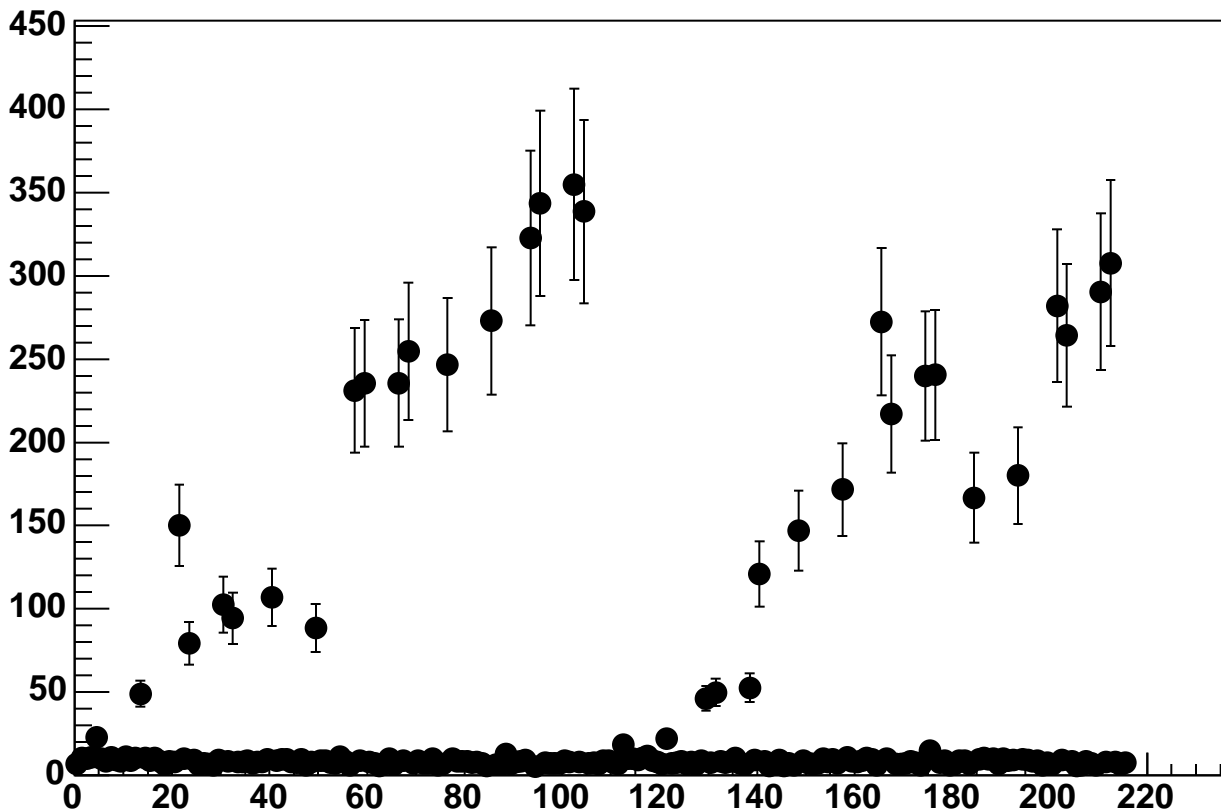
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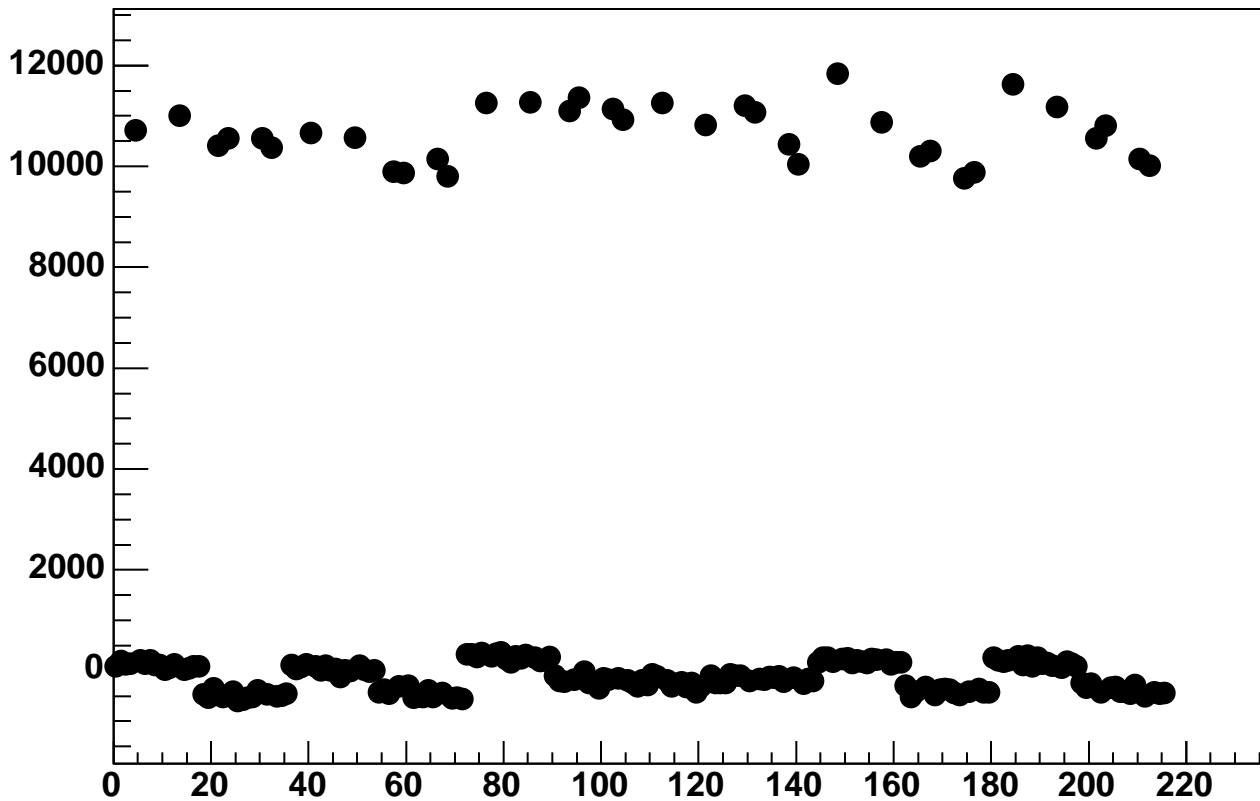
Enable 4, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



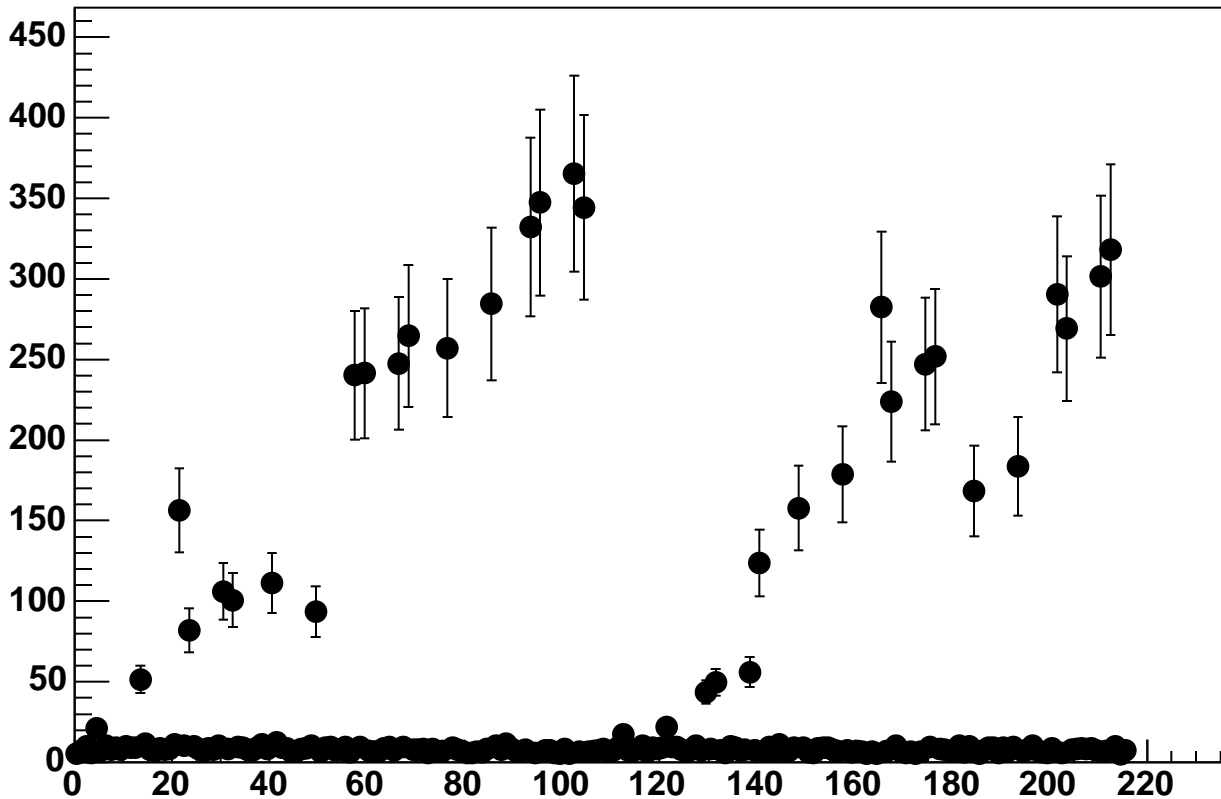
Enable 4, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



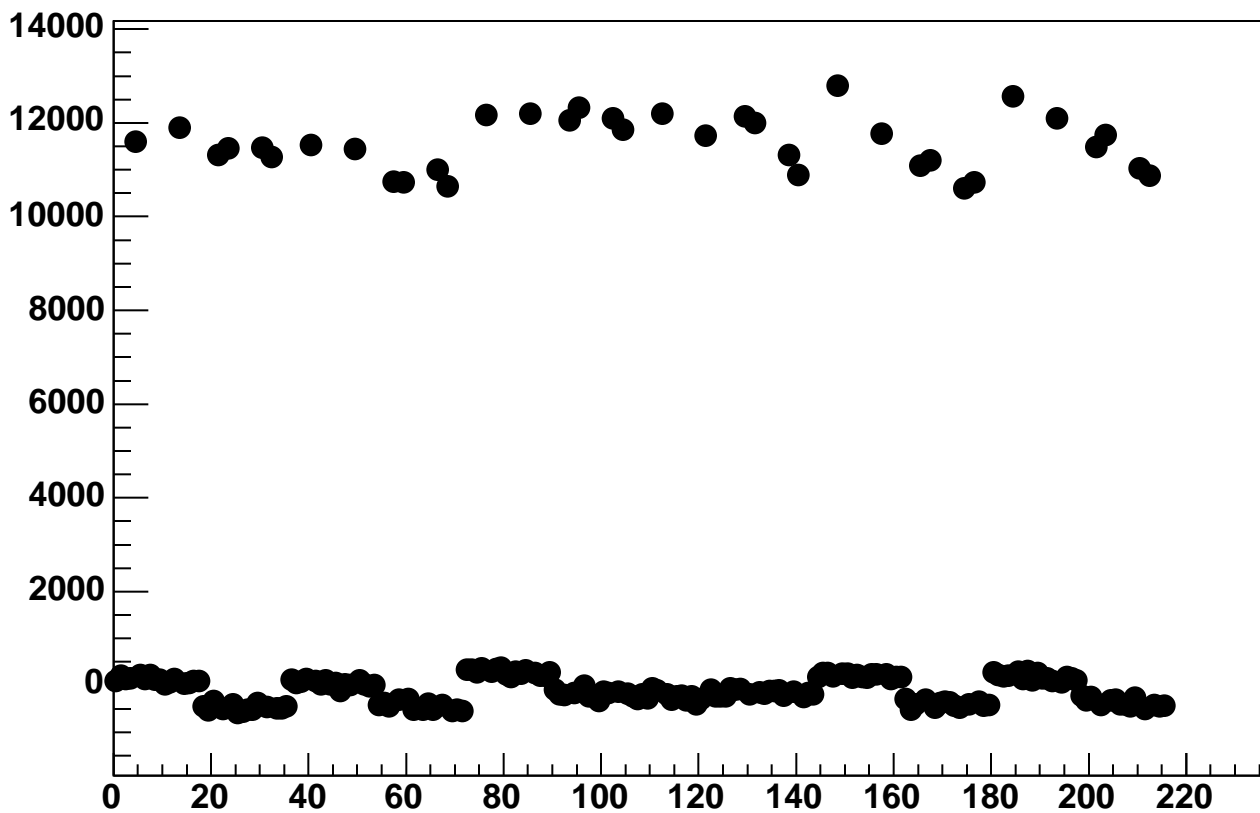
Enable 4, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



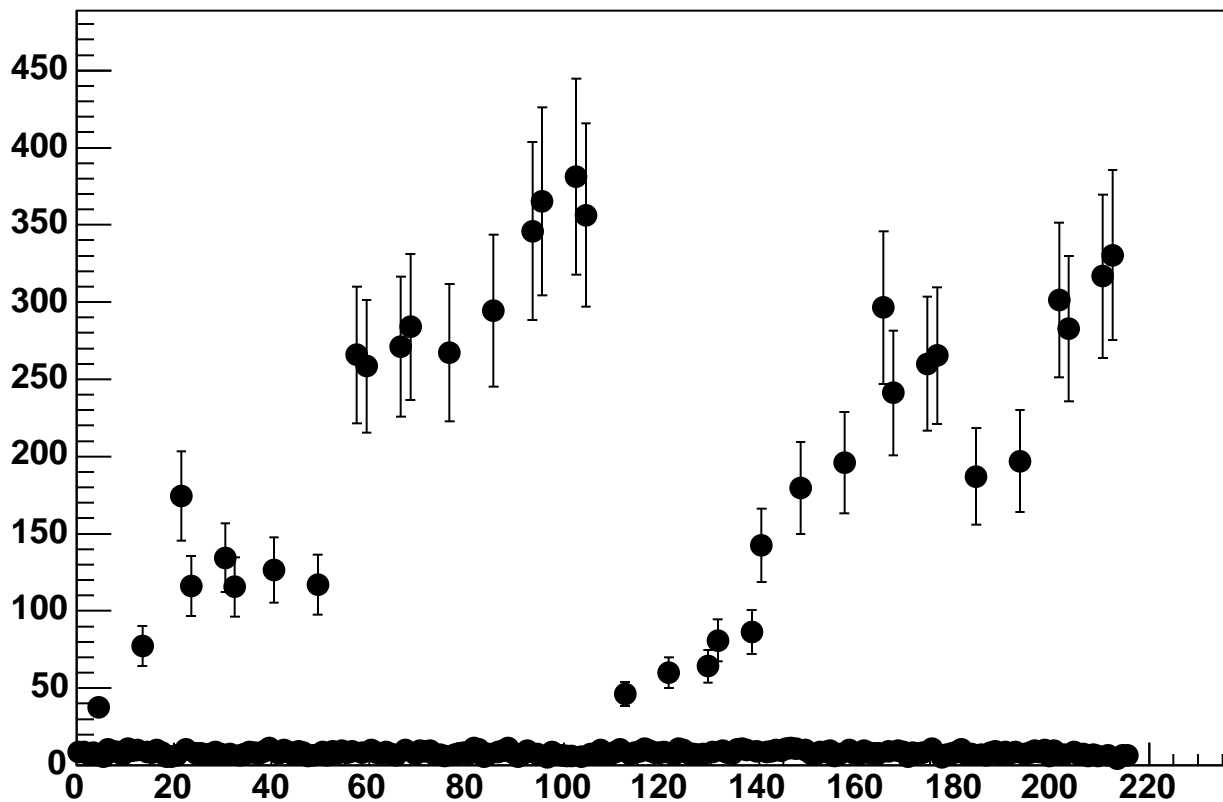
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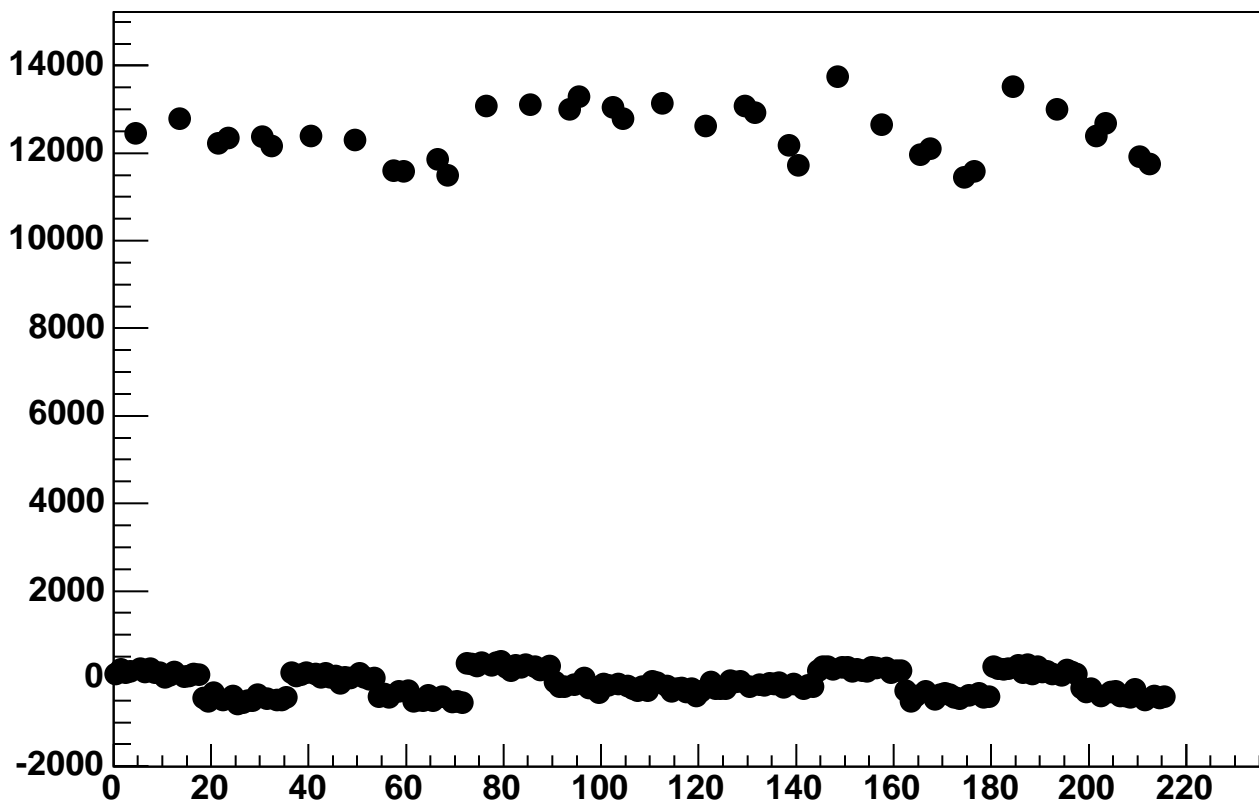
Enable 4, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



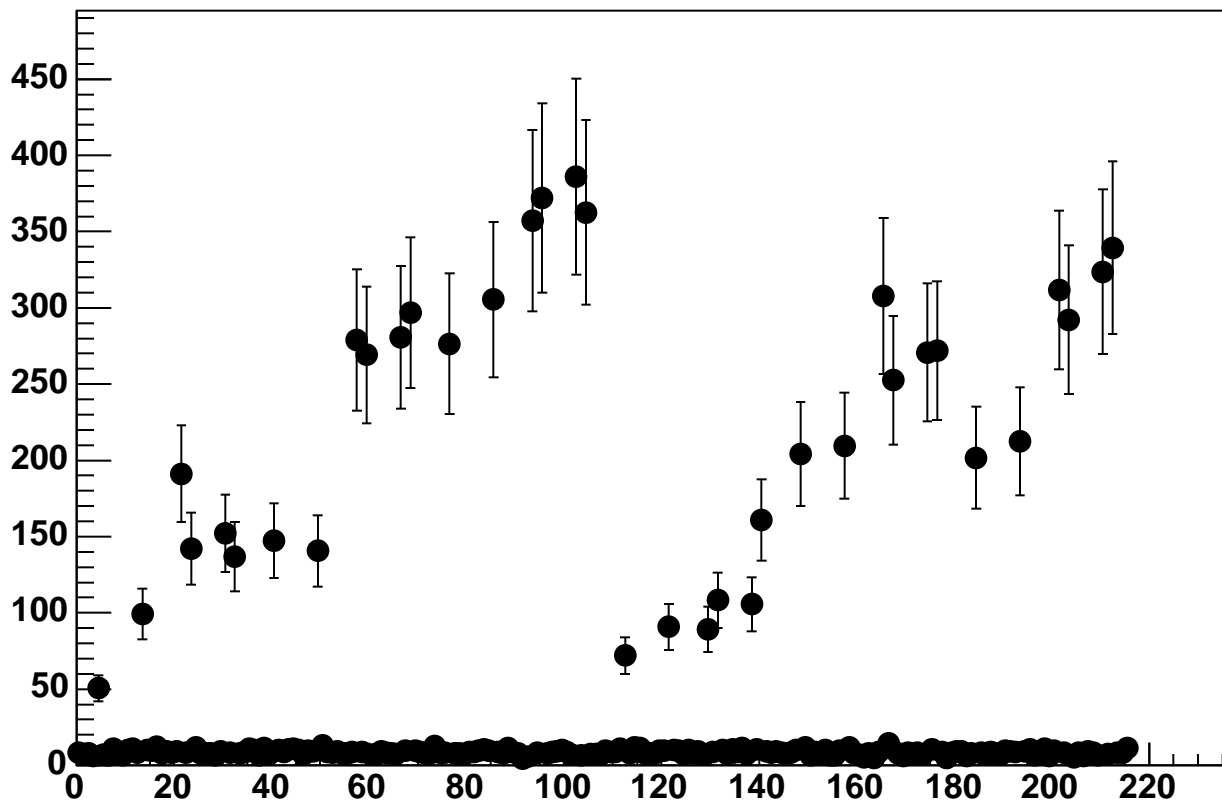
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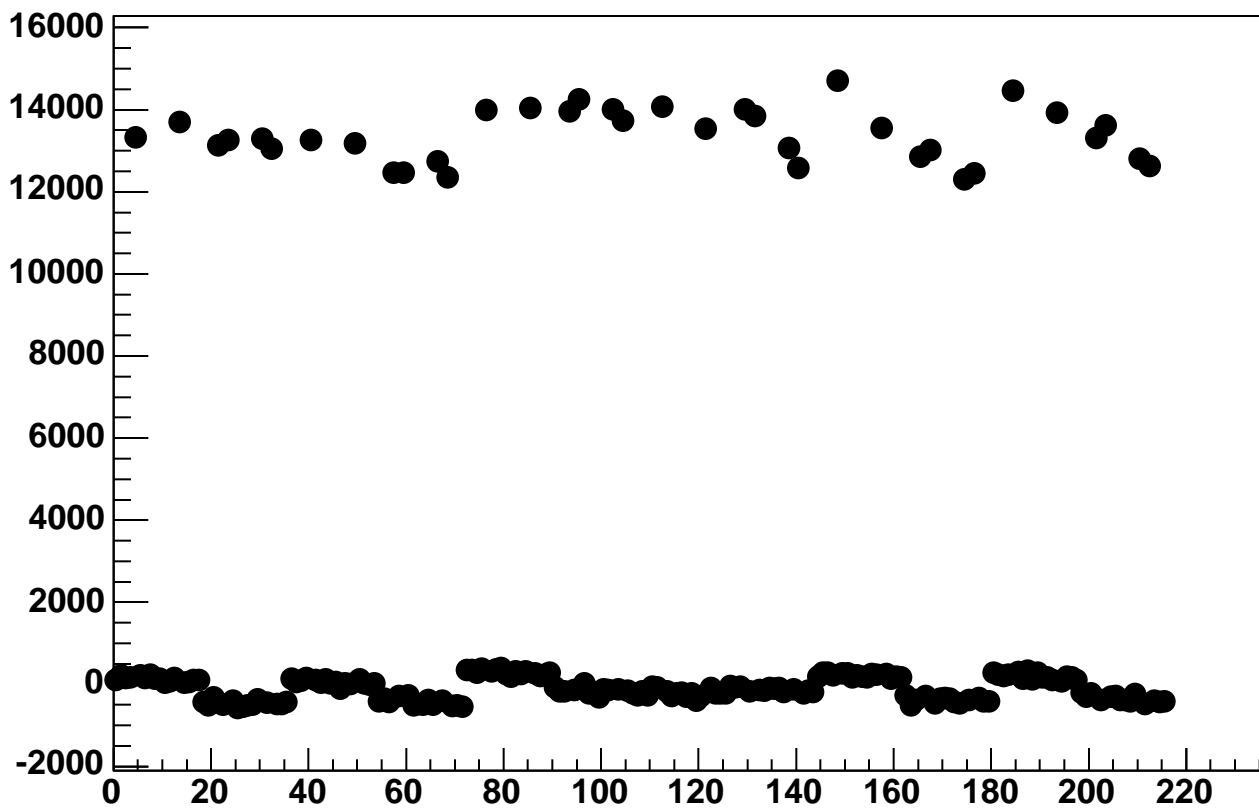
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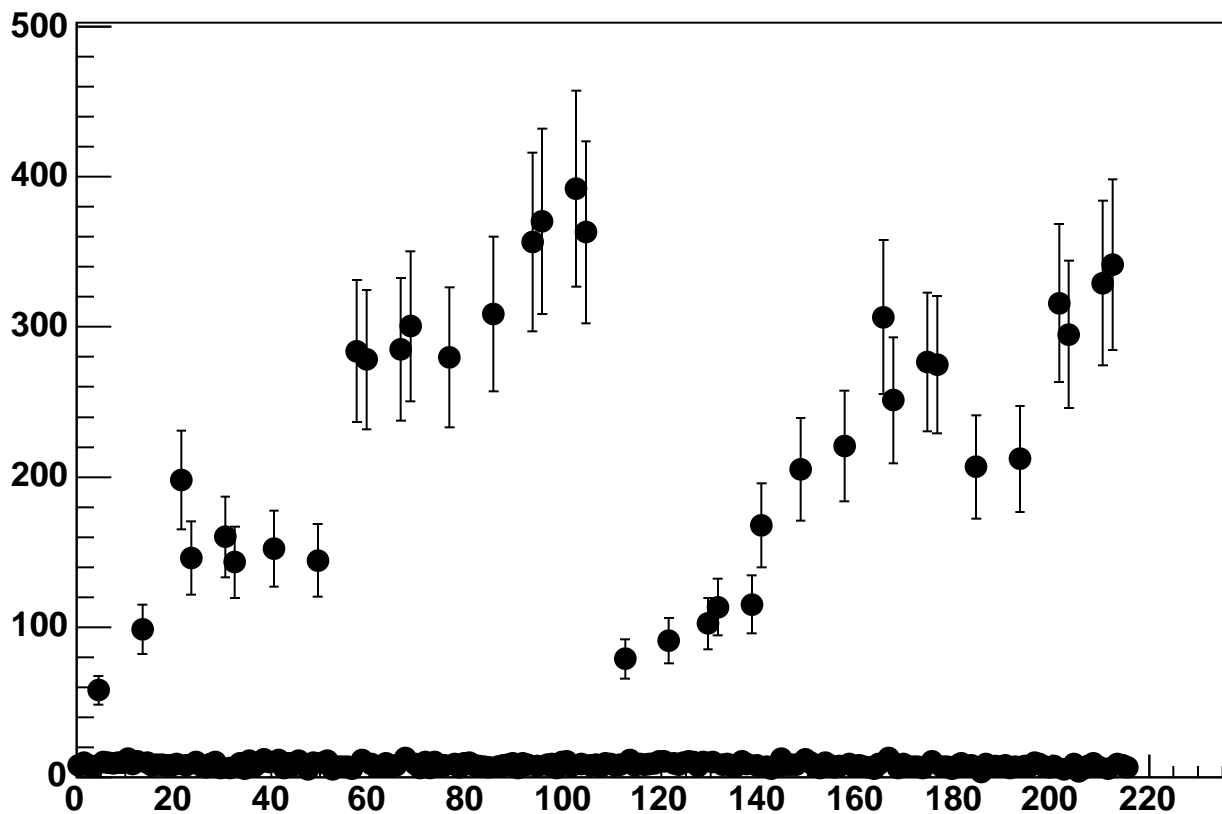
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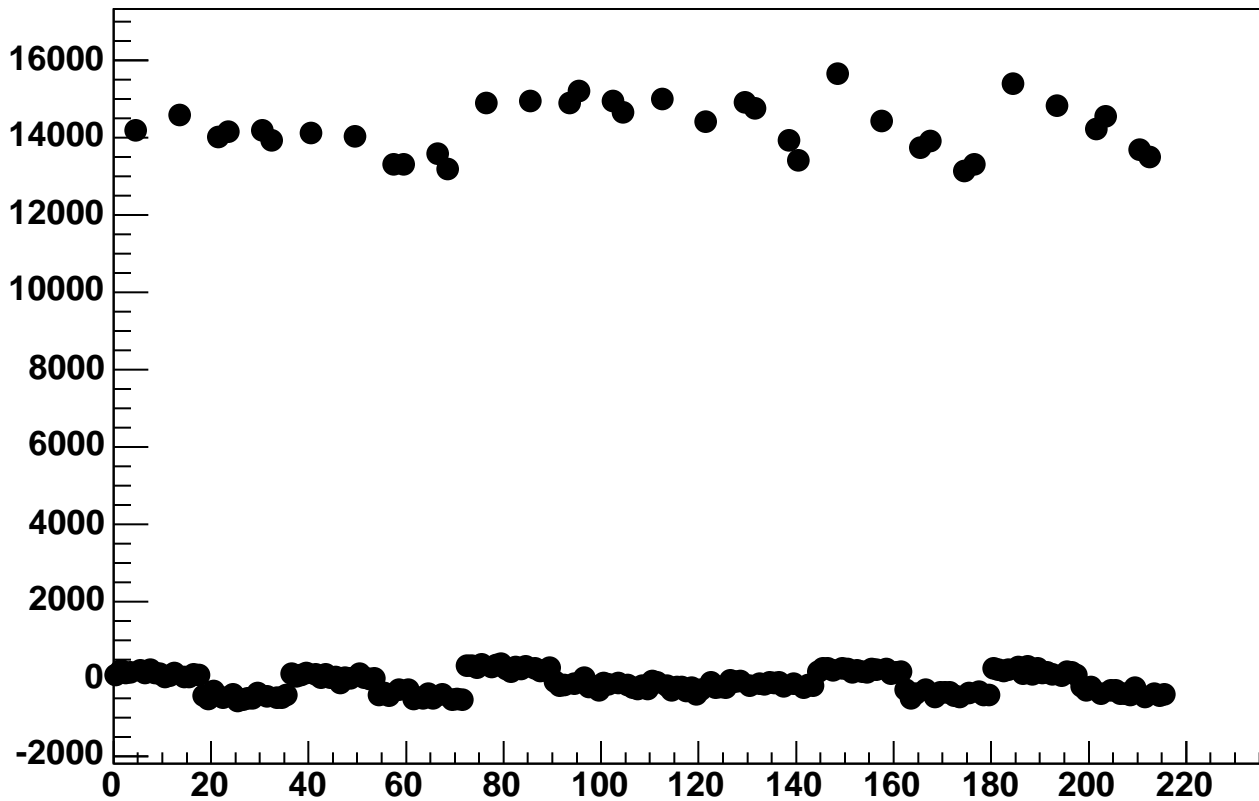
Enable 4, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



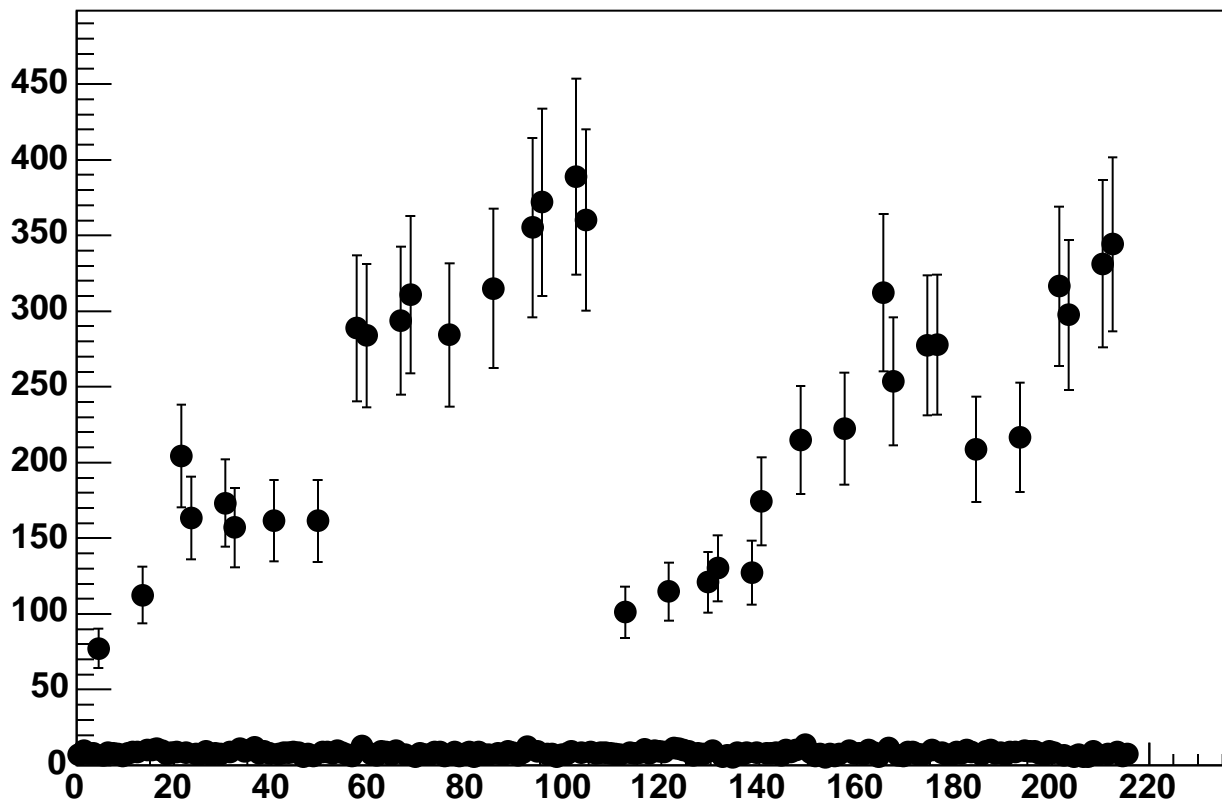
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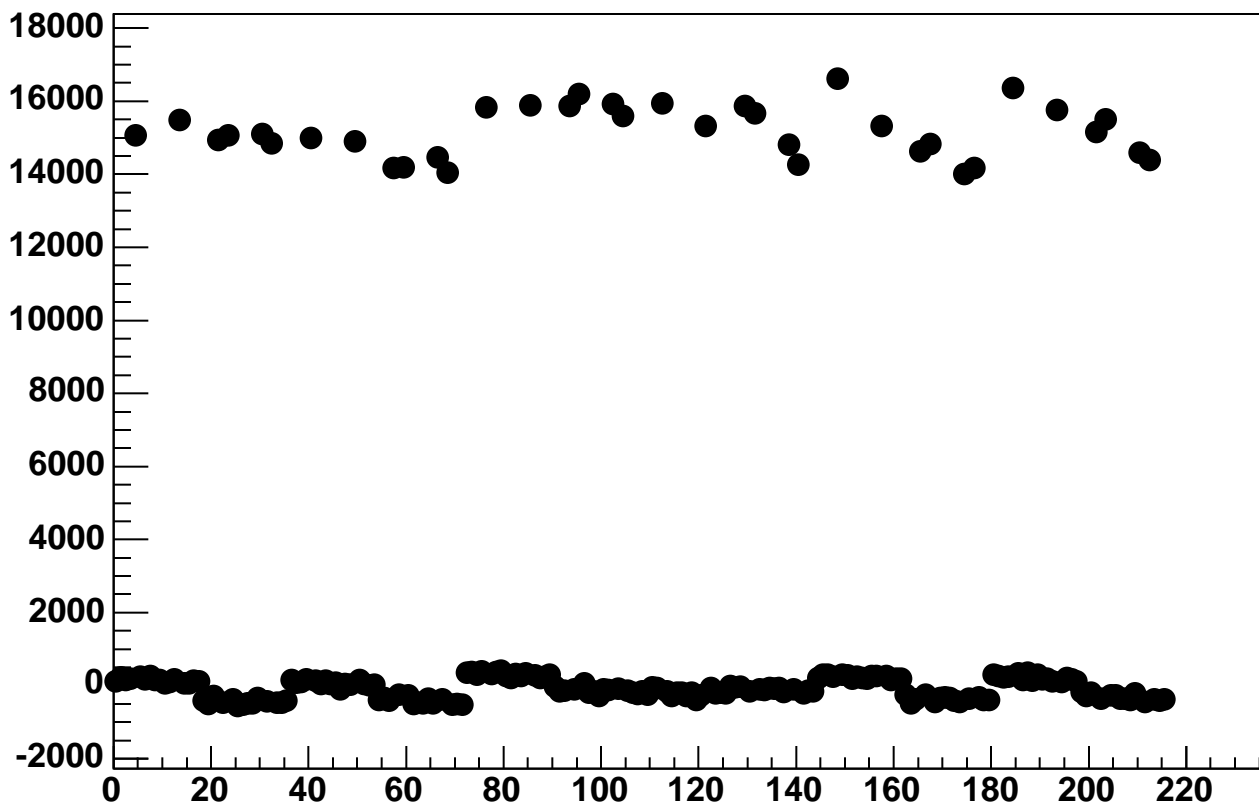
Enable 4, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



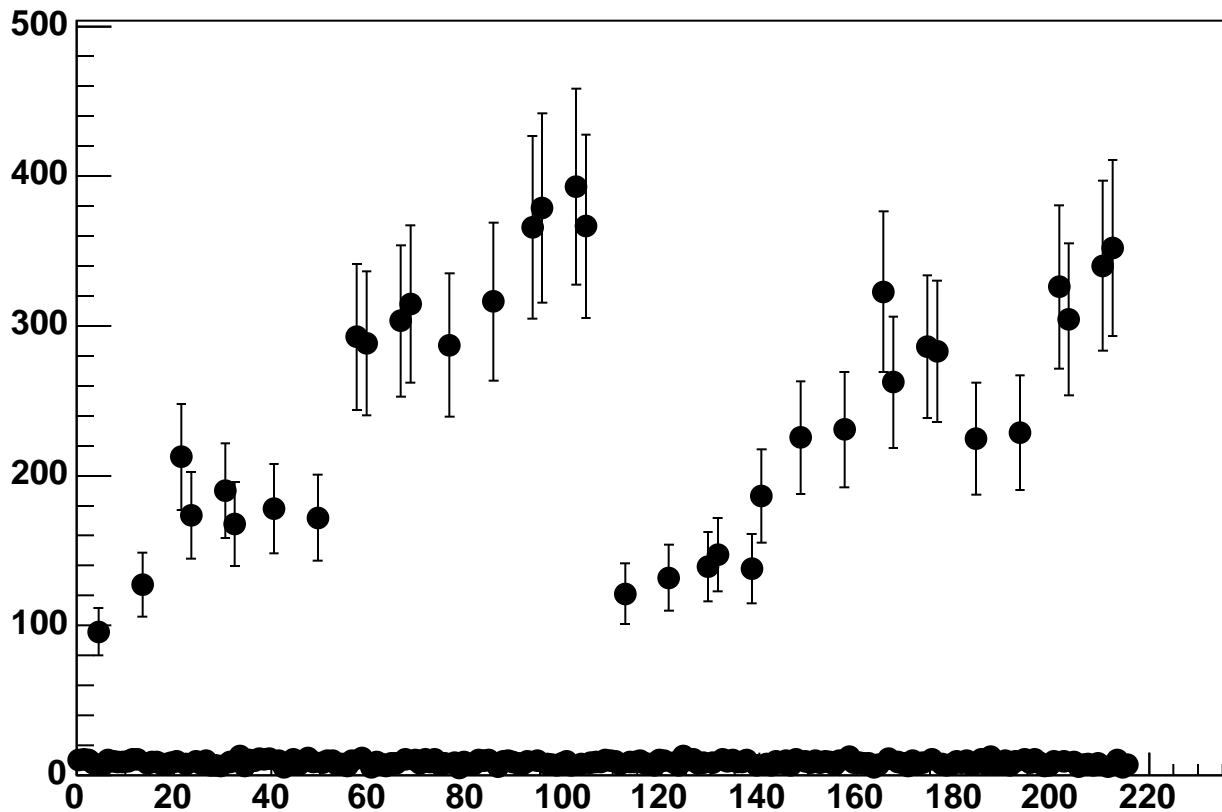
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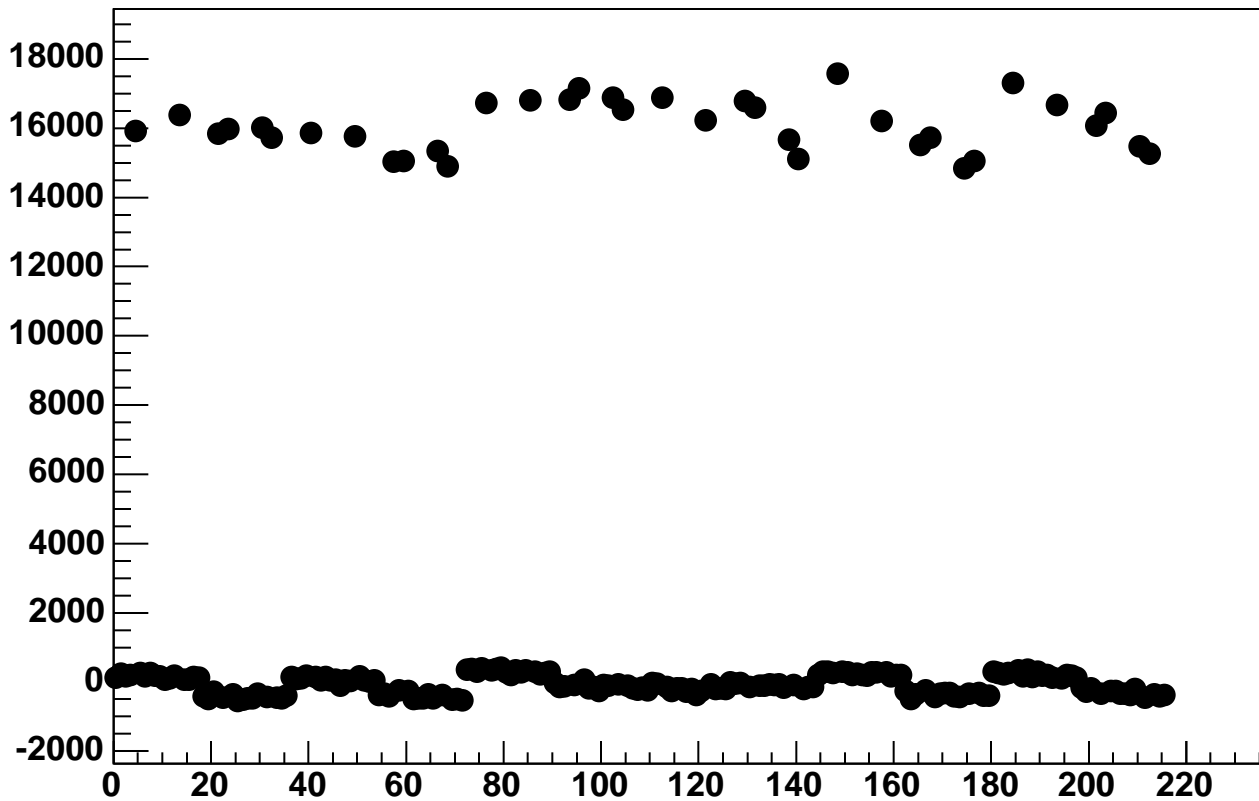
Enable 4, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



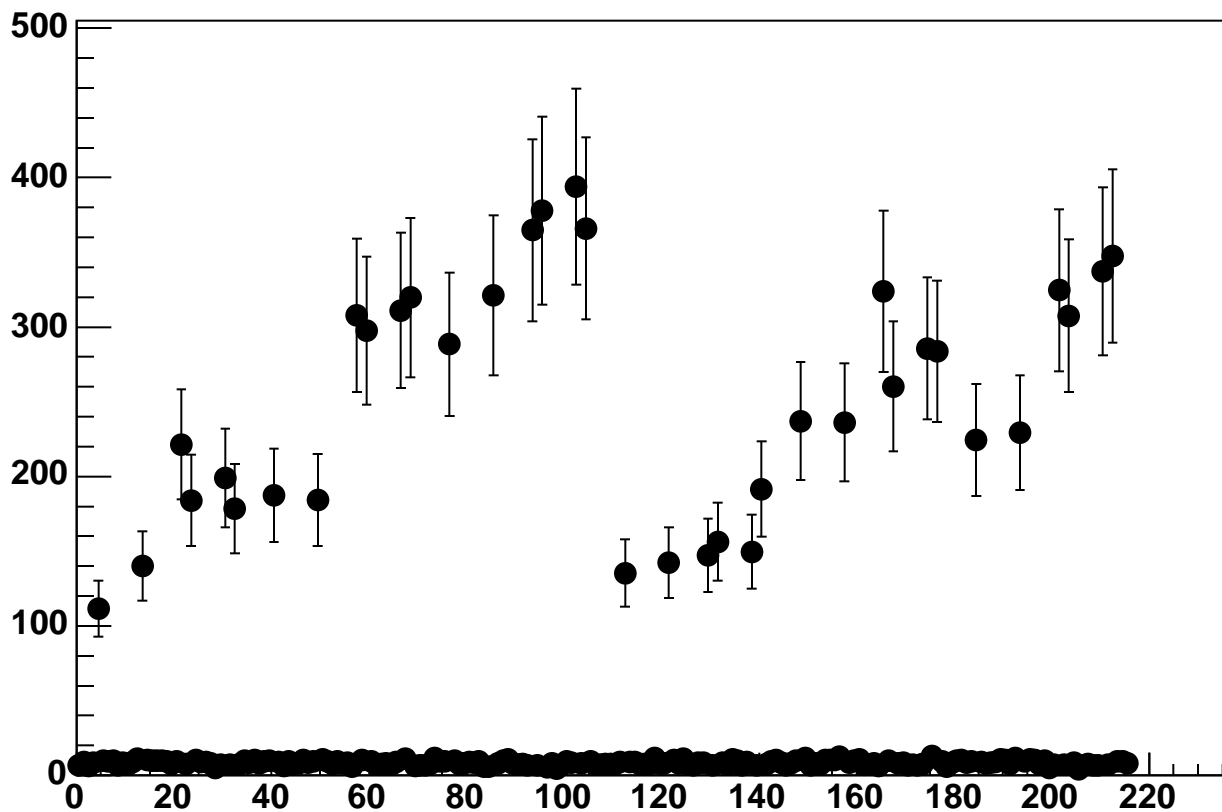
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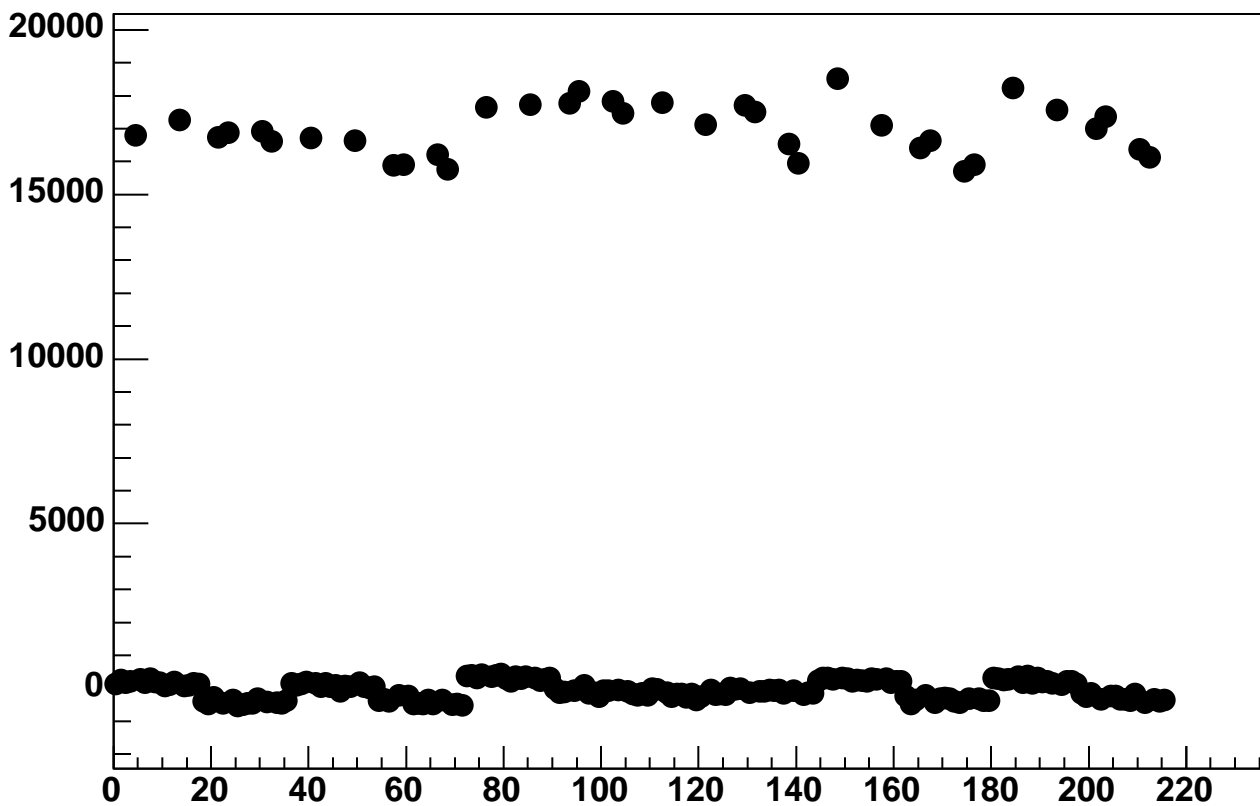
Enable 4, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



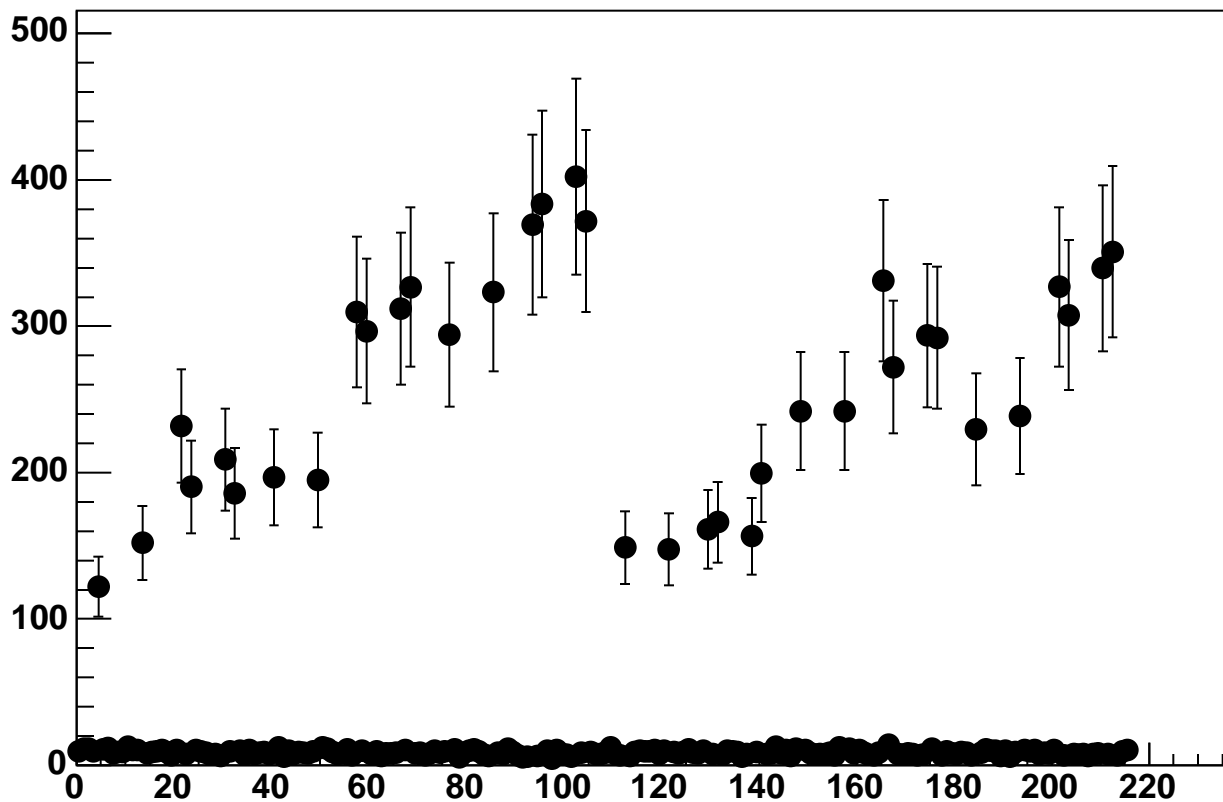
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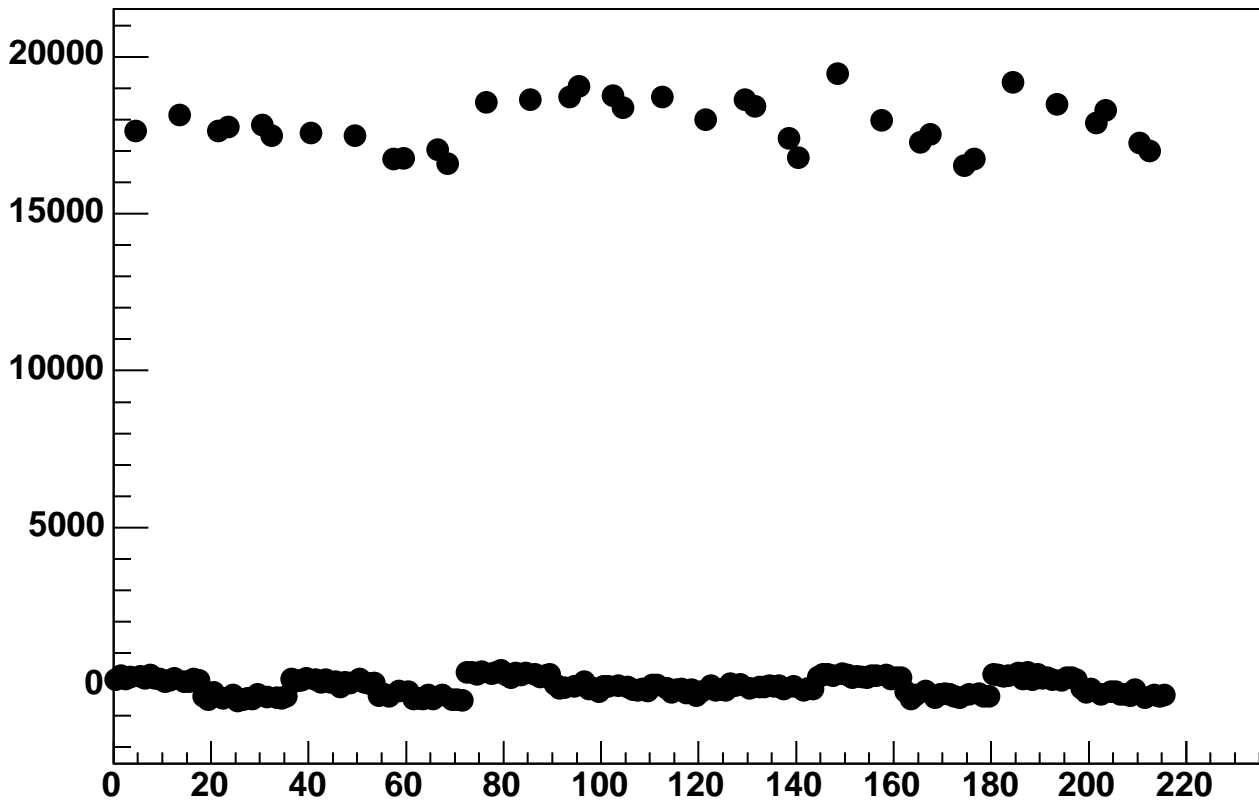
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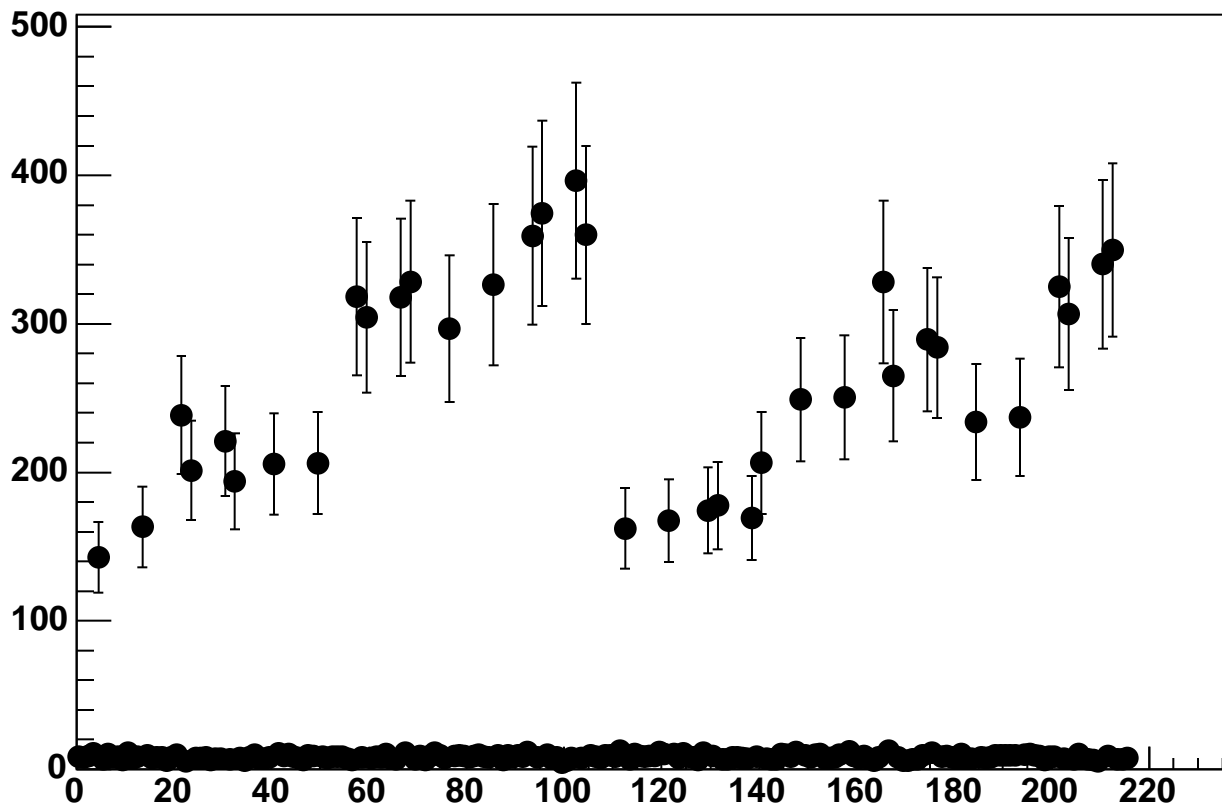
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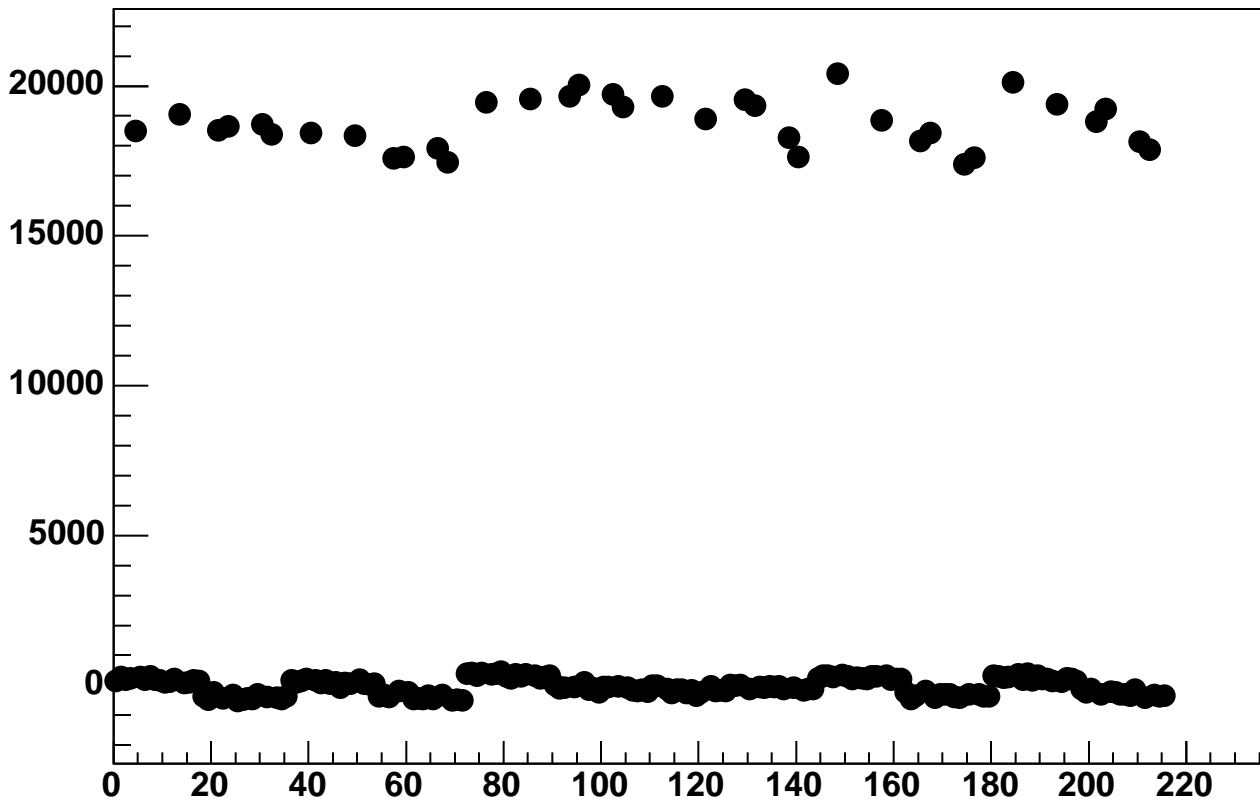
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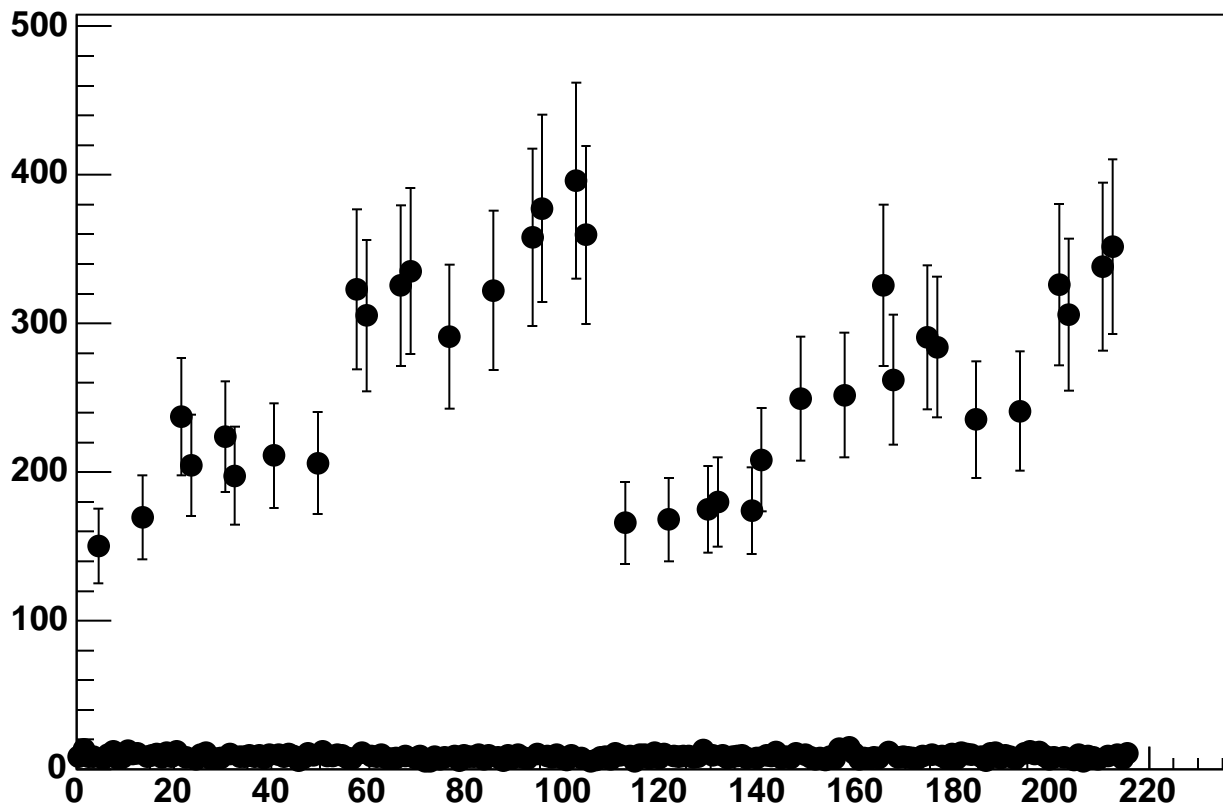
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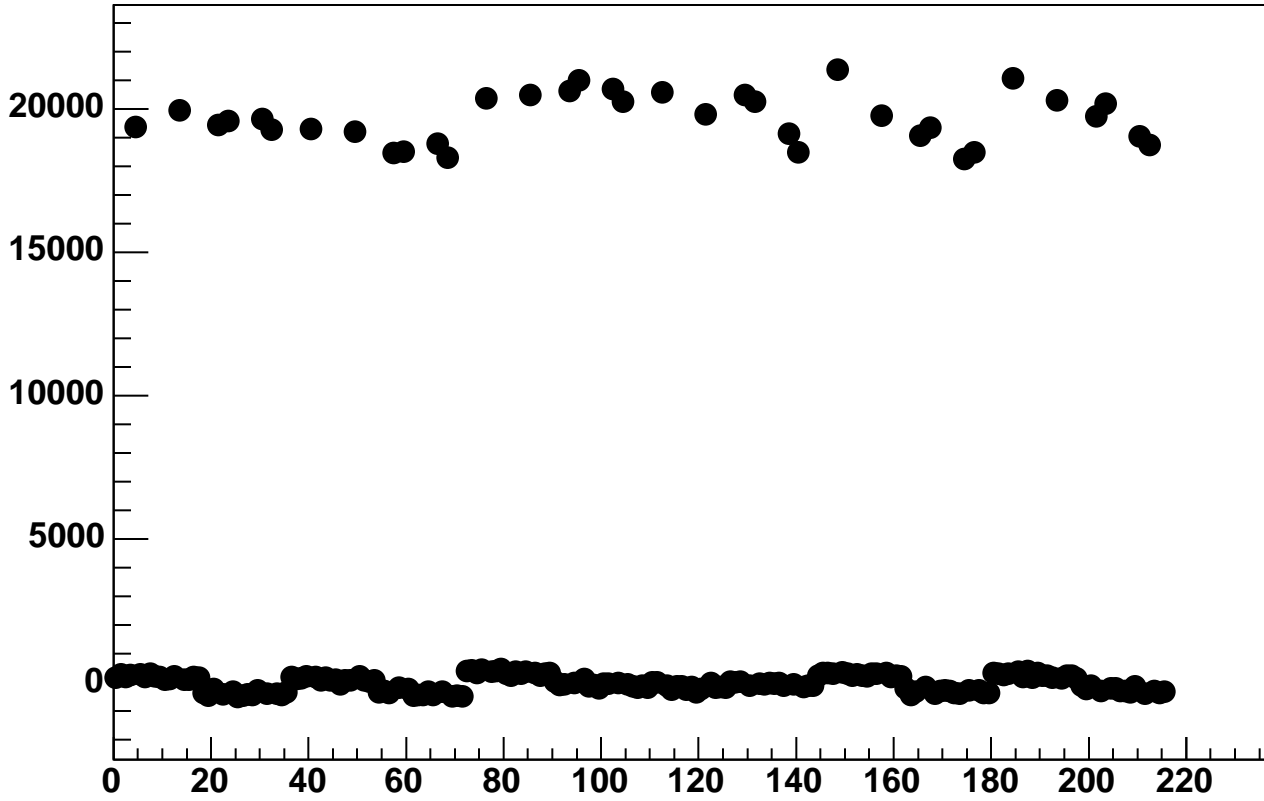
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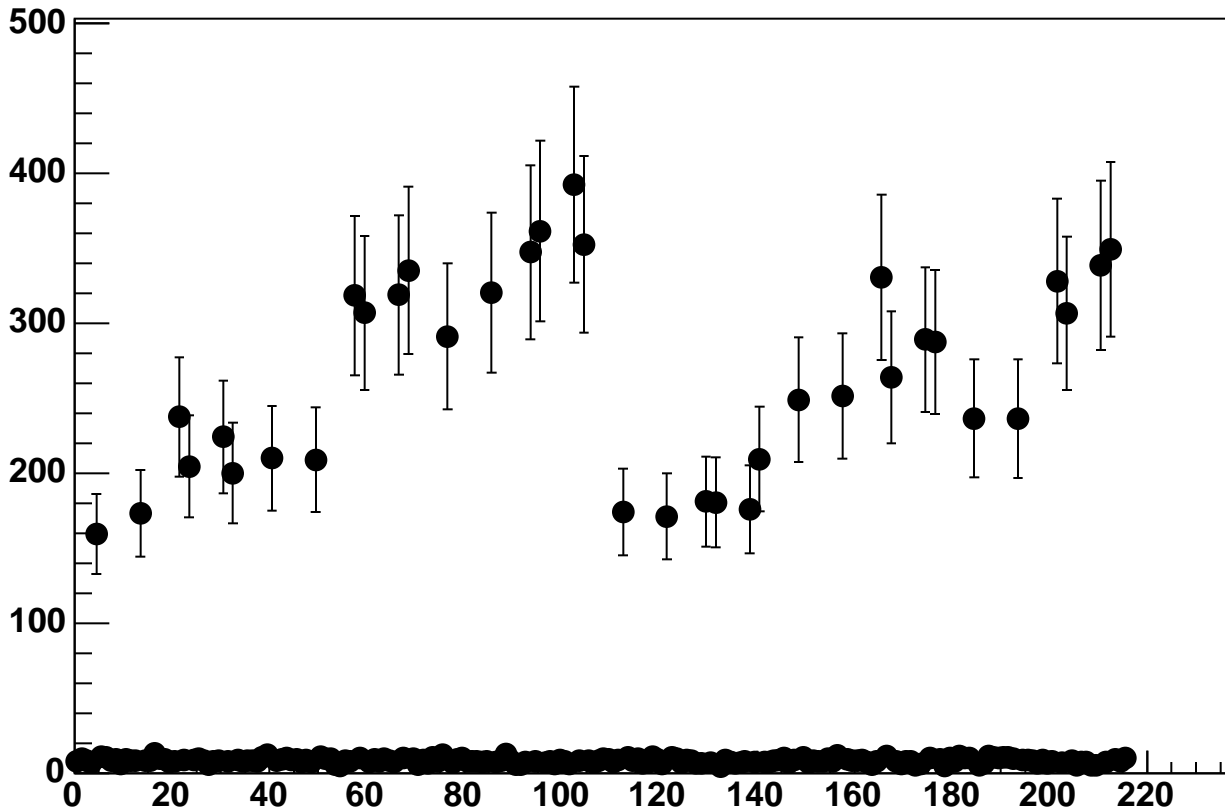
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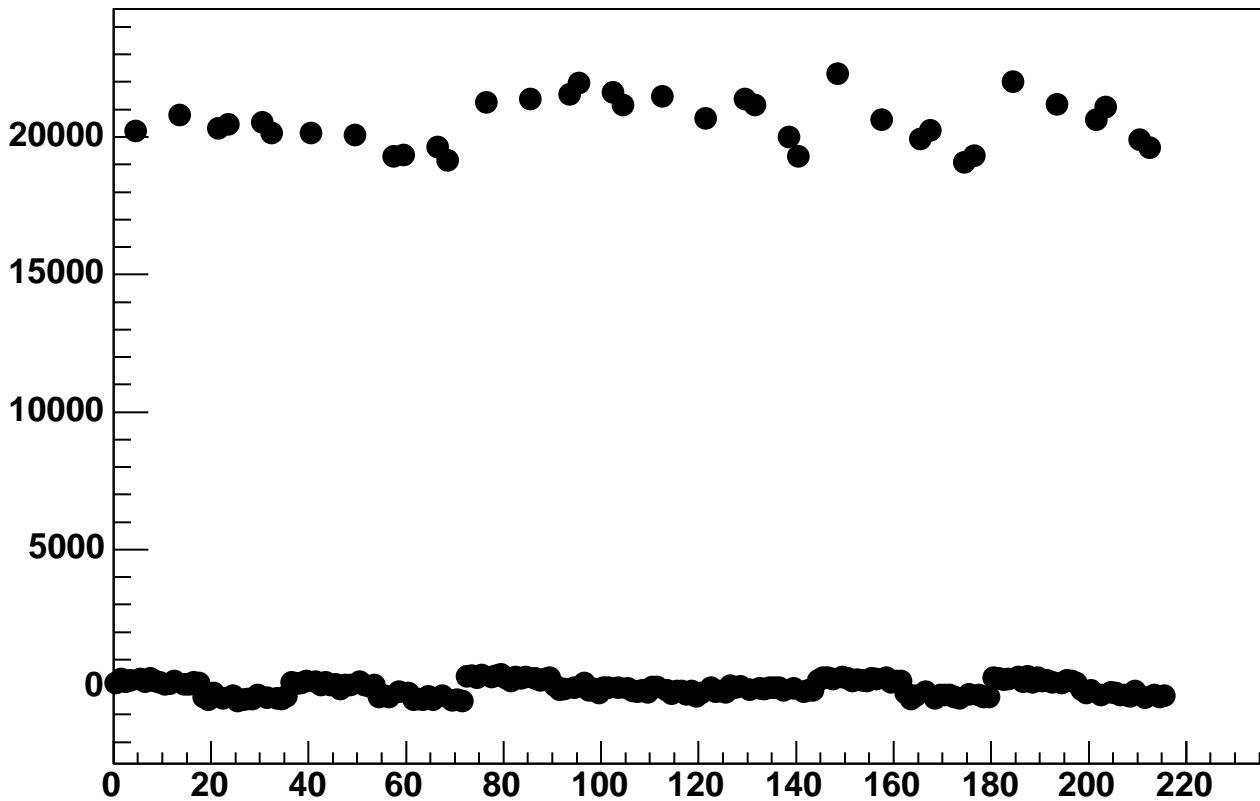
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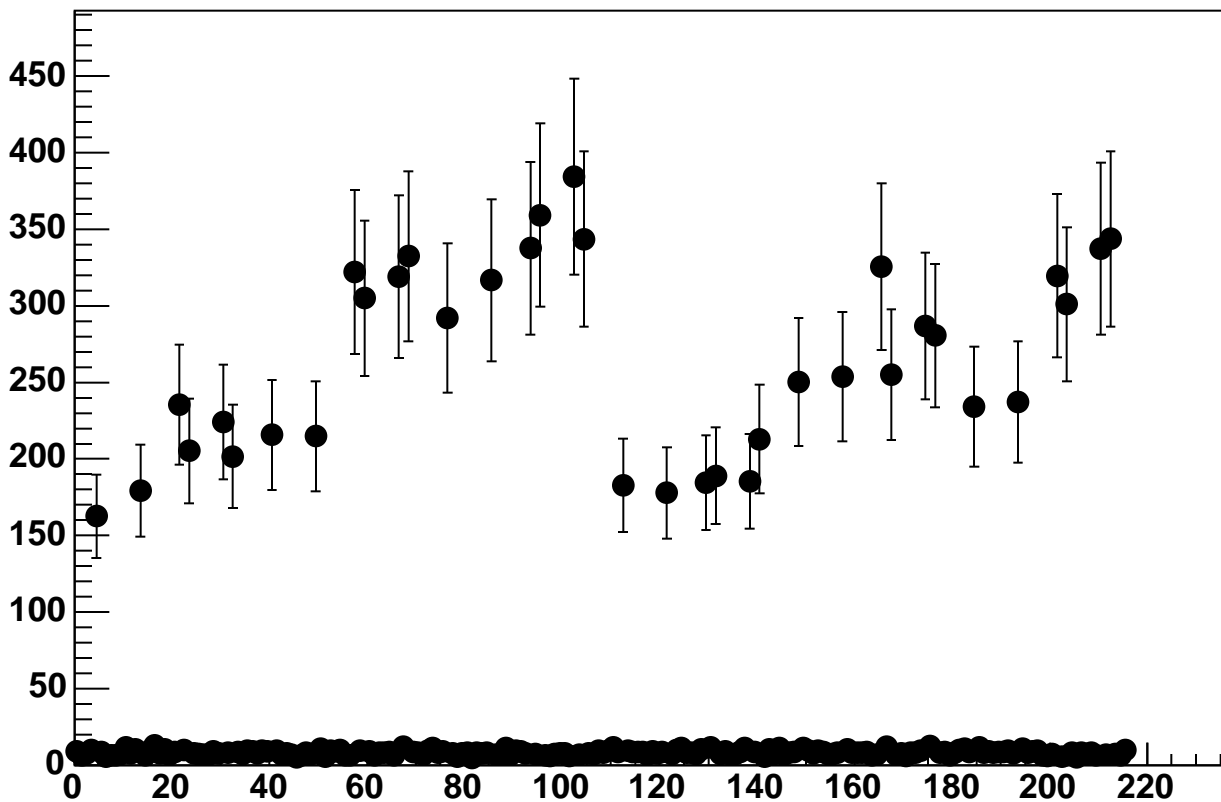
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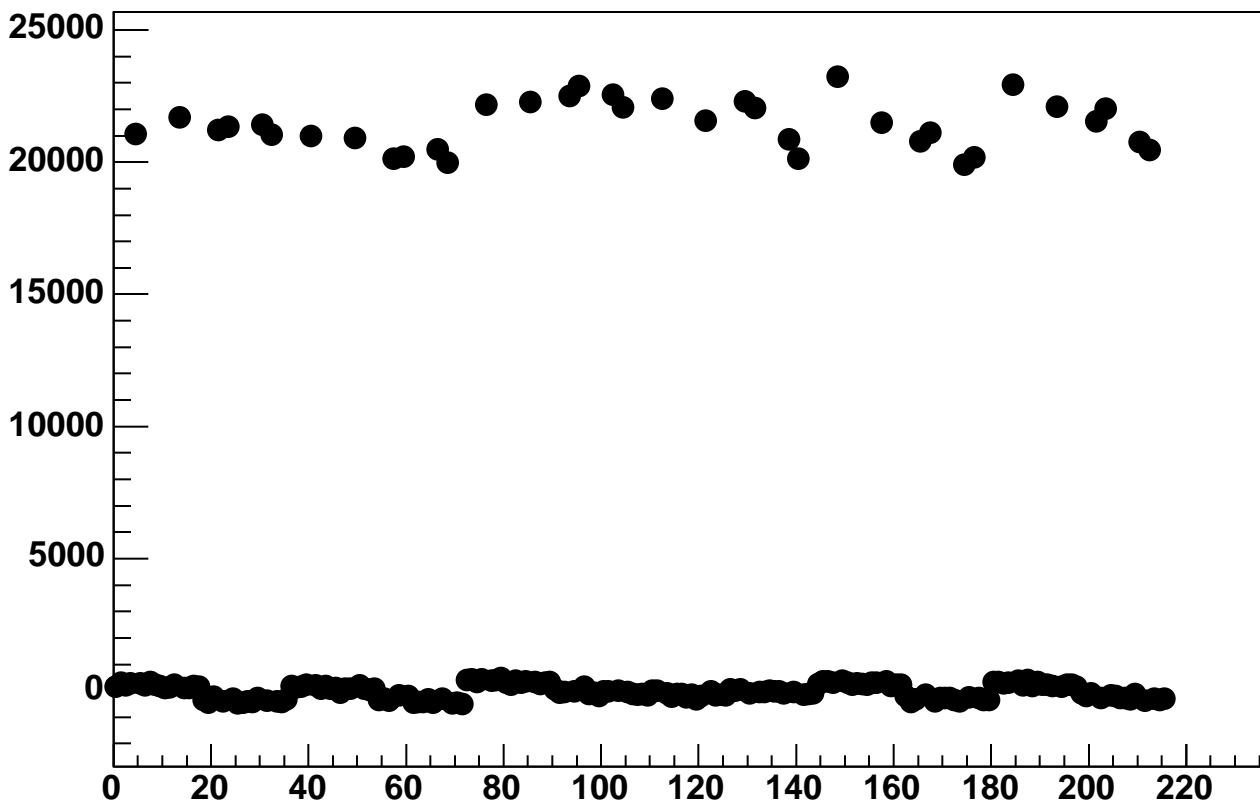
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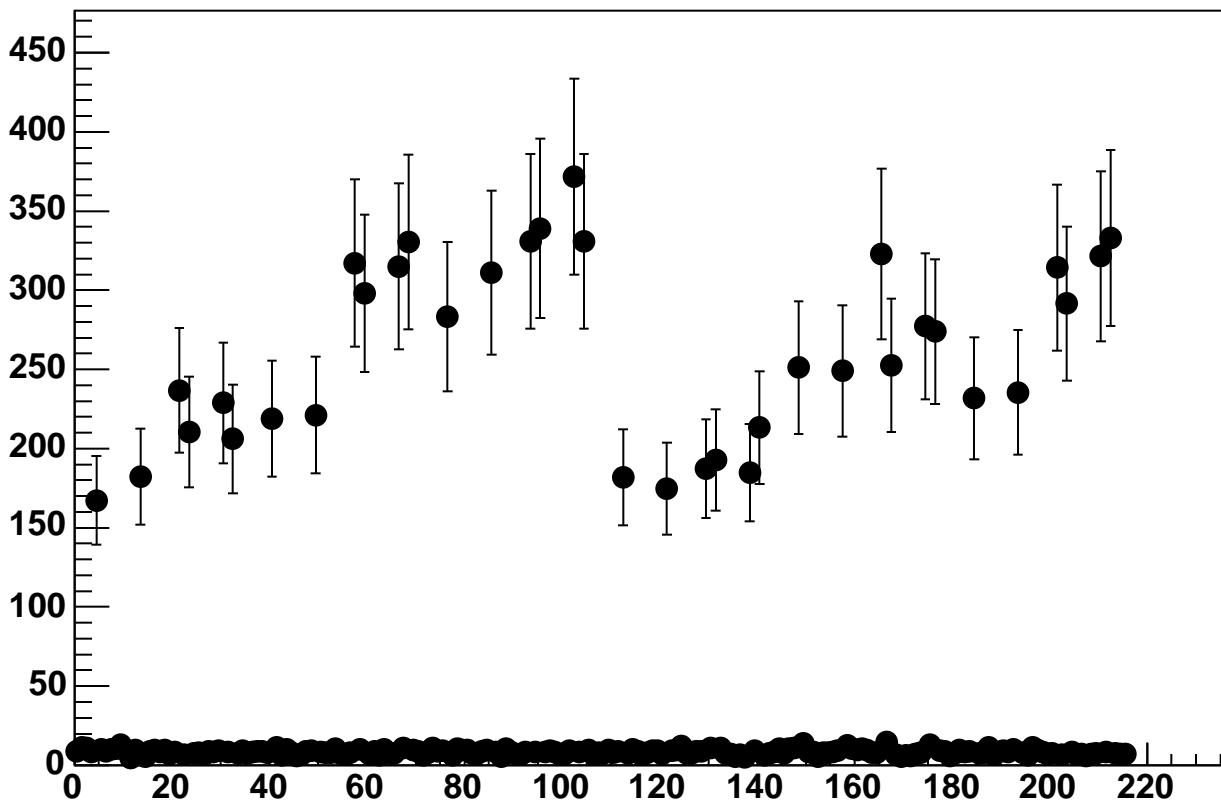
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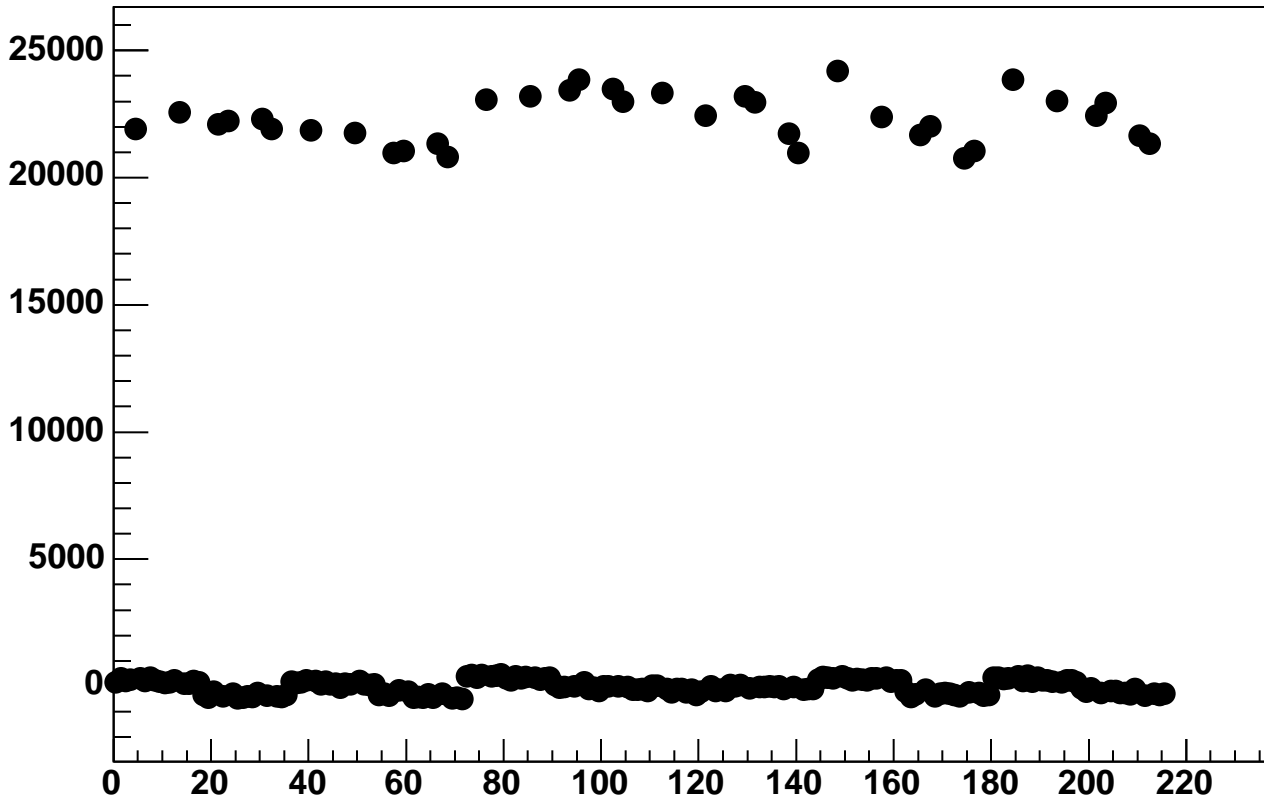
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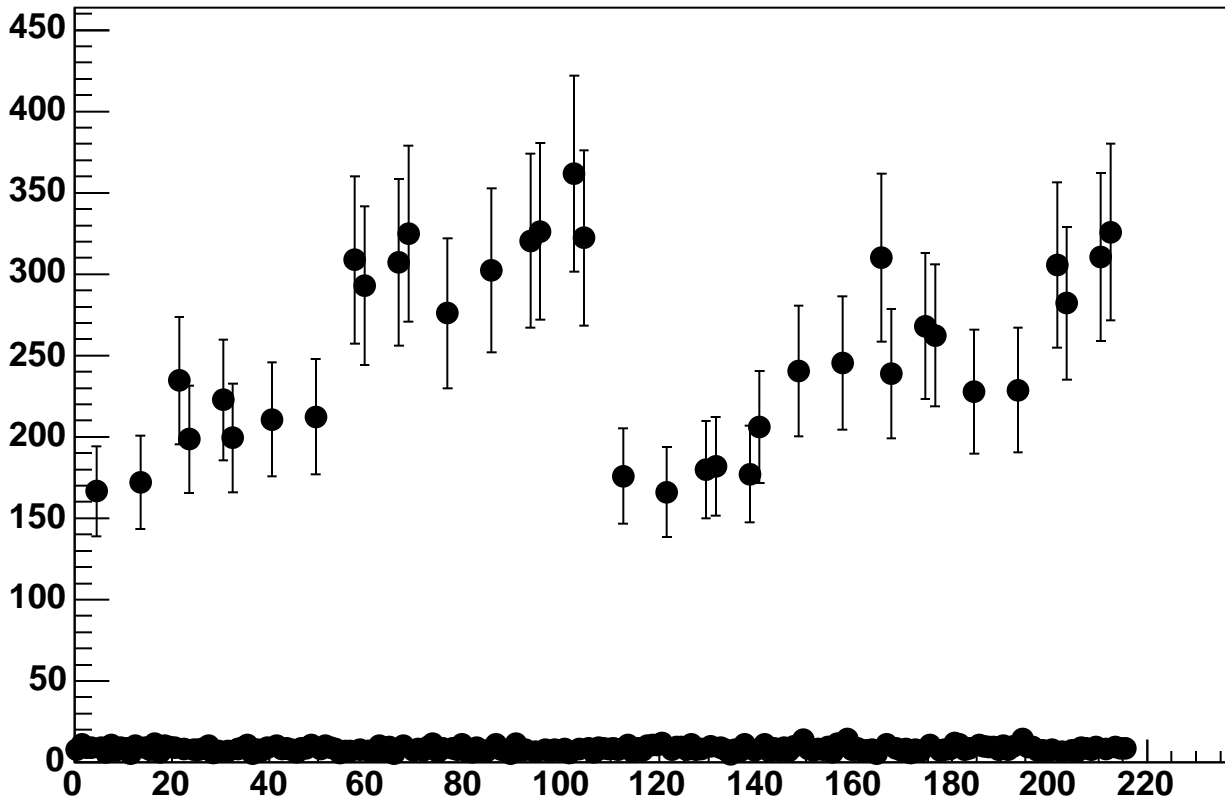
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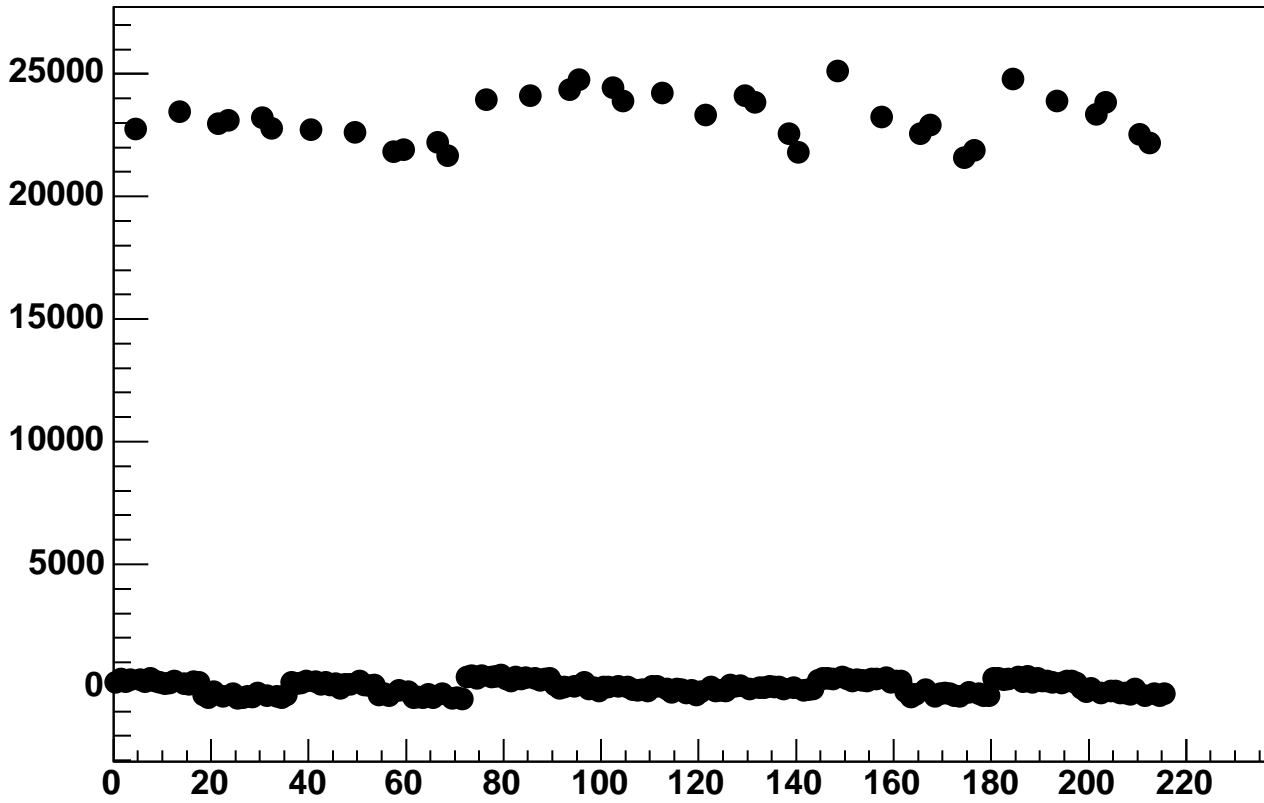
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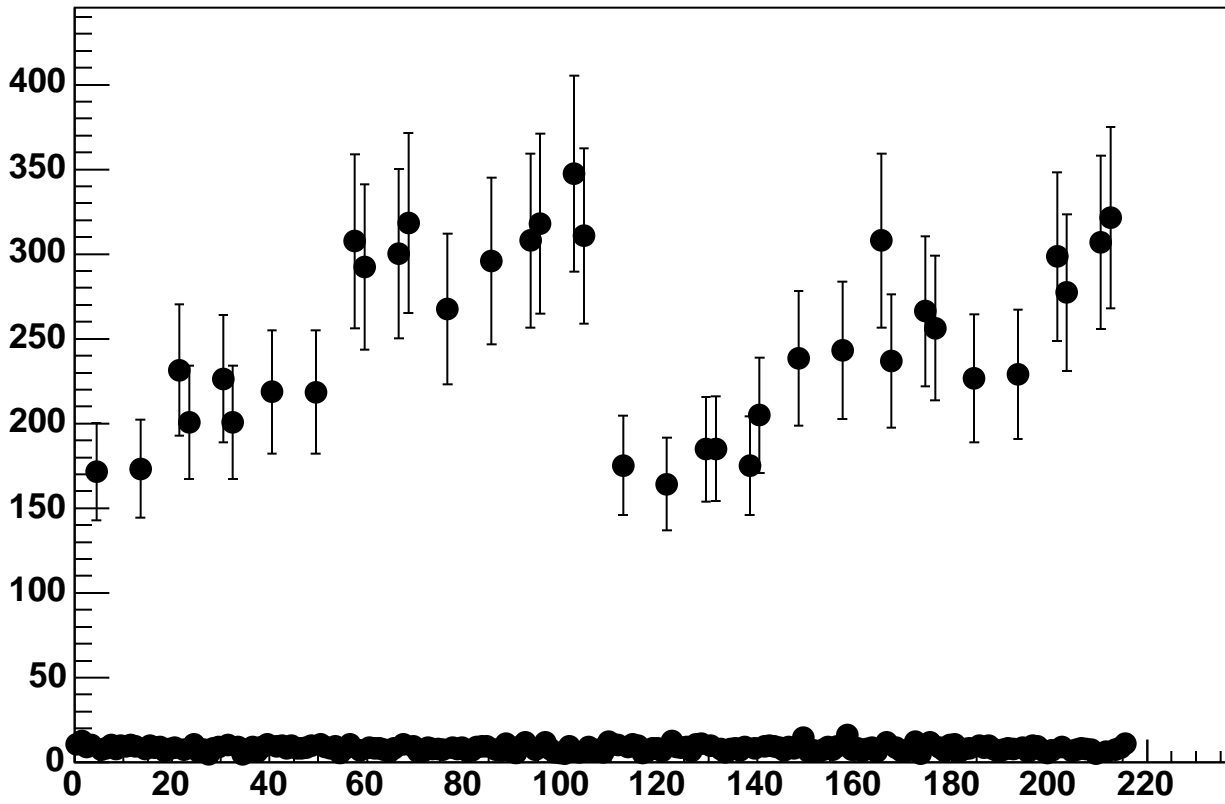
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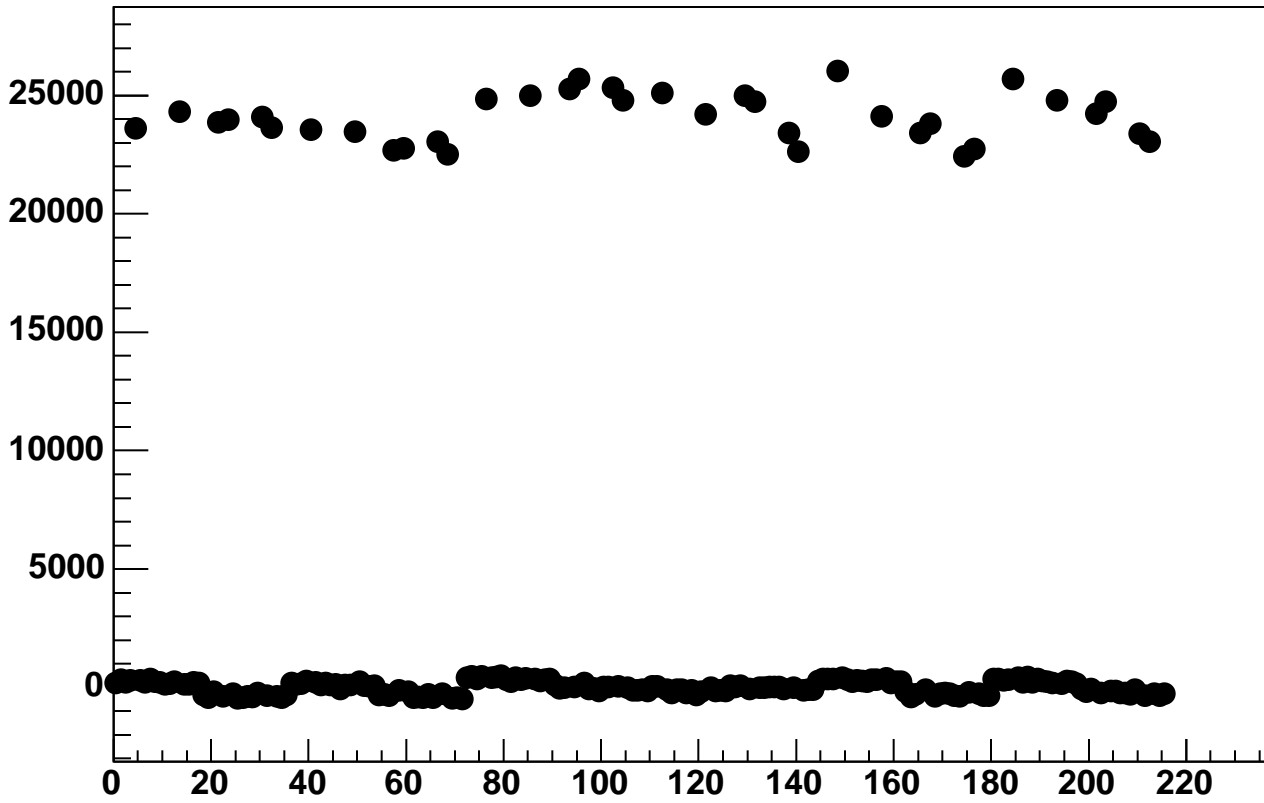
Enable 4, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



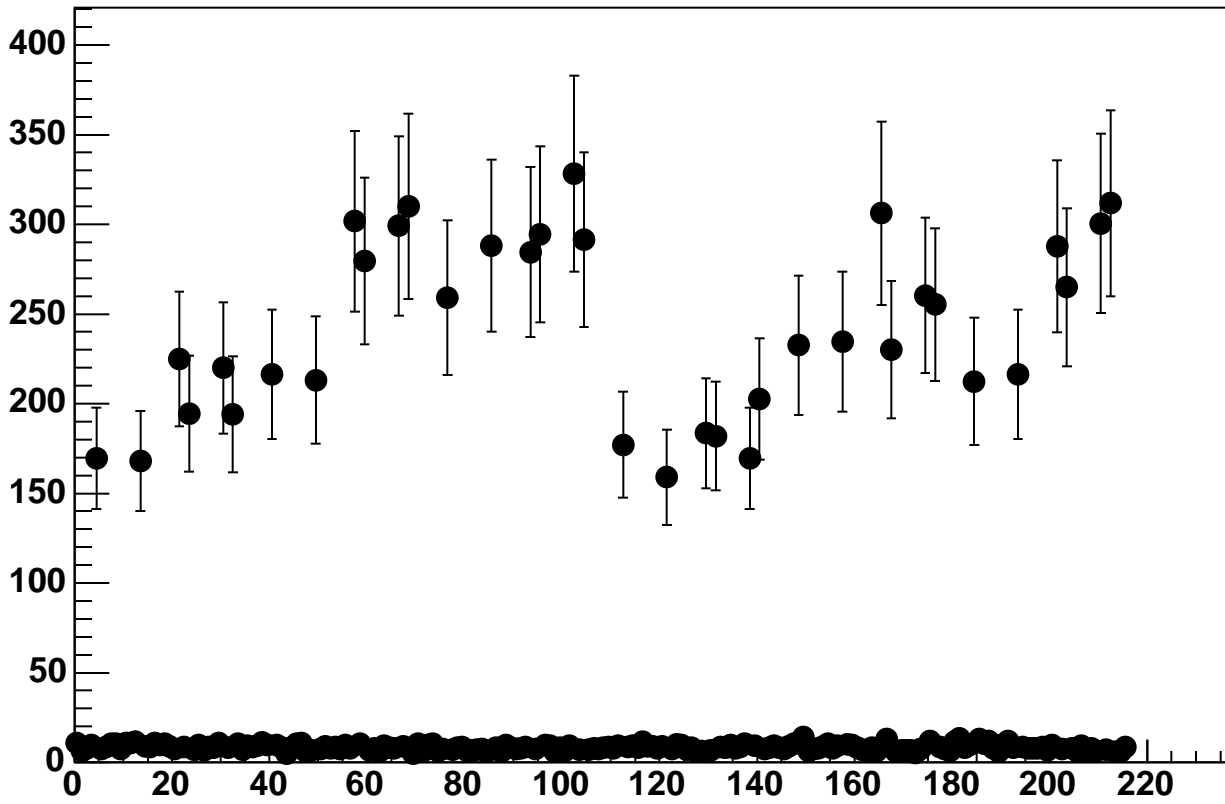
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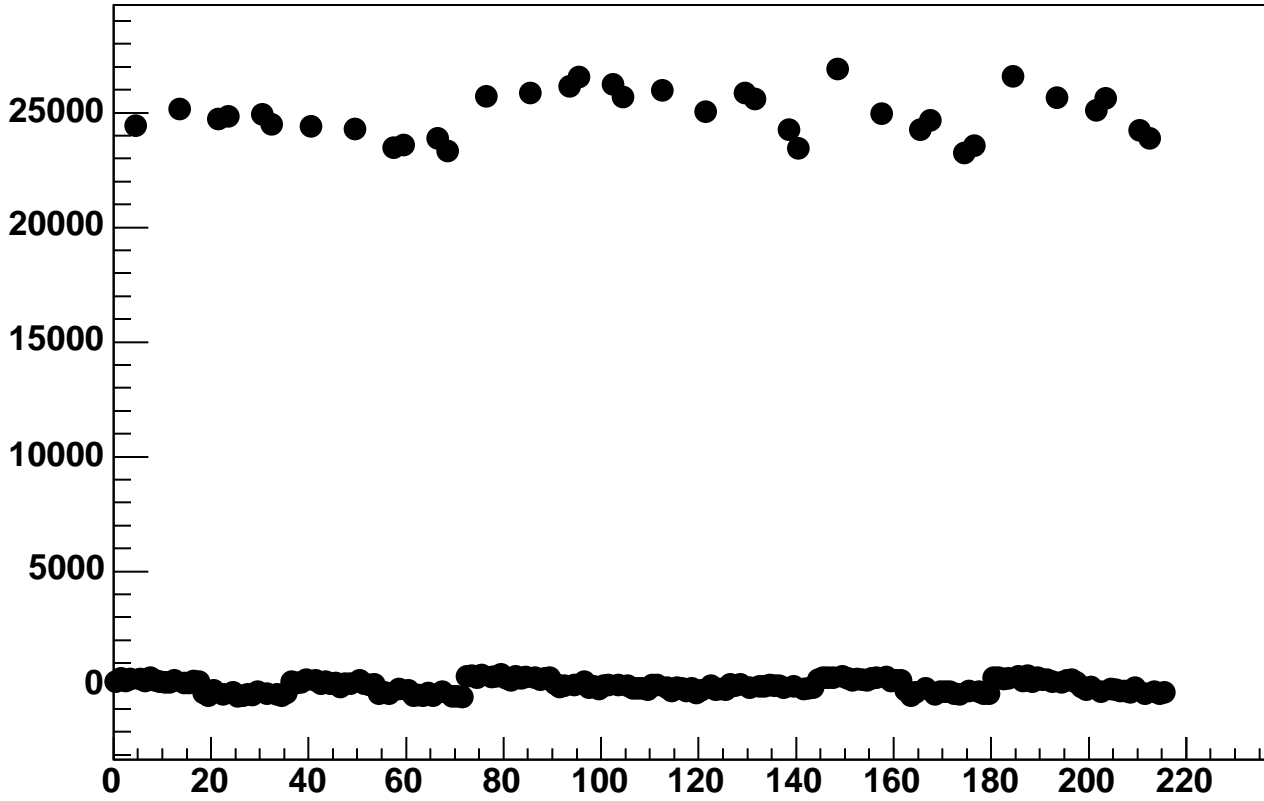
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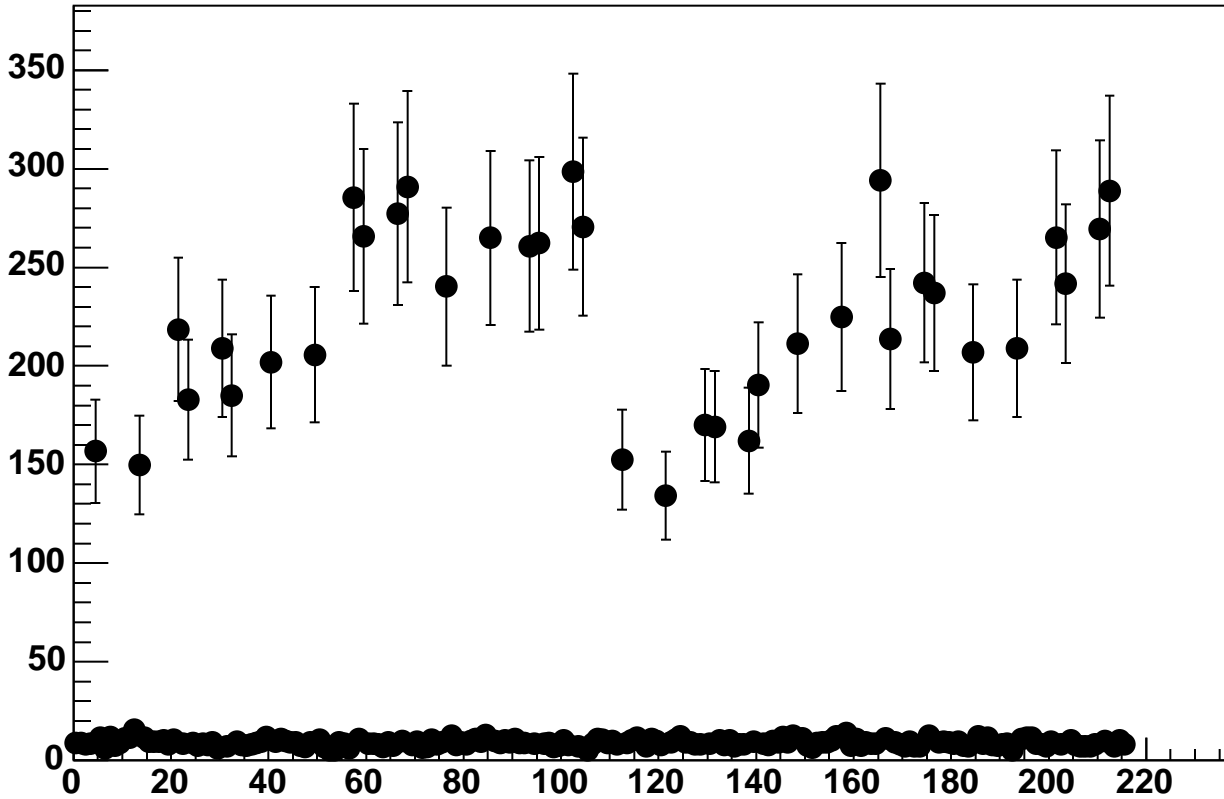
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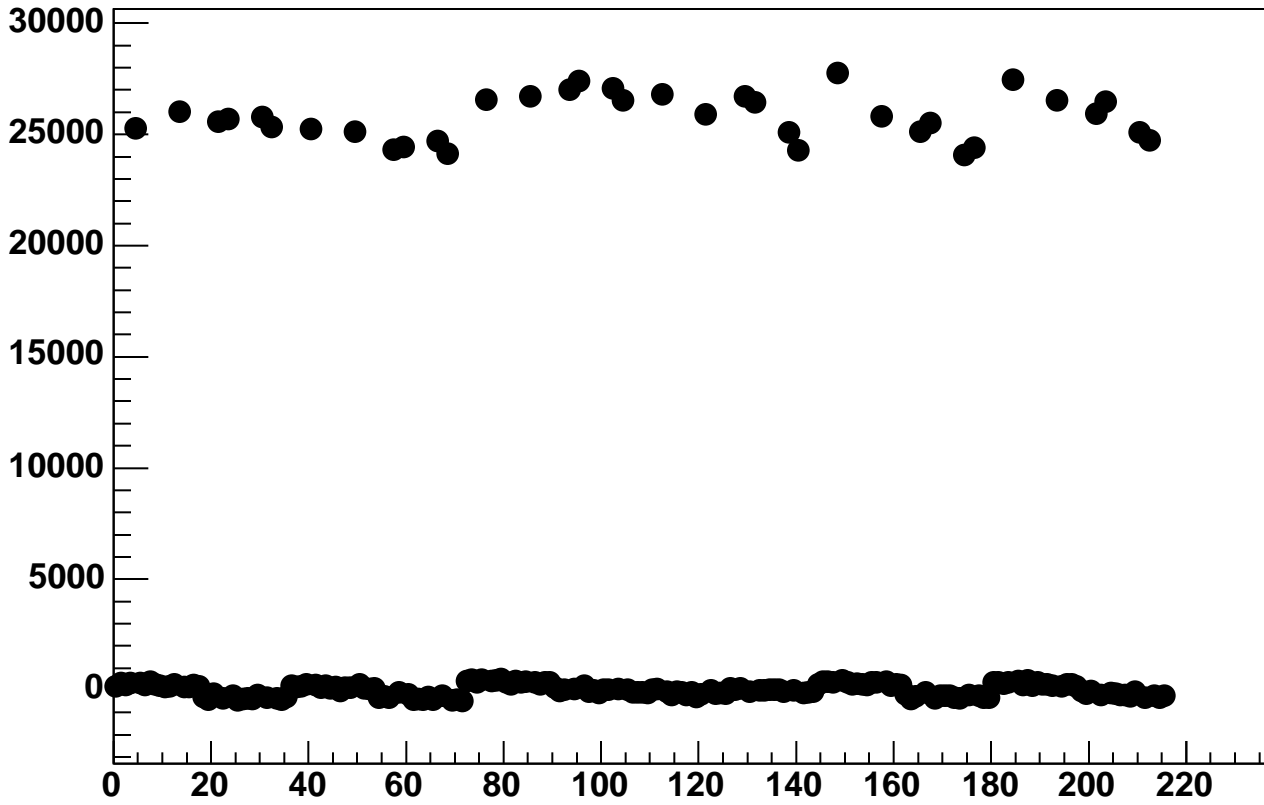
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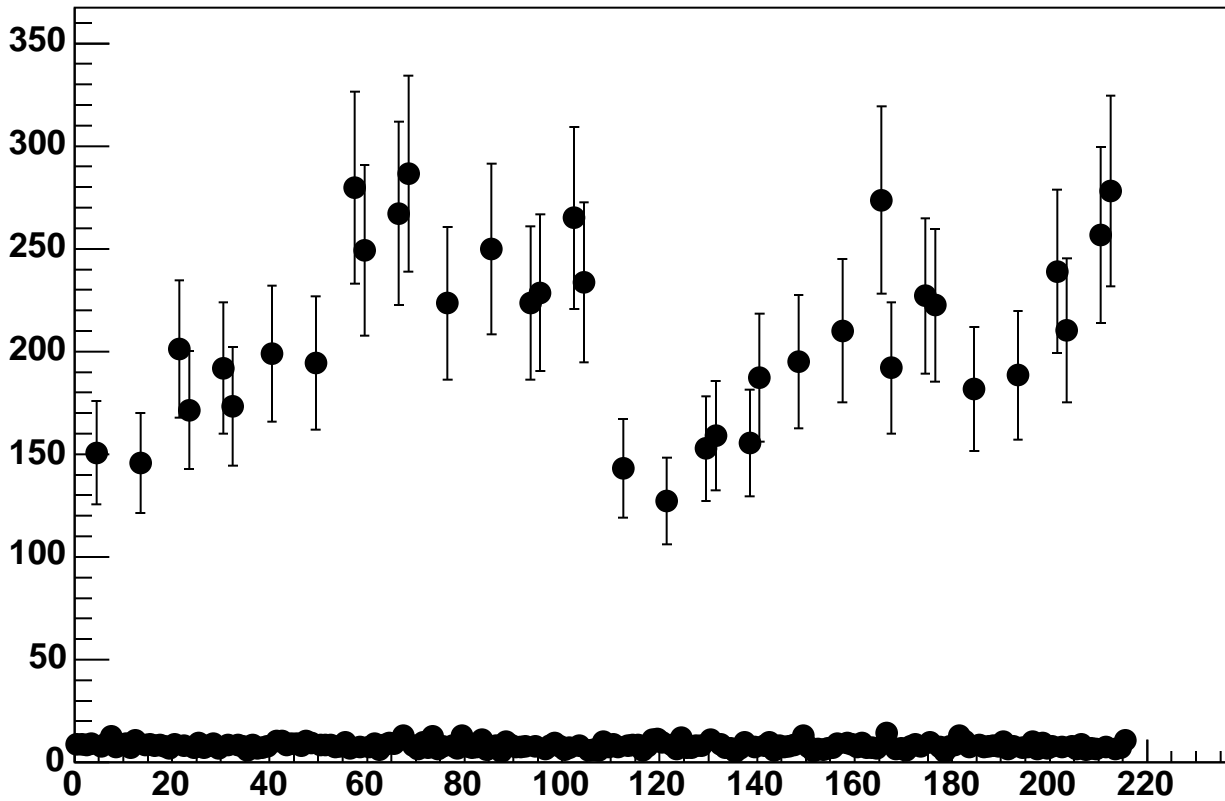
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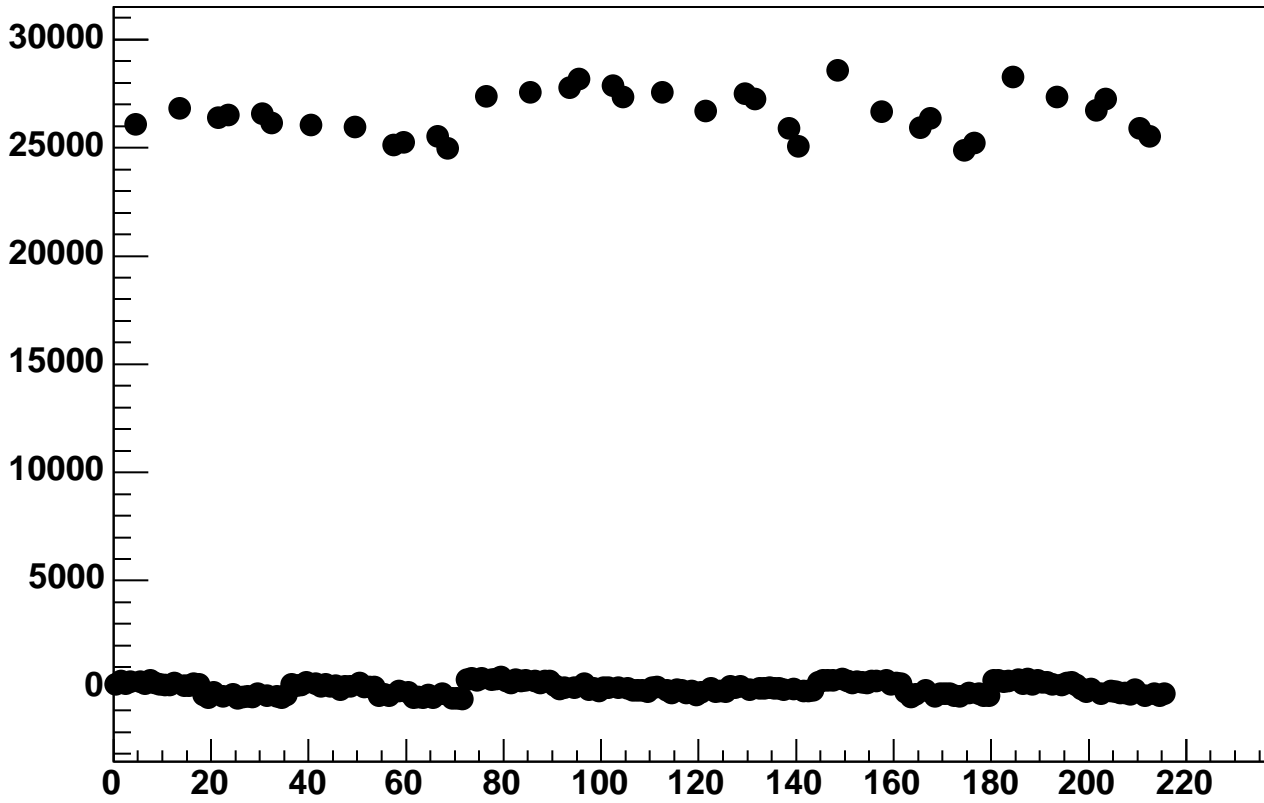
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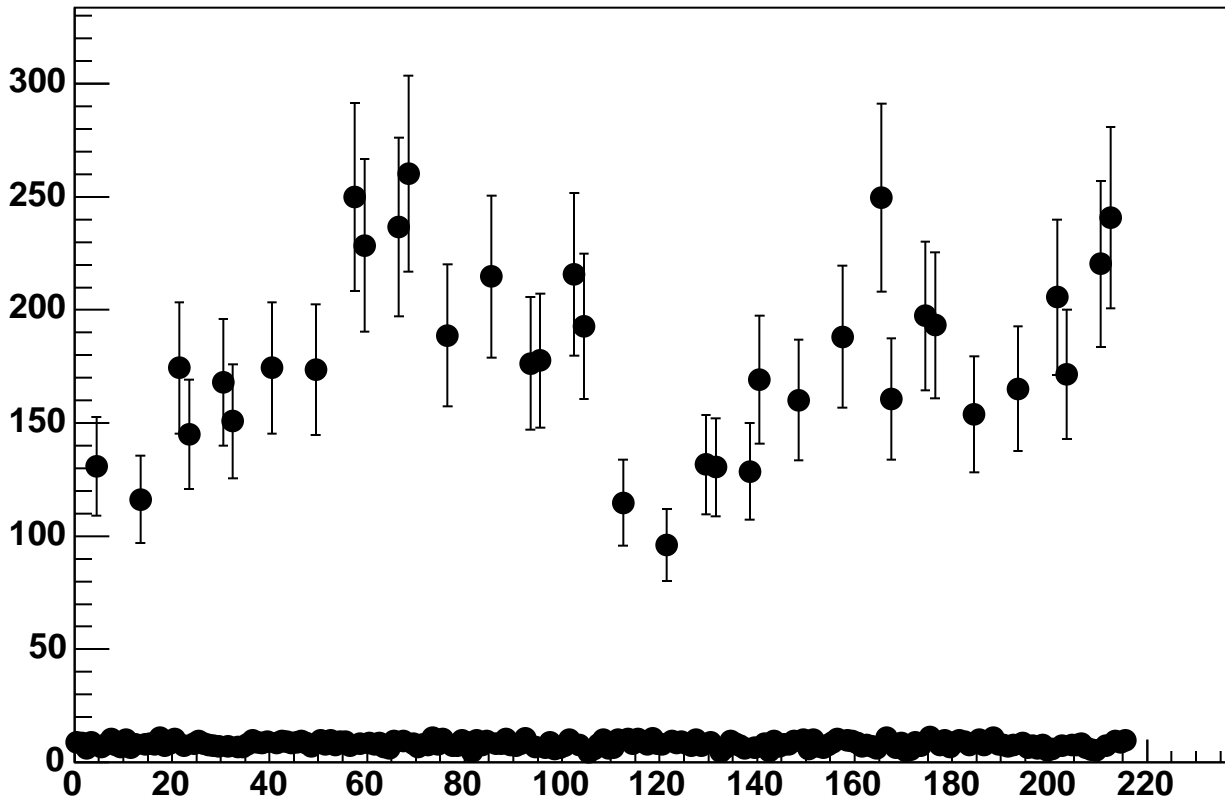
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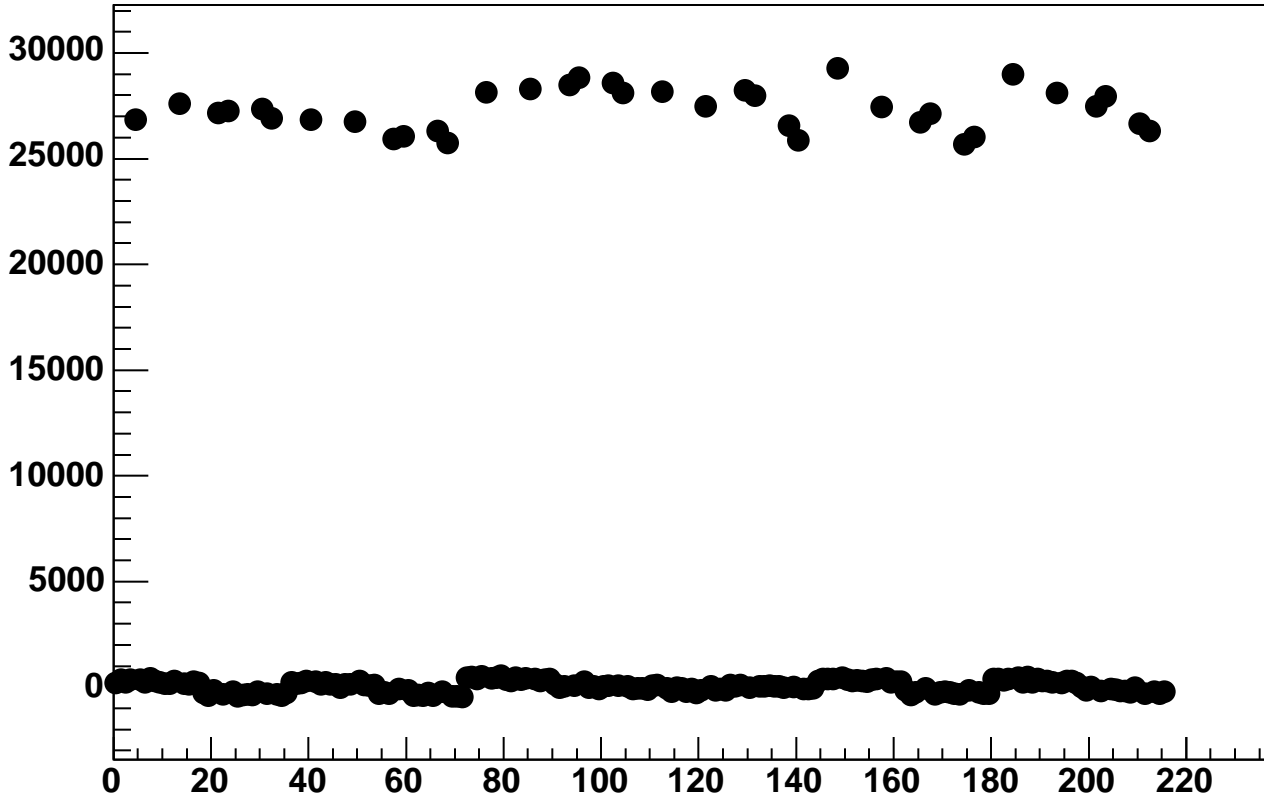
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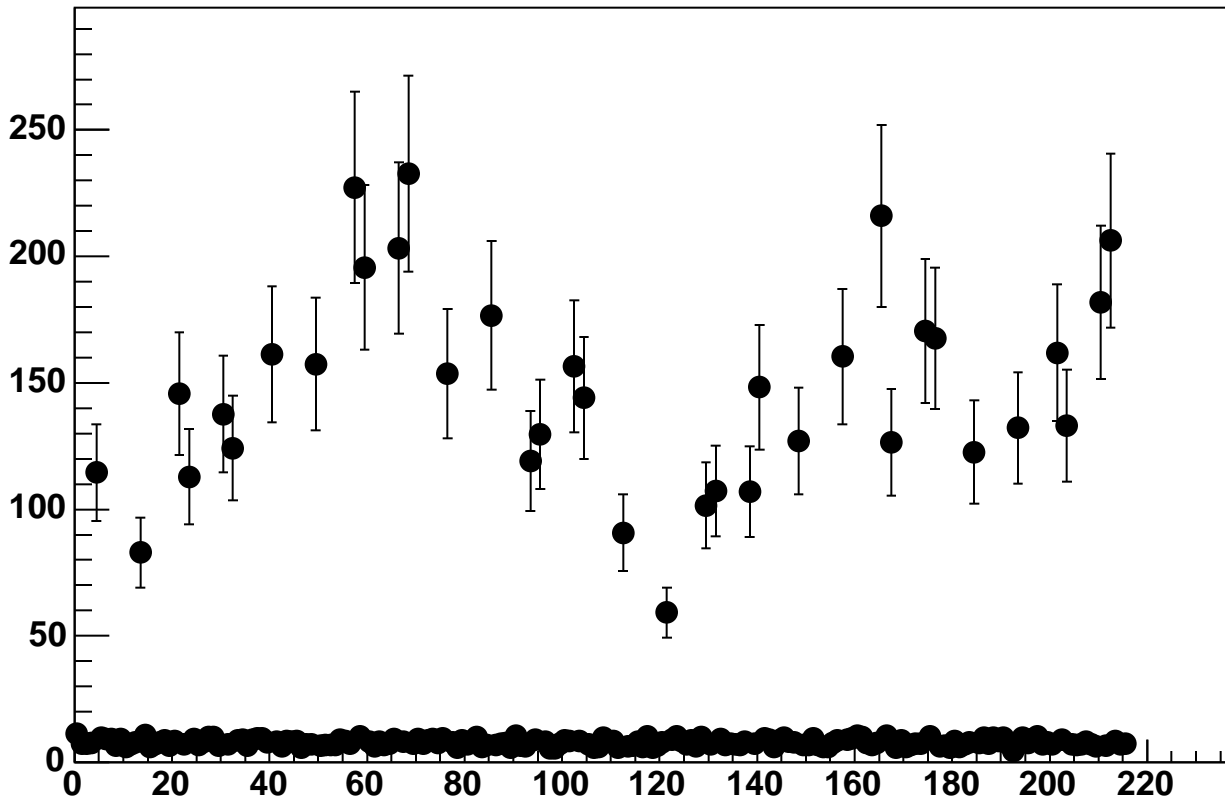
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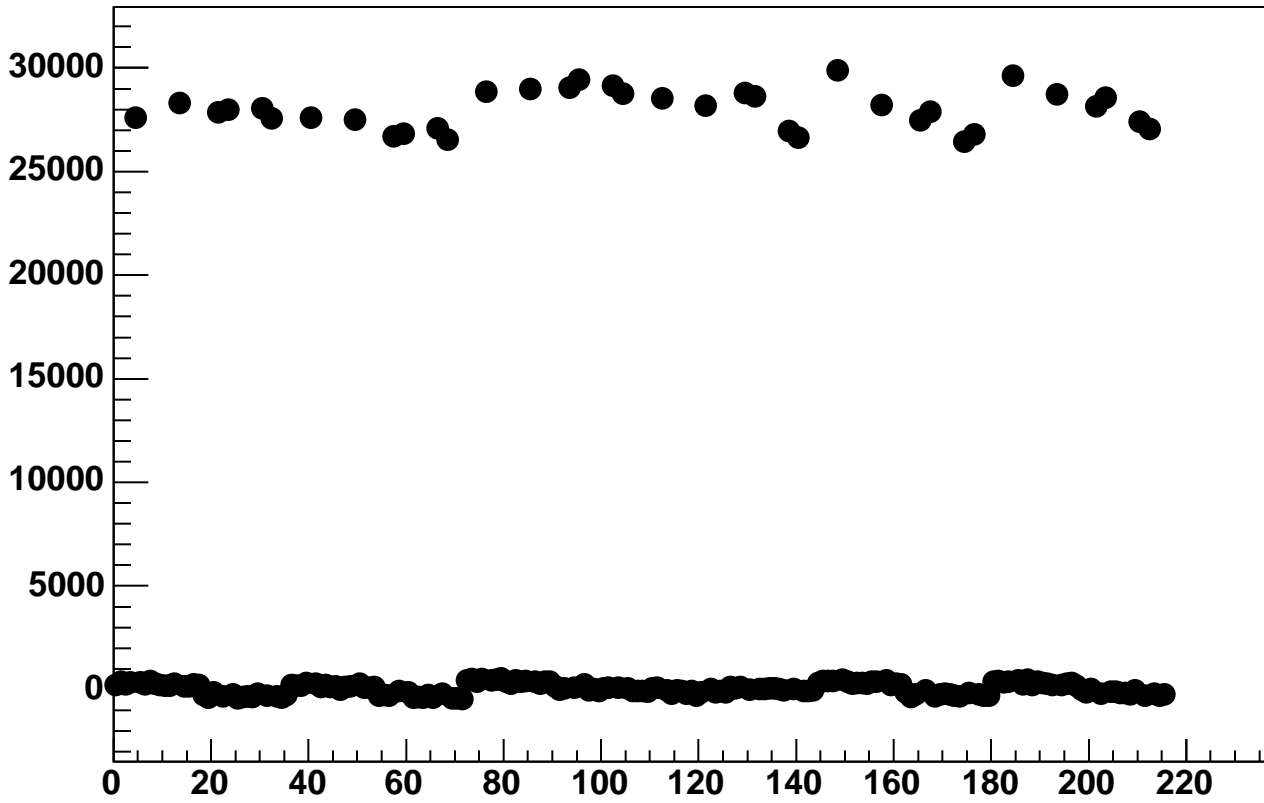
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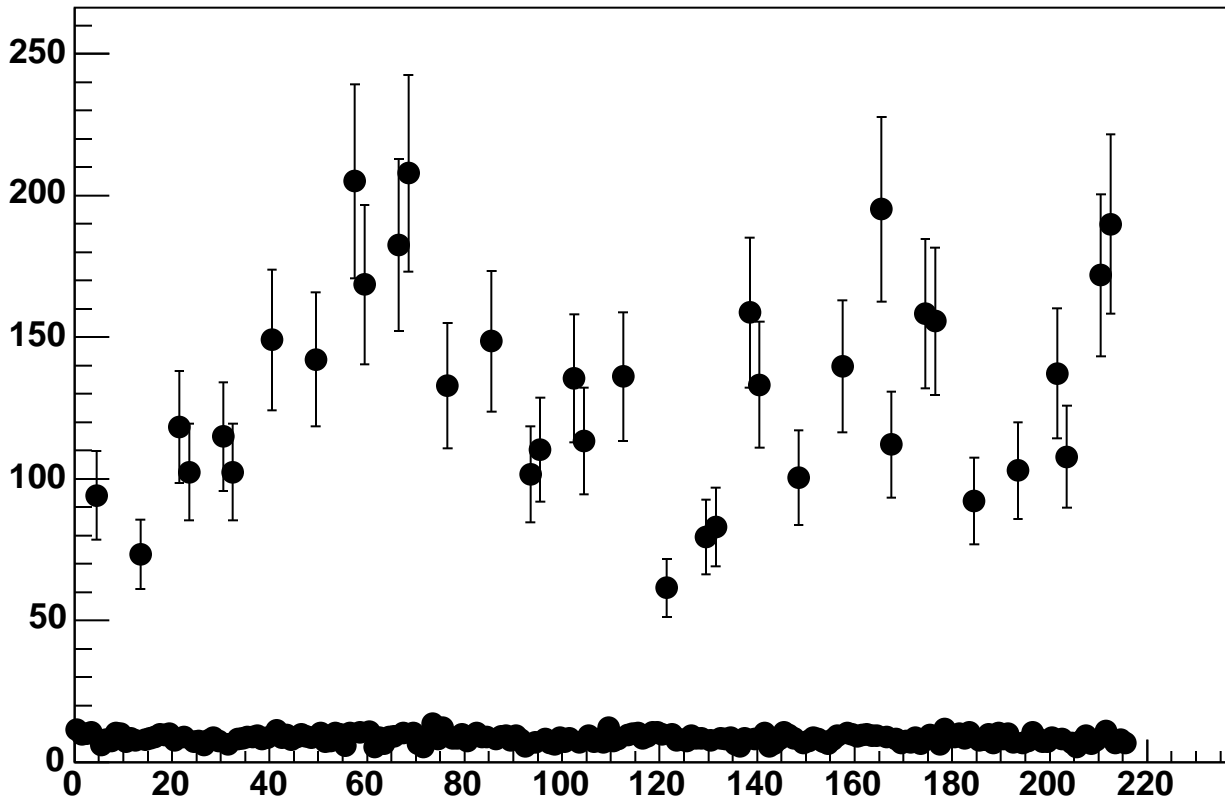
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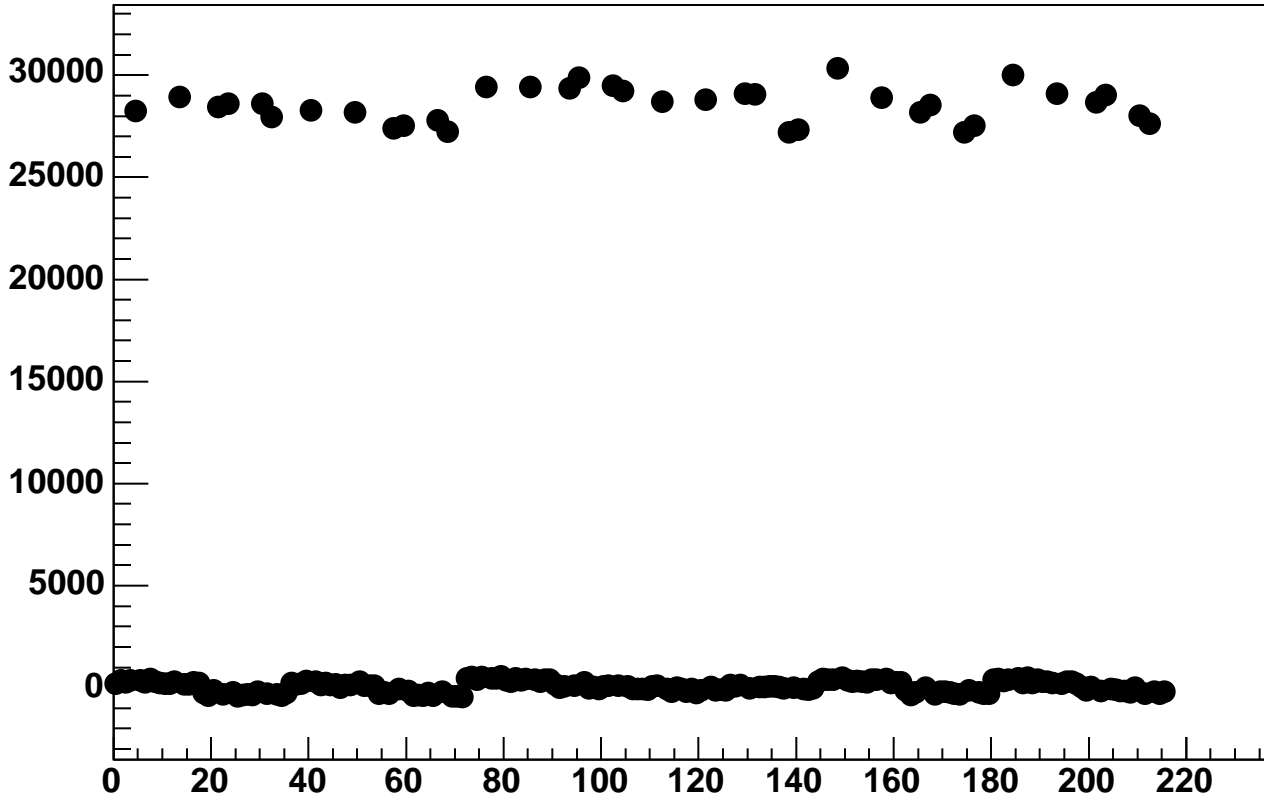
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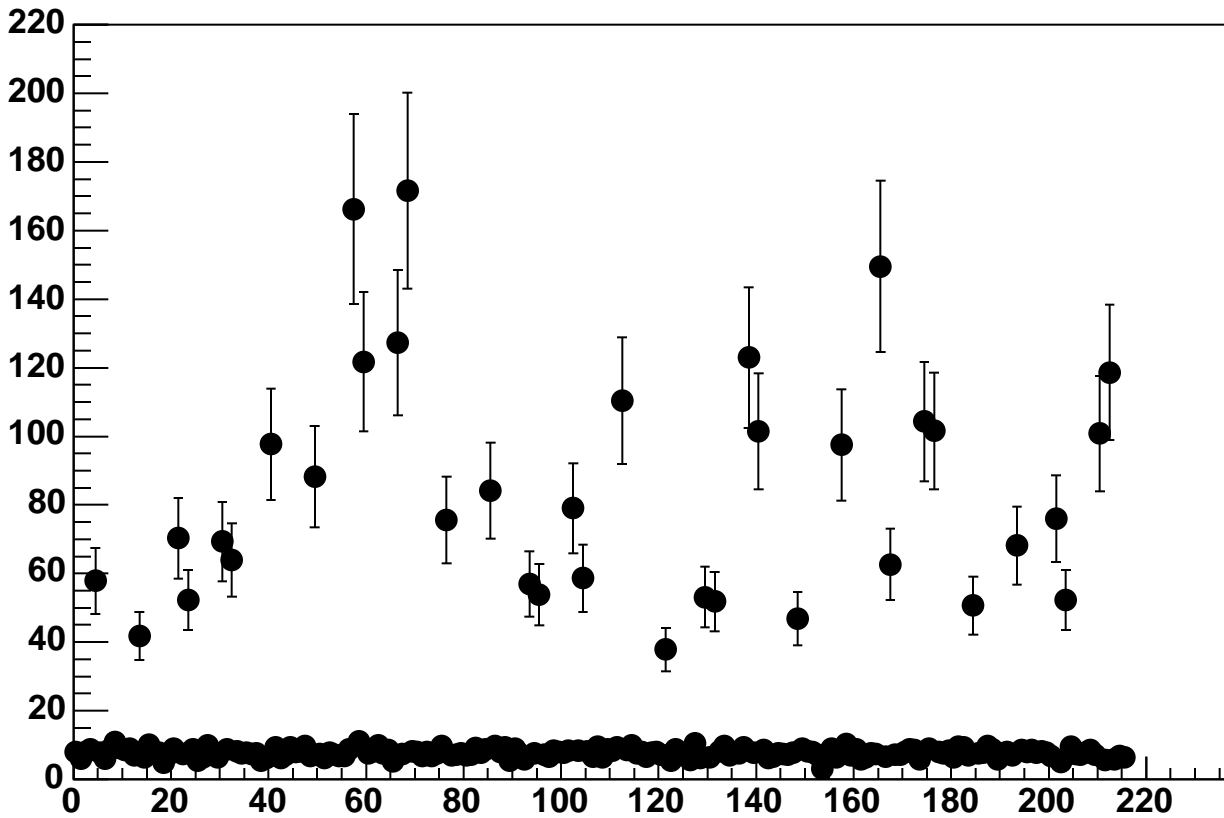
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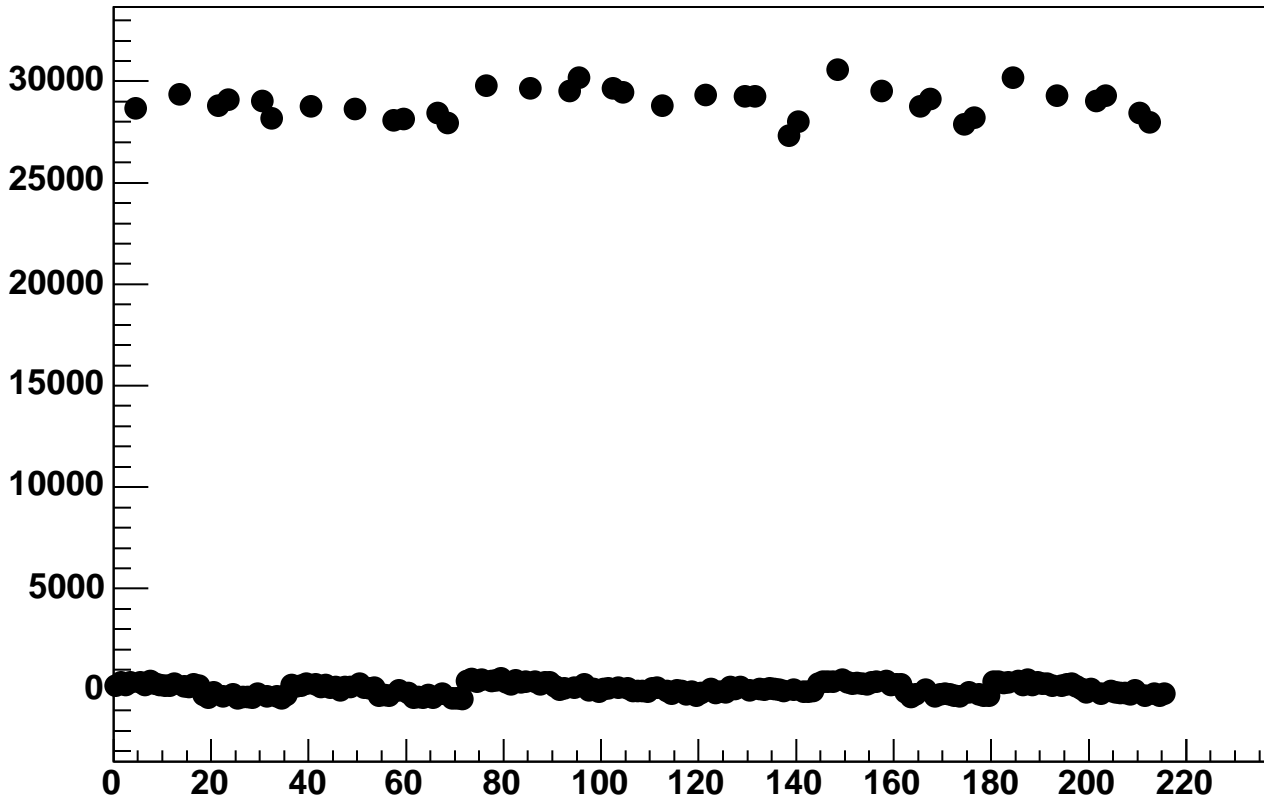
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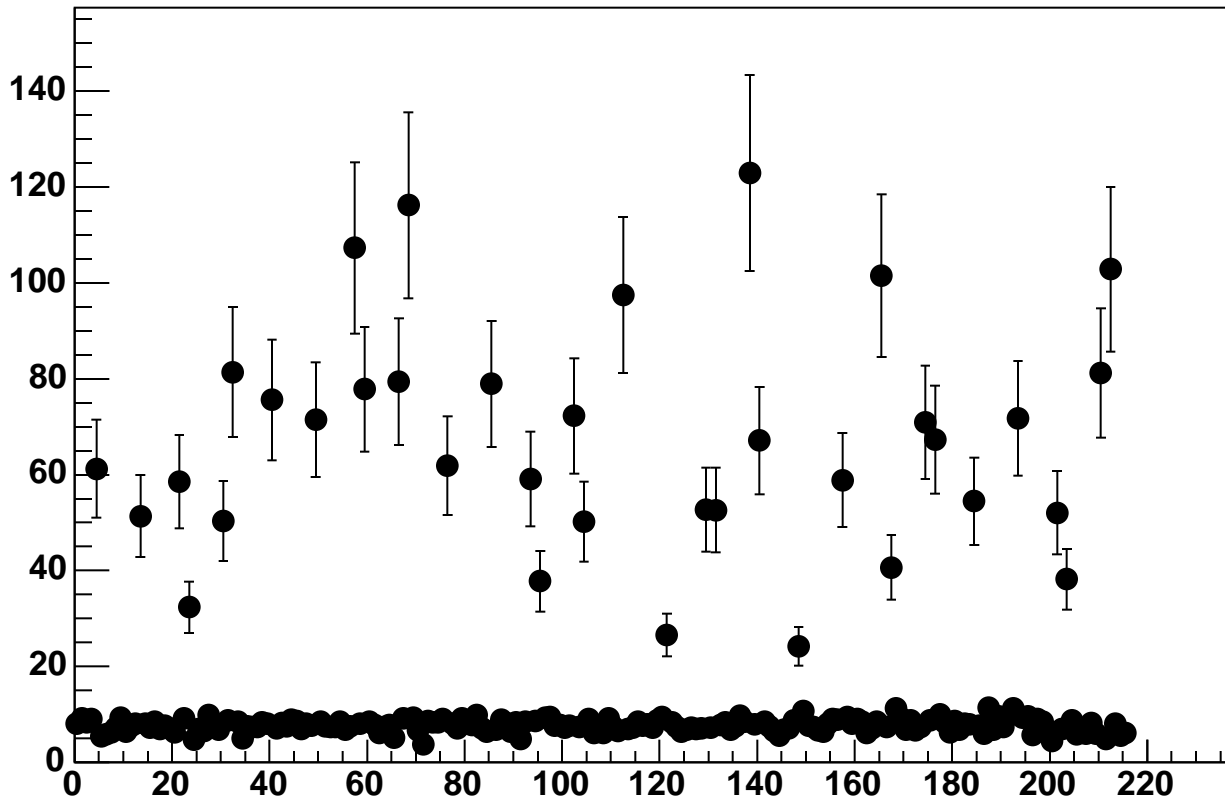
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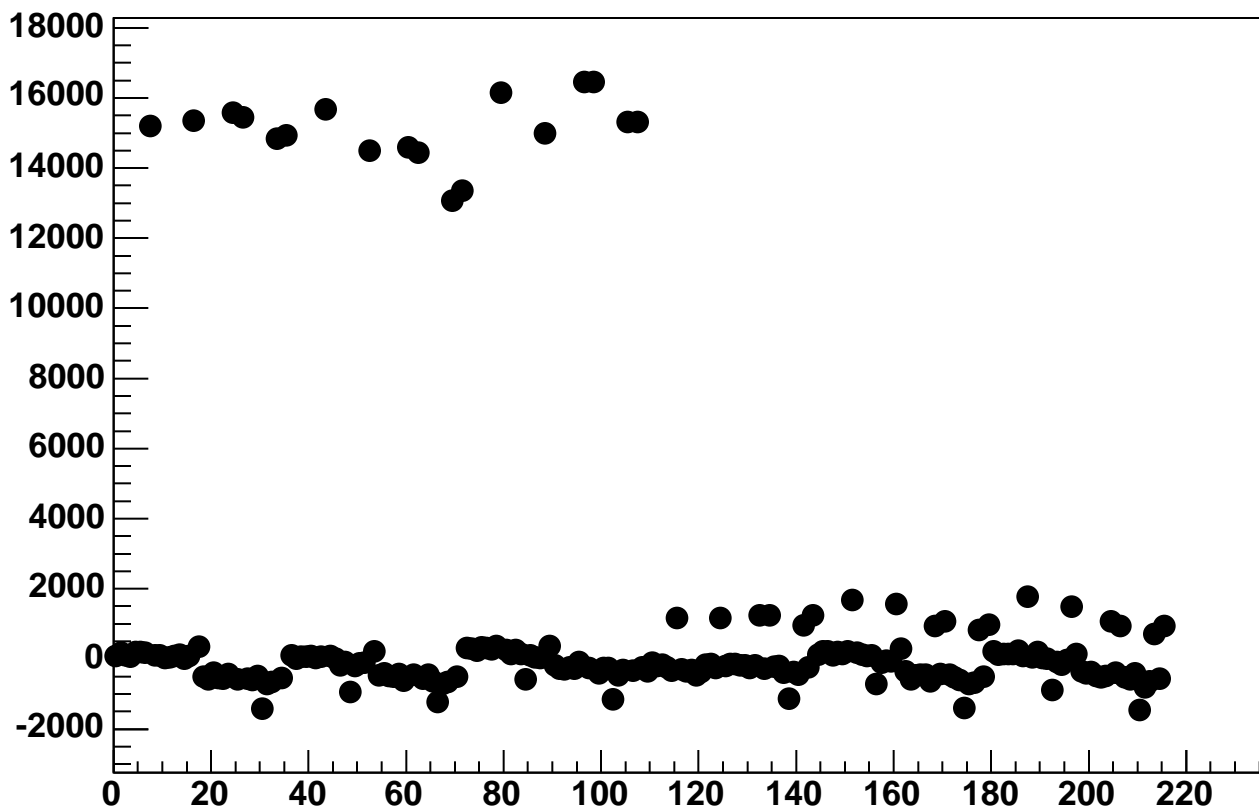
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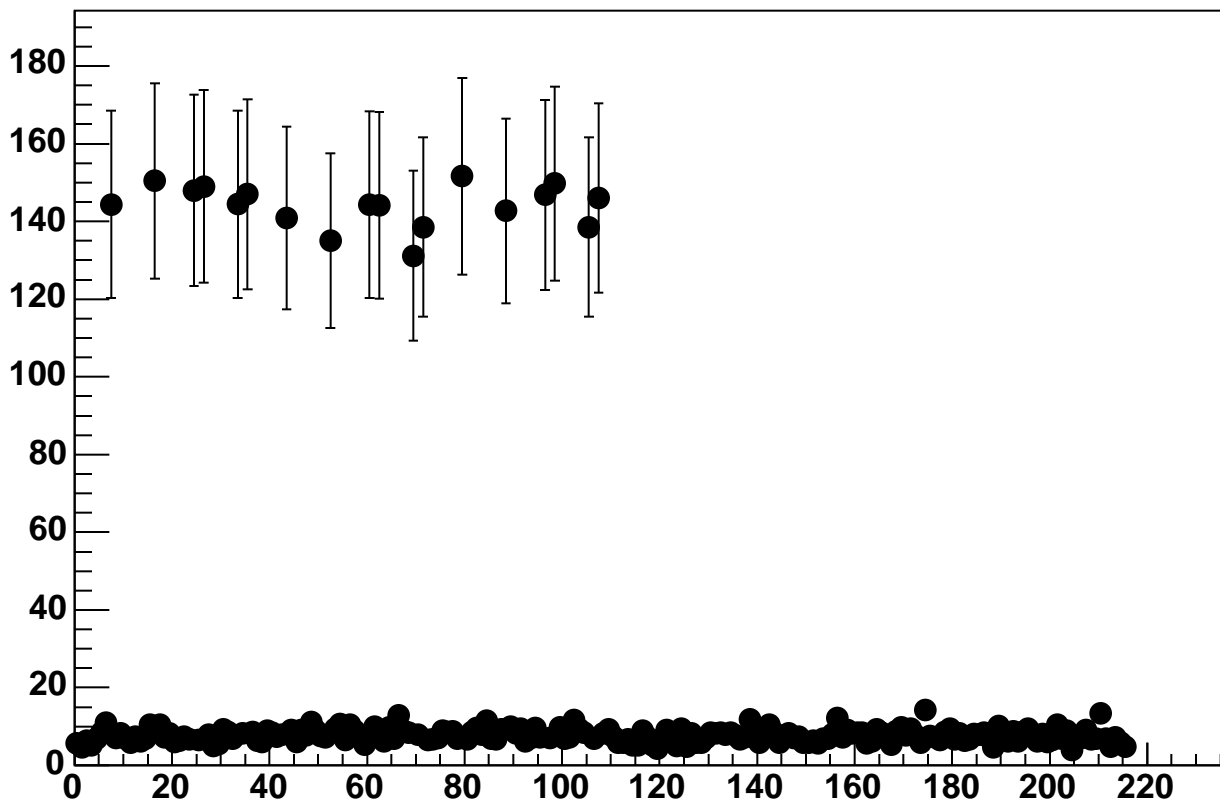
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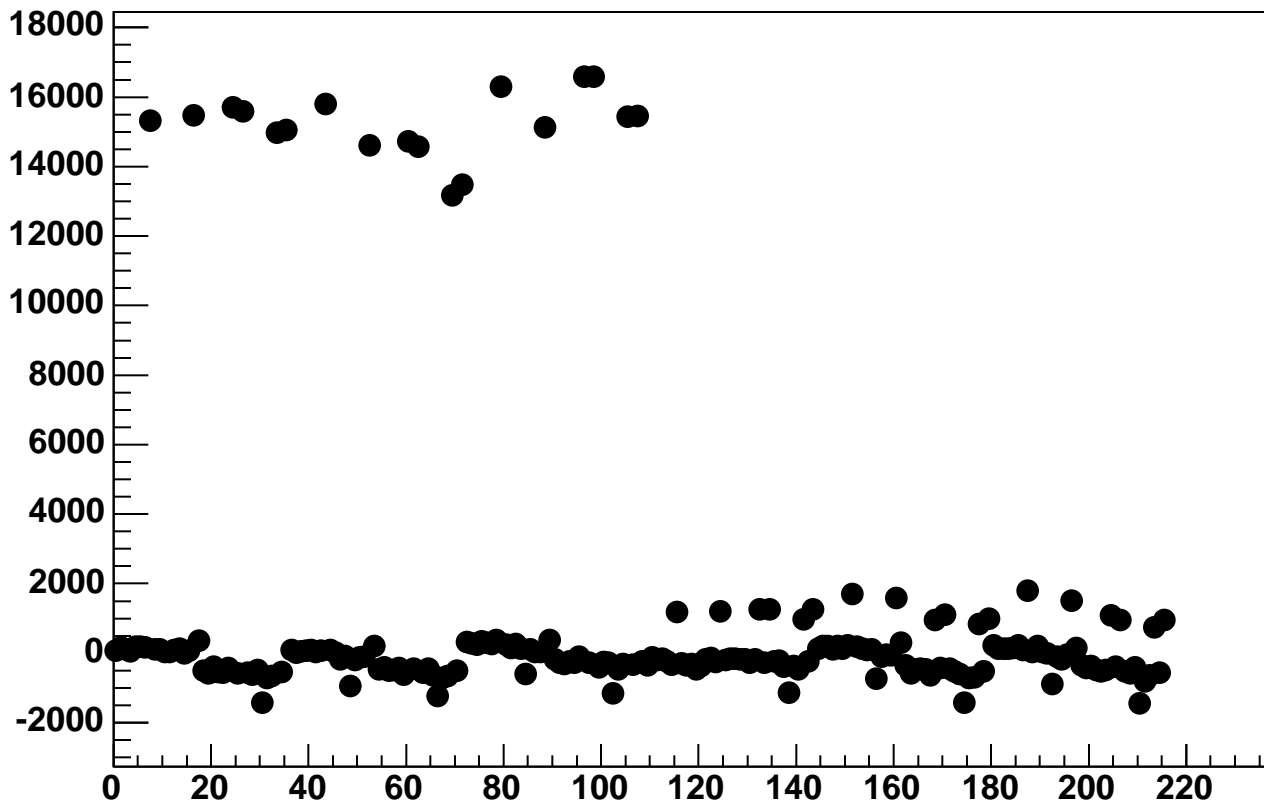
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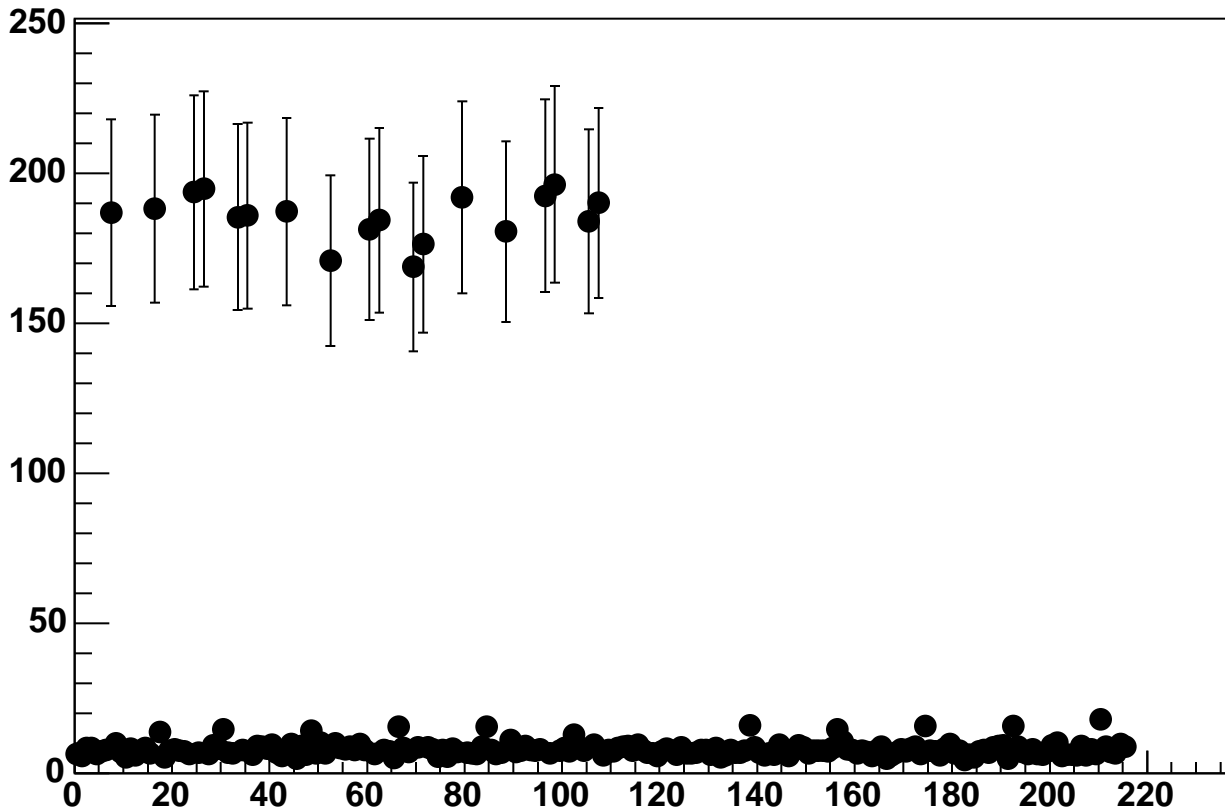
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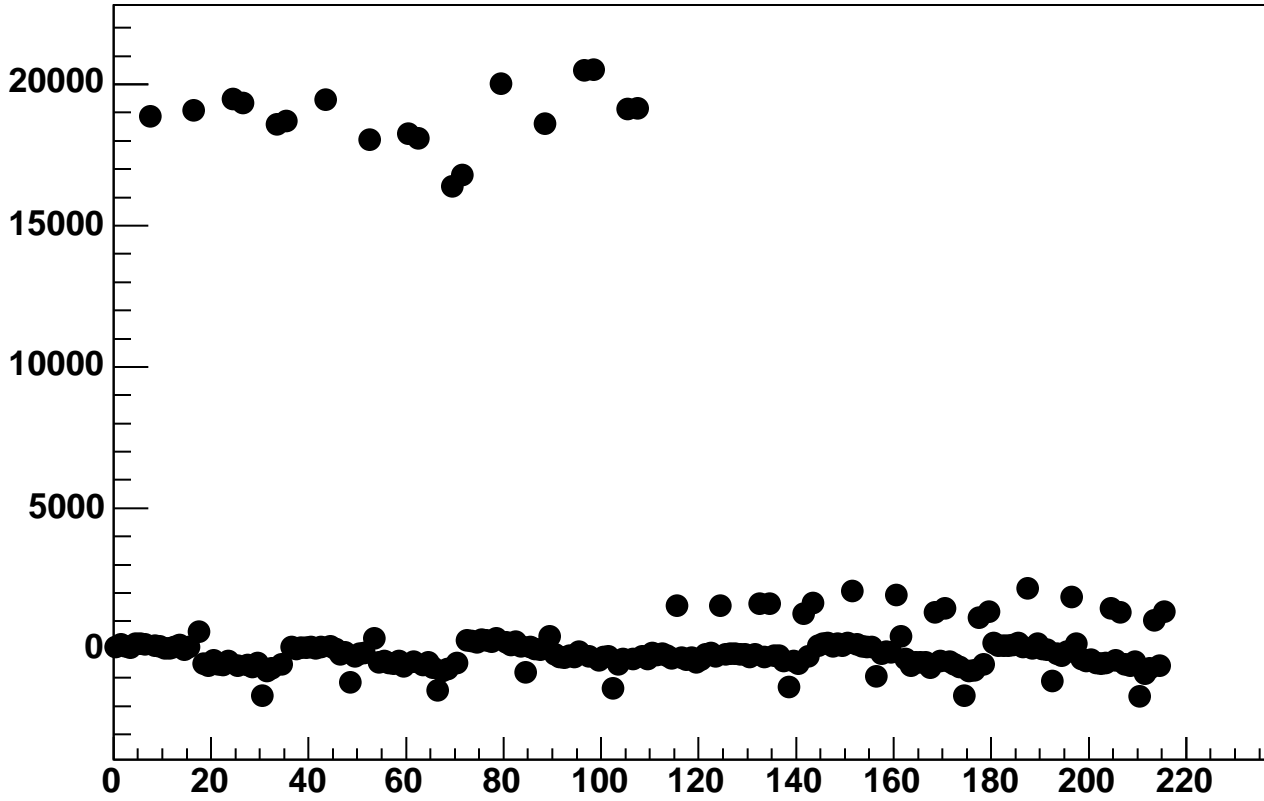
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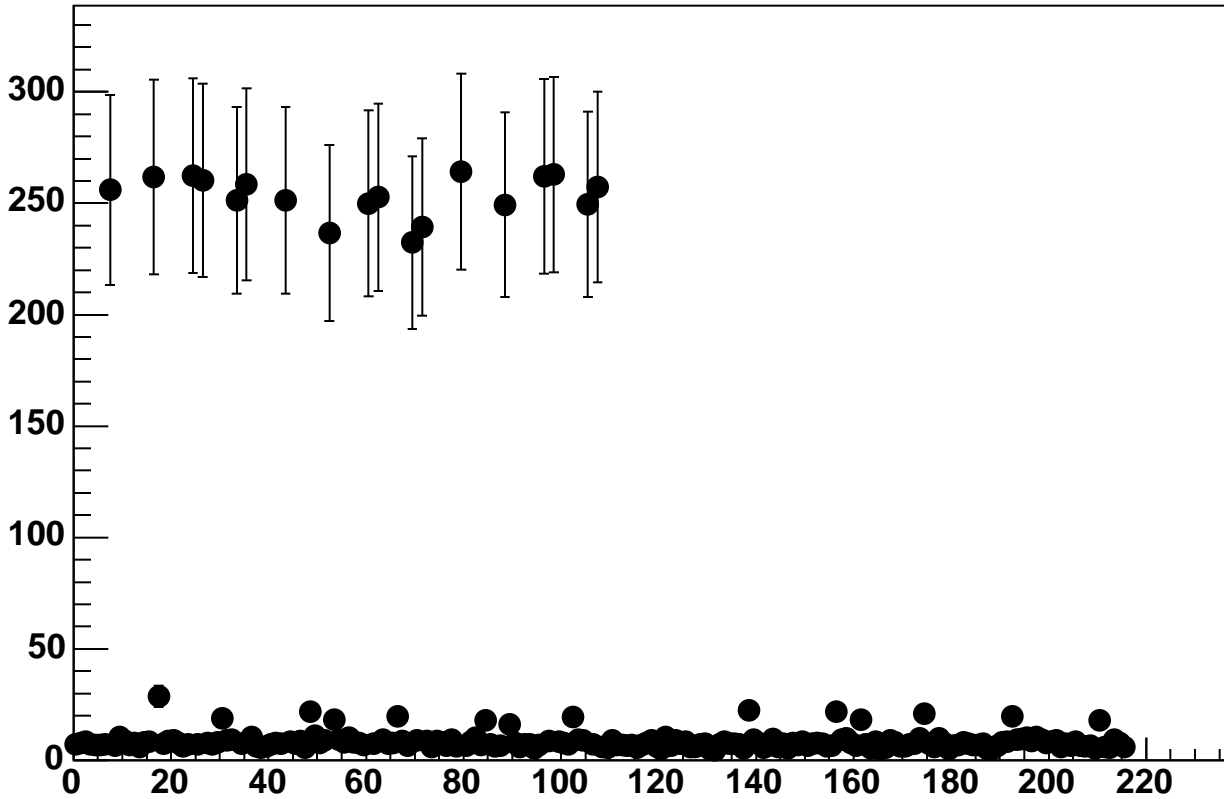
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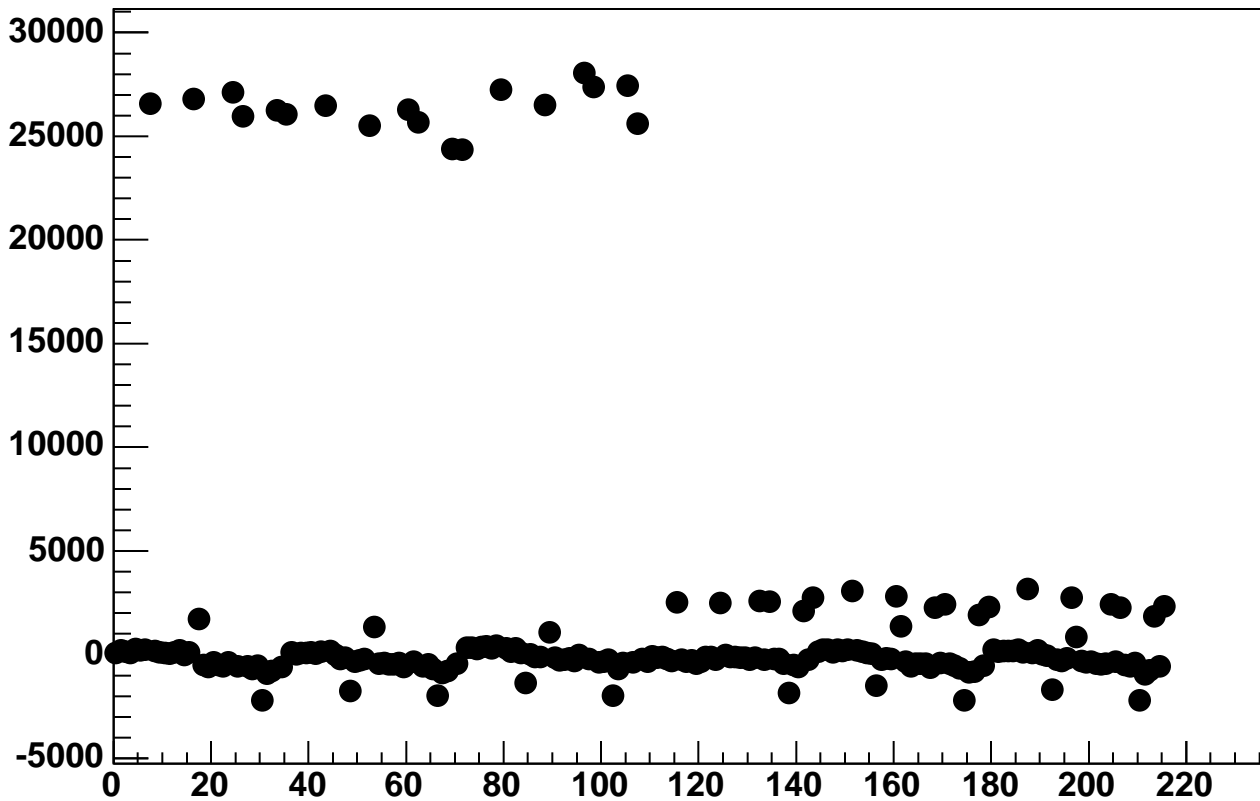
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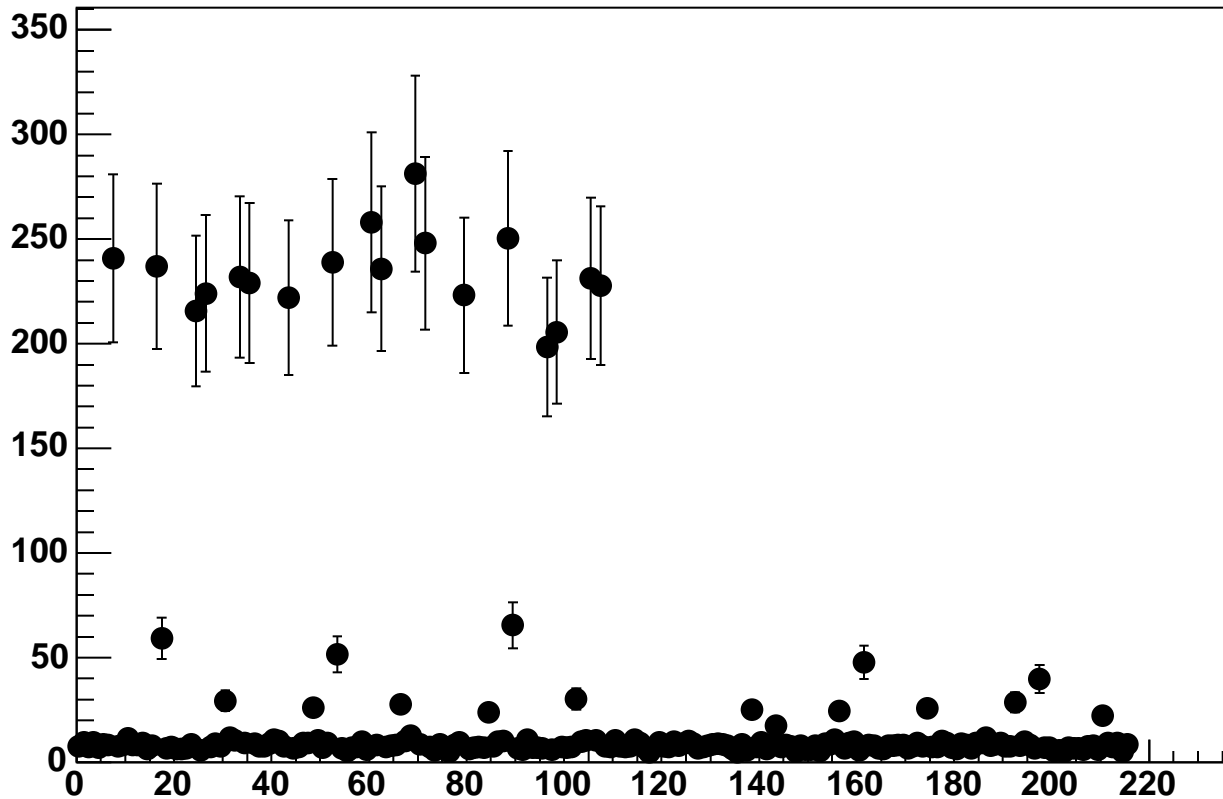
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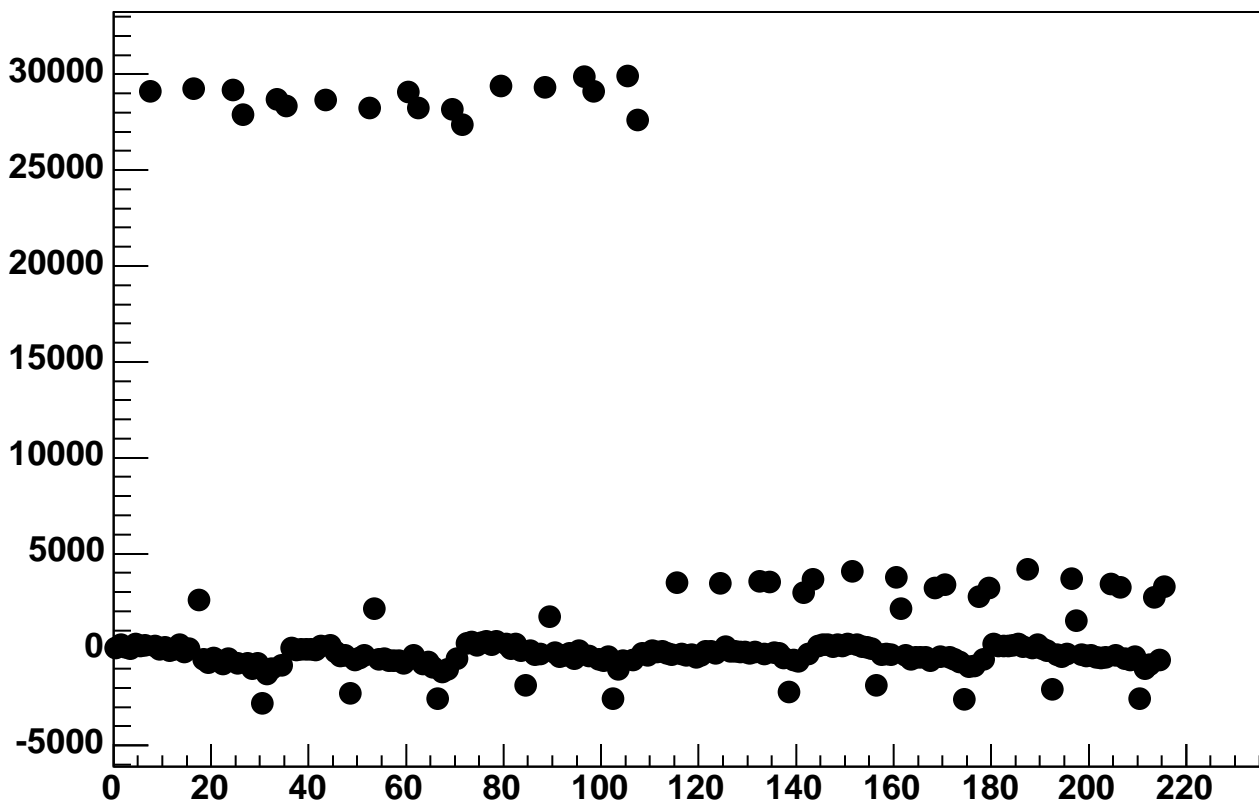
Enable 5, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



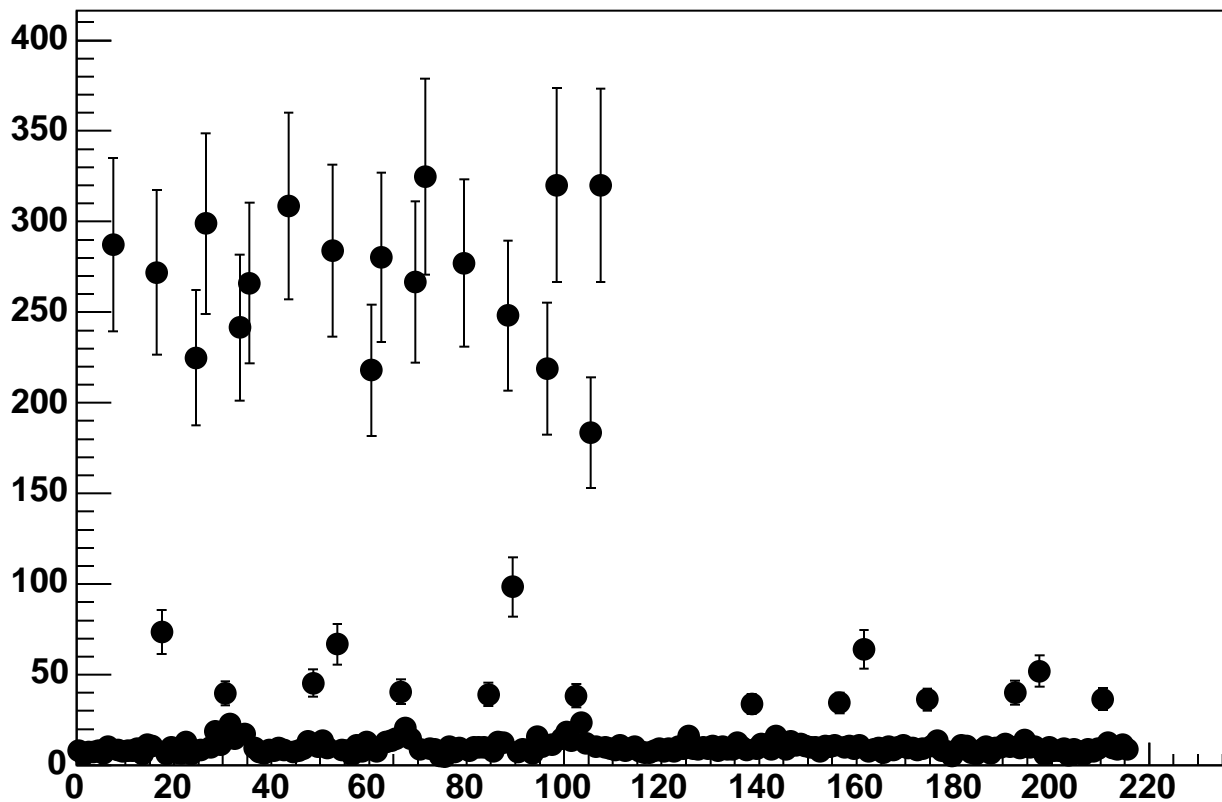
Enable 5, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



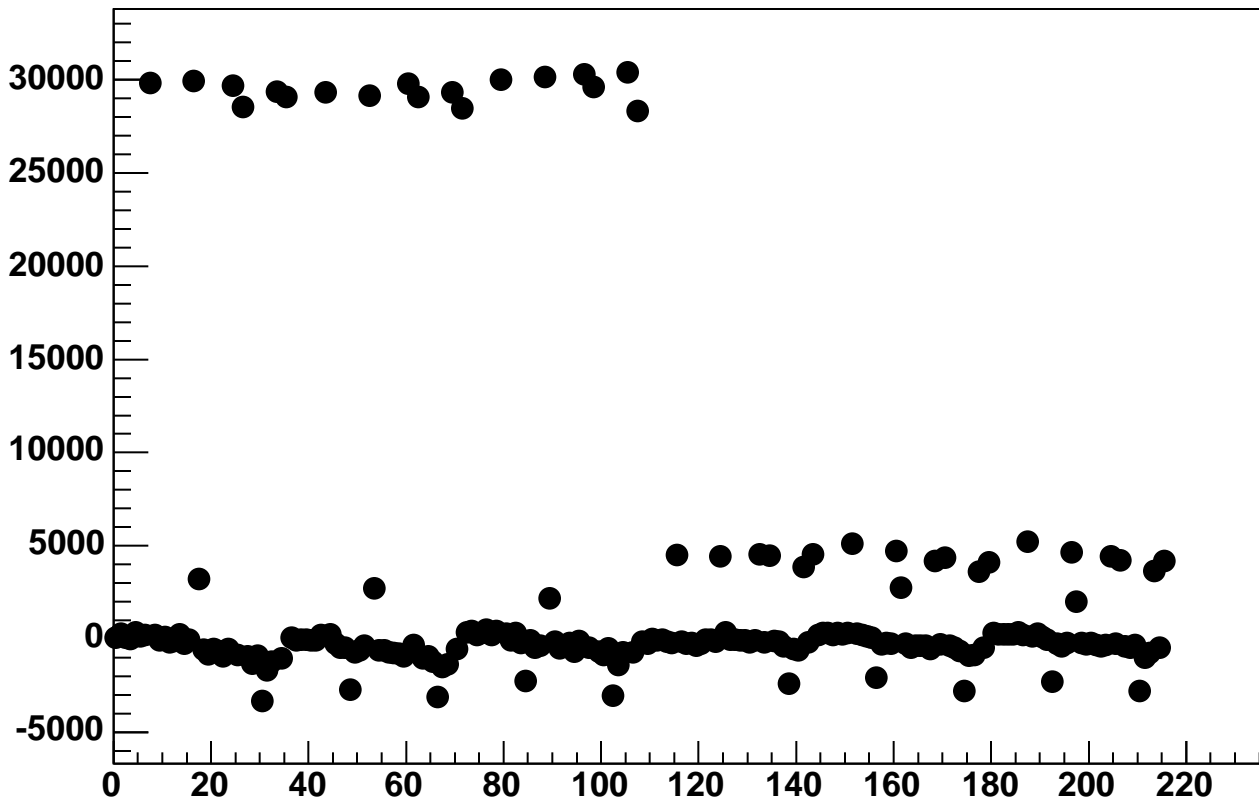
Enable 5, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



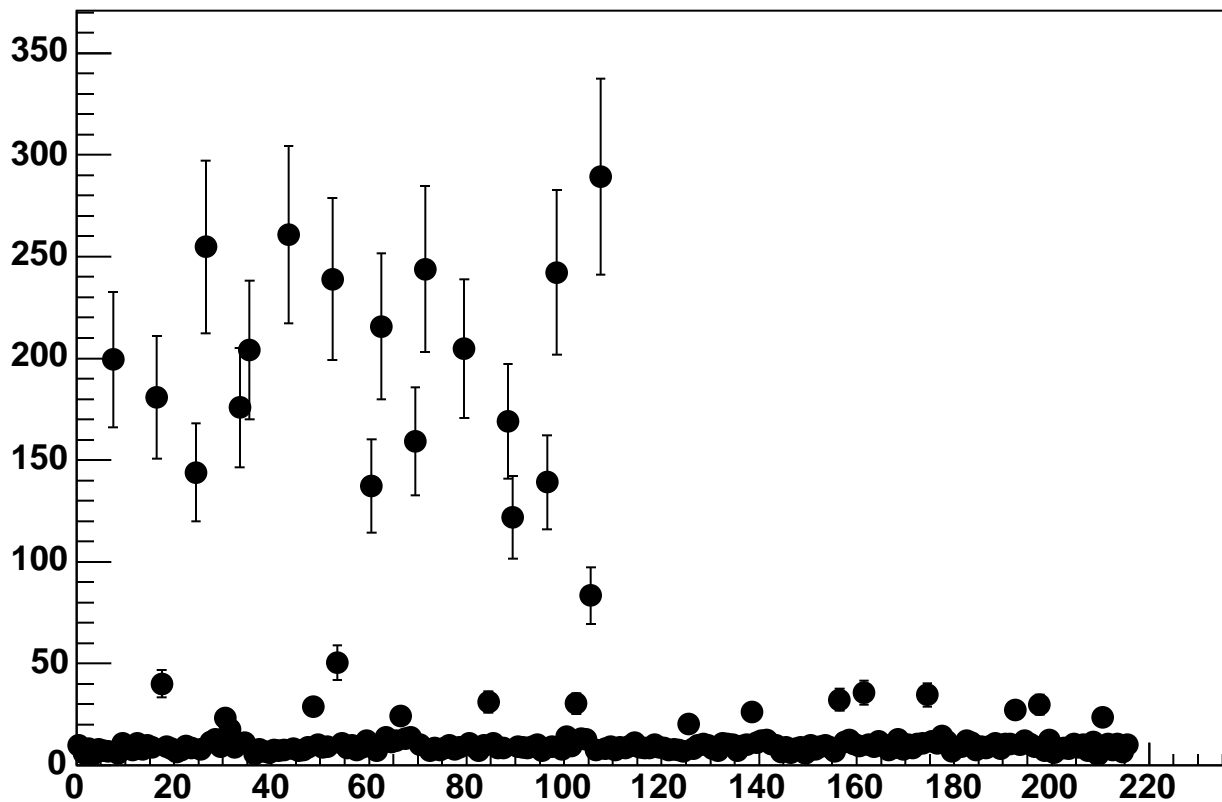
Enable 5, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



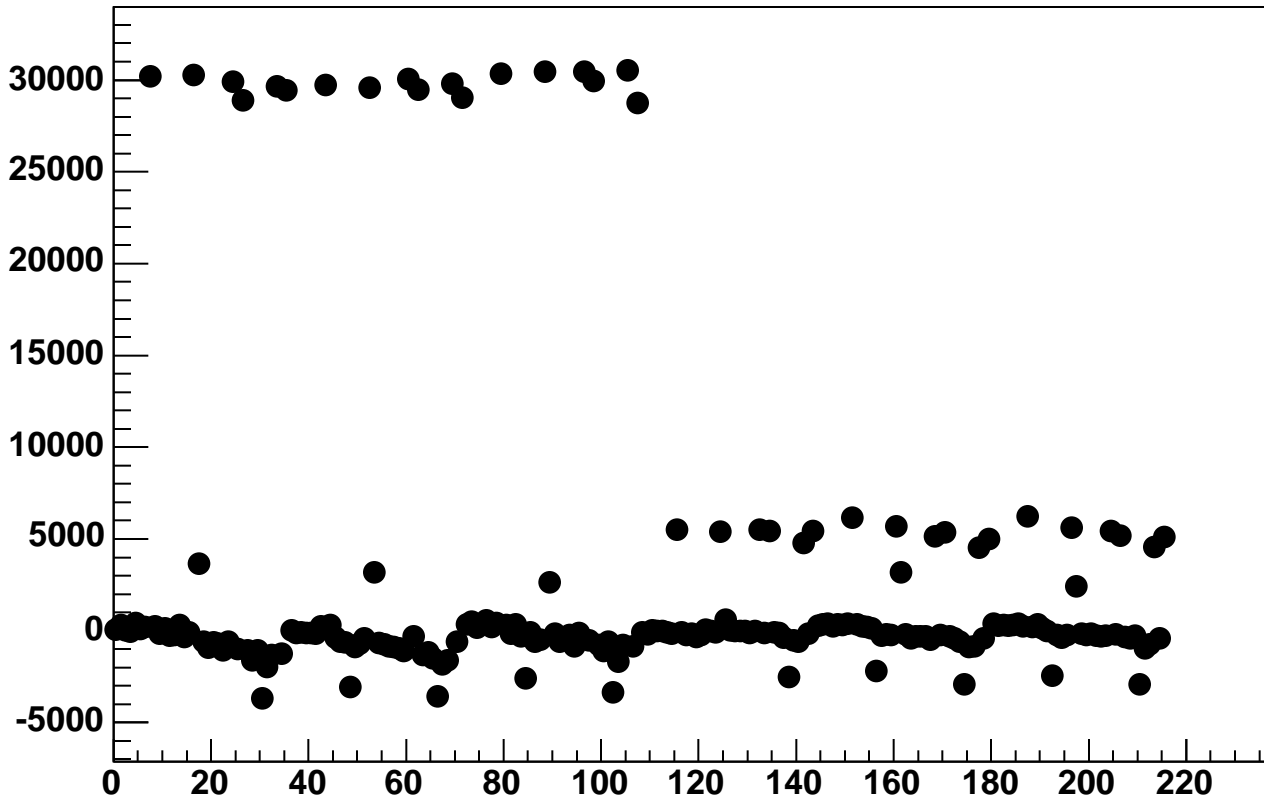
Enable 5, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



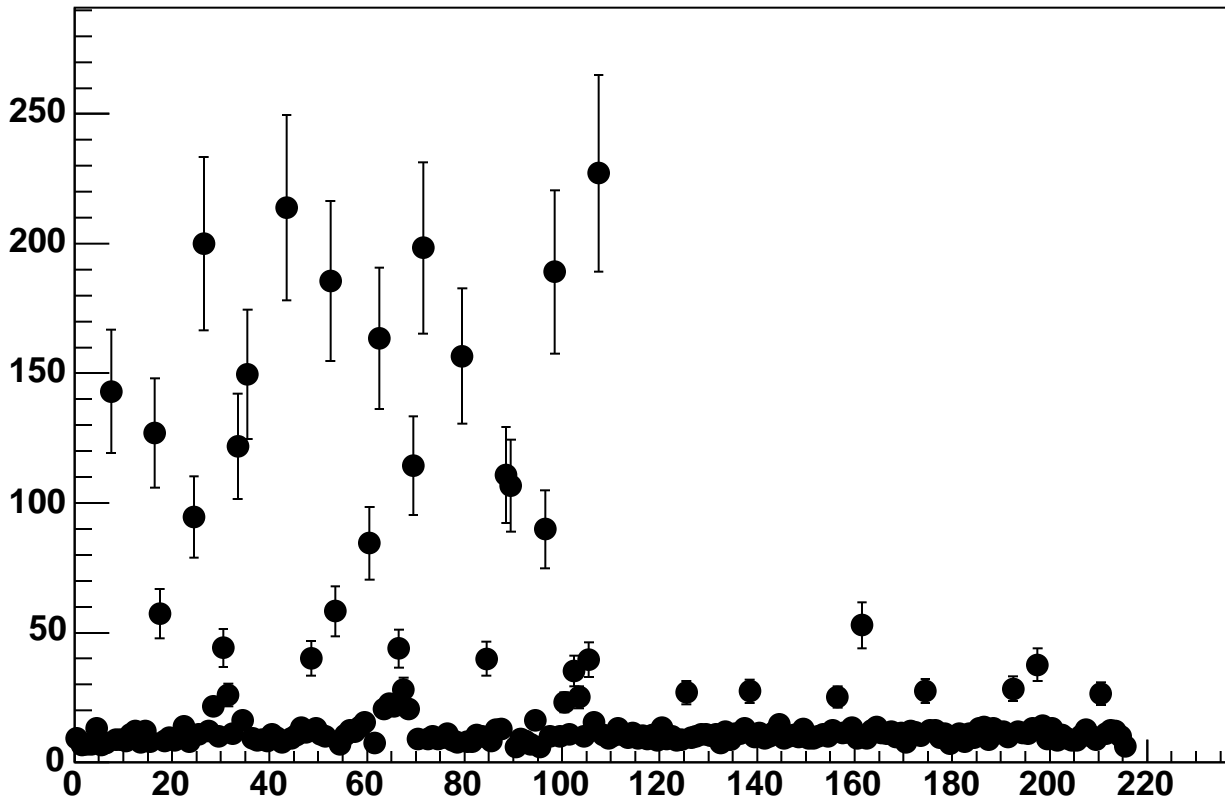
Enable 5, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



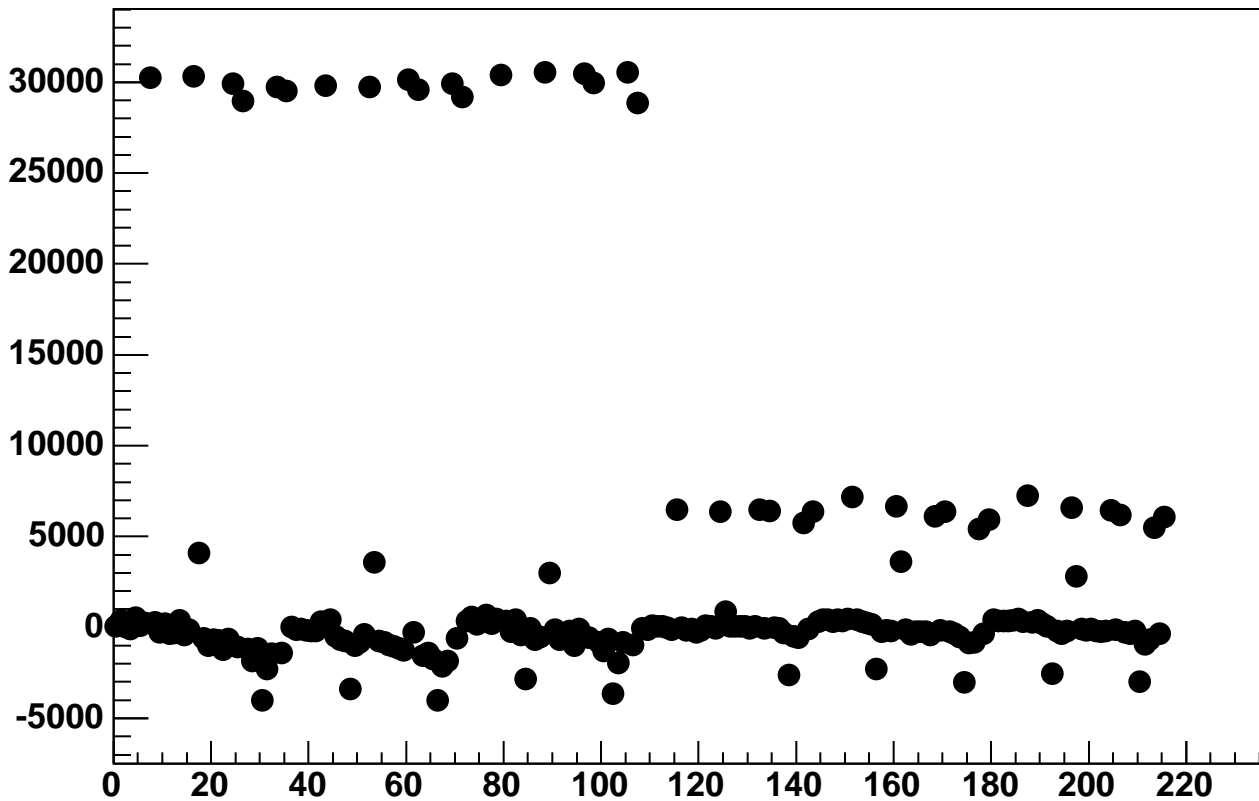
Enable 5, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



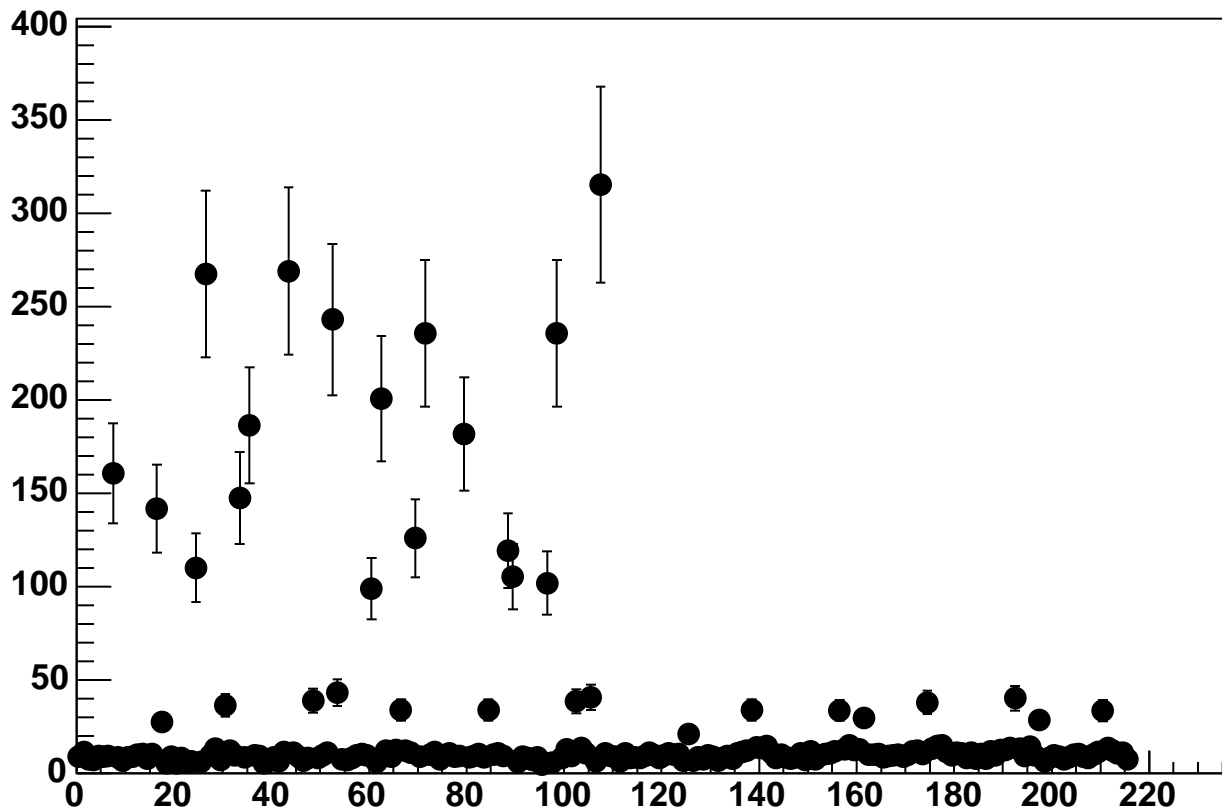
Enable 5, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



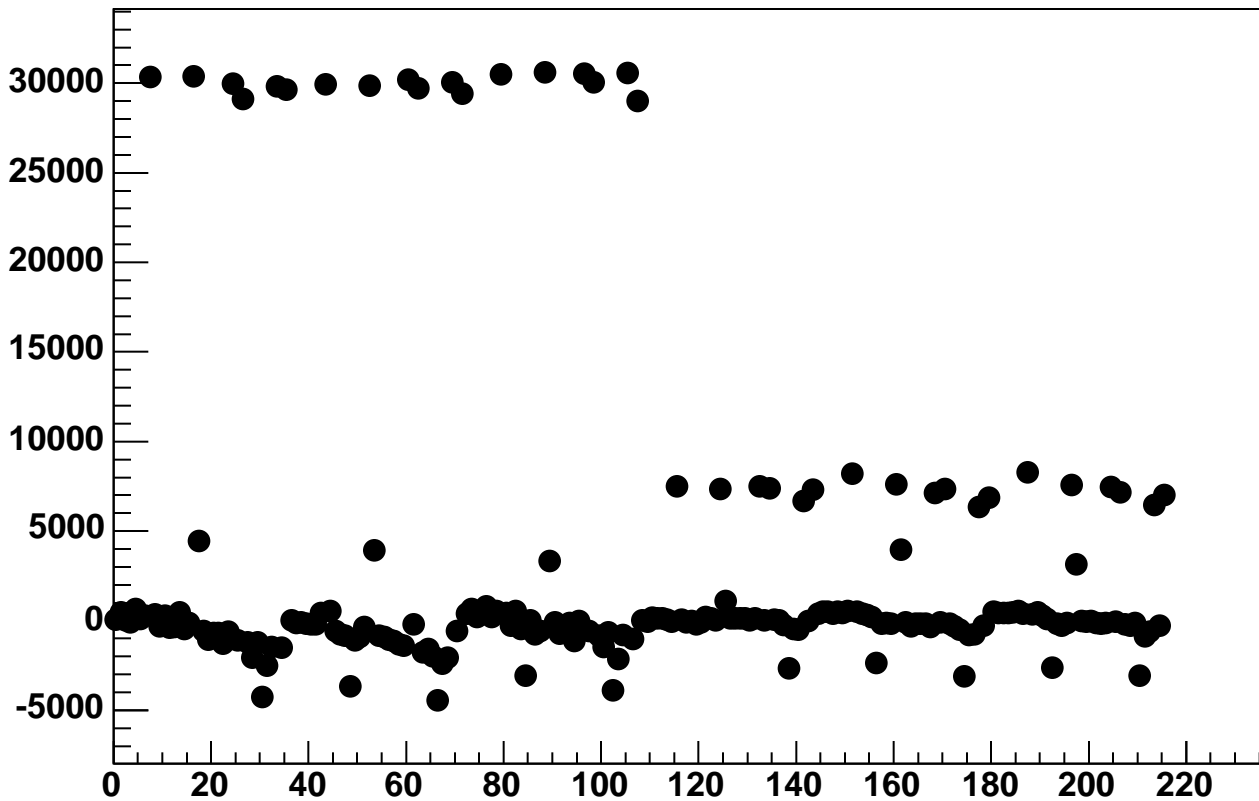
Enable 5, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



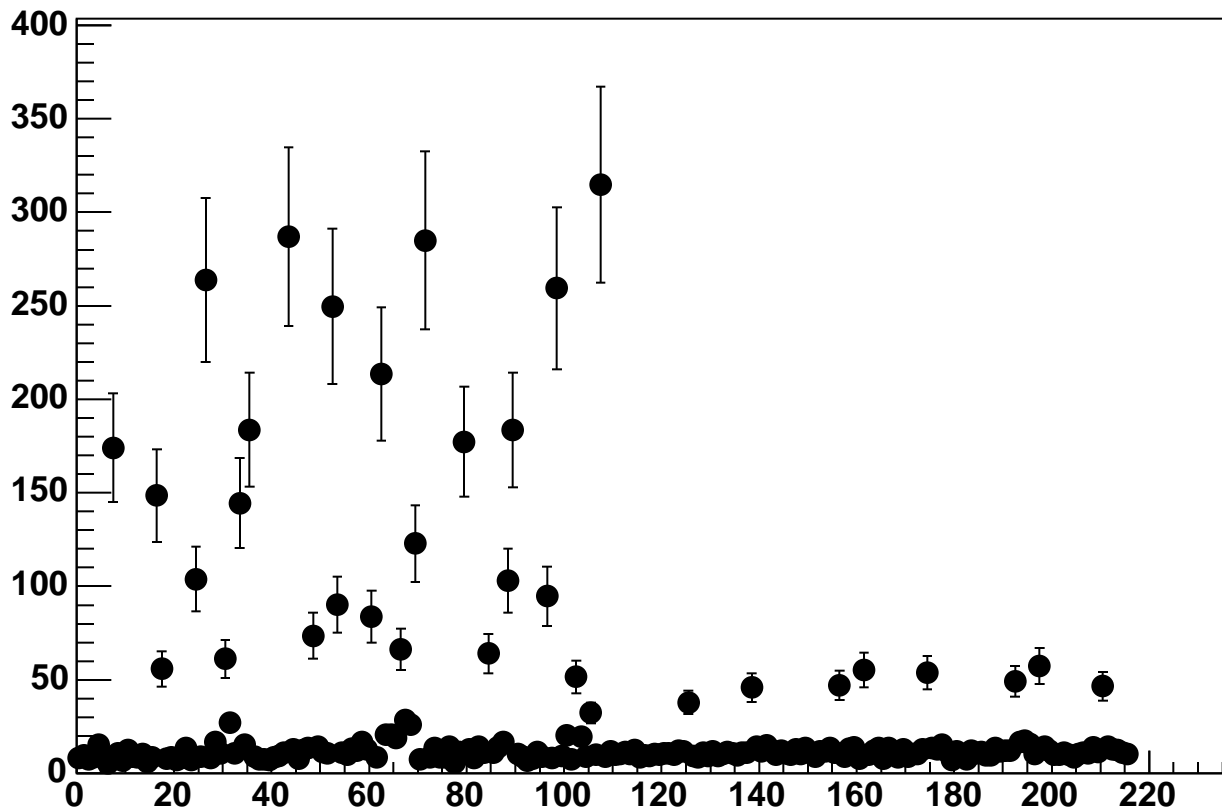
Enable 5, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



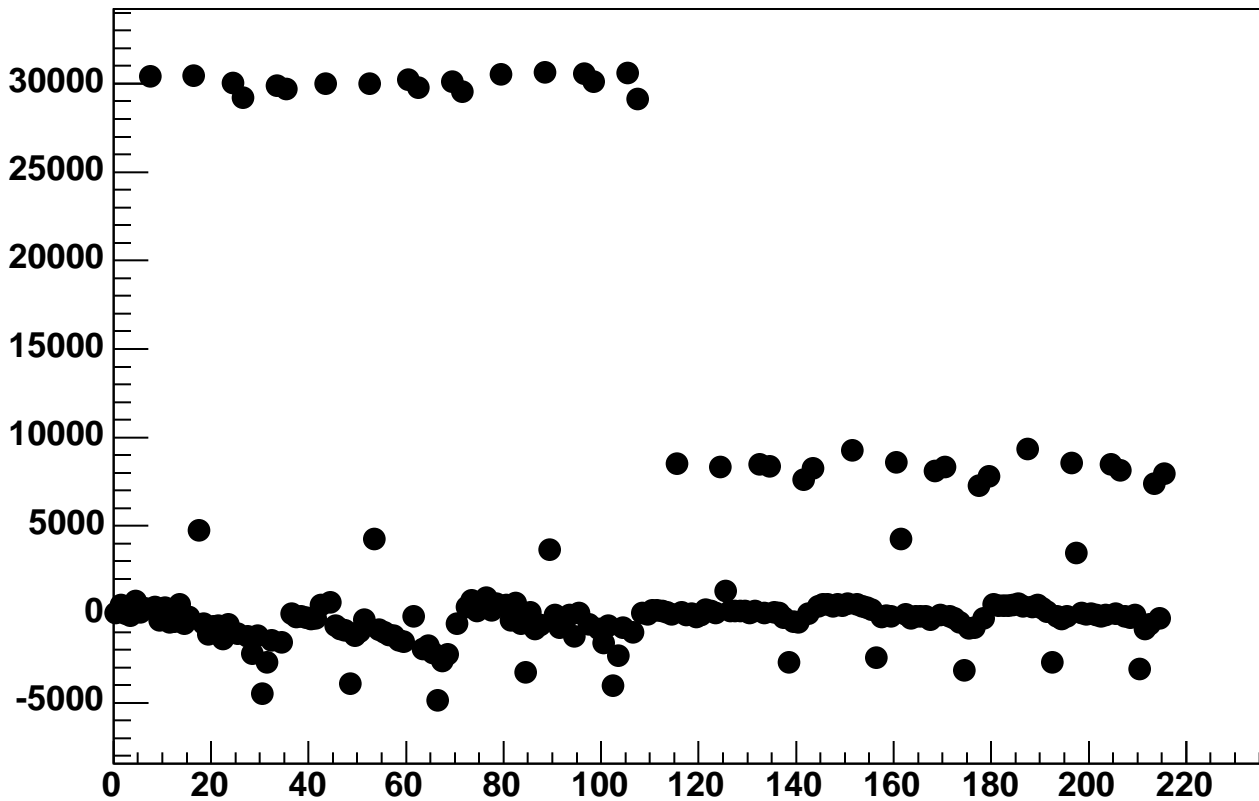
Enable 5, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



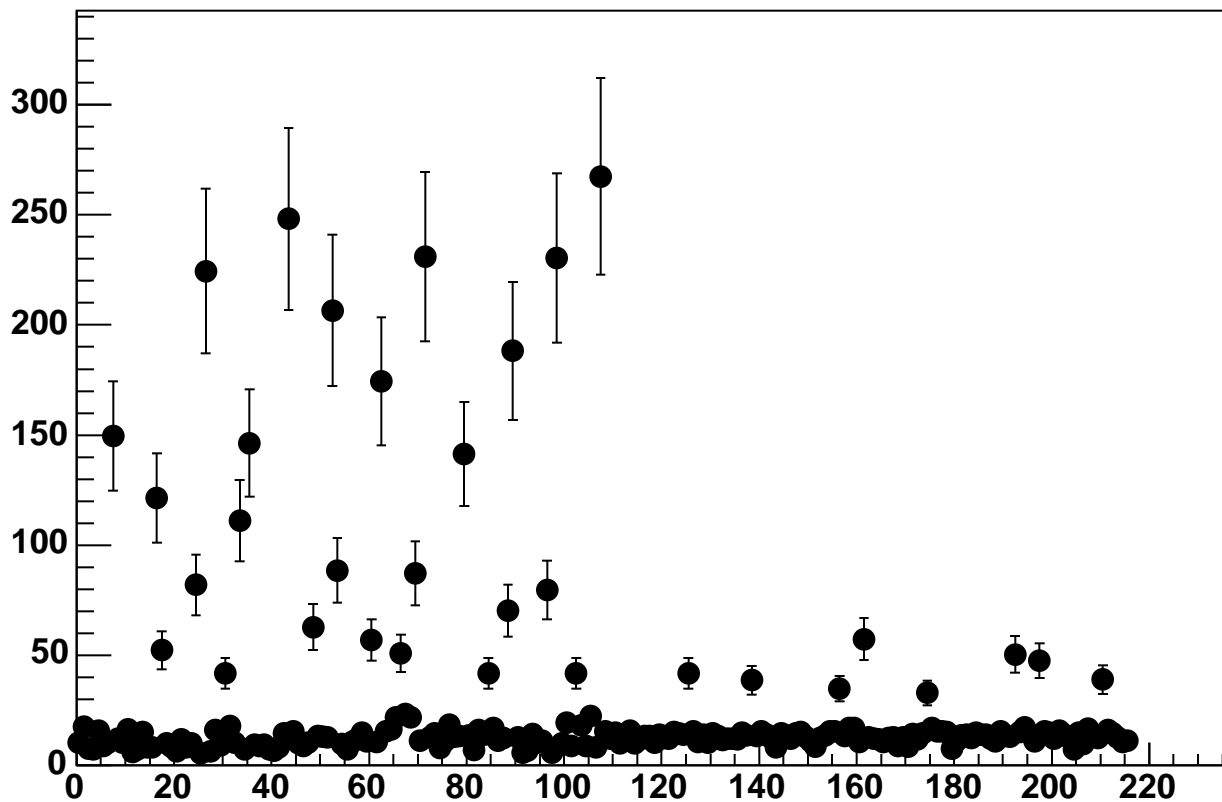
Enable 5, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



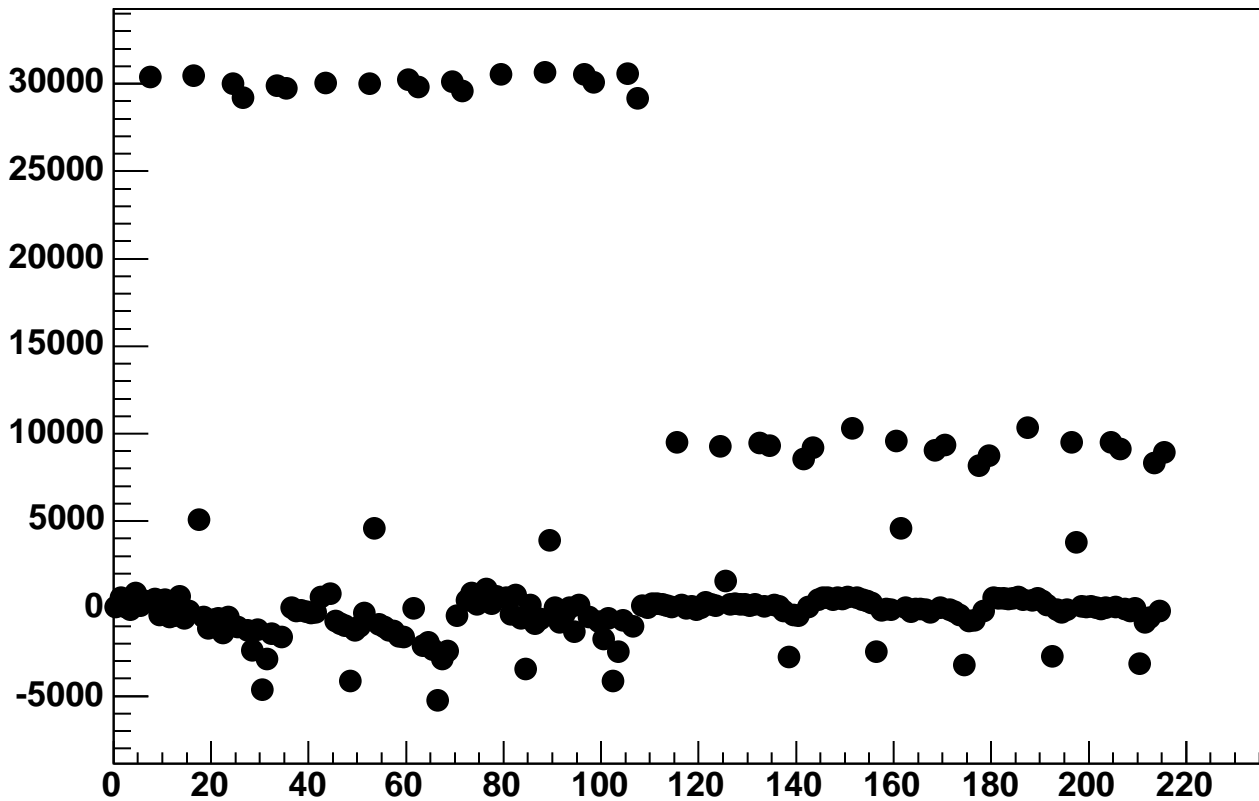
Enable 5, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



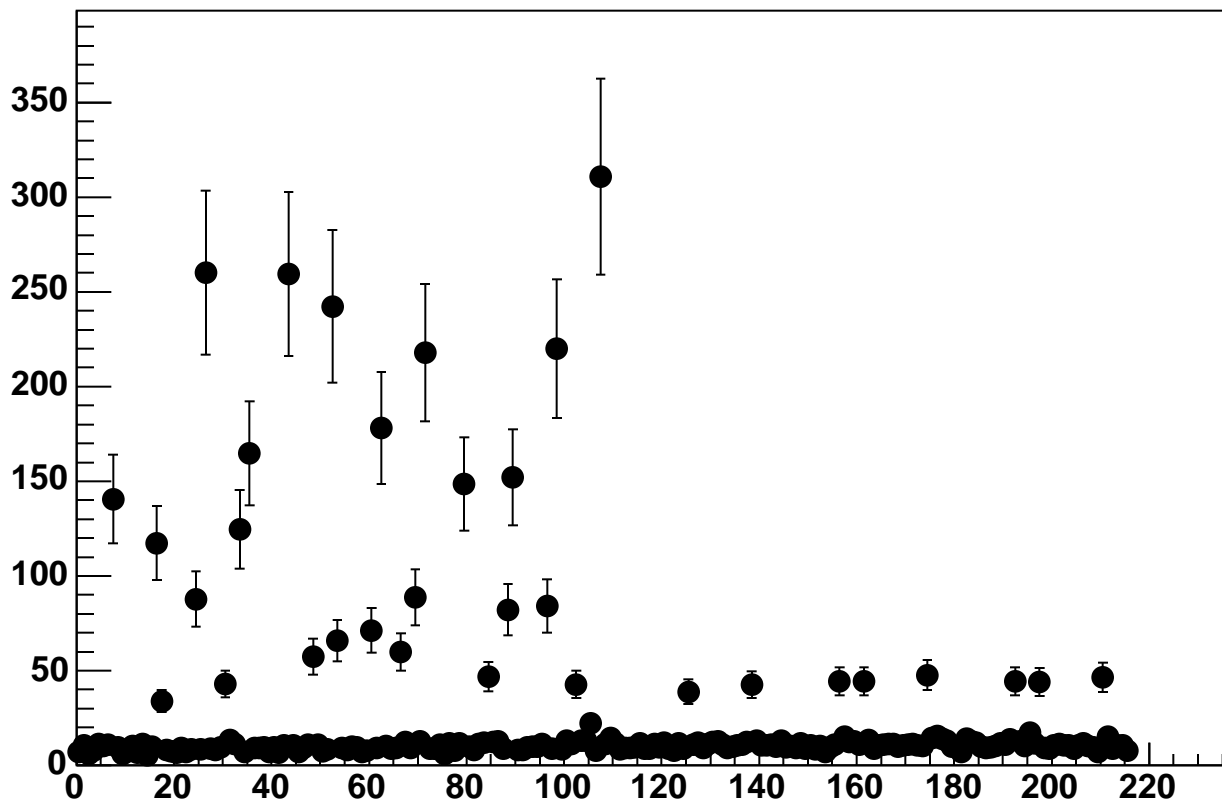
Enable 5, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



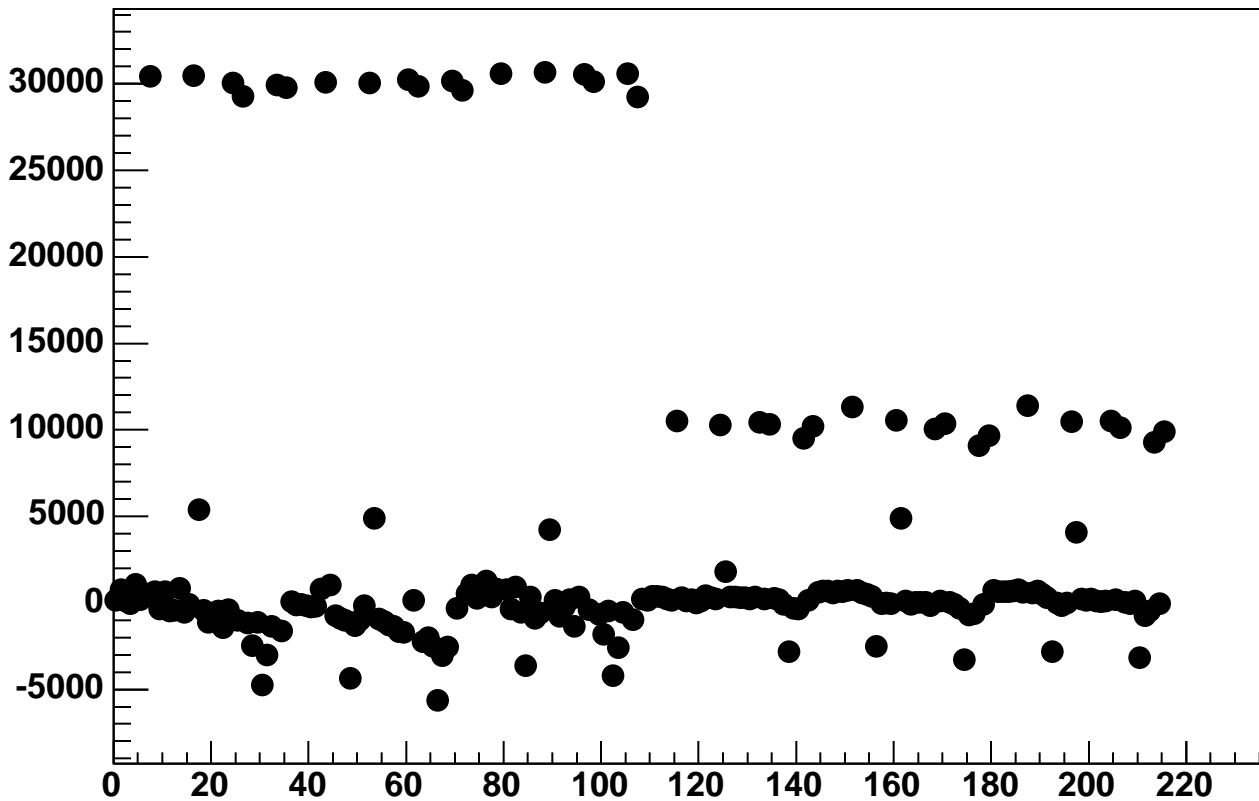
Enable 5, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



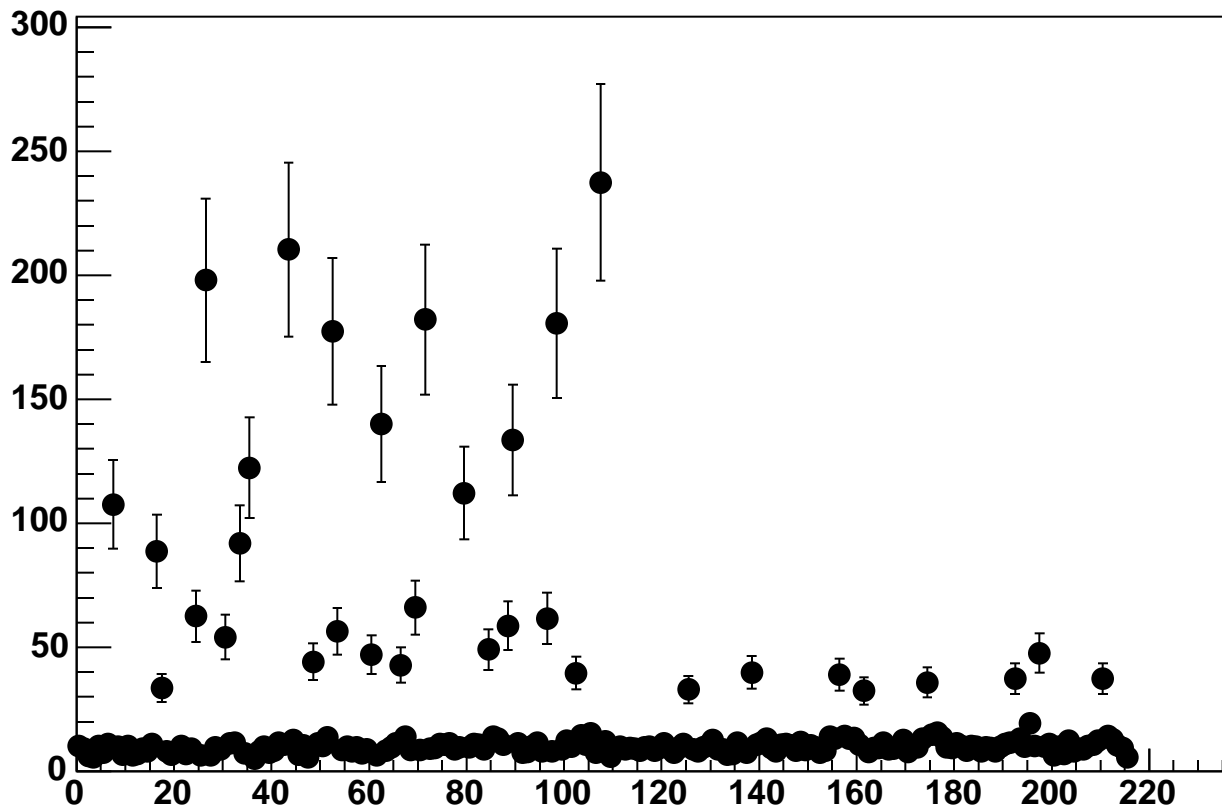
Enable 5, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



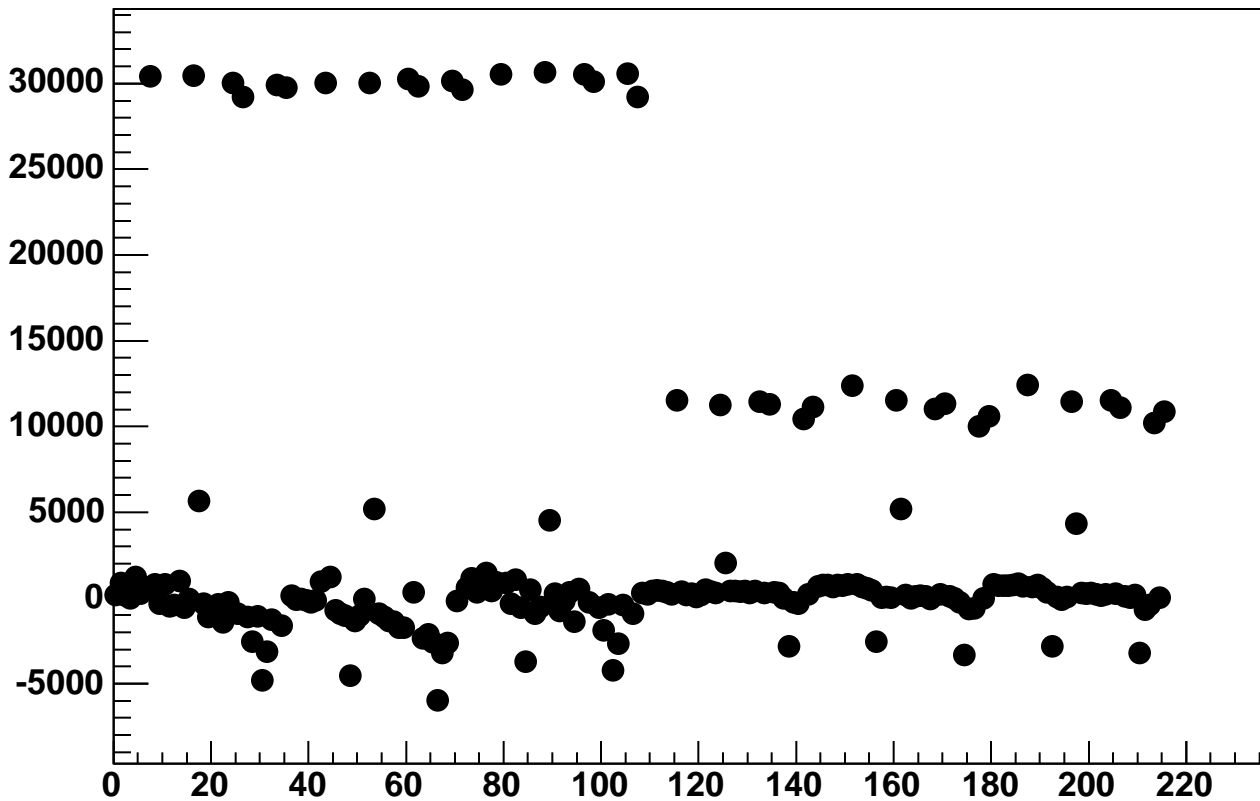
Enable 5, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



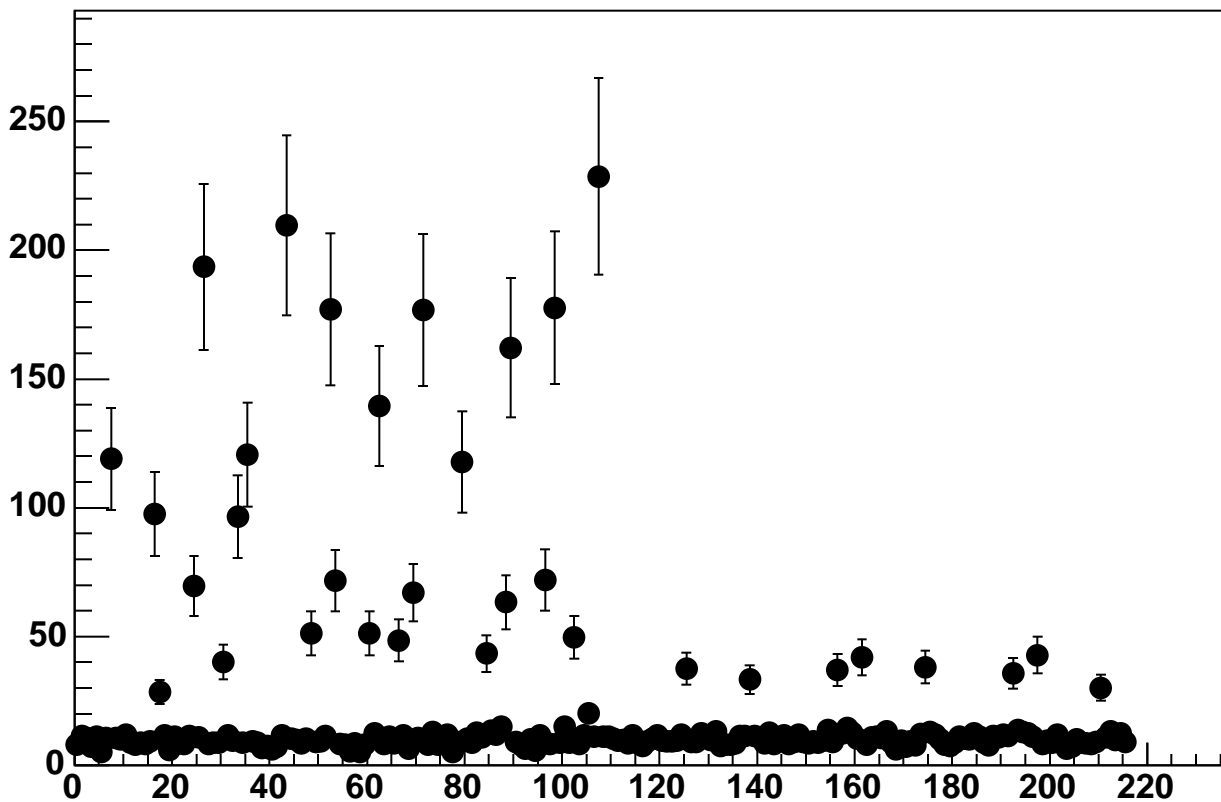
Enable 5, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



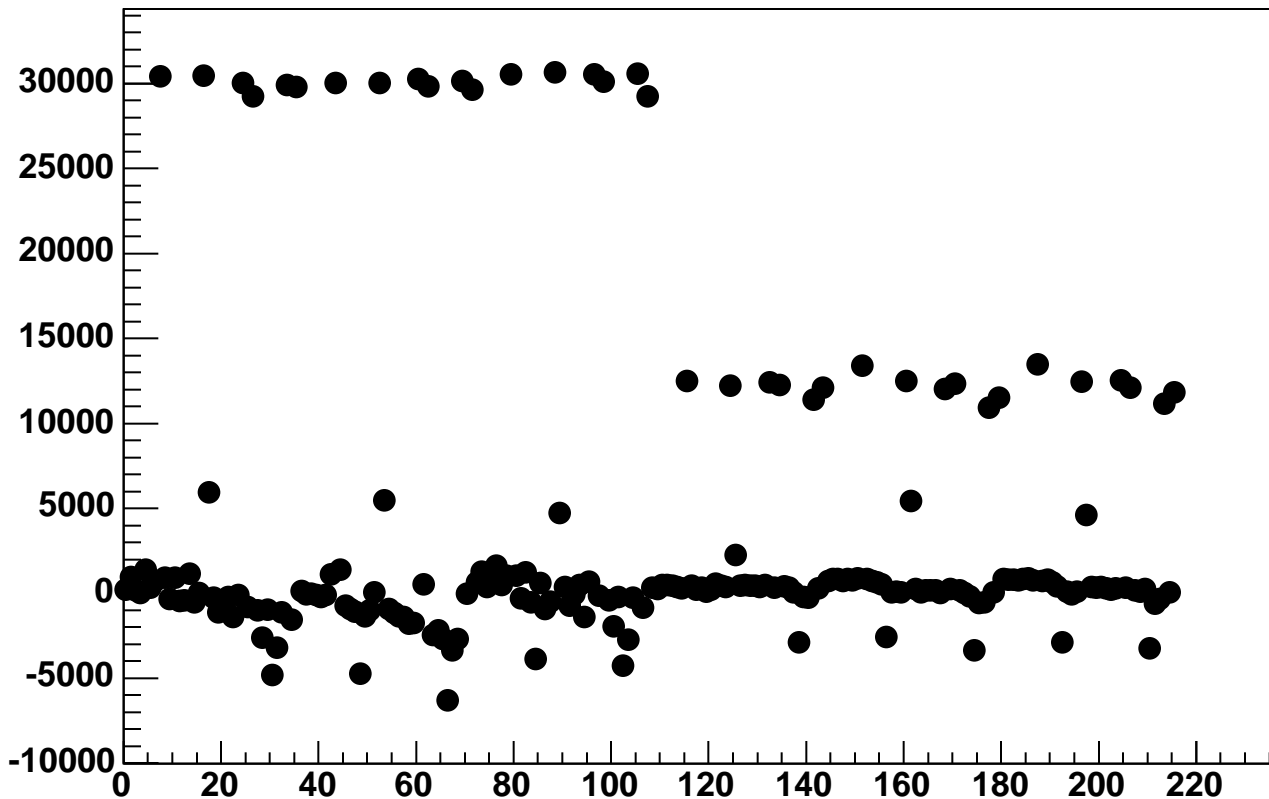
Enable 5, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



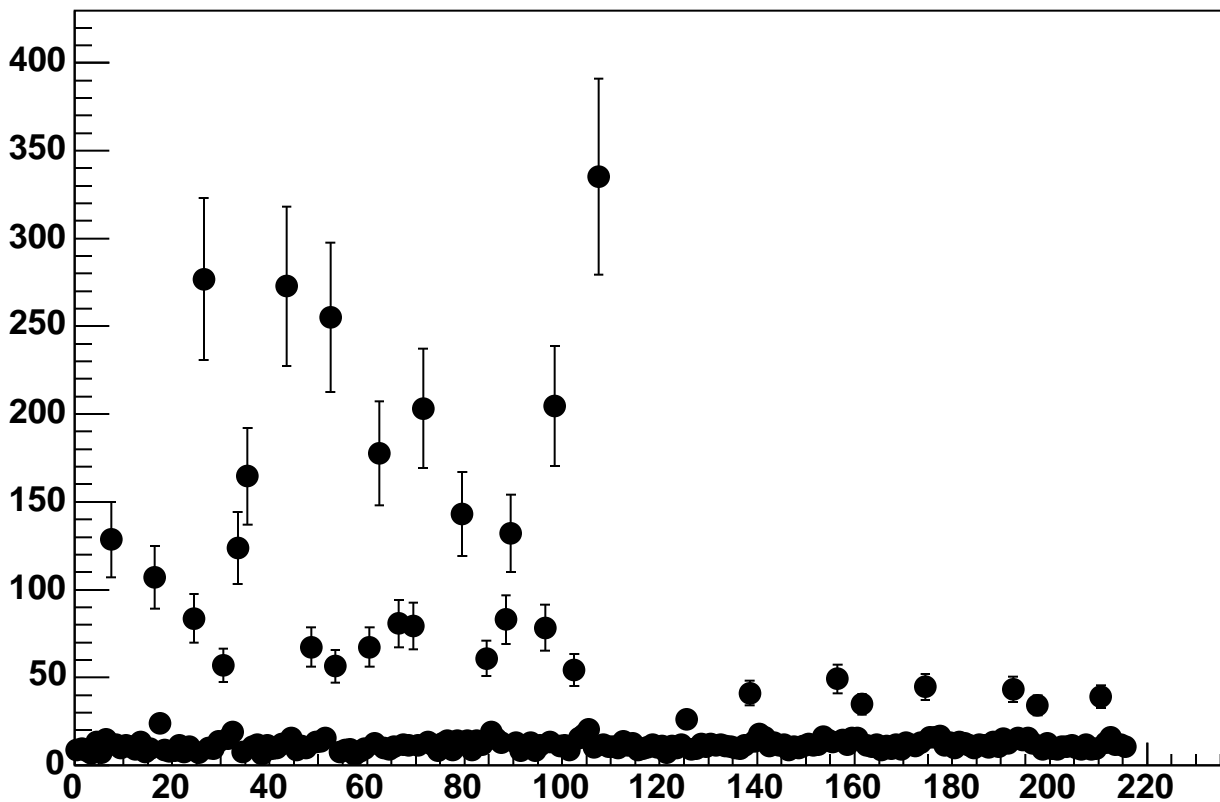
Enable 5, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



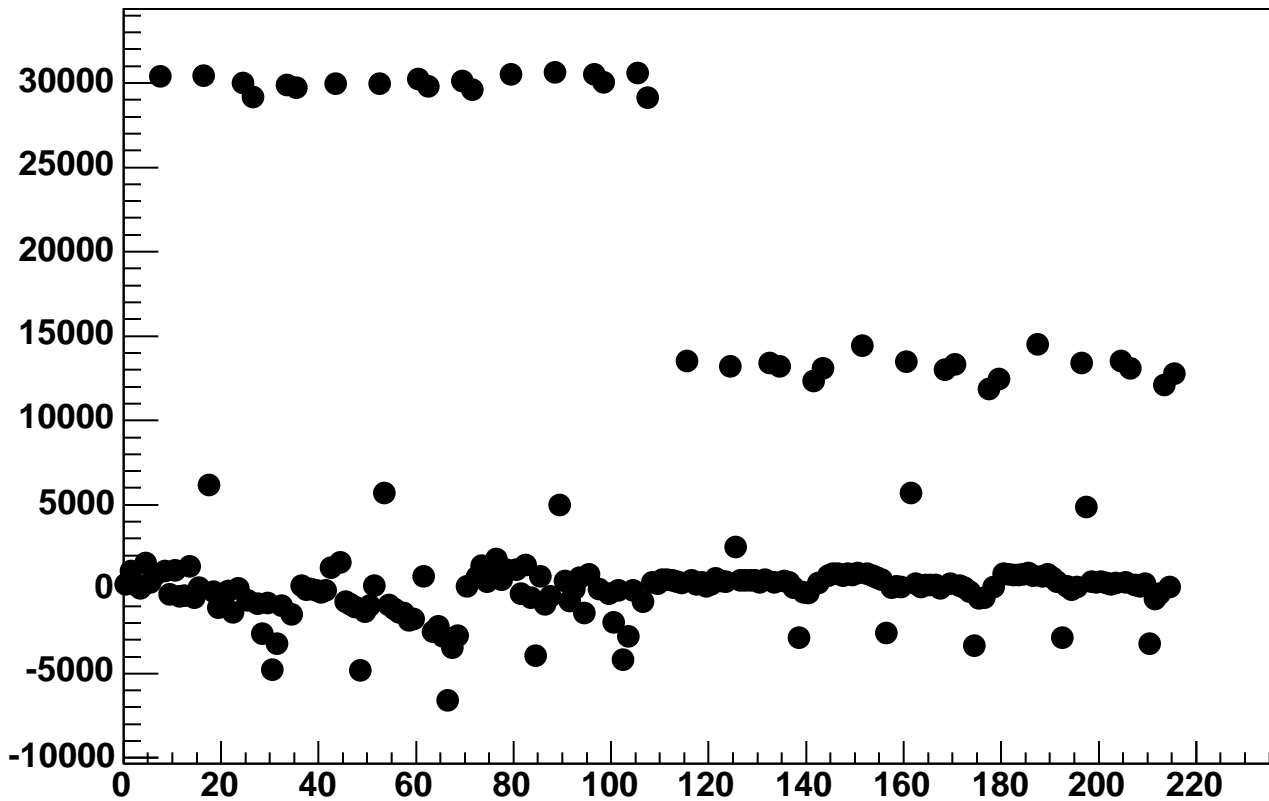
Enable 5, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



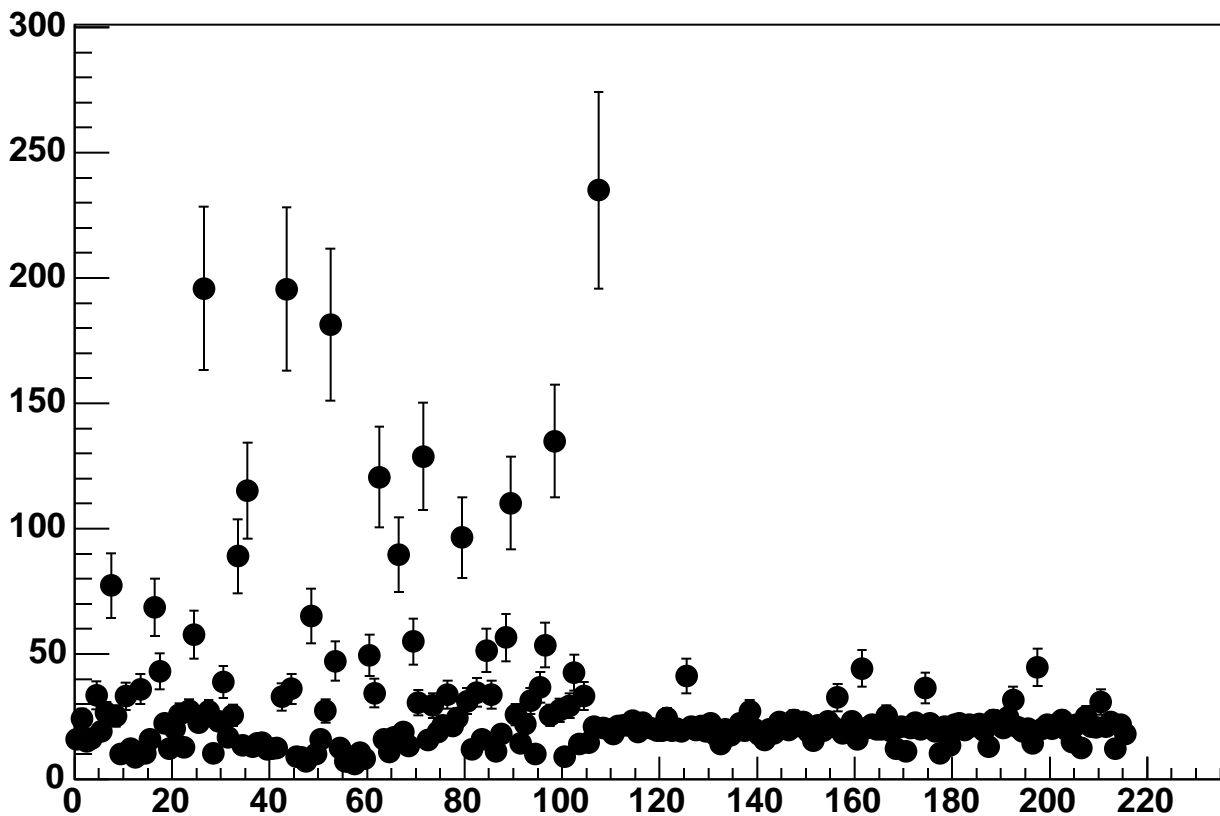
Enable 5, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



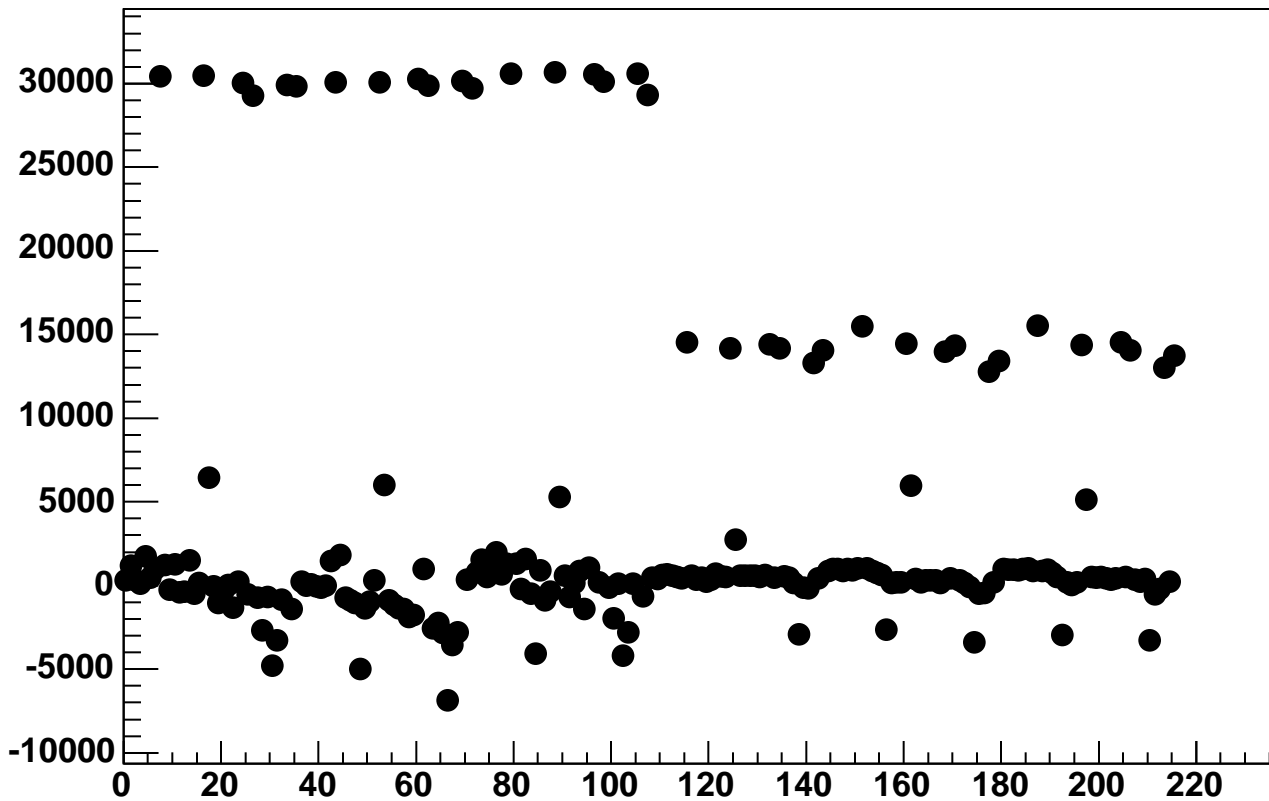
Enable 5, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



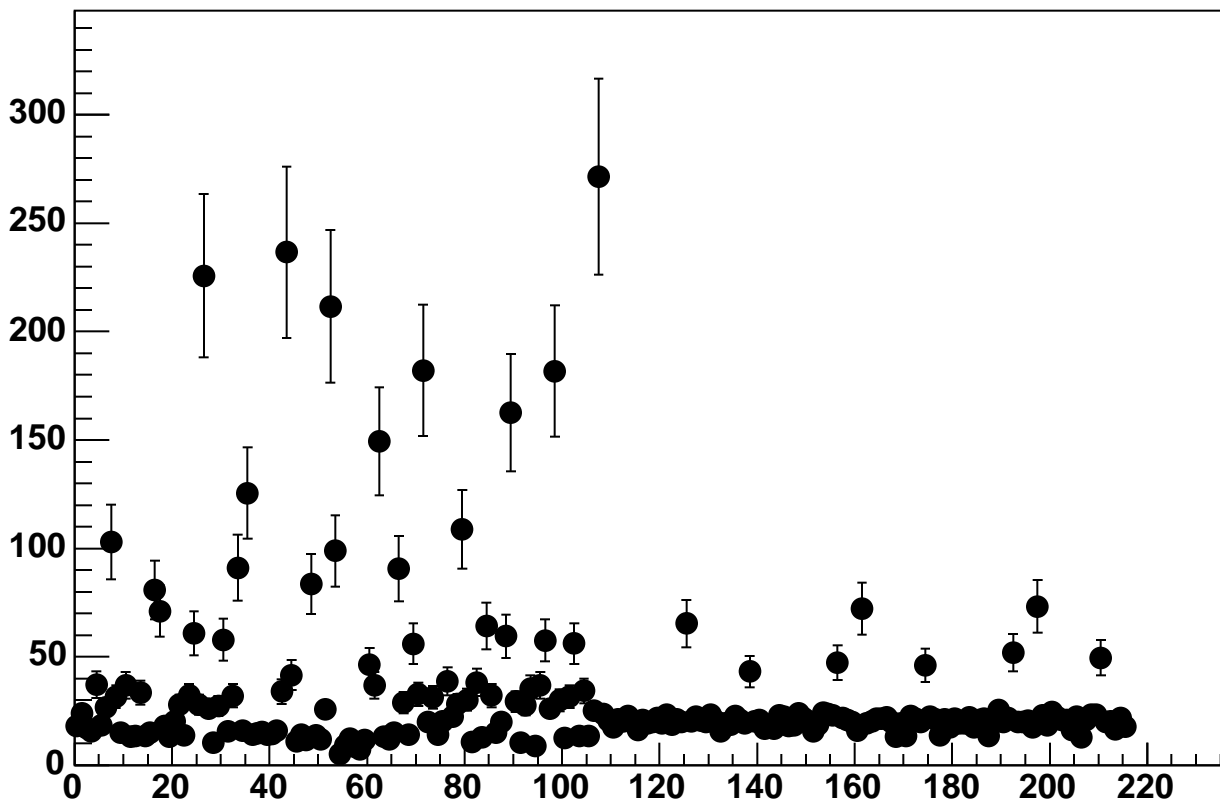
Enable 5, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



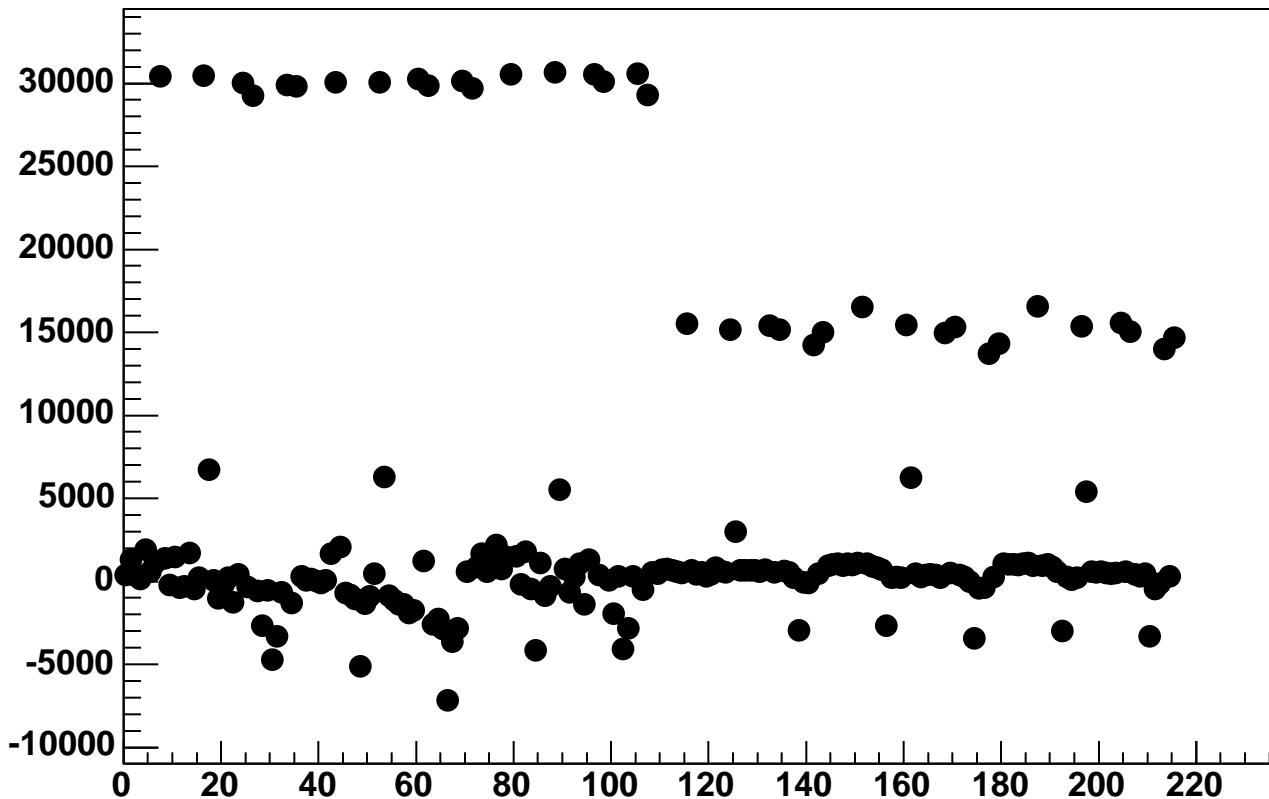
Enable 5, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



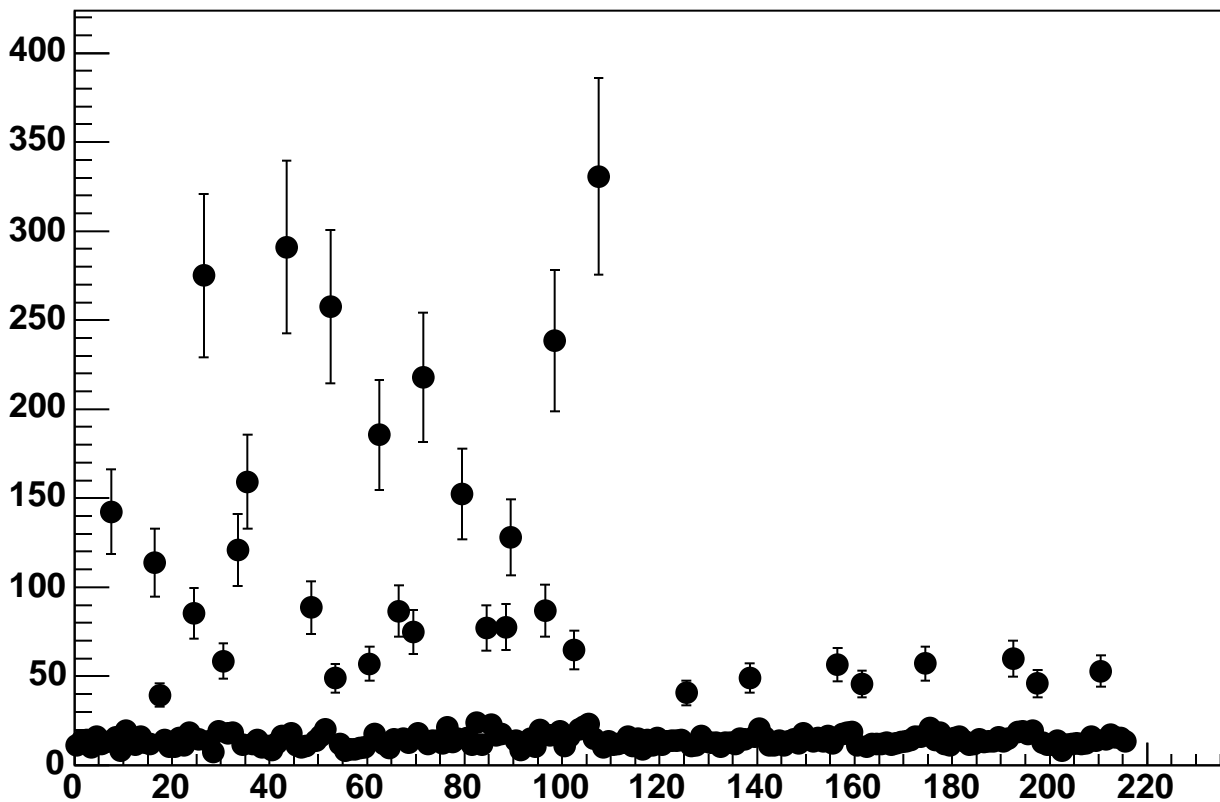
Enable 5, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



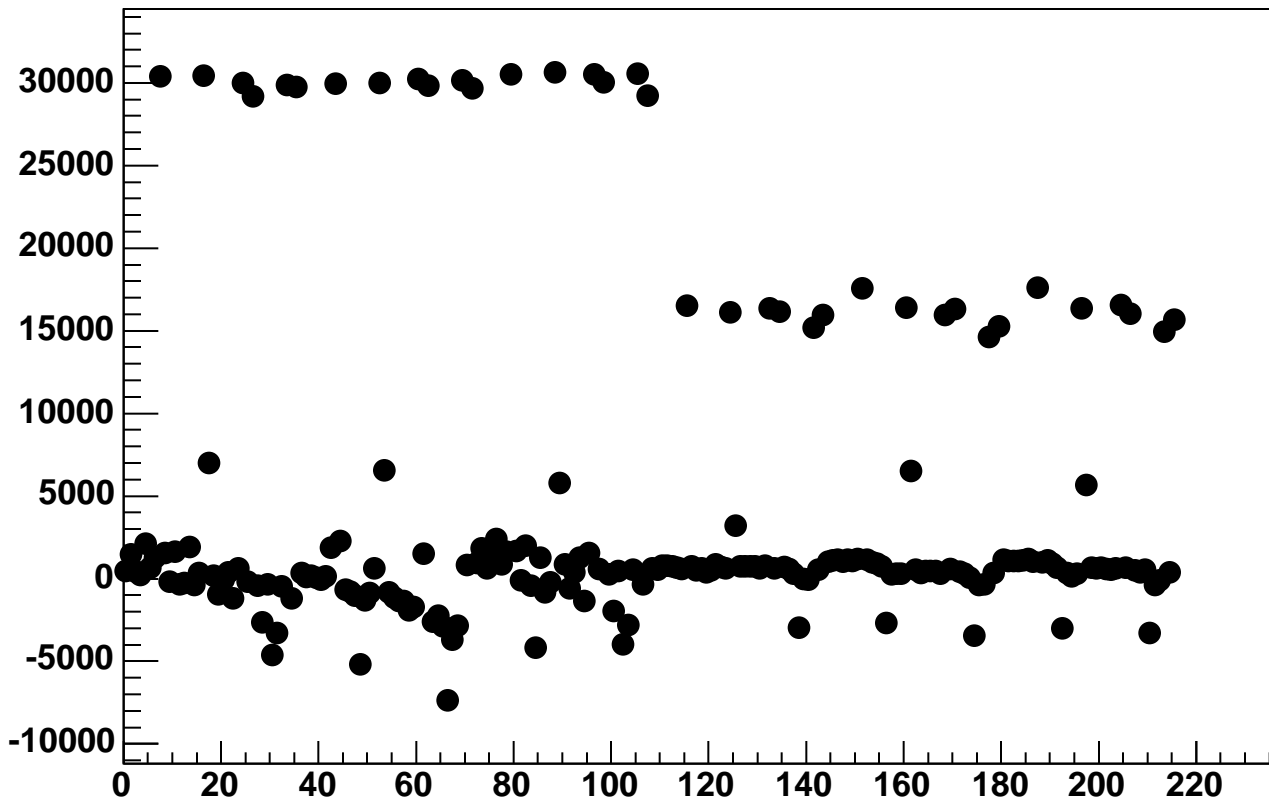
Enable 5, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



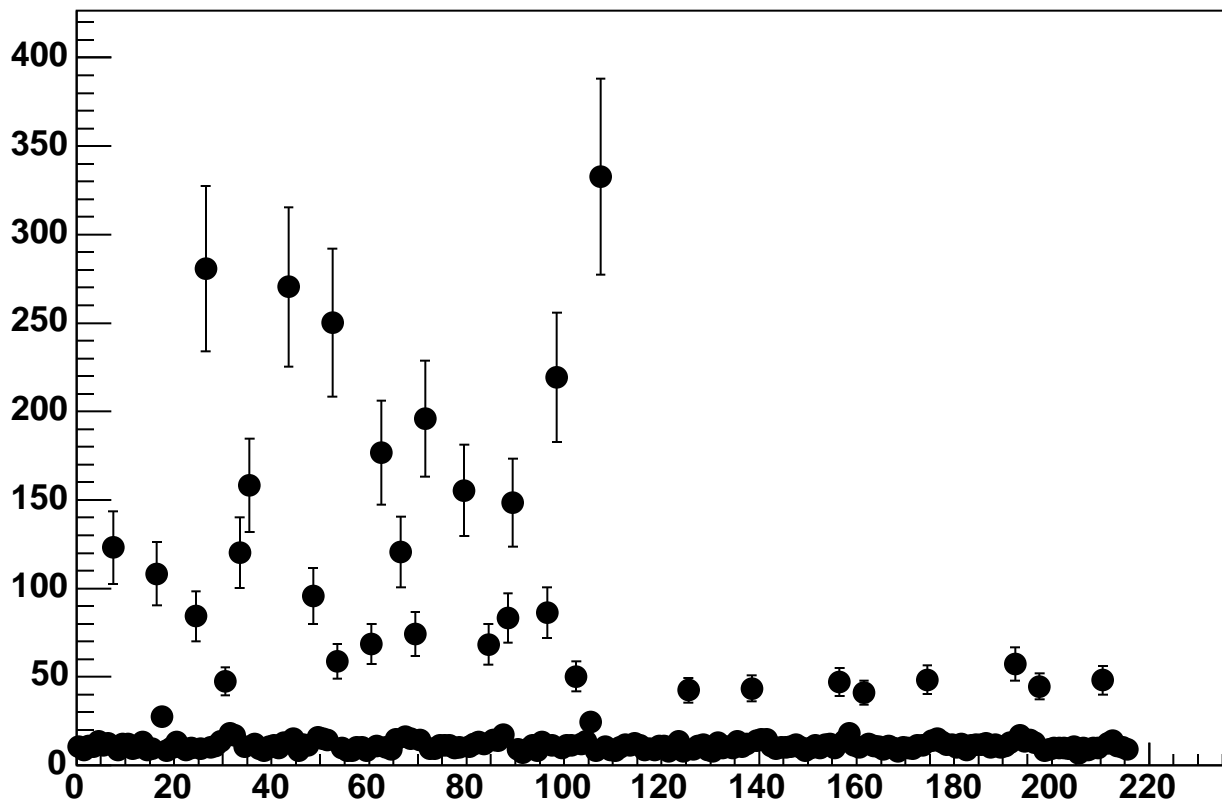
Enable 5, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



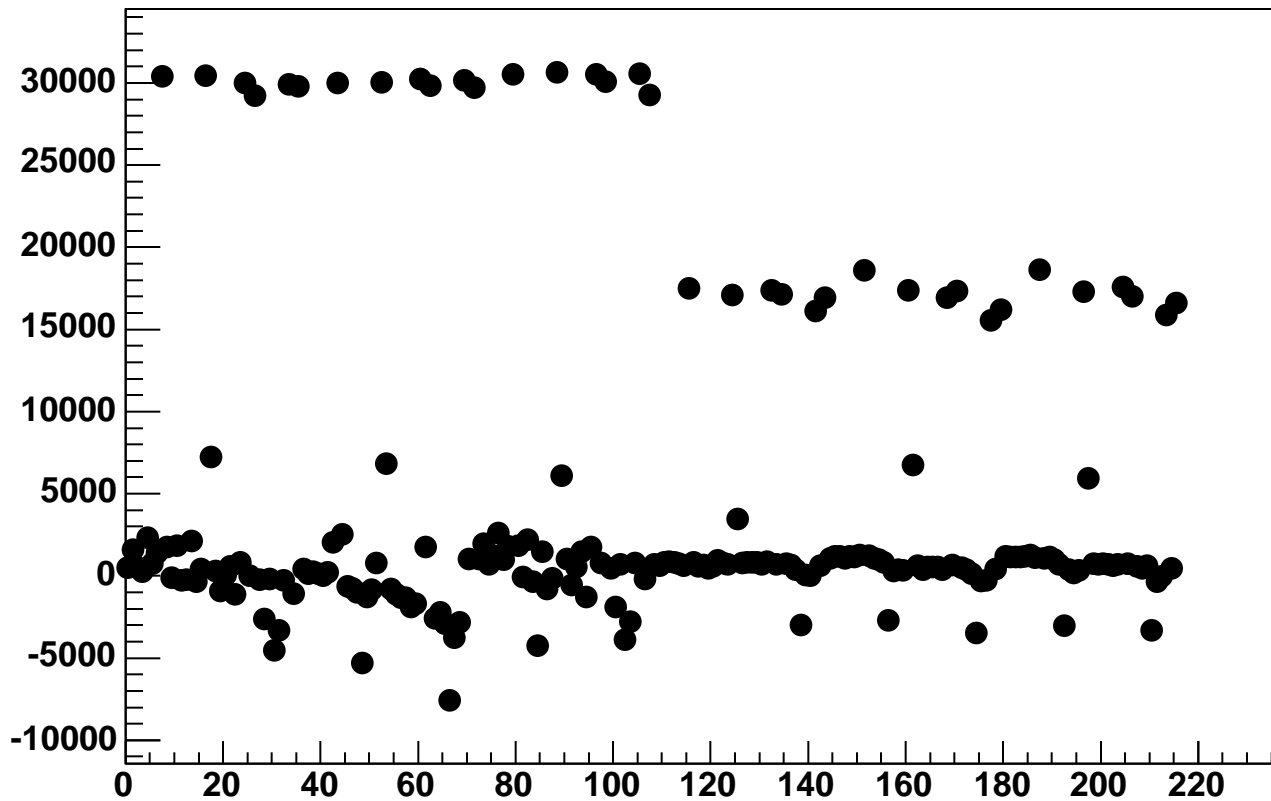
Enable 5, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



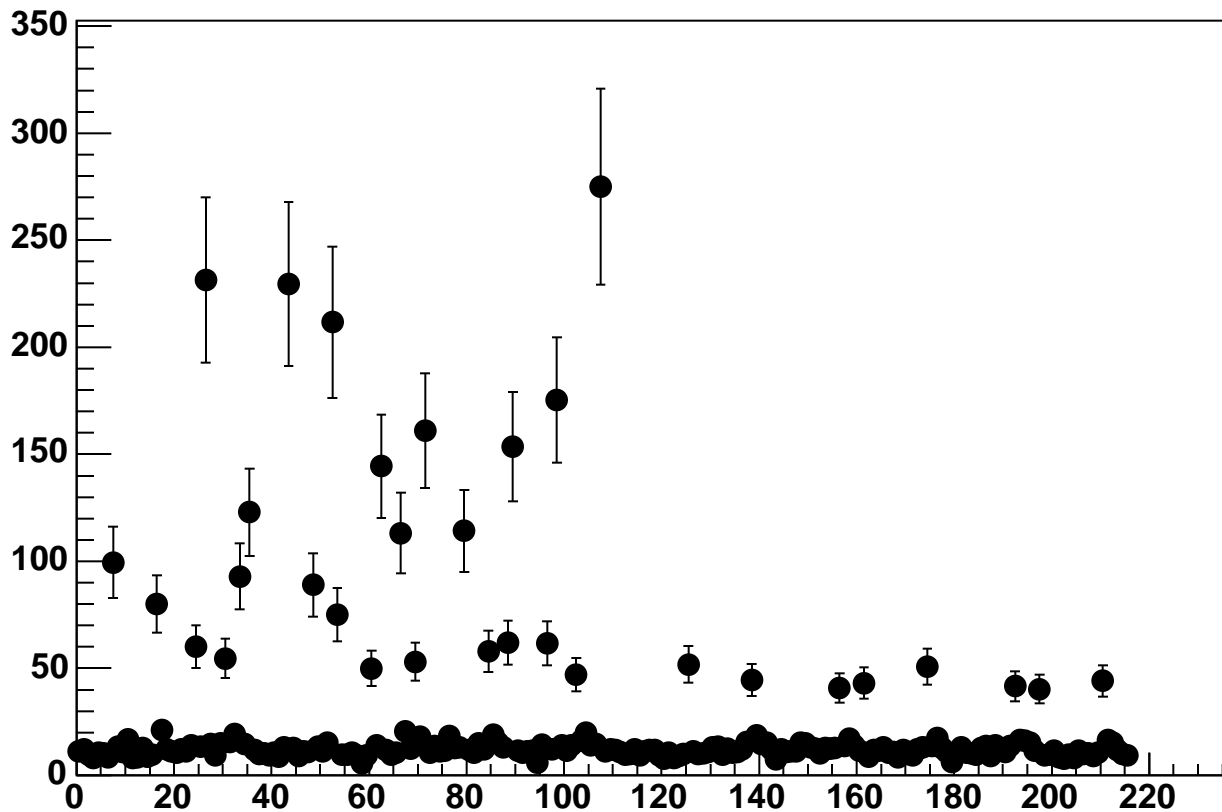
Enable 5, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



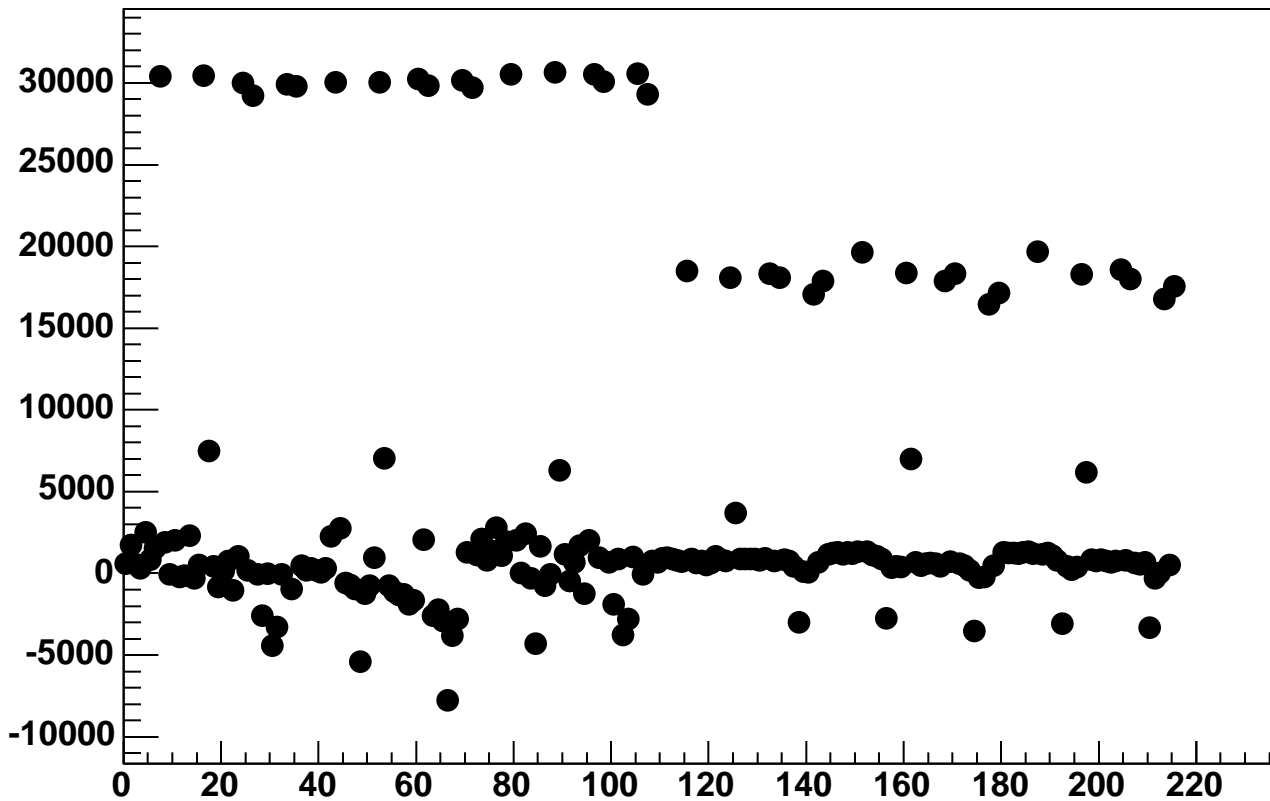
Enable 5, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



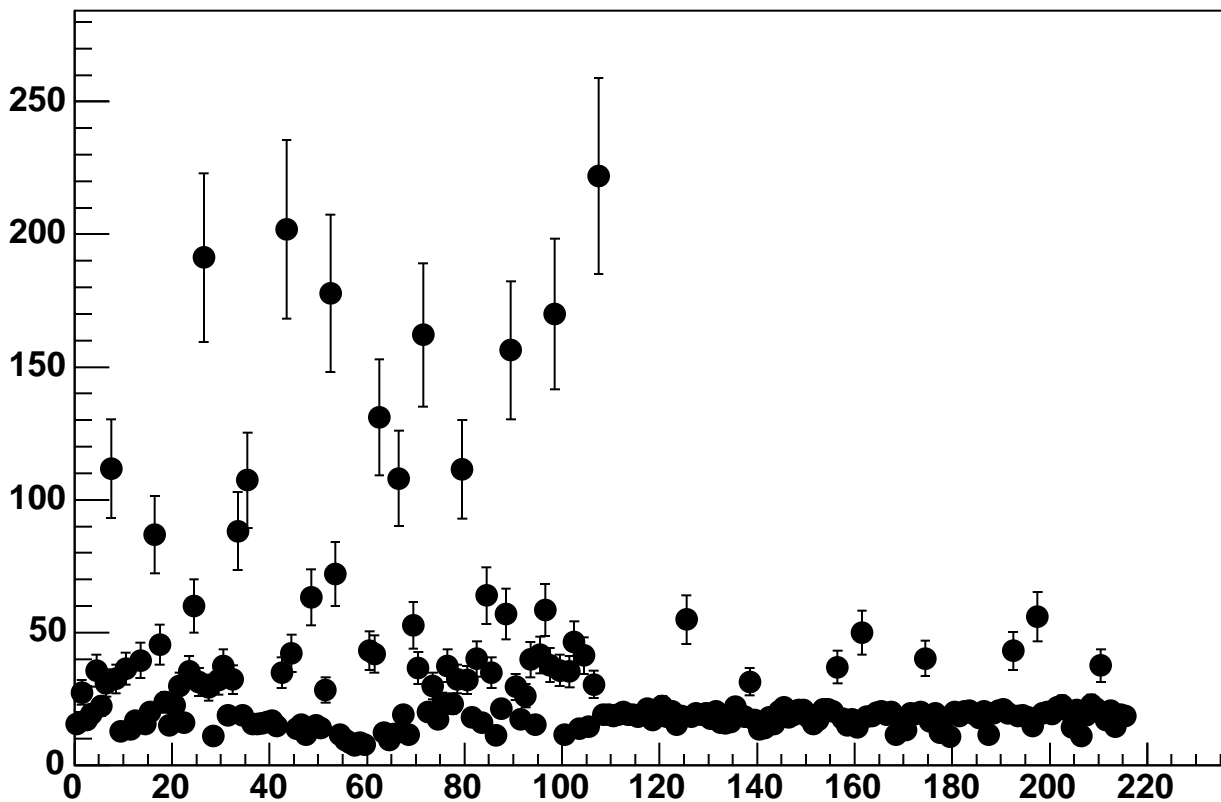
Enable 5, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



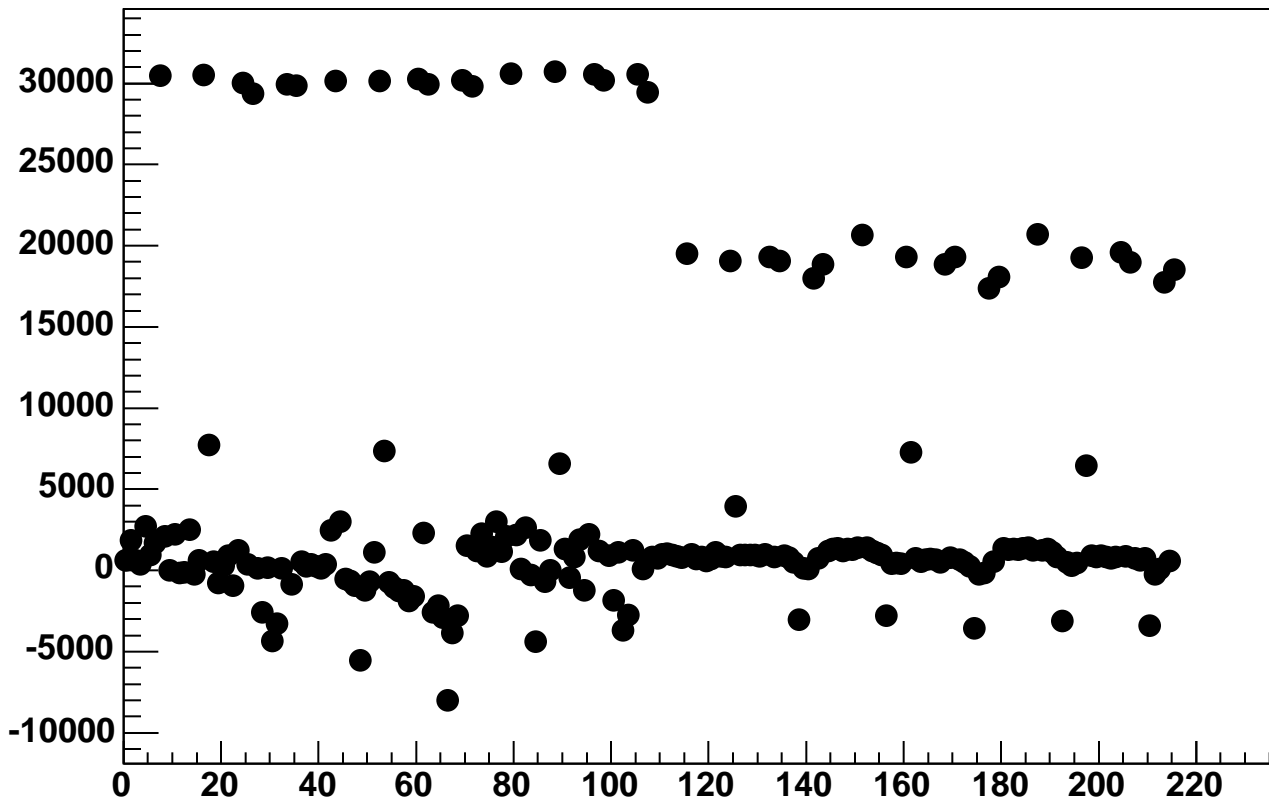
Enable 5, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



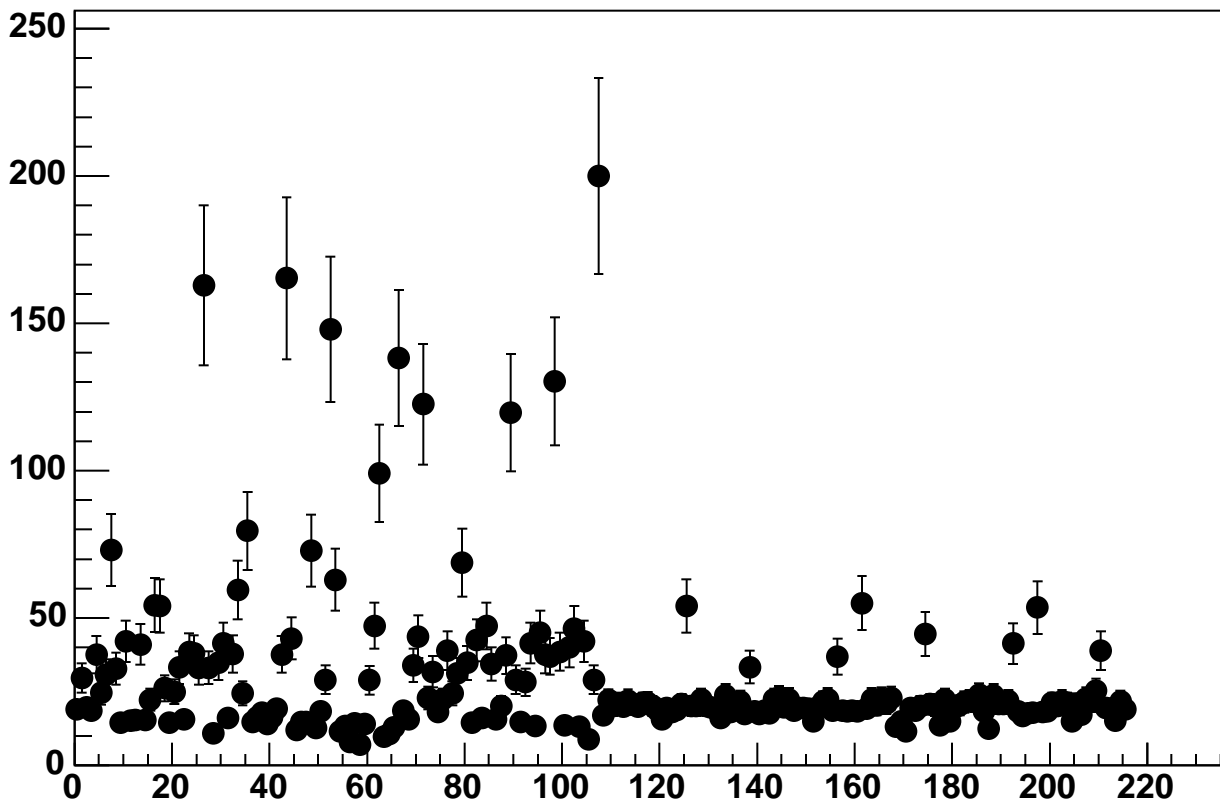
Enable 5, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



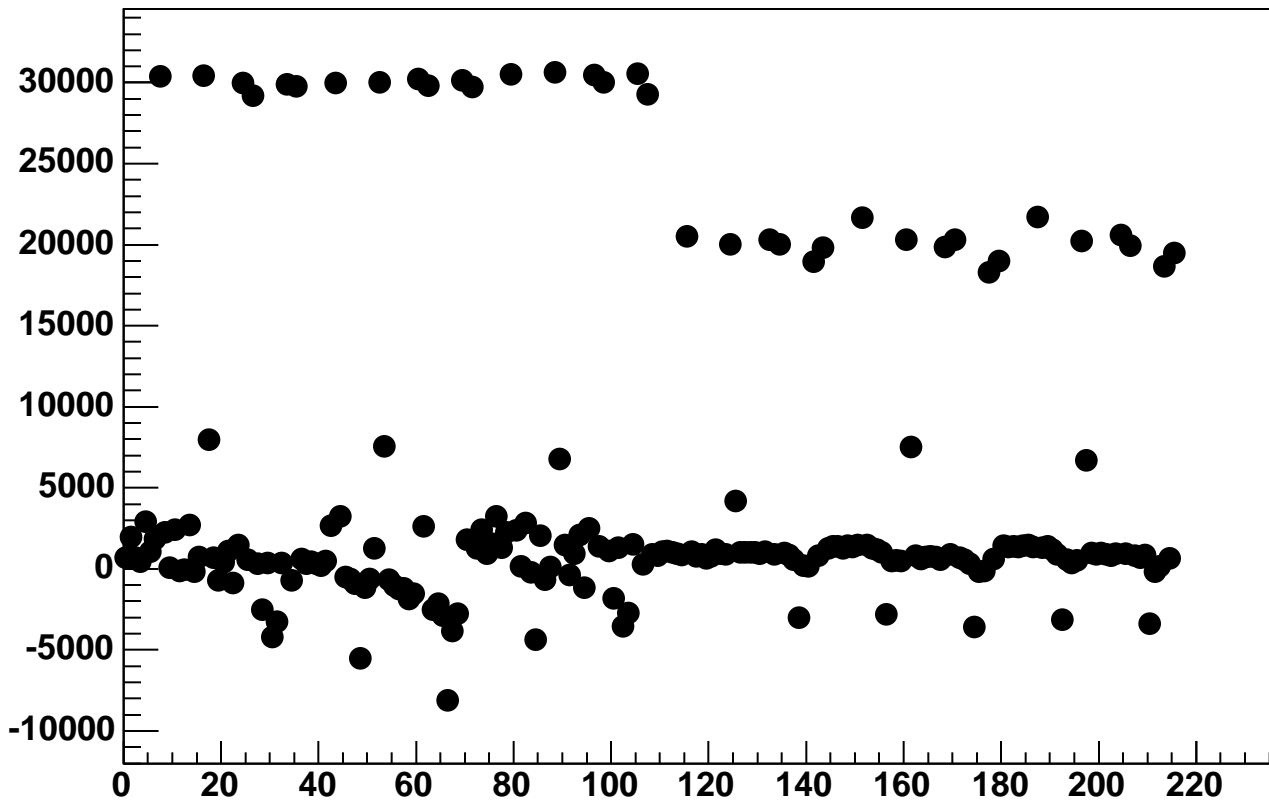
Enable 5, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



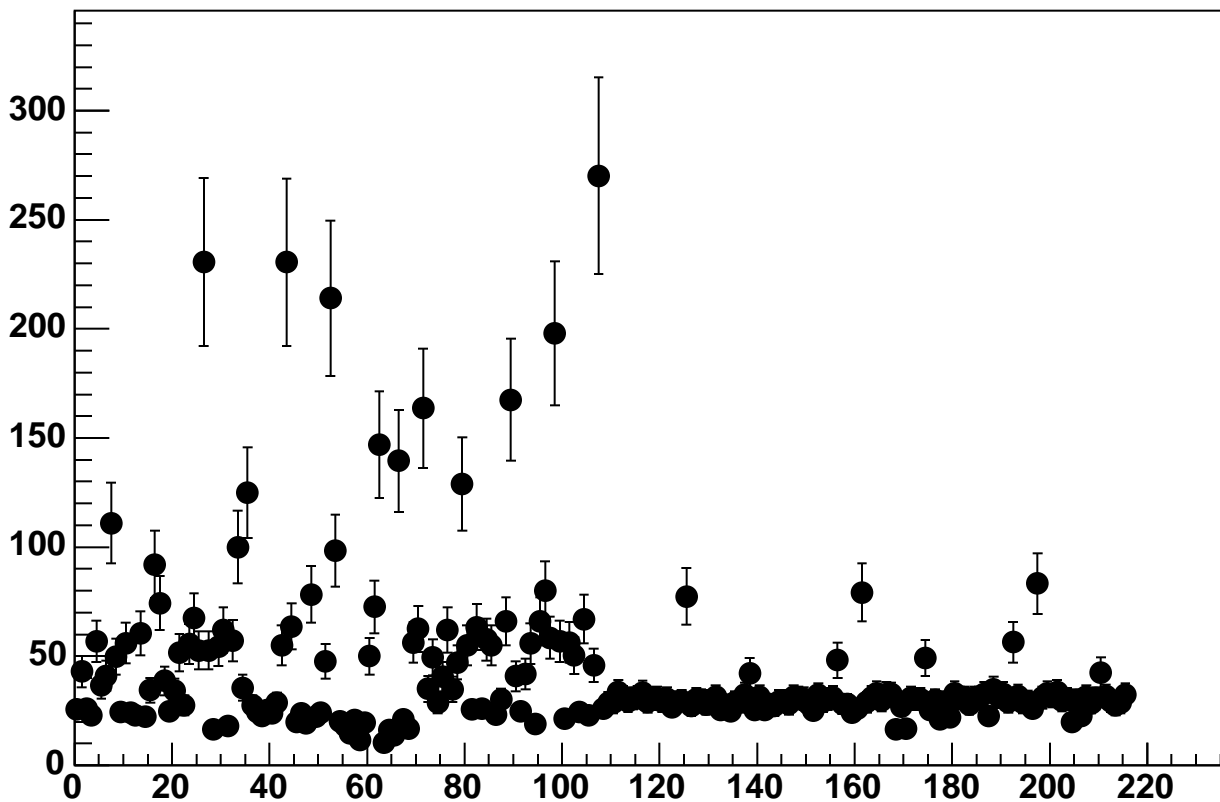
Enable 5, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



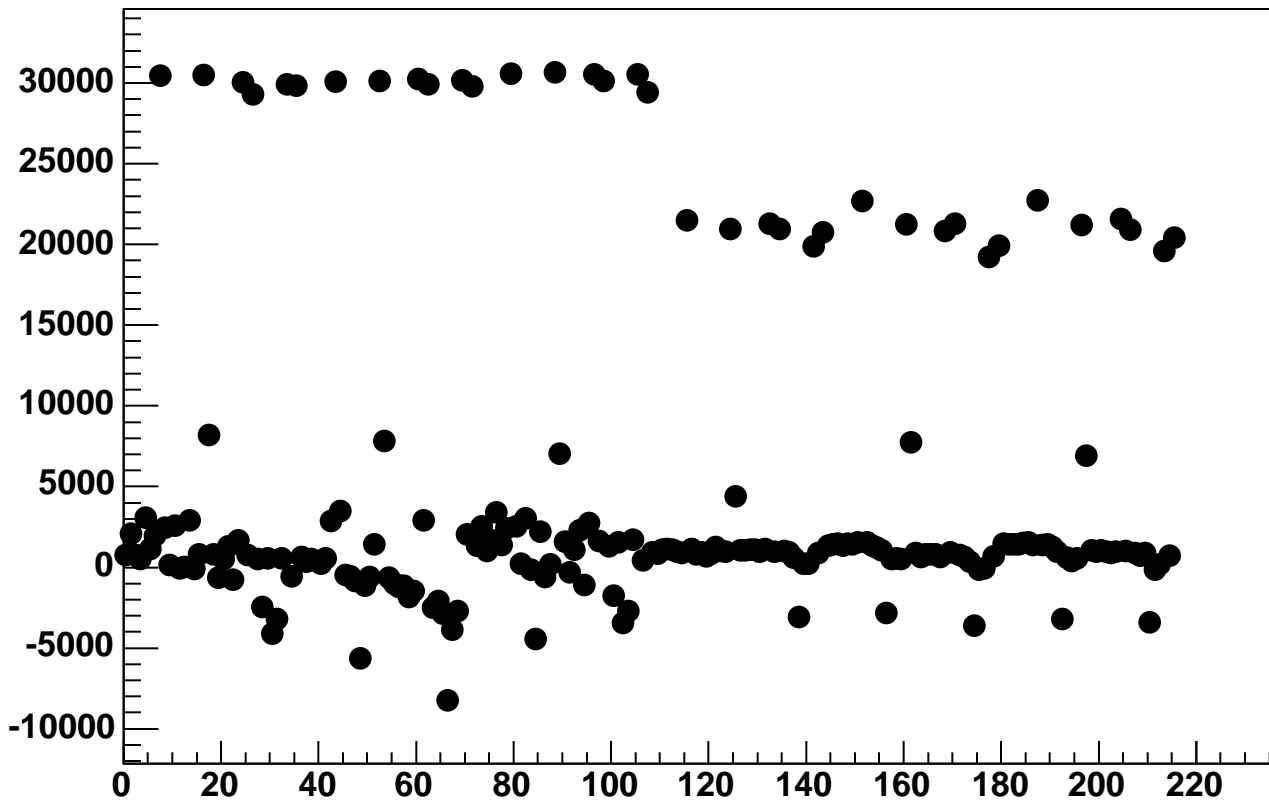
Enable 5, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



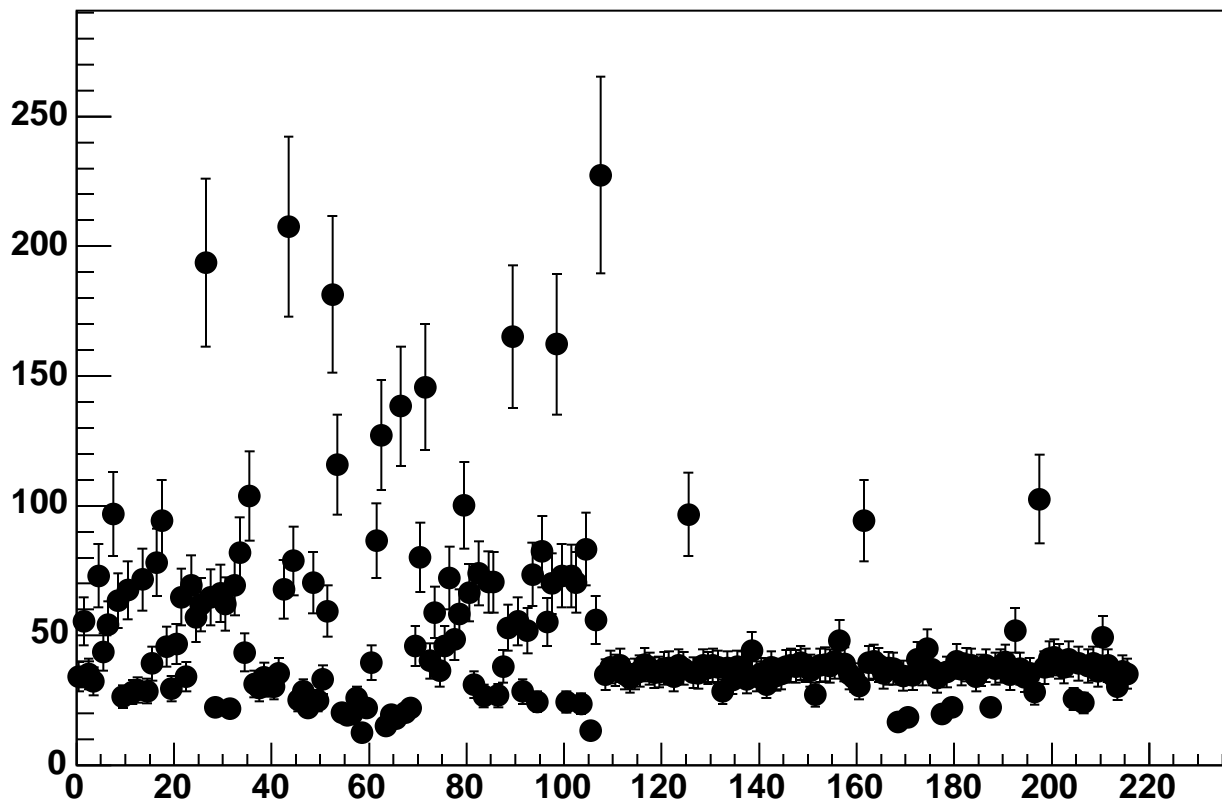
Enable 5, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



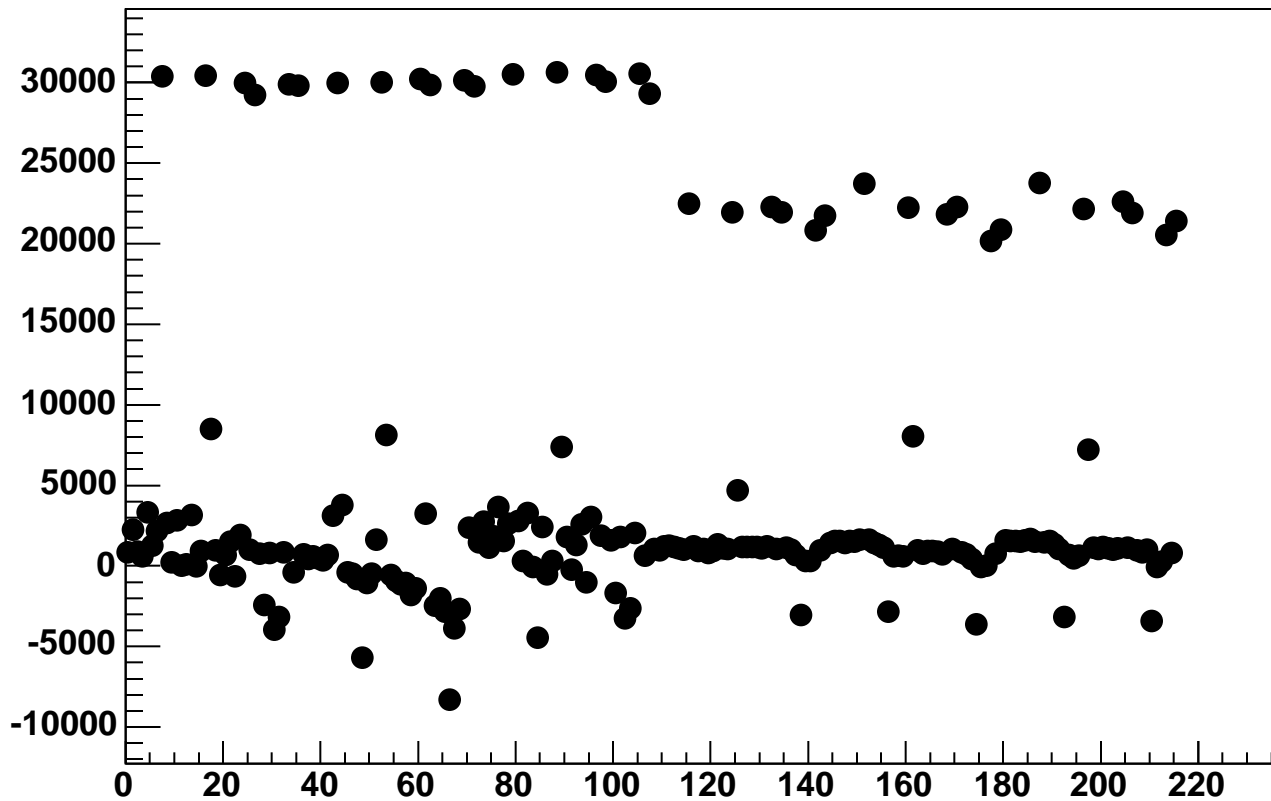
Enable 5, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



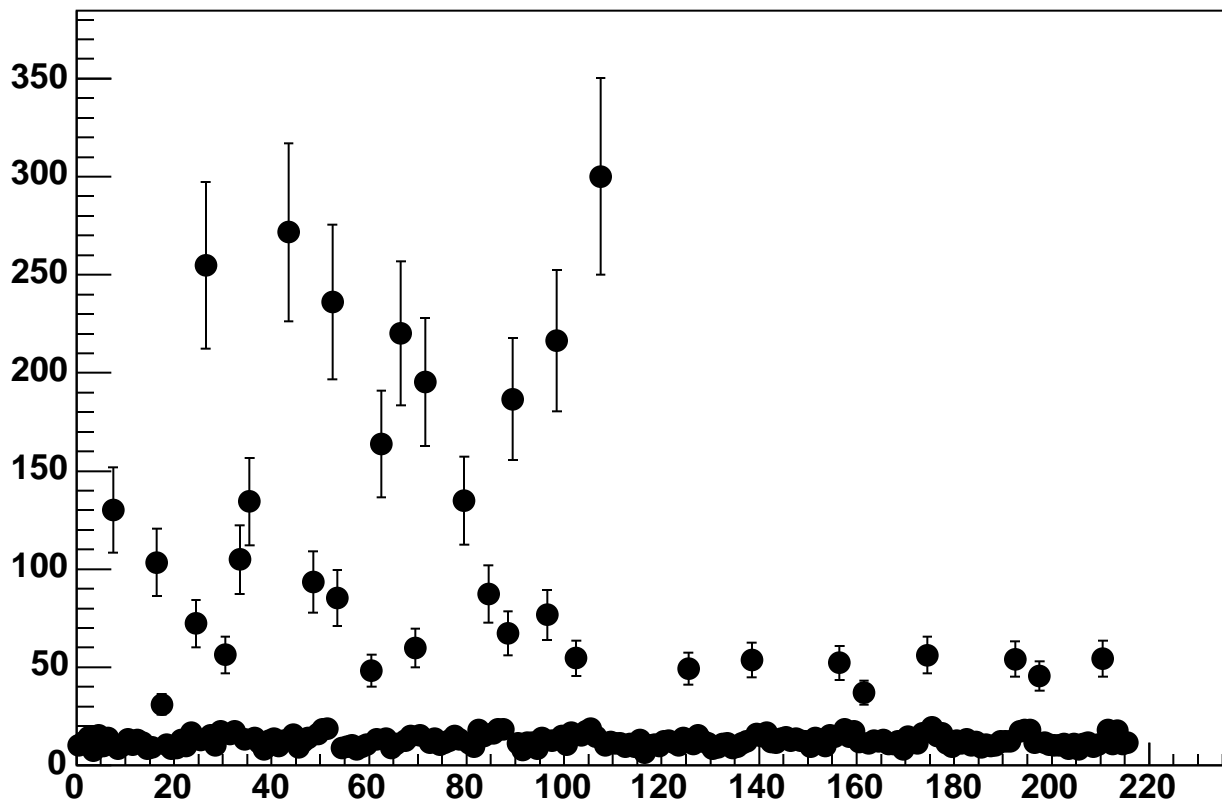
Enable 5, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



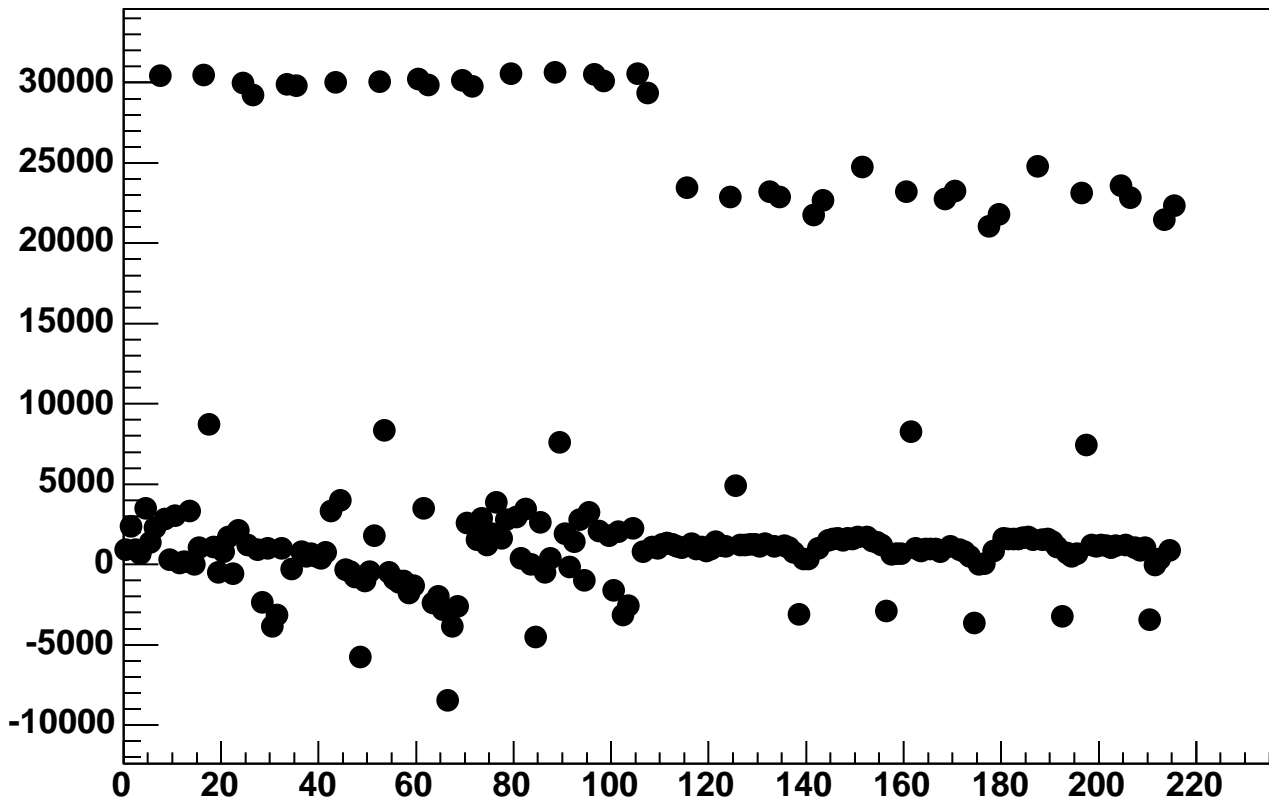
Enable 5, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



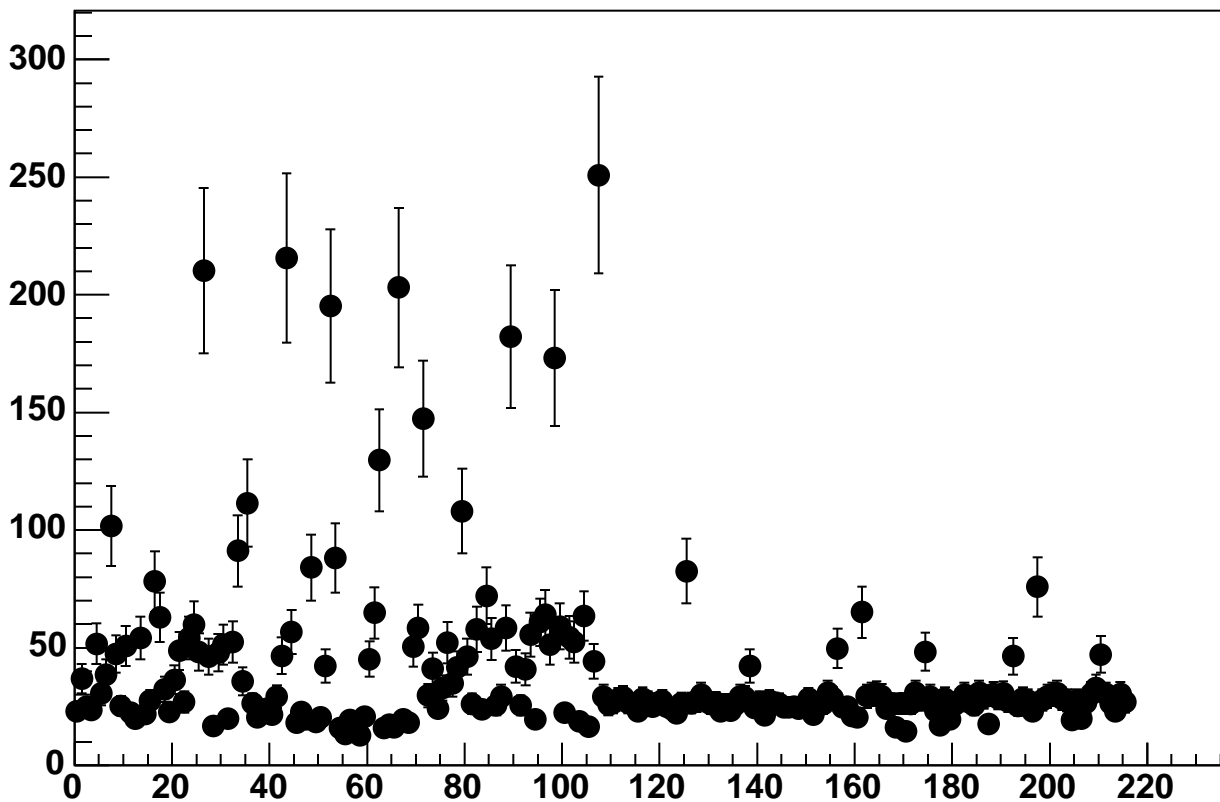
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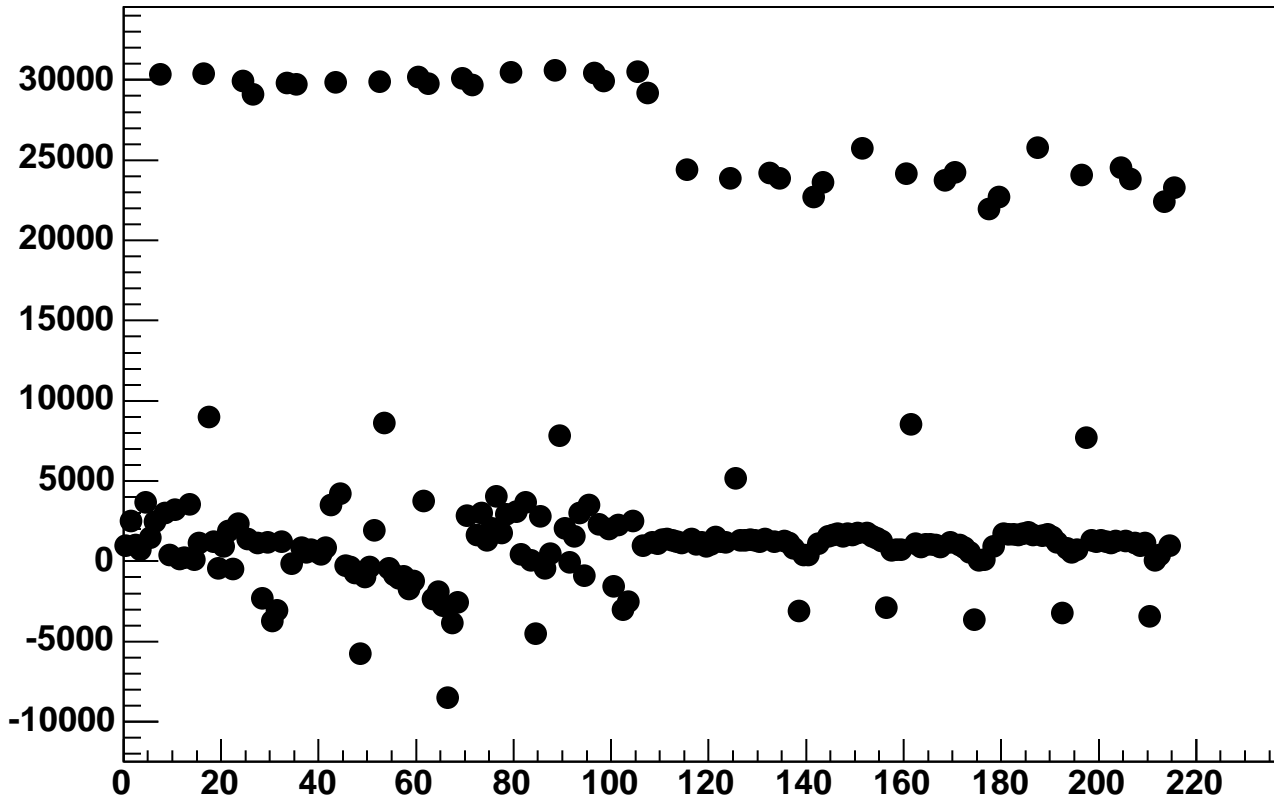
Enable 5, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



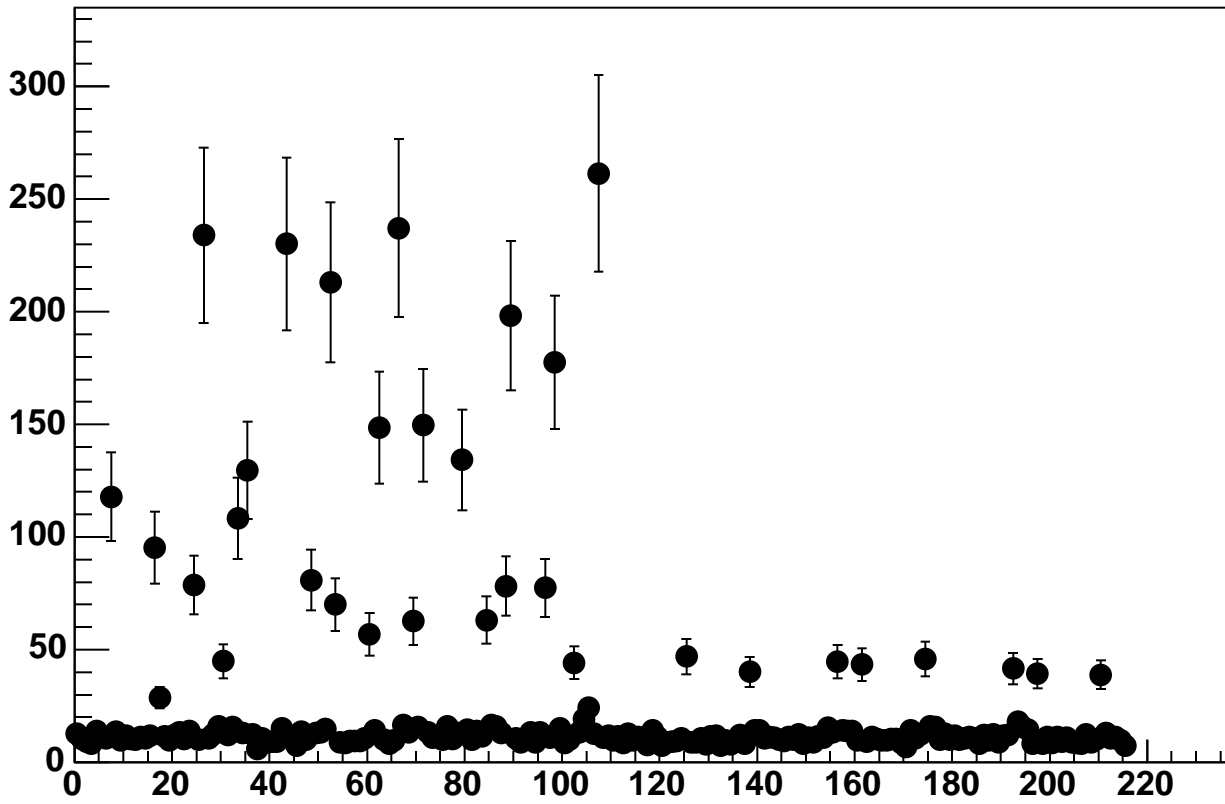
Enable 5, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



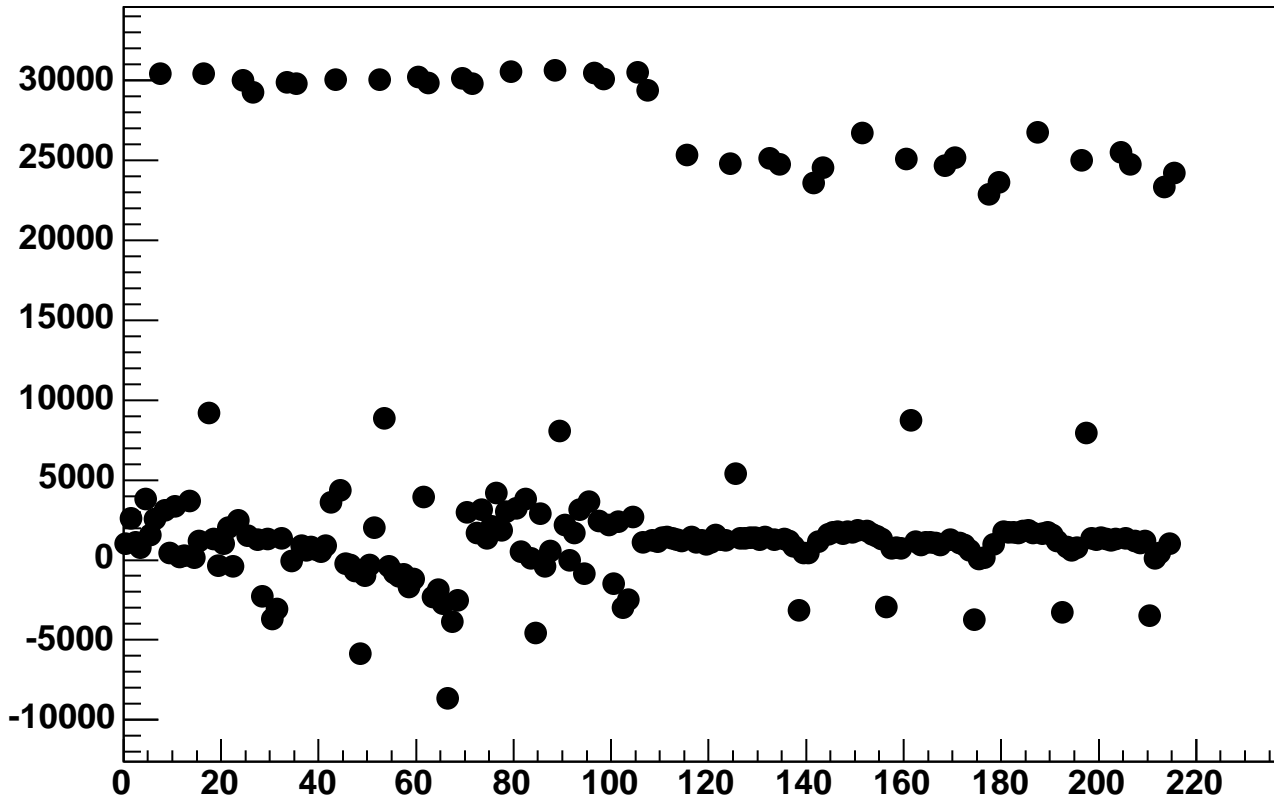
Enable 5, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



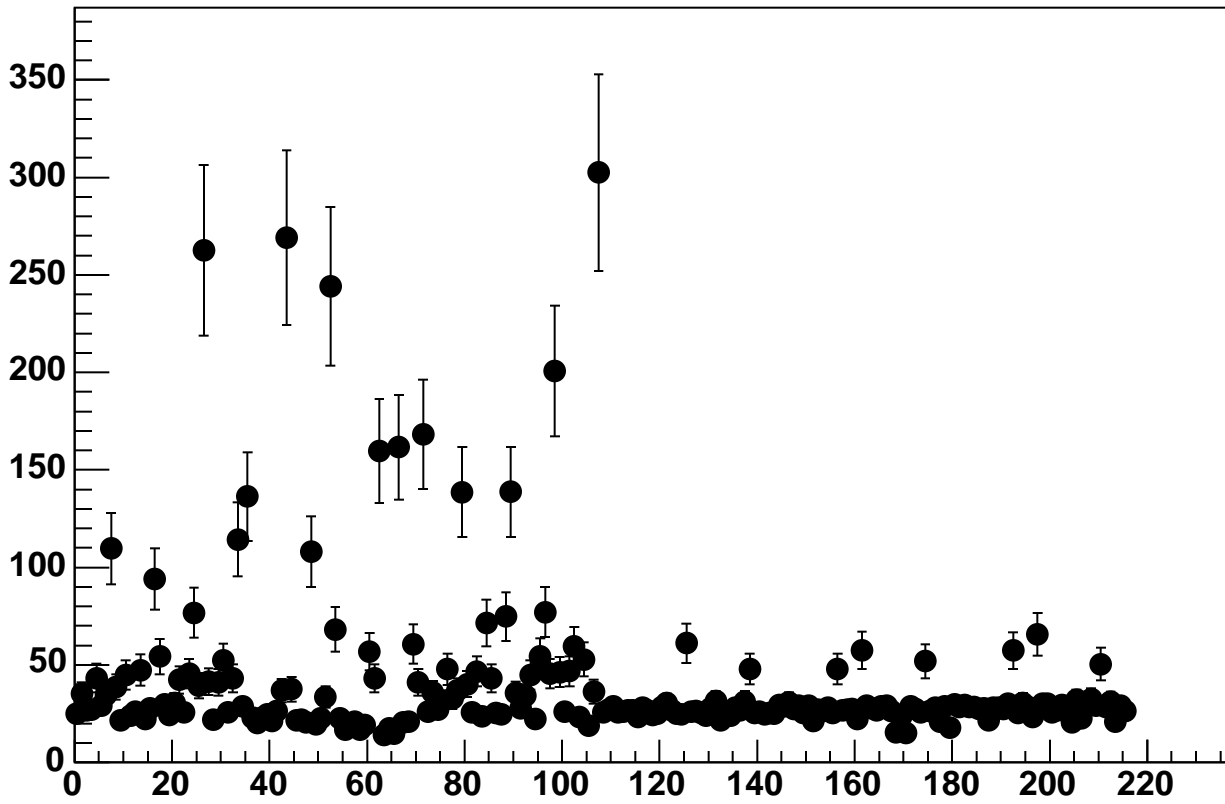
Enable 5, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



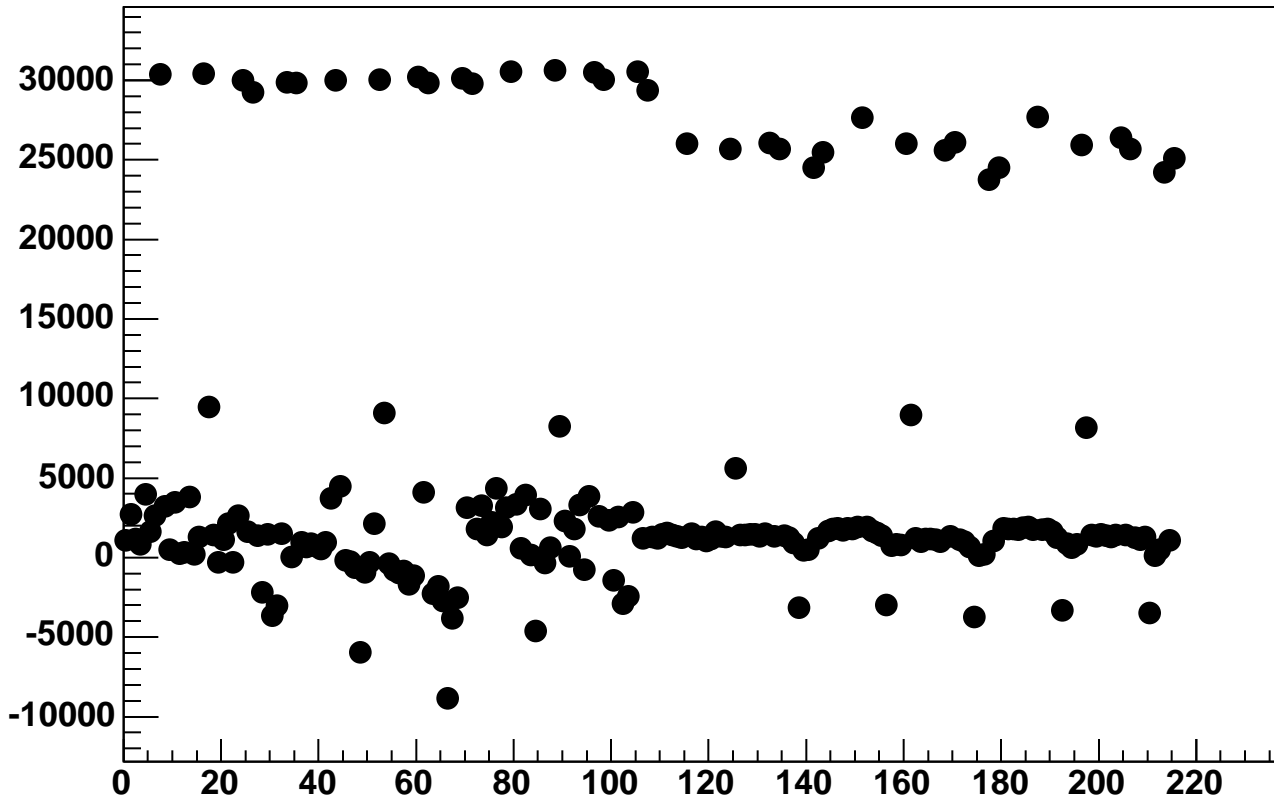
Enable 5, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



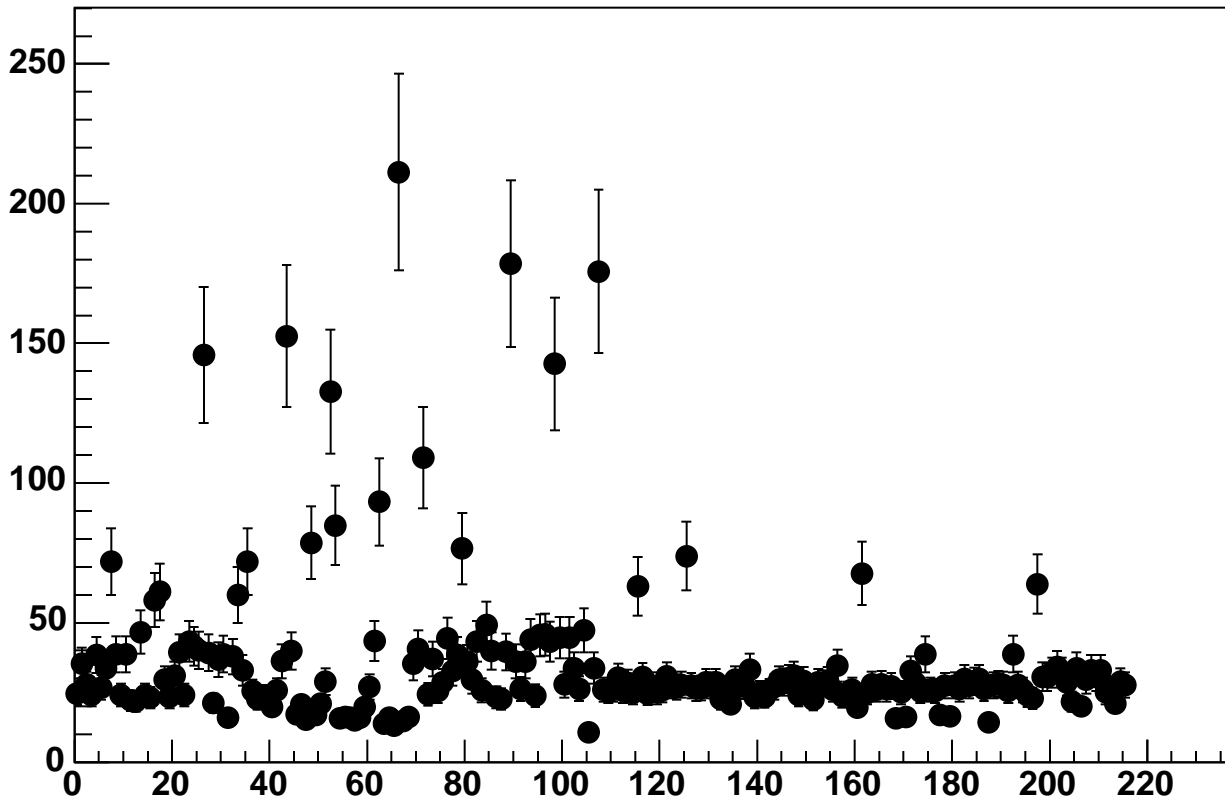
Enable 5, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



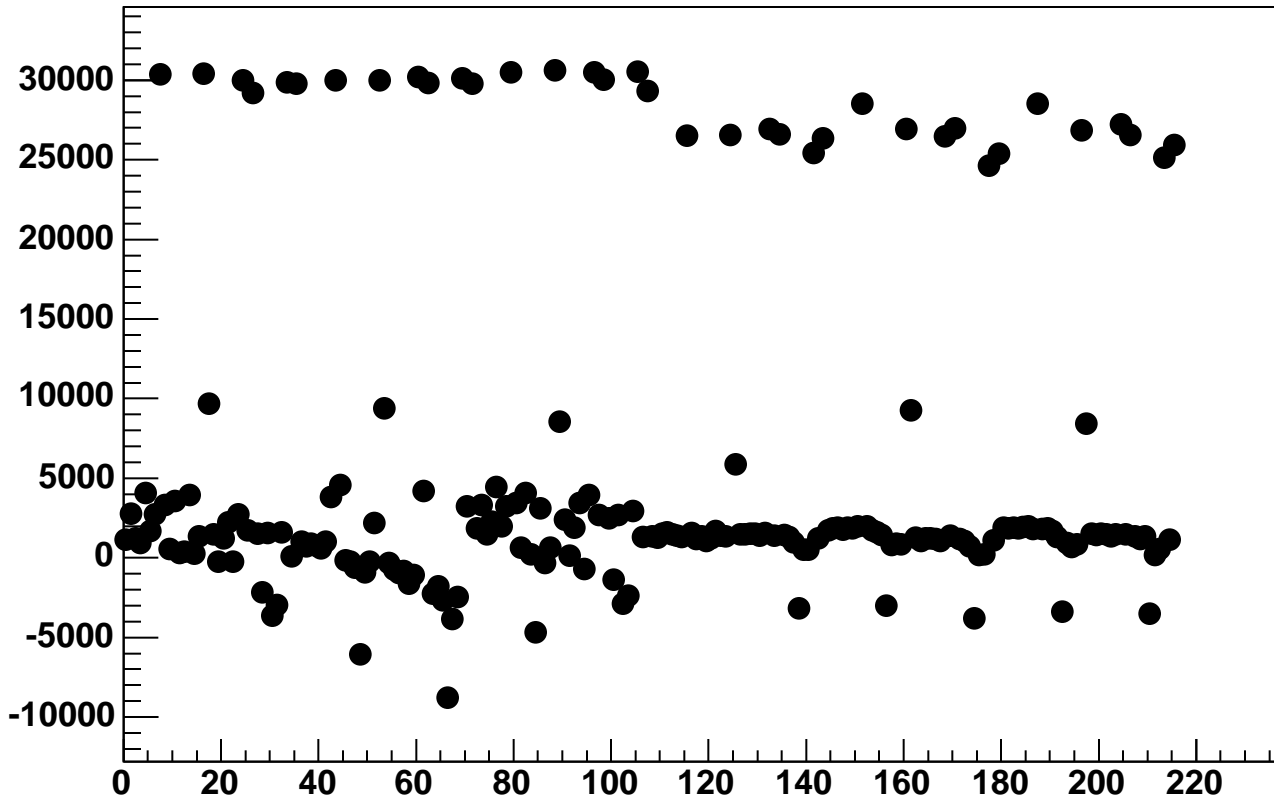
Enable 5, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



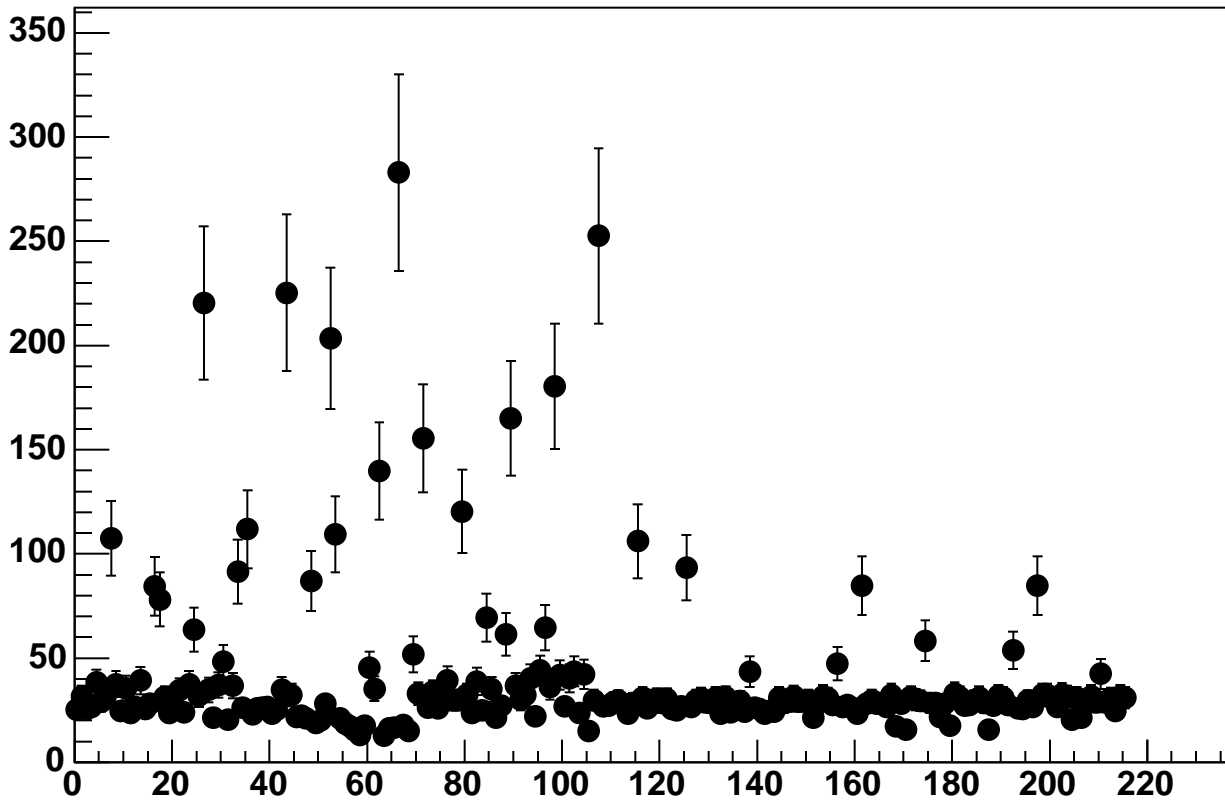
Enable 5, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



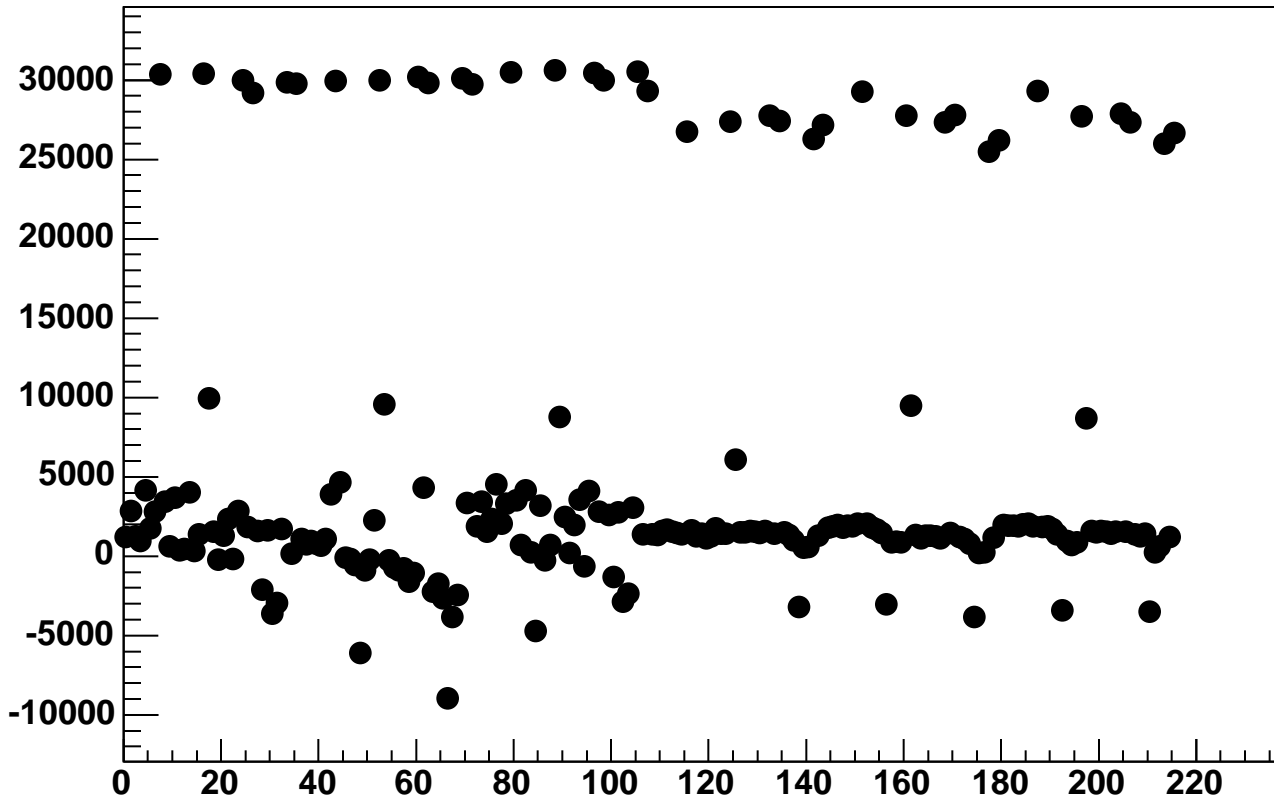
Enable 5, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



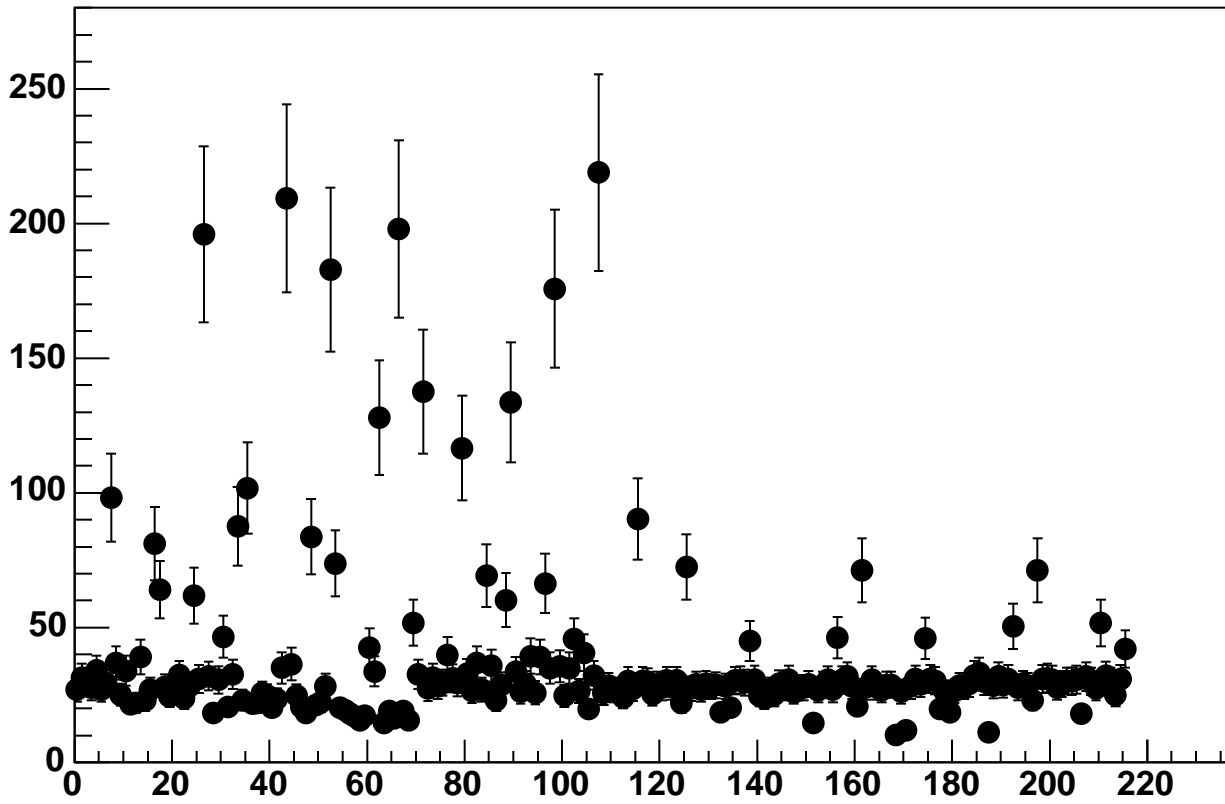
Enable 5, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



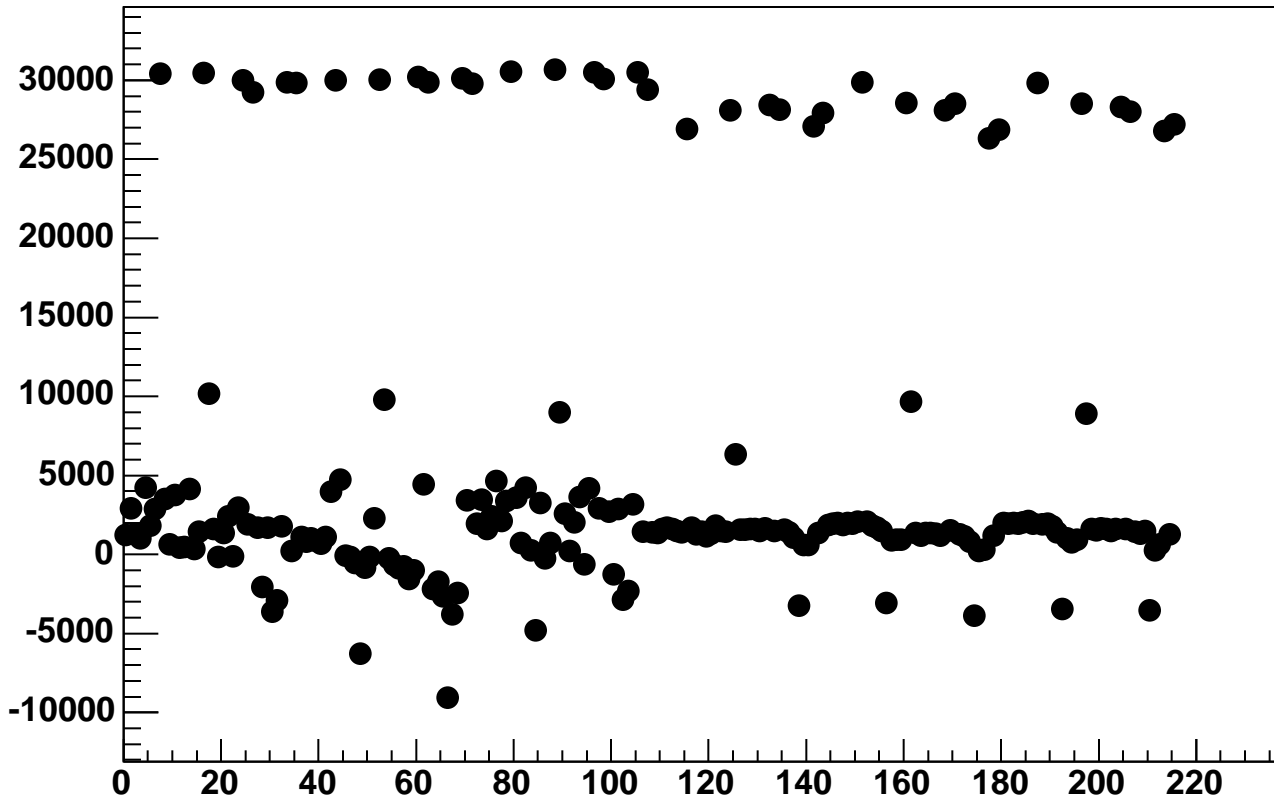
Enable 5, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



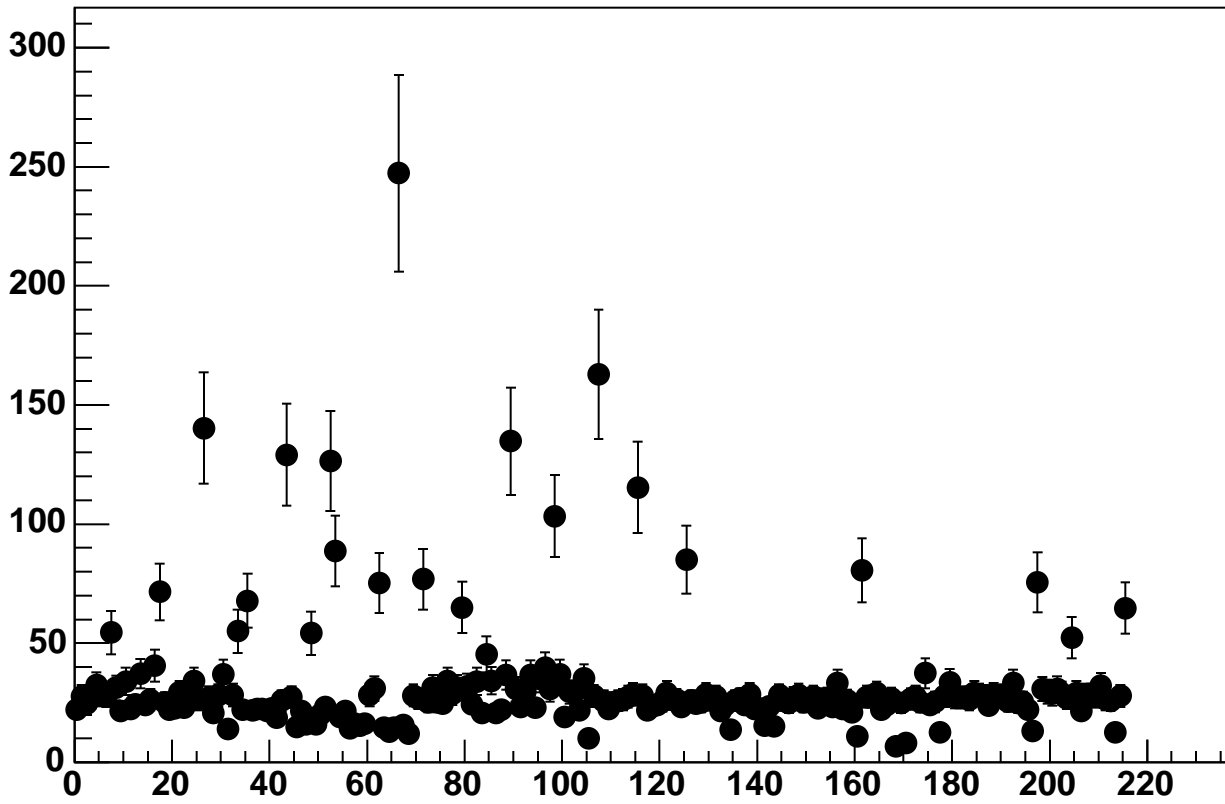
Enable 5, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



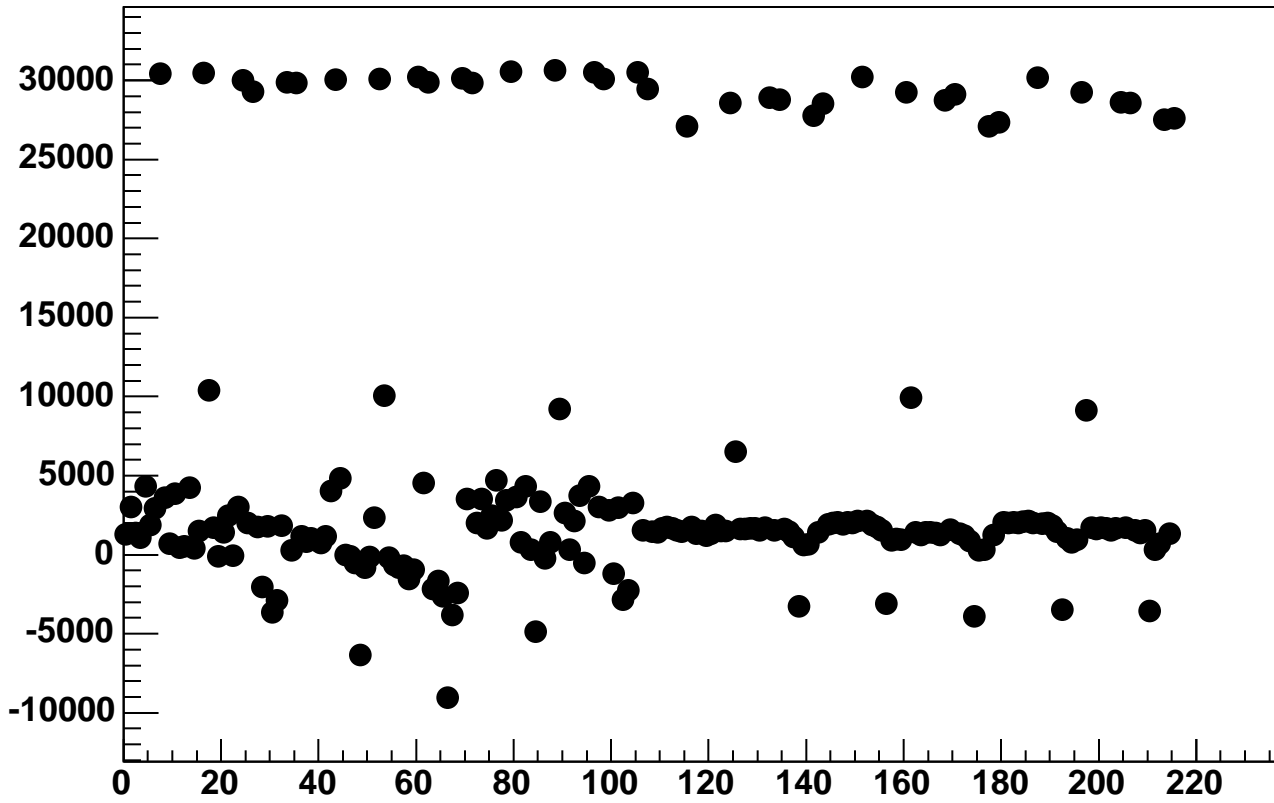
Enable 5, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



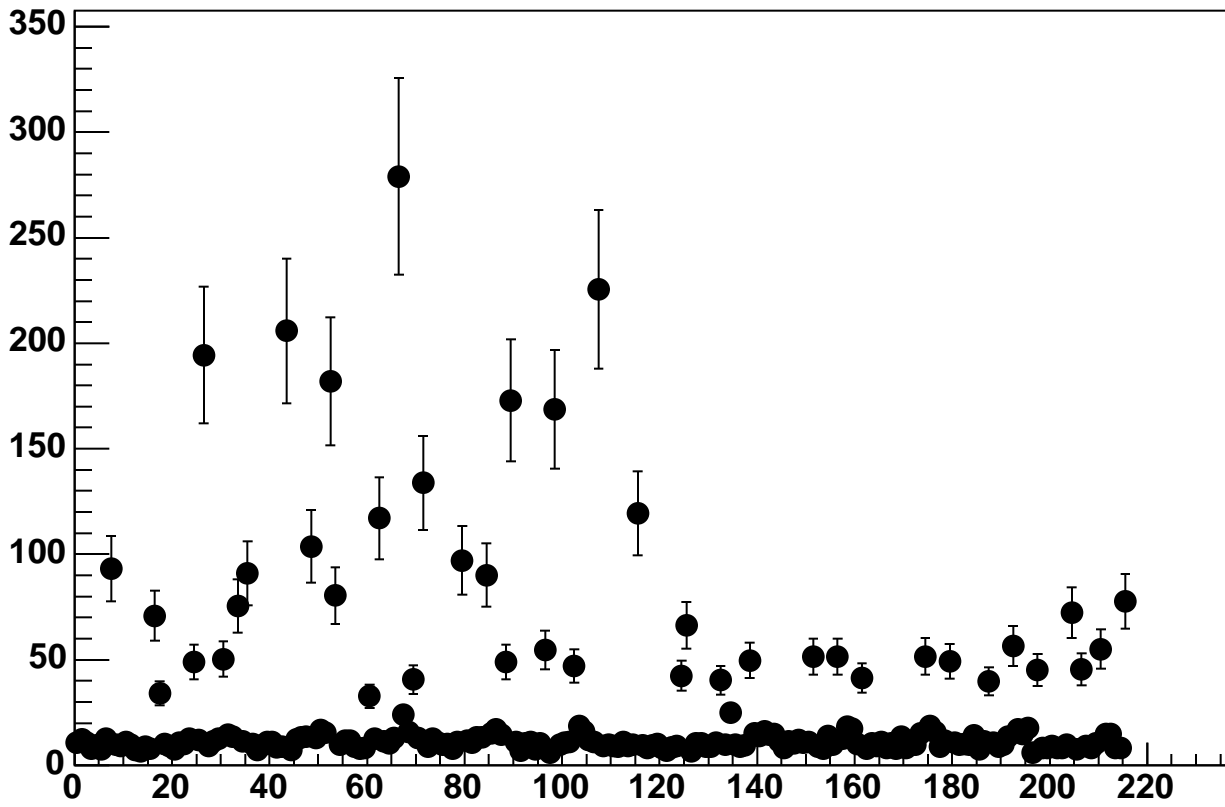
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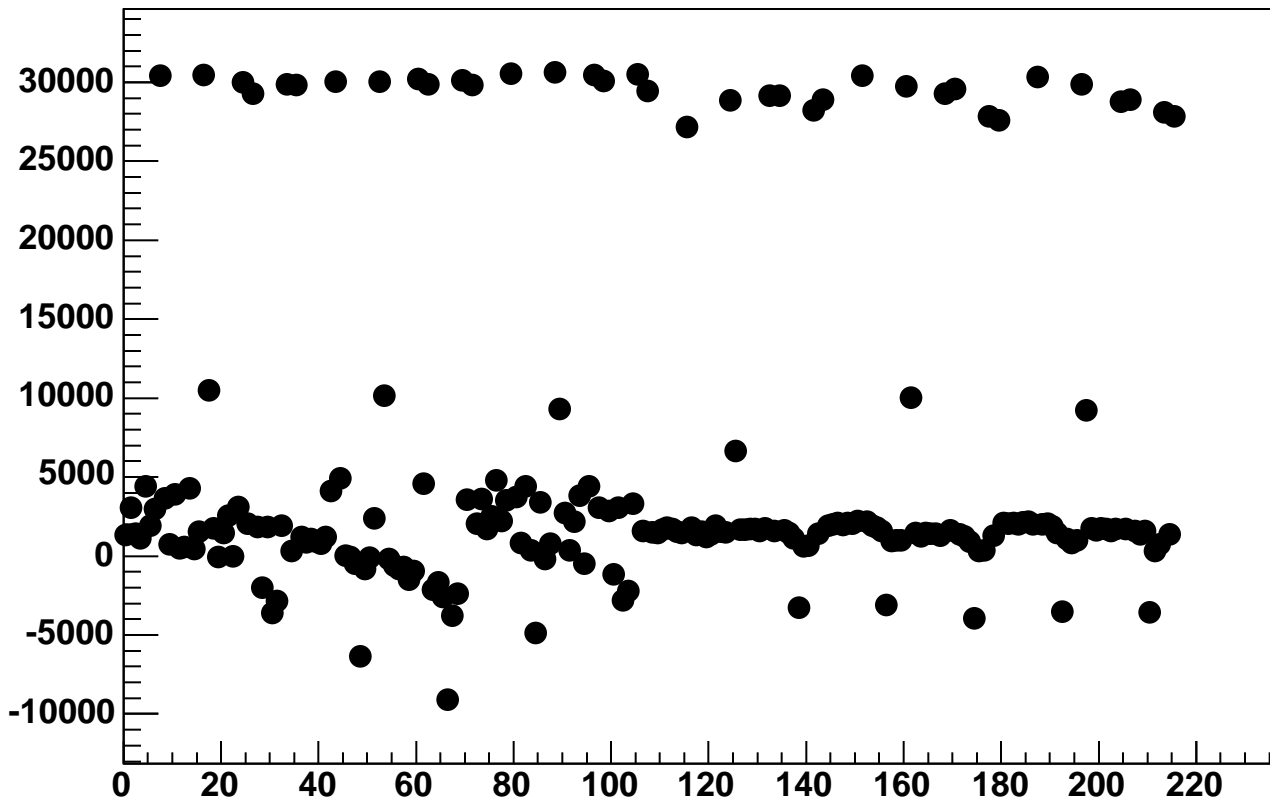
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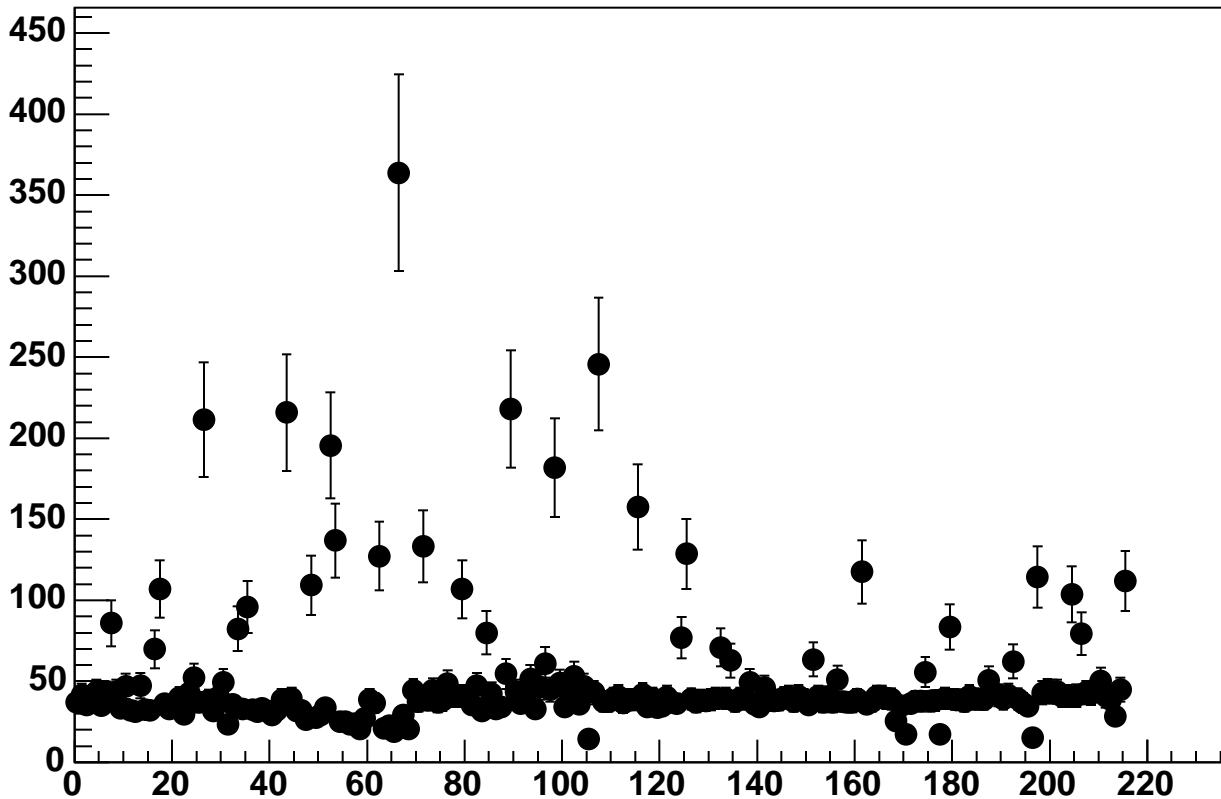
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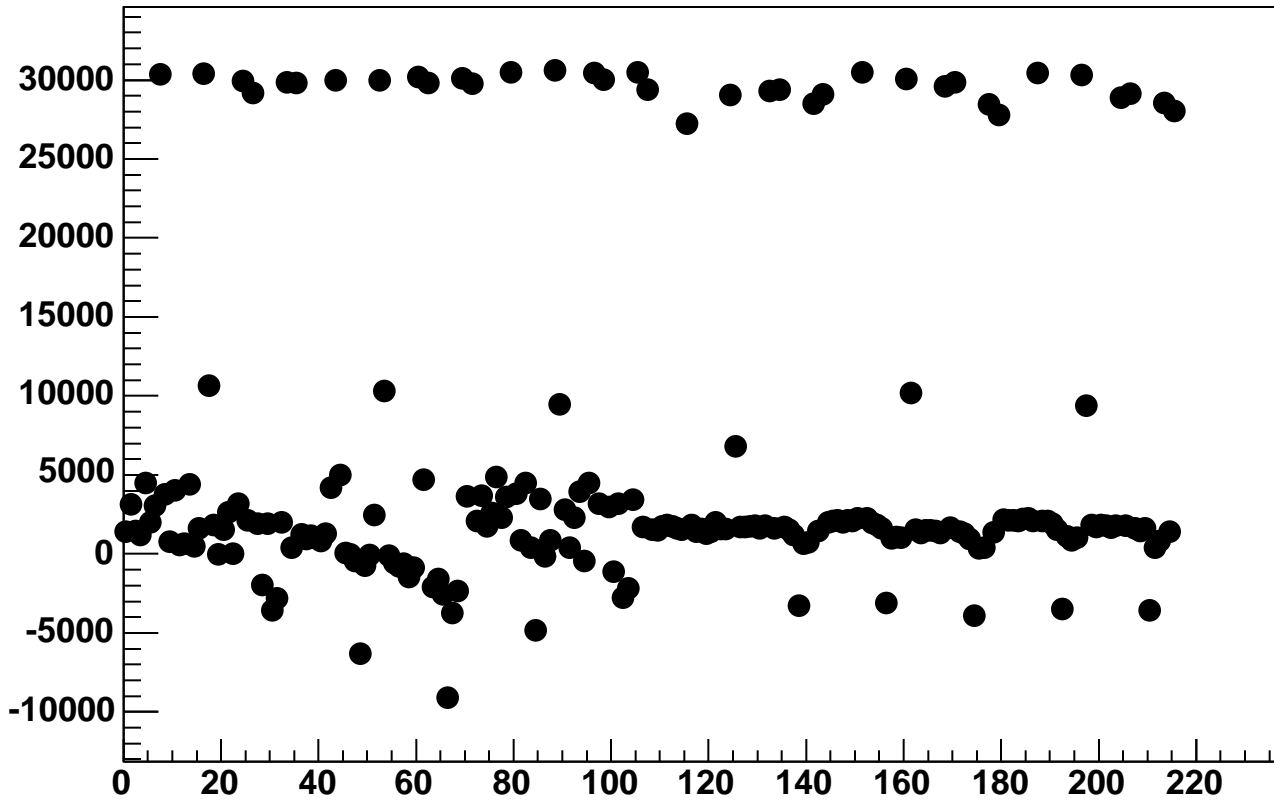
Enable 5, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



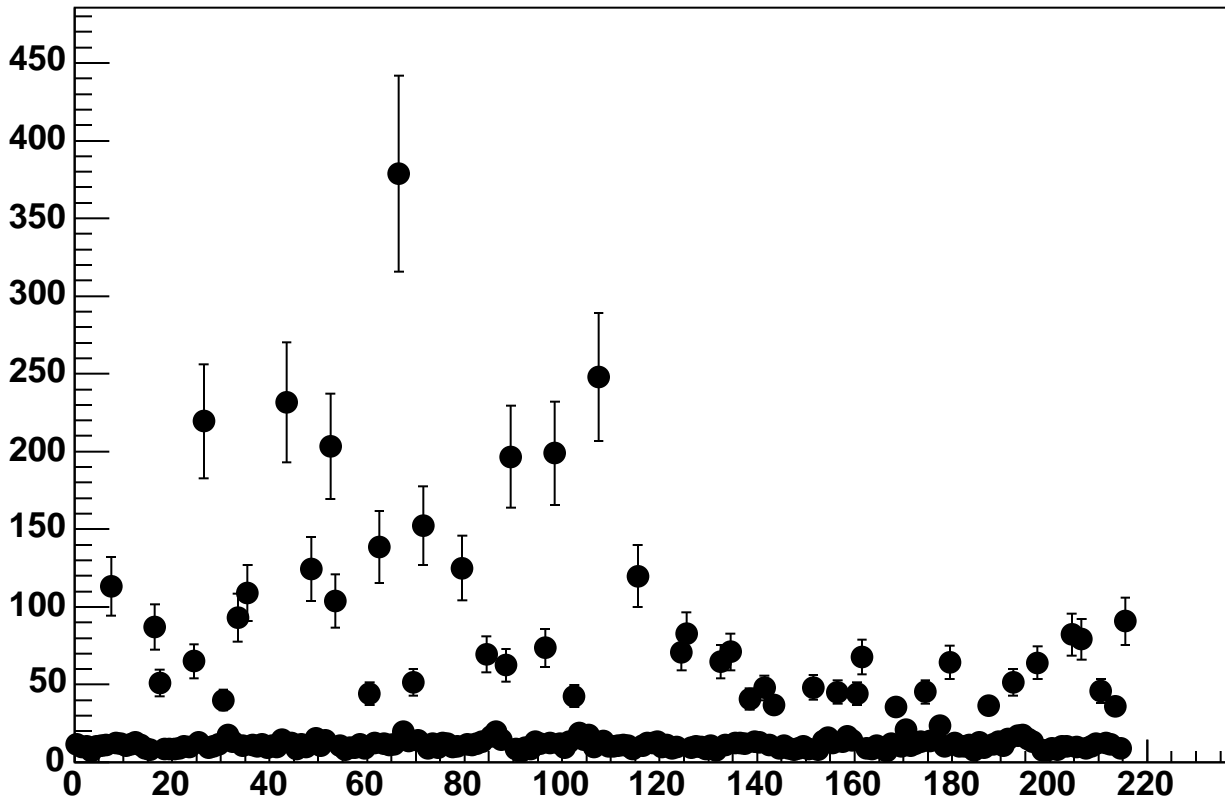
Enable 5, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



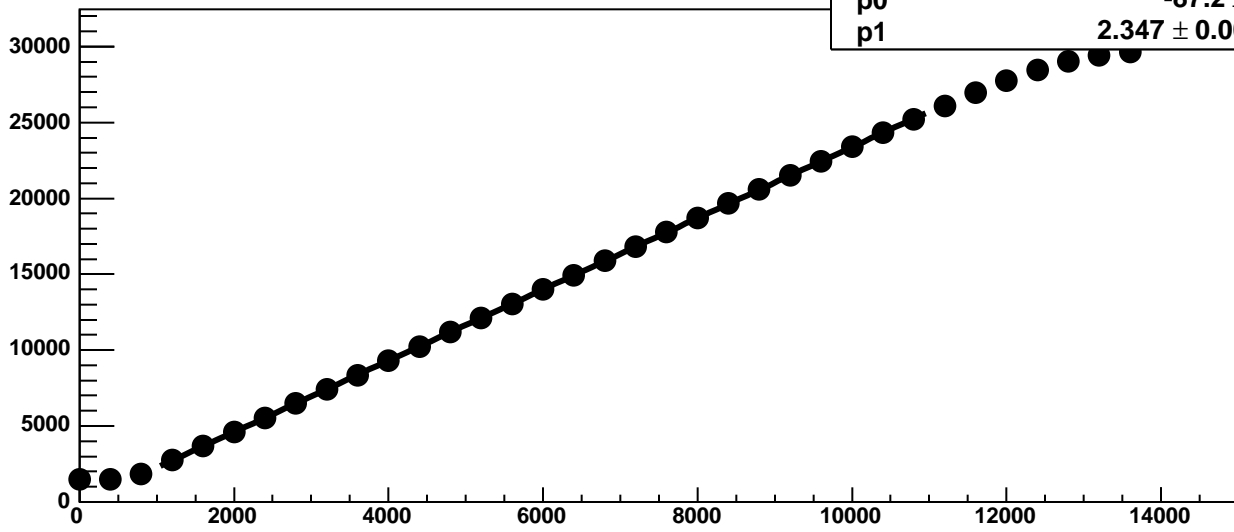
Enable 5, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



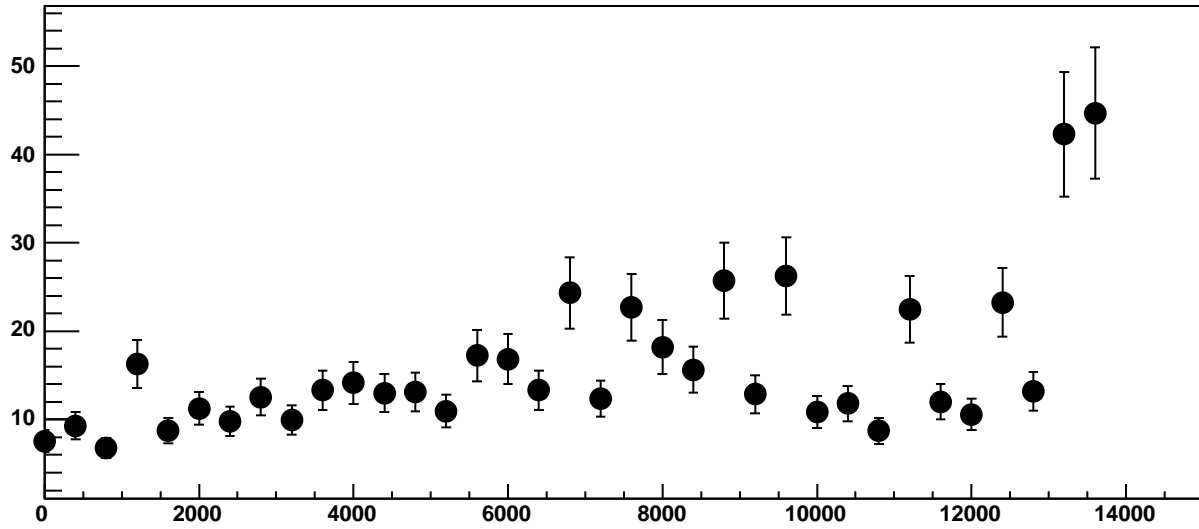
Enable 5, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



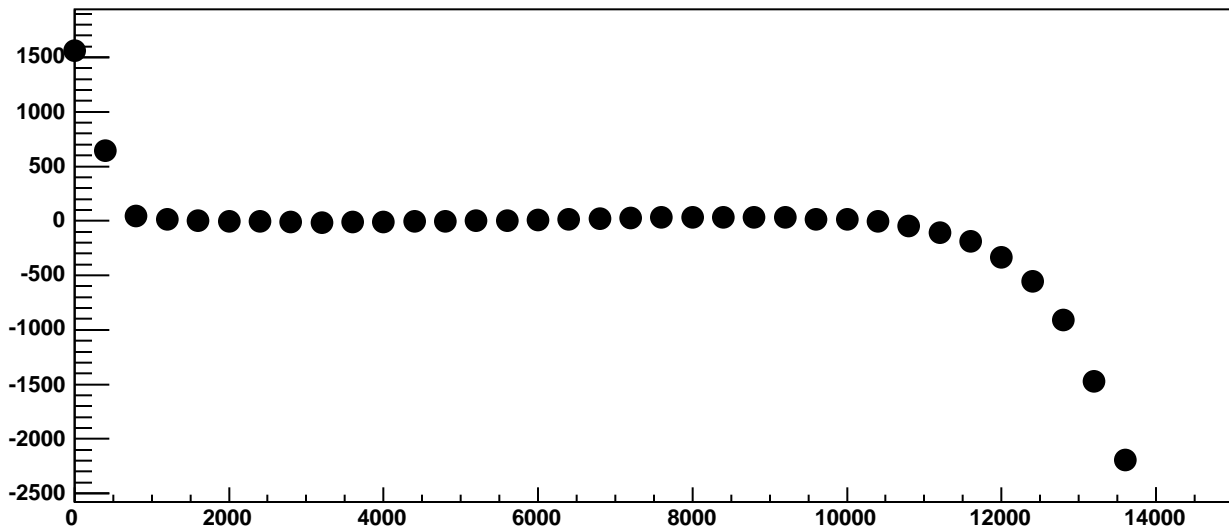
Chip 0, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



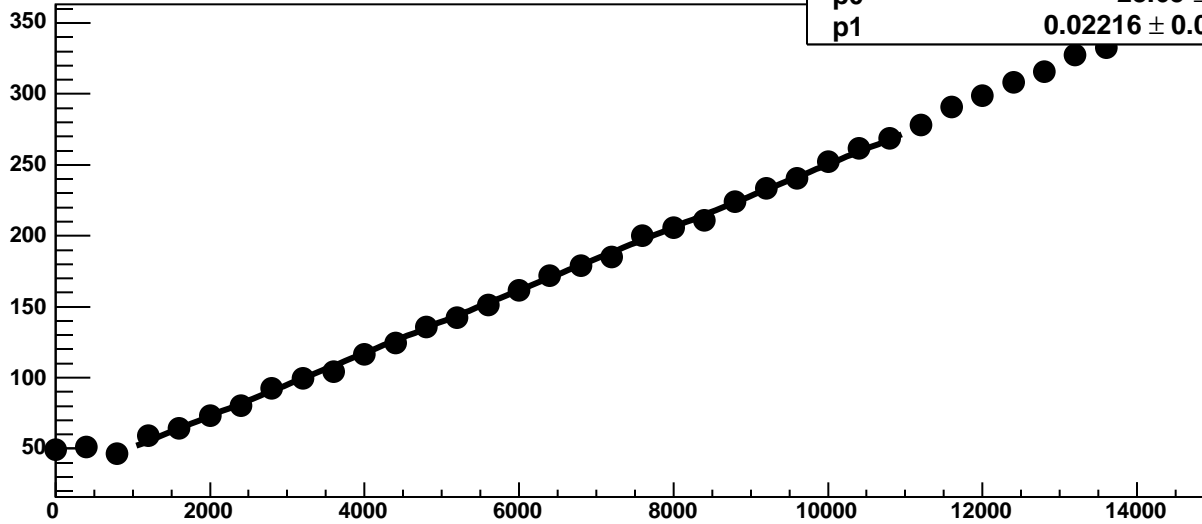
Chip 0, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC

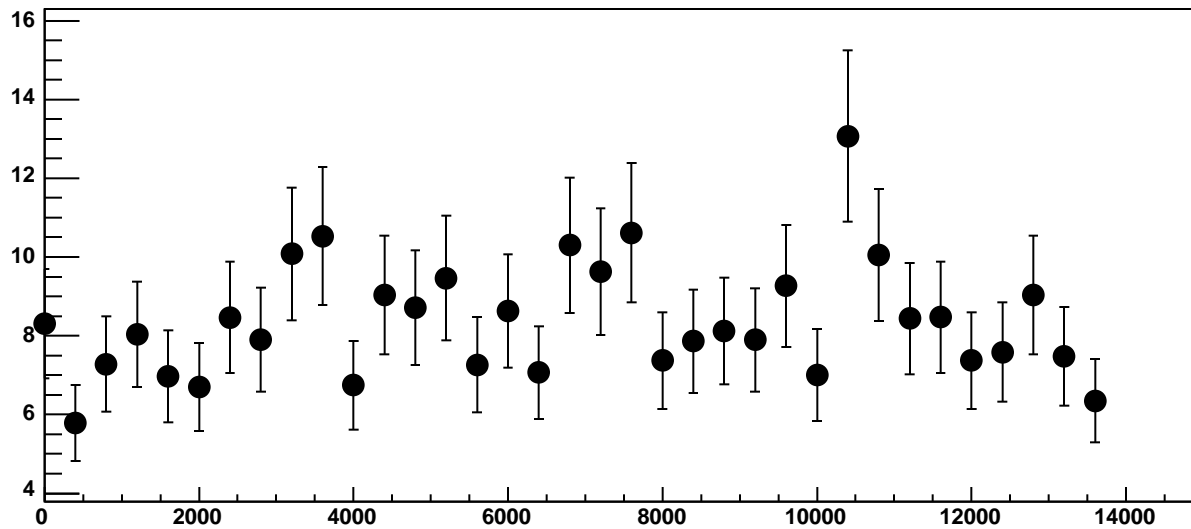


Chip 0, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

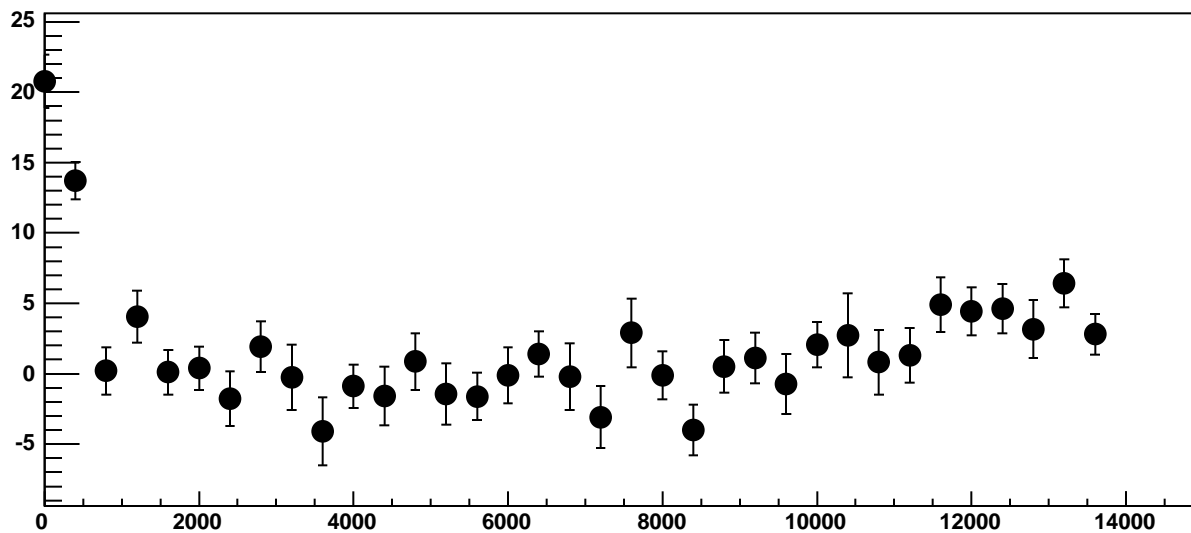


χ^2 / ndf 24.44 / 23
p0 28.65 ± 0.8488
p1 0.02216 ± 0.0001319

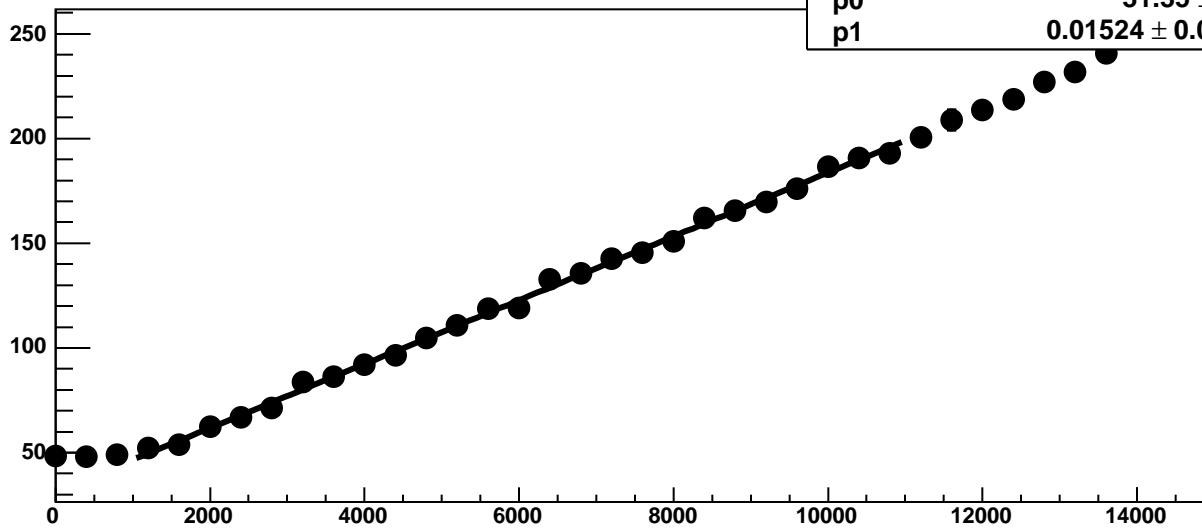
Chip 0, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



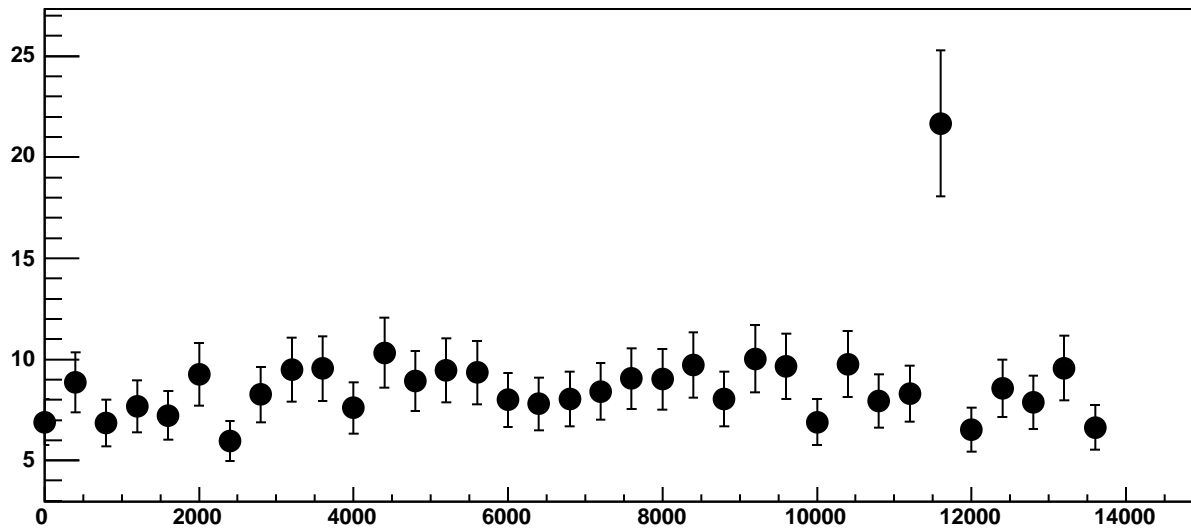
Chip 0, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



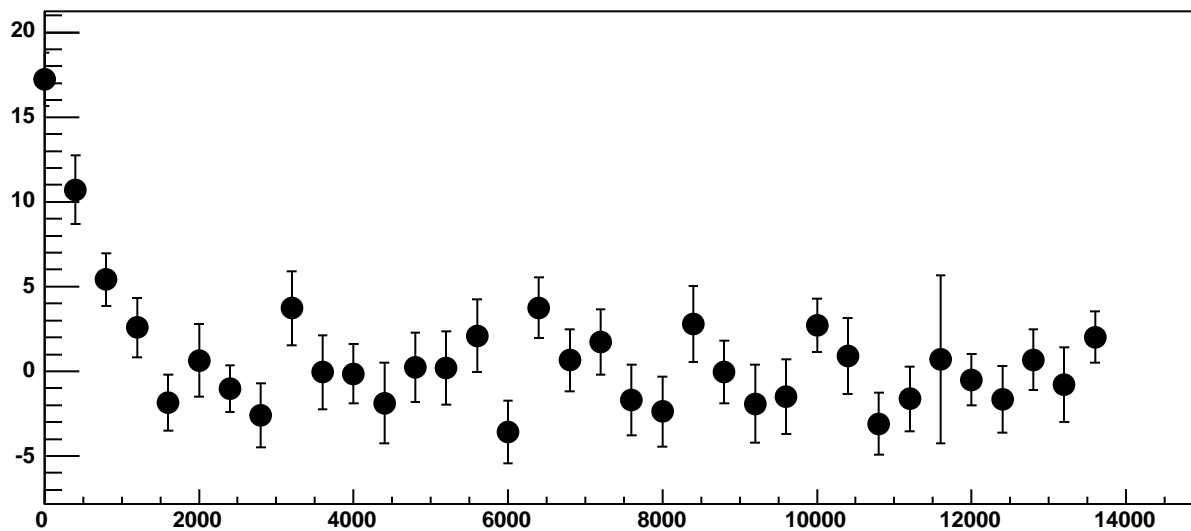
Chip 0, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



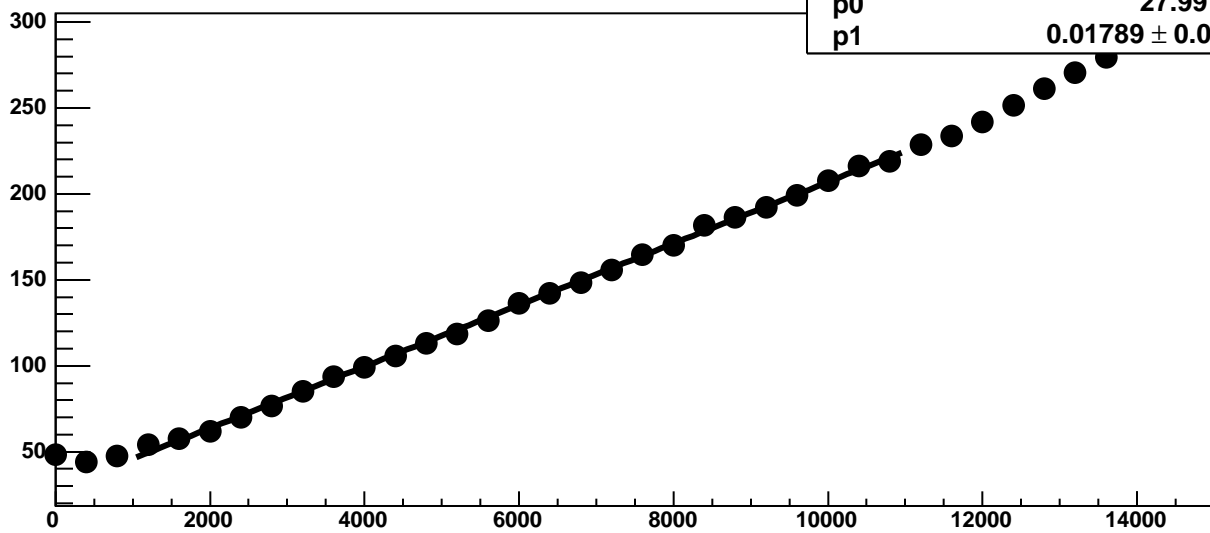
Chip 0, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

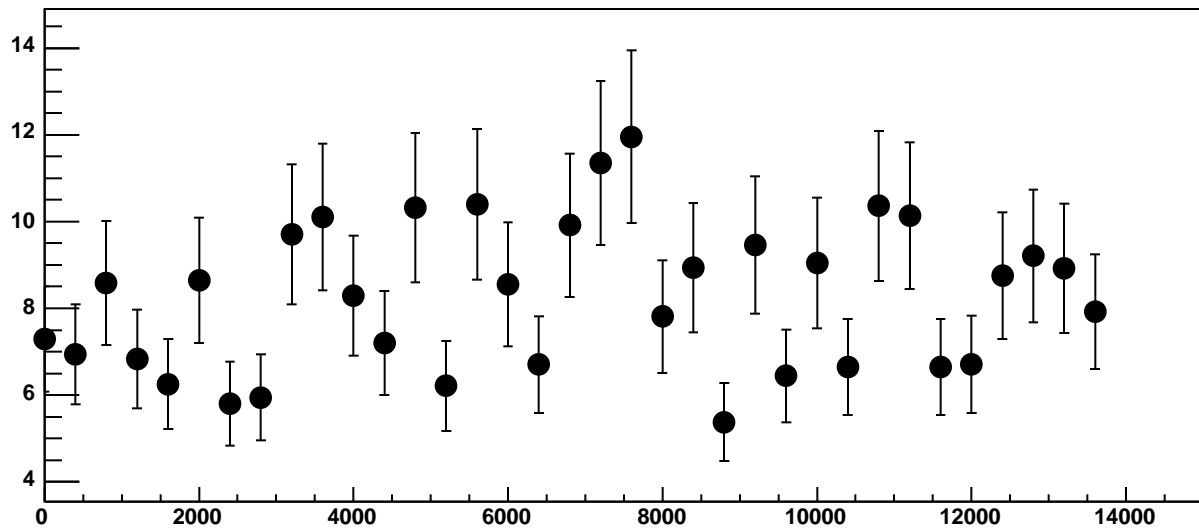


Chip 0, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

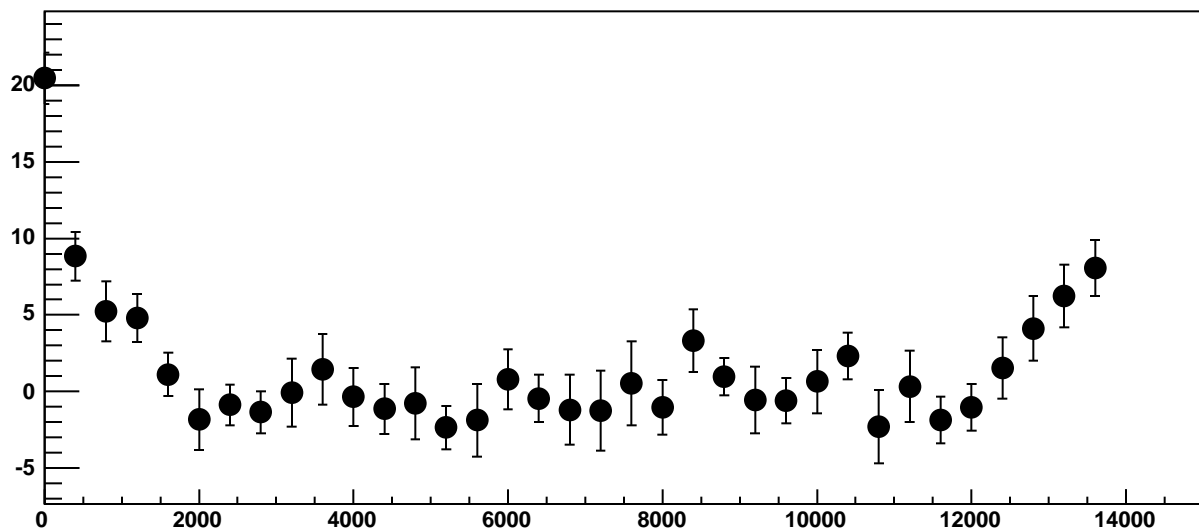


χ^2 / ndf 24.63 / 23
p0 27.99 ± 0.76
p1 0.01789 ± 0.0001169

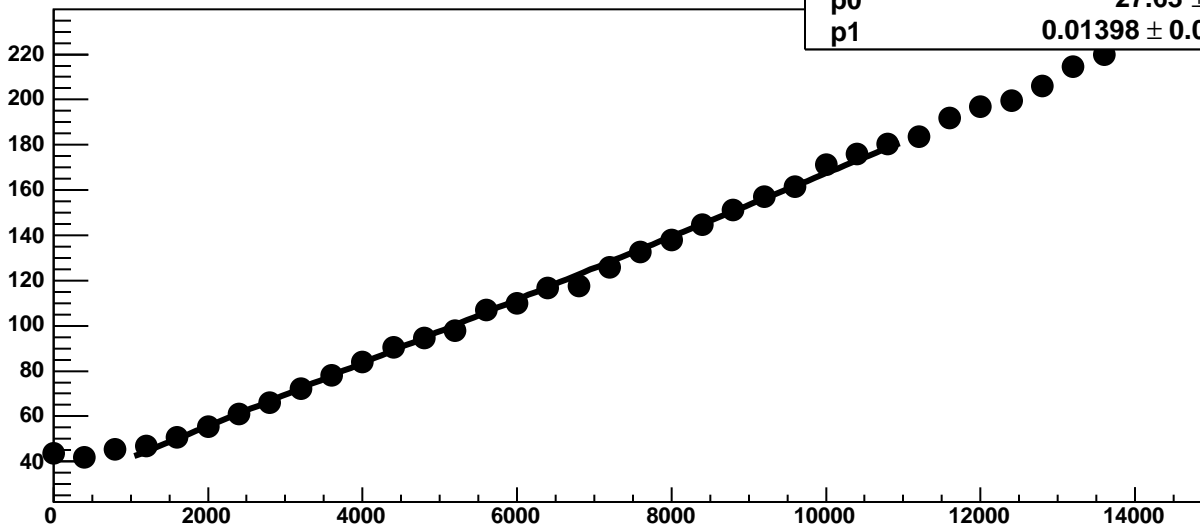
Chip 0, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

20.52 / 23

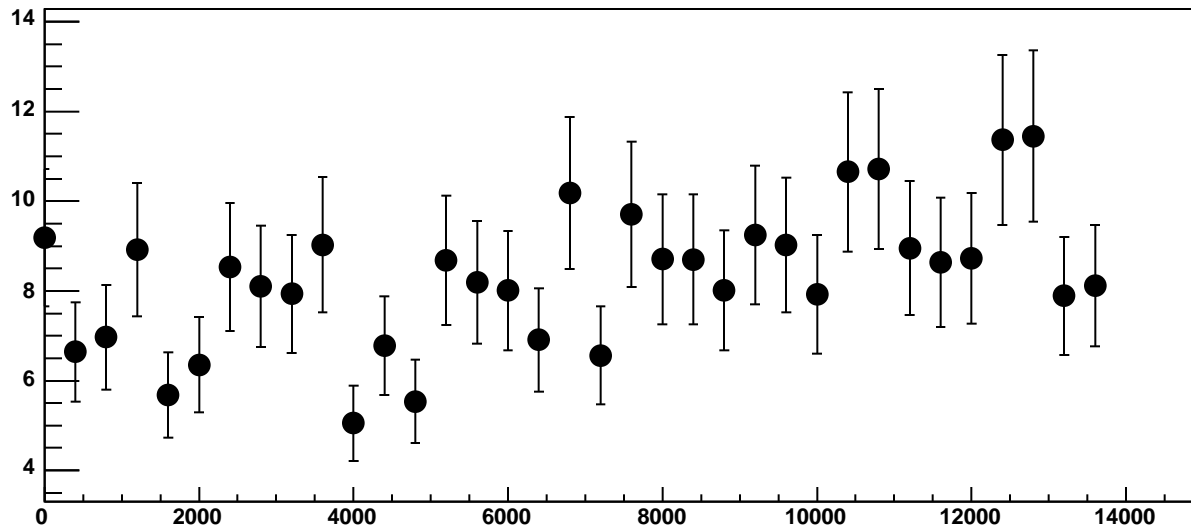
p0

27.63 ± 0.7812

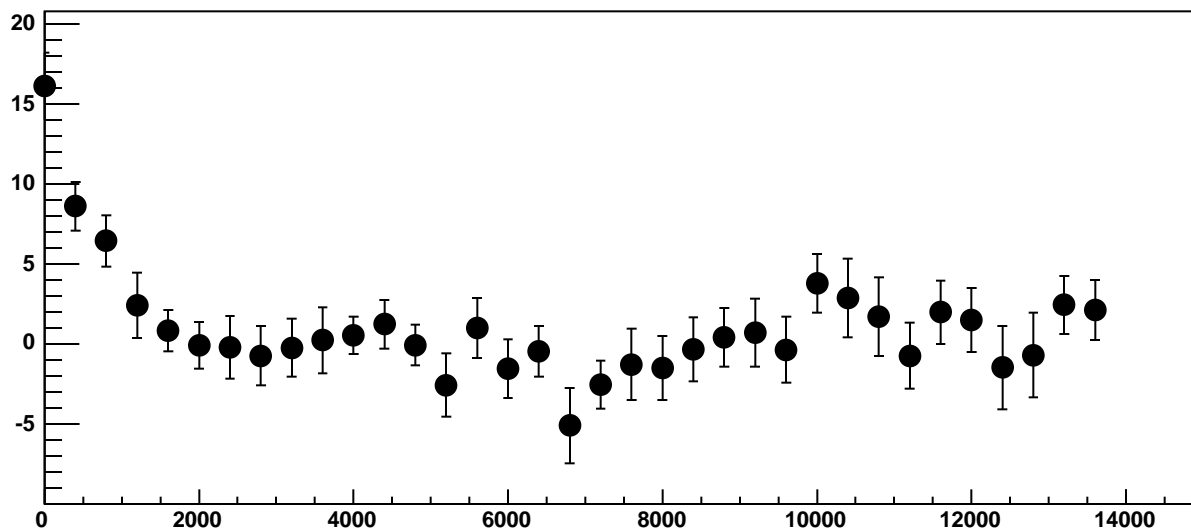
p1

0.01398 ± 0.0001289

Chip 0, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

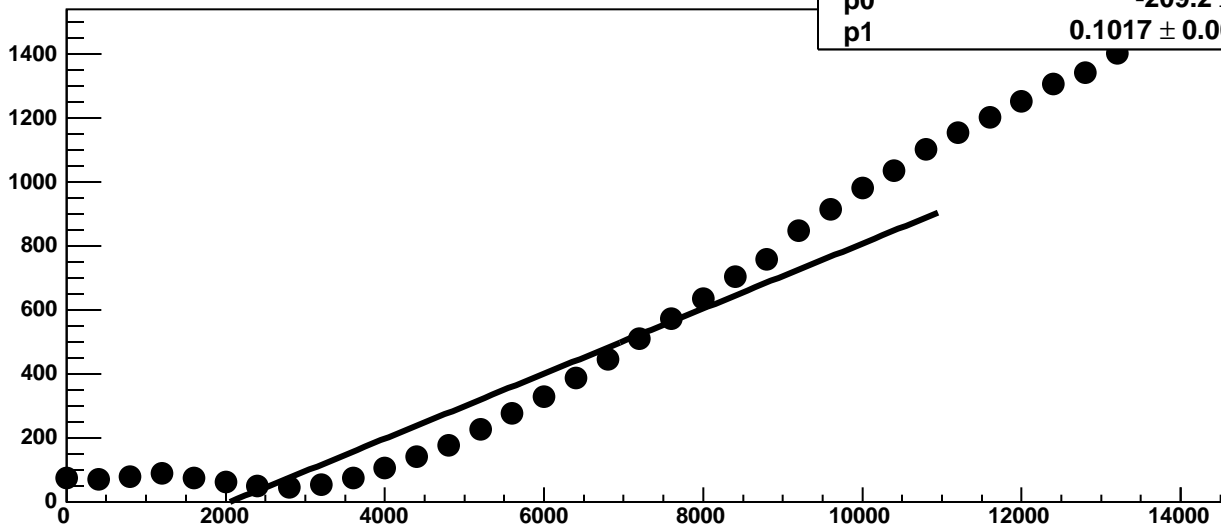


Chip 0, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

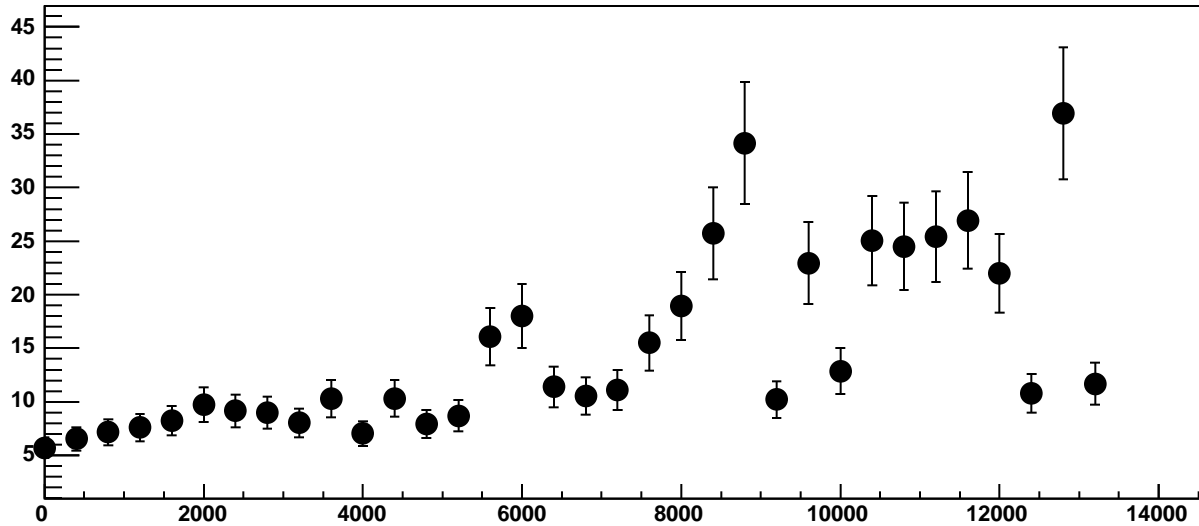


Chip 0, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

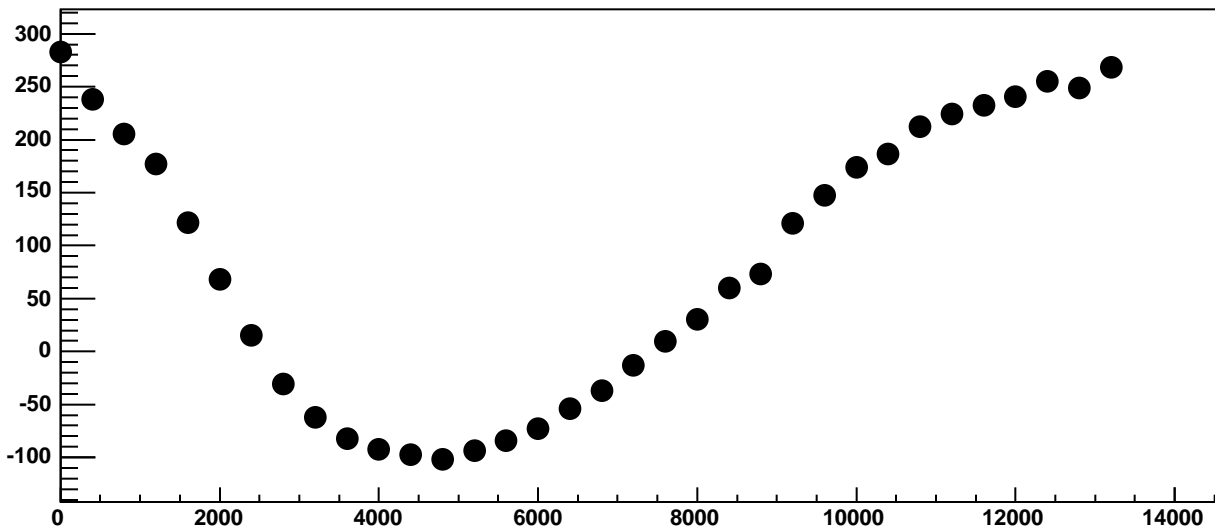
χ^2 / ndf 3.94e+04 / 23
p0 -209.2 ± 1.029
p1 0.1017 ± 0.0001985



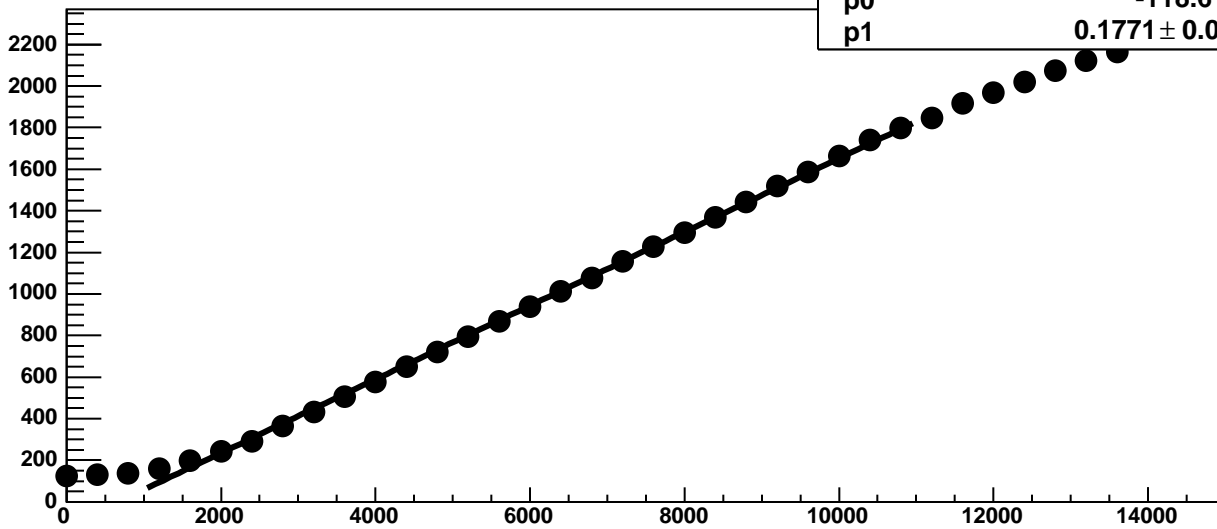
Chip 0, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



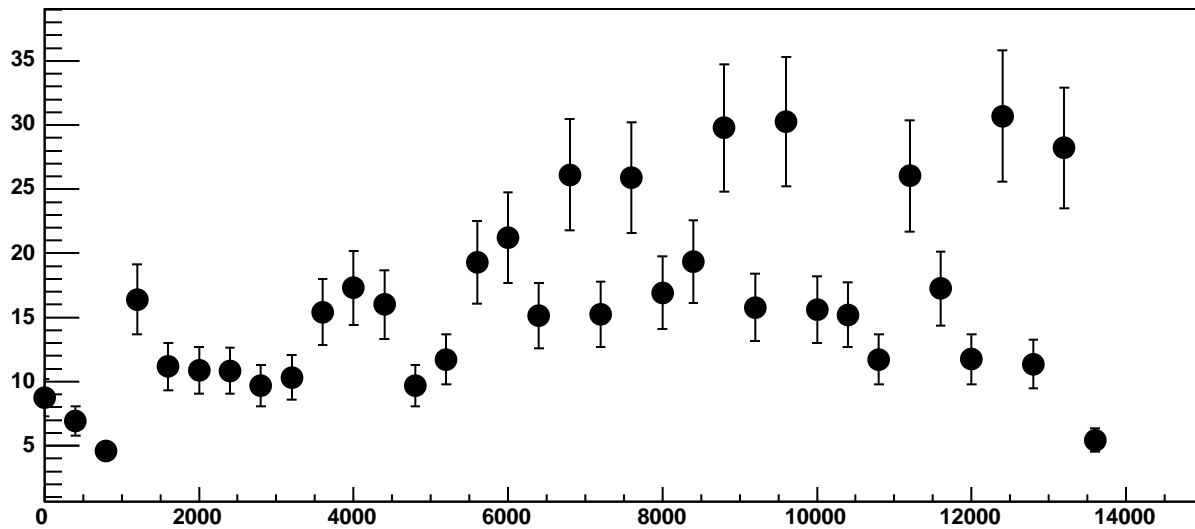
Chip 0, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



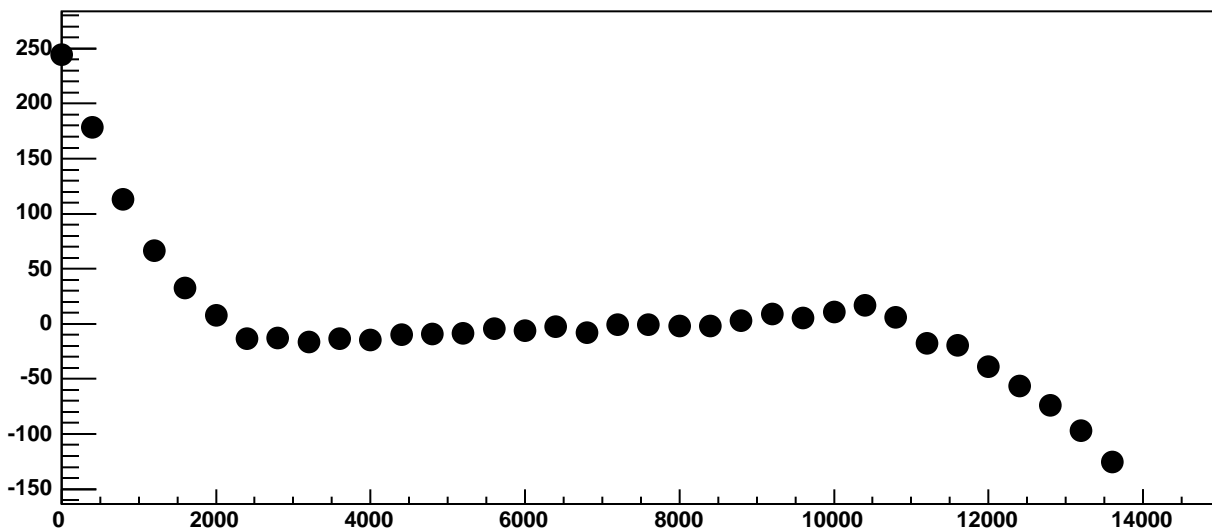
Chip 0, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC



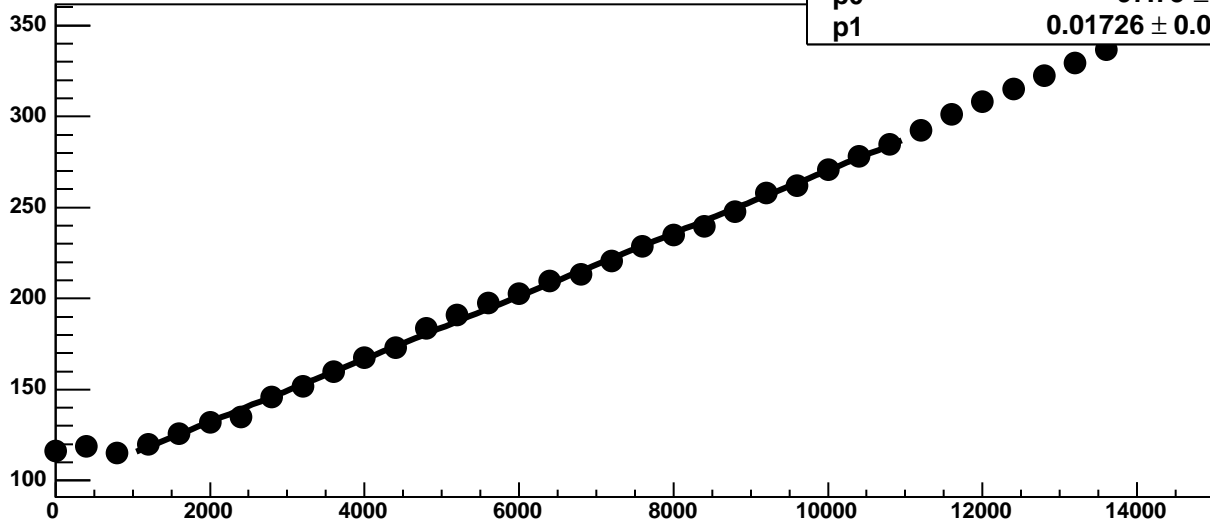
Chip 0, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

24.03 / 23

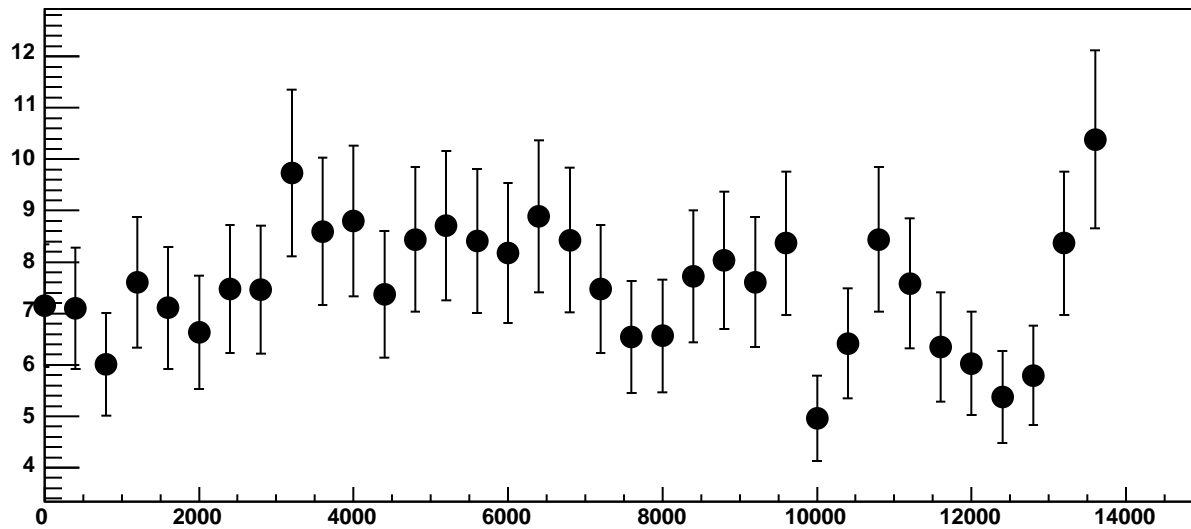
p0

97.73 ± 0.7943

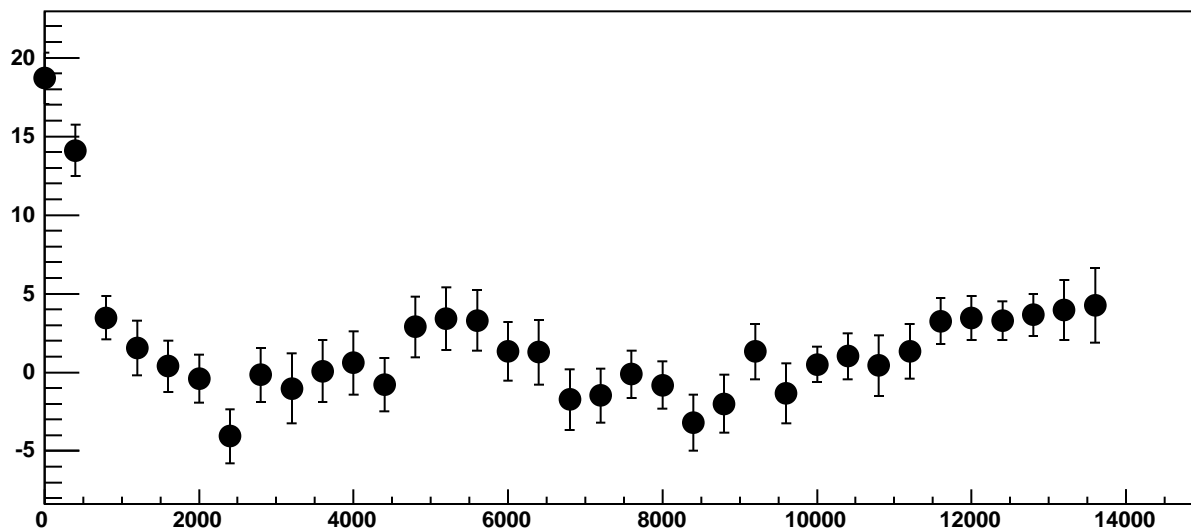
p1

0.01726 ± 0.0001142

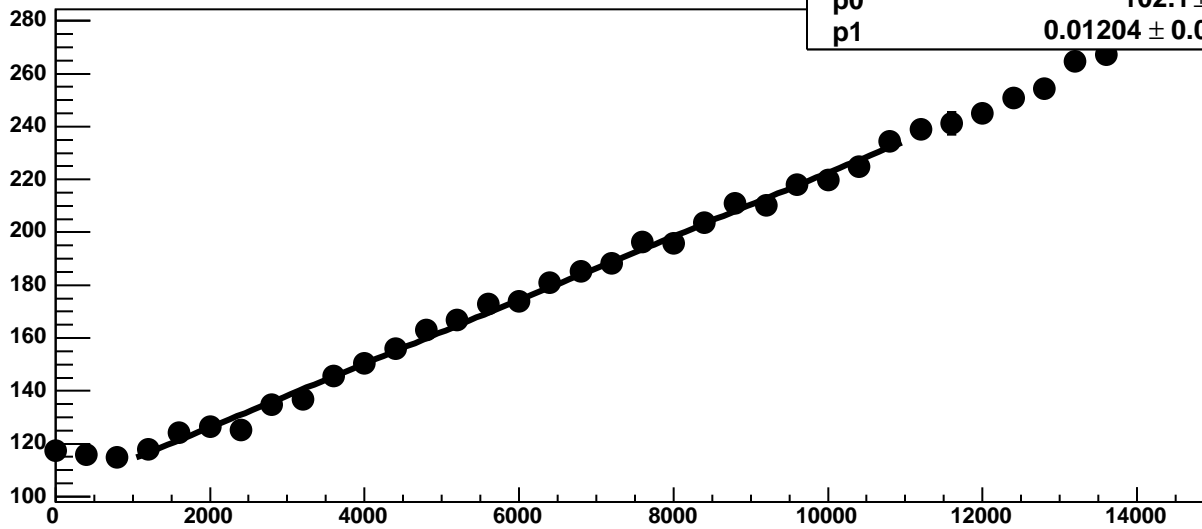
Chip 0, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

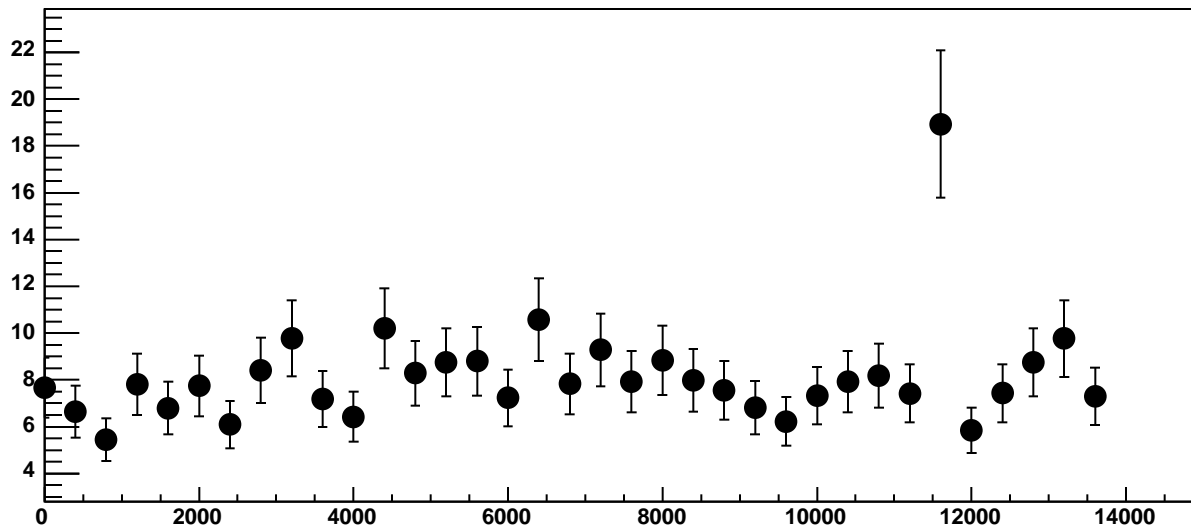


Chip 0, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

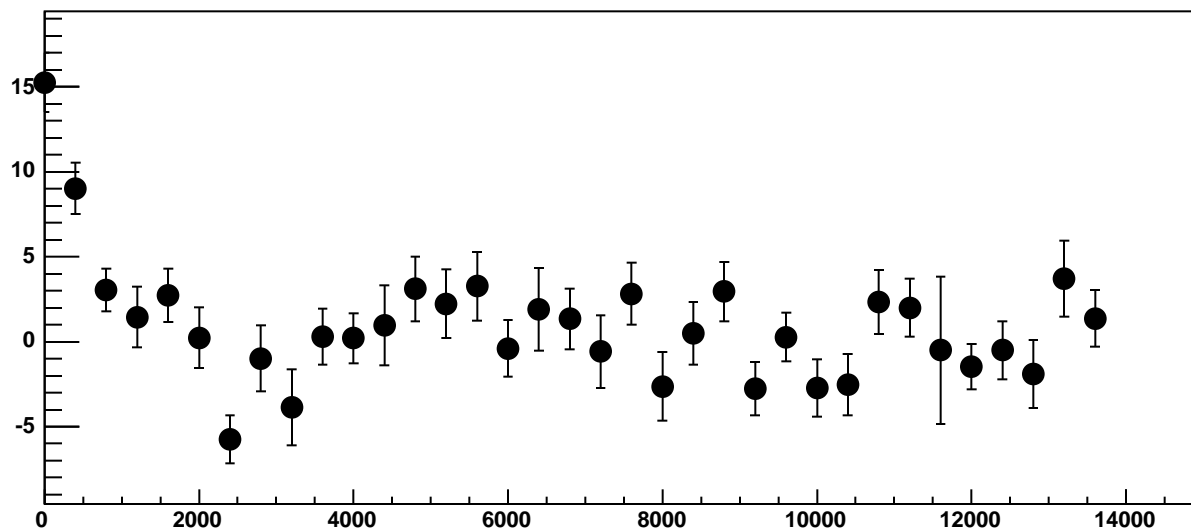


χ^2 / ndf 48.13 / 23
p0 102.1 ± 0.7919
p1 0.01204 ± 0.0001183

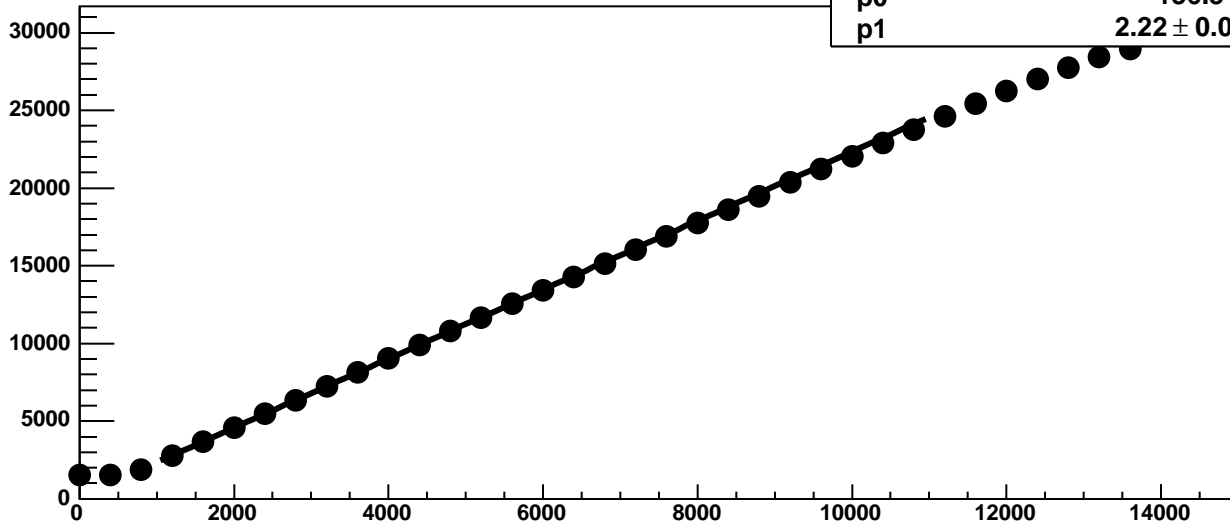
Chip 0, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

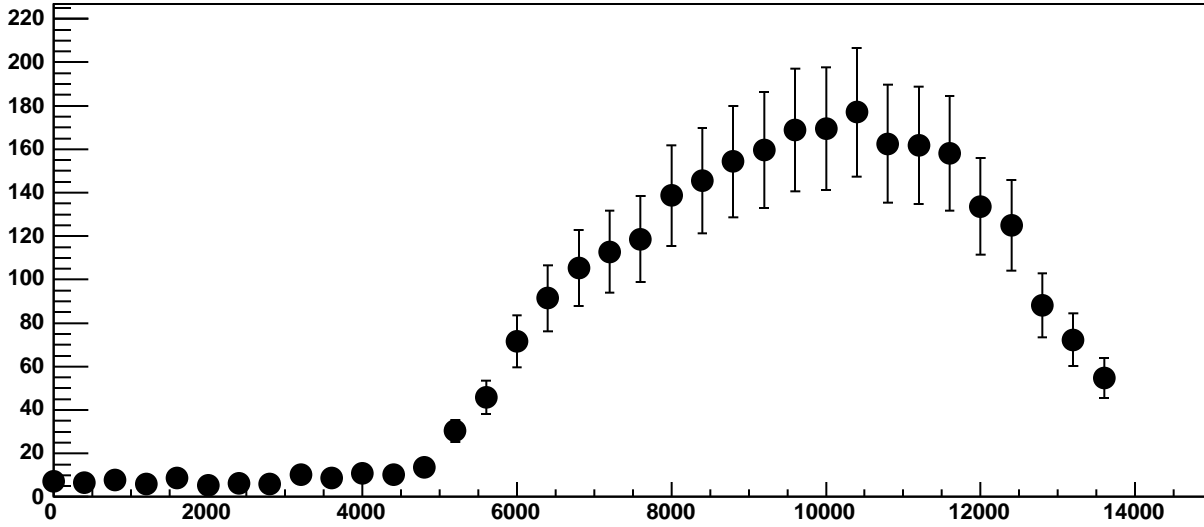


Chip 0, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC

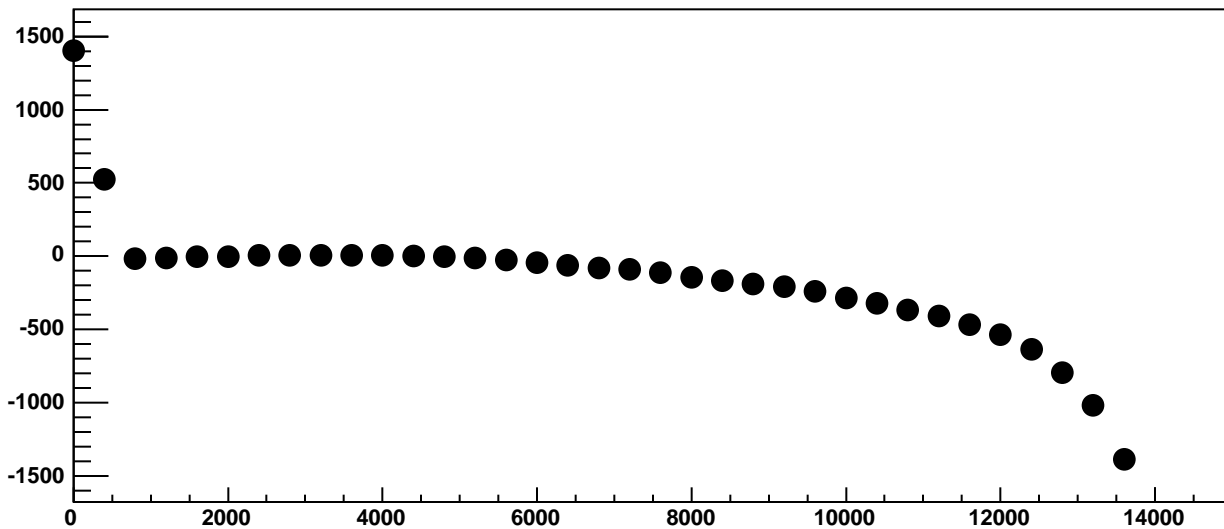


χ^2 / ndf 590.3 / 23
p0 136.5 ± 1.419
p1 2.22 ± 0.0005069

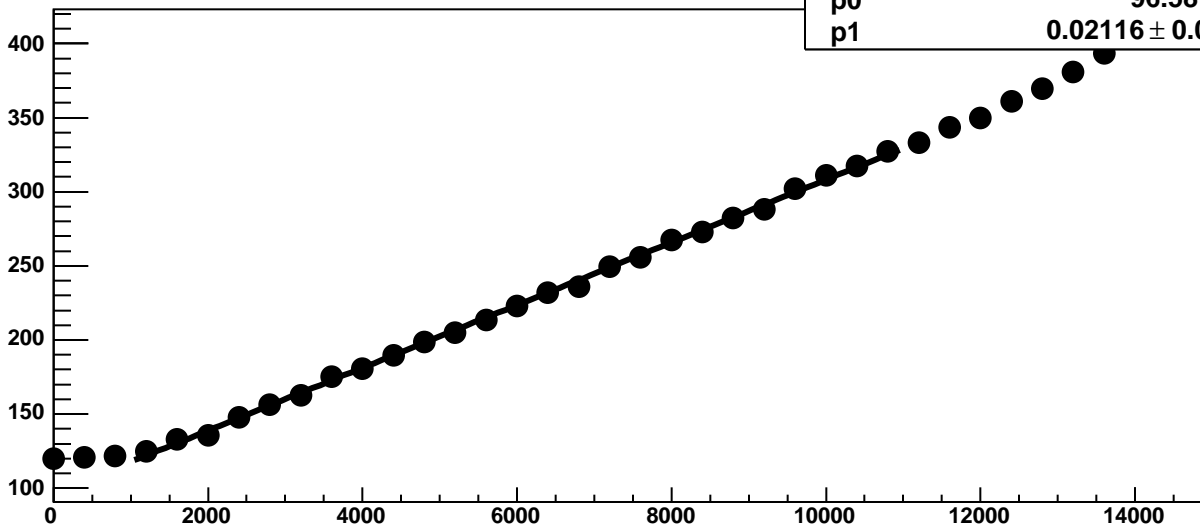
Chip 0, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC

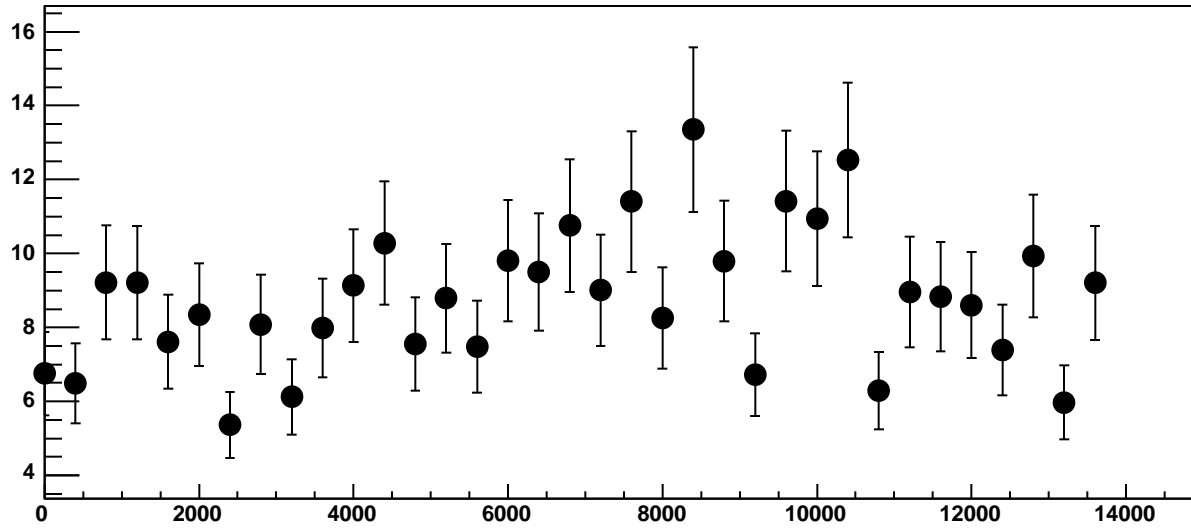


Chip 0, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

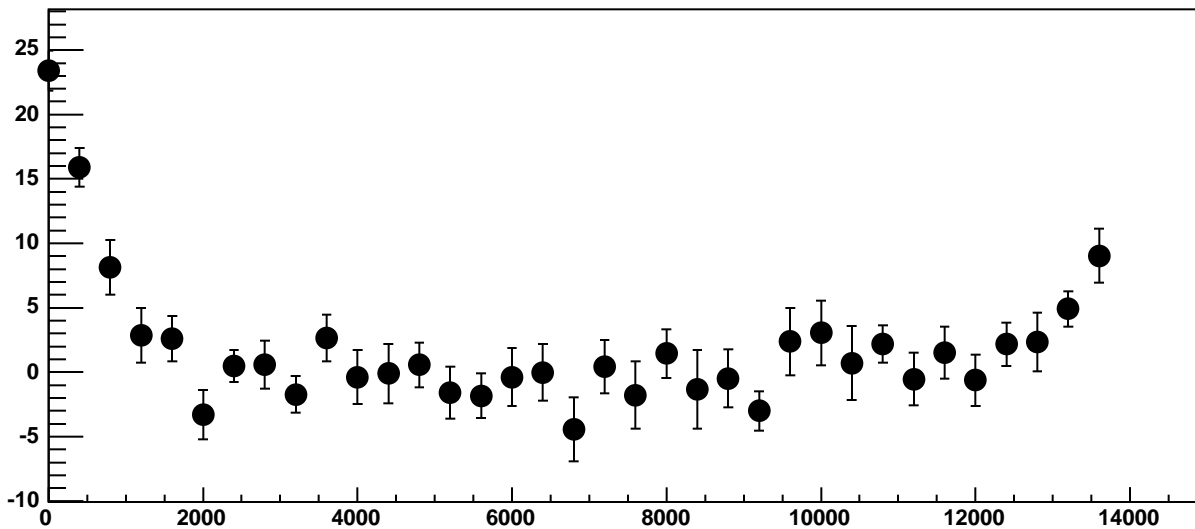


χ^2 / ndf 25.78 / 23
p0 96.58 ± 0.812
p1 0.02116 ± 0.0001291

Chip 0, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

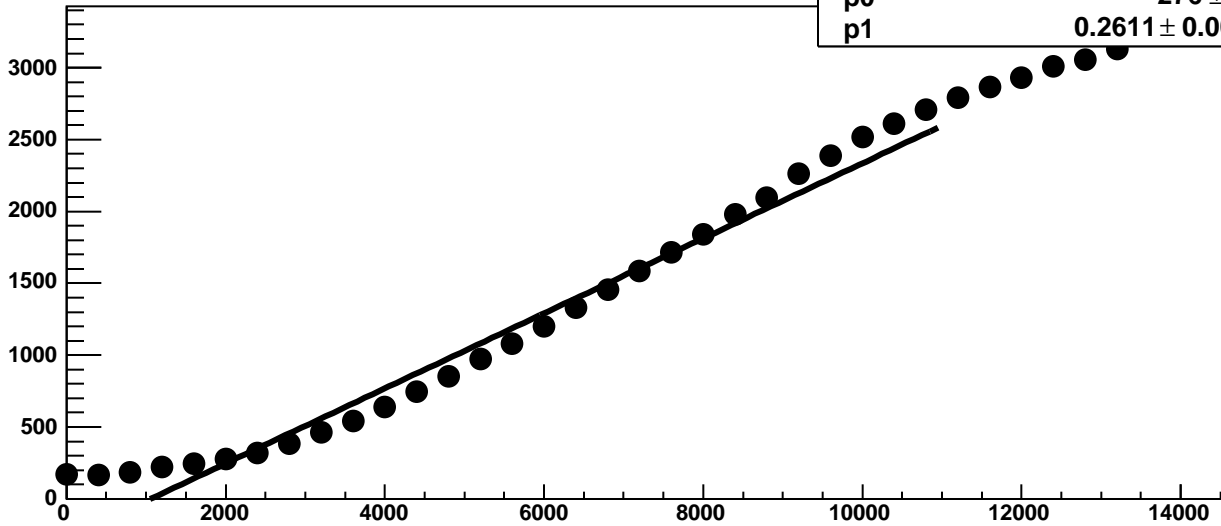


Chip 0, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

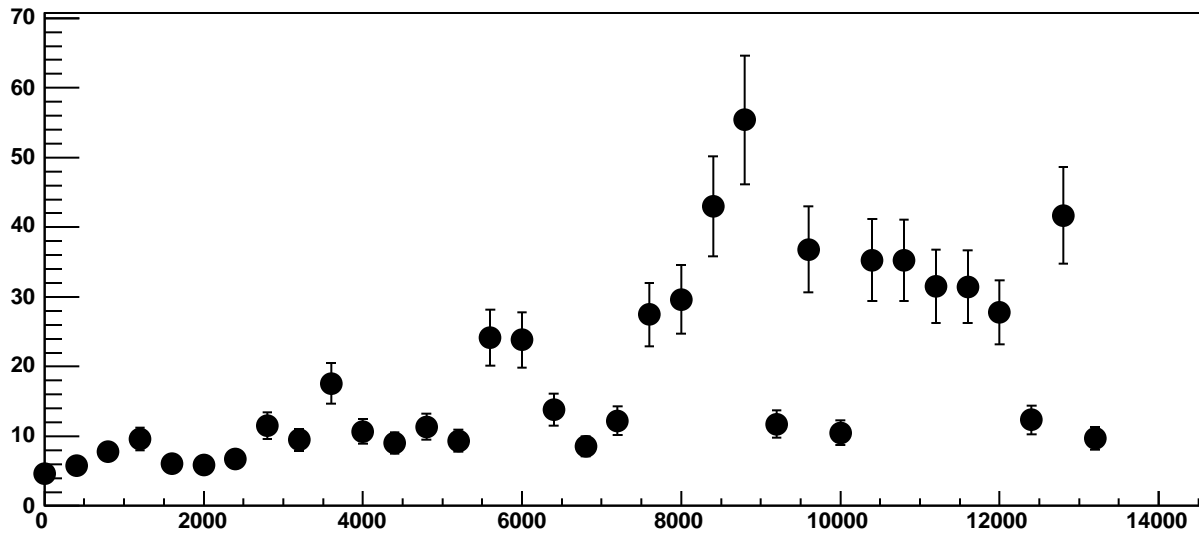


Chip 0, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

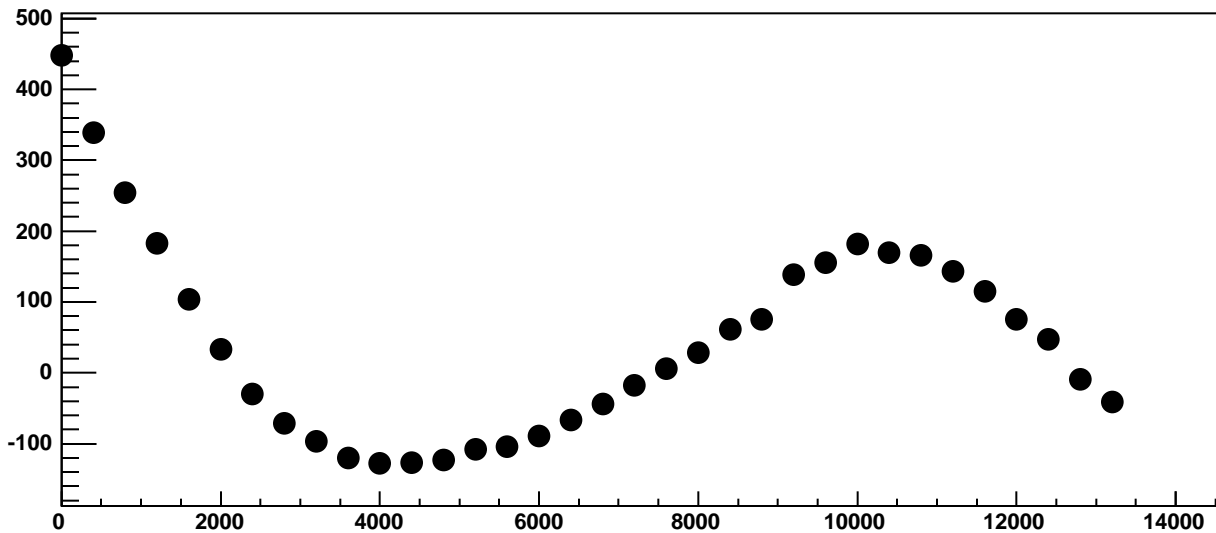
χ^2 / ndf 3.956e+04 / 23
p0 -276 ± 0.9531
p1 0.2611 ± 0.0001979



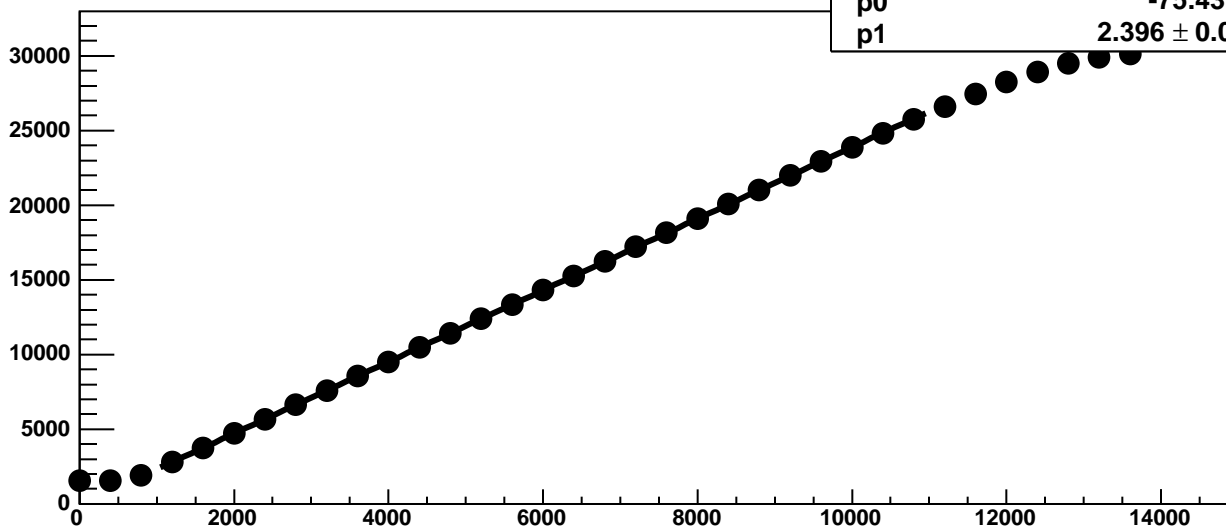
Chip 0, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

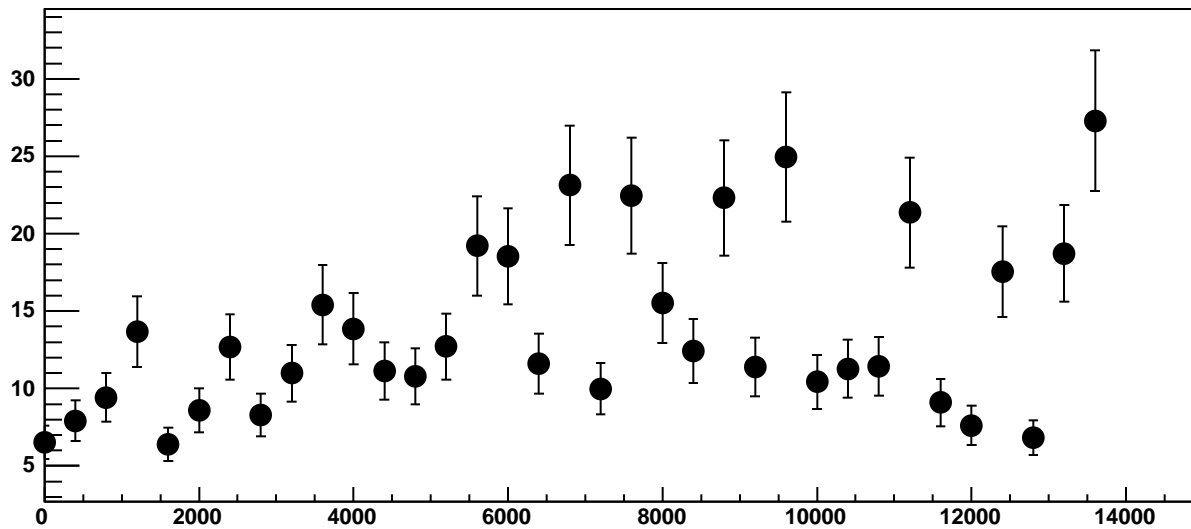


Chip 0, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC

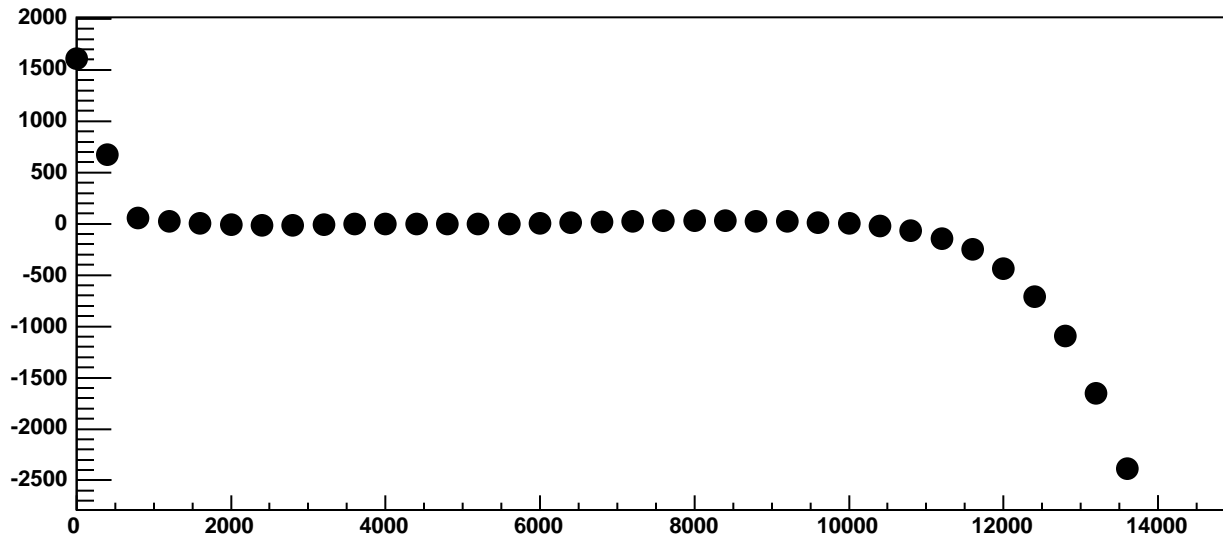


χ^2 / ndf 1357 / 23
p0 -75.43 ± 1.054
p1 2.396 ± 0.0001737

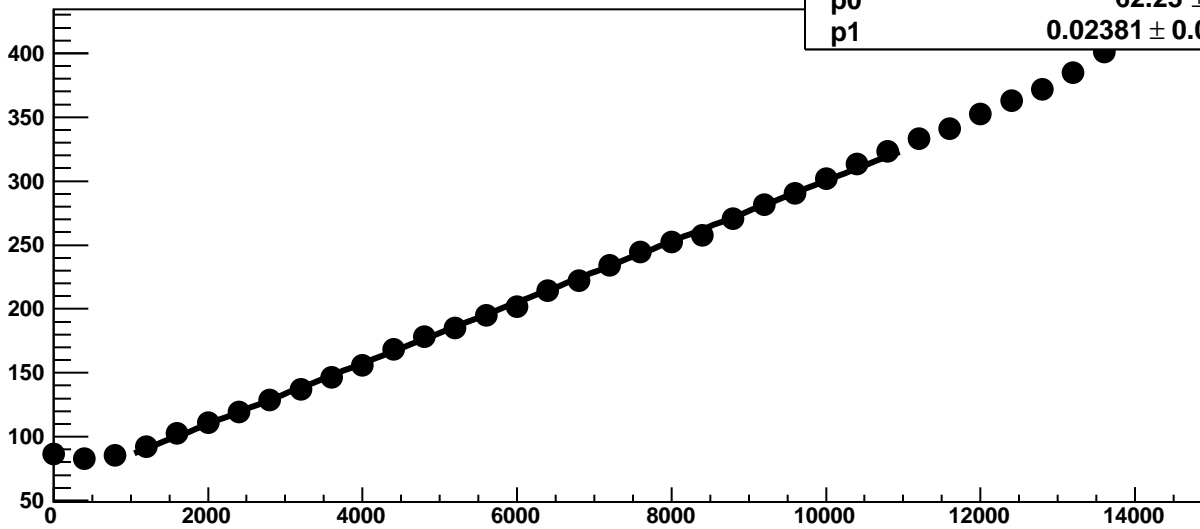
Chip 0, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC

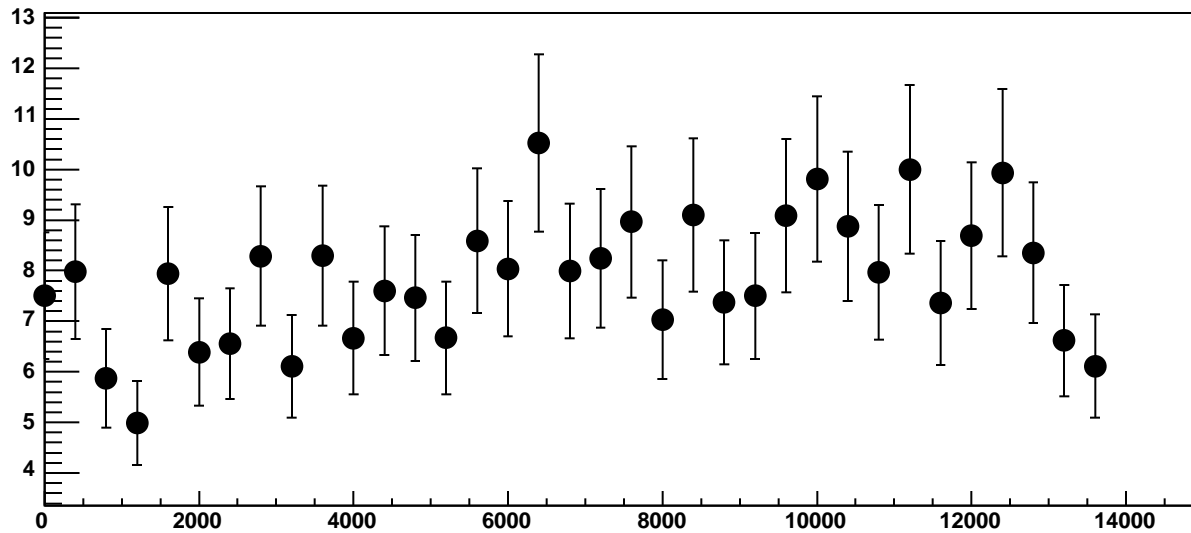


Chip 0, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

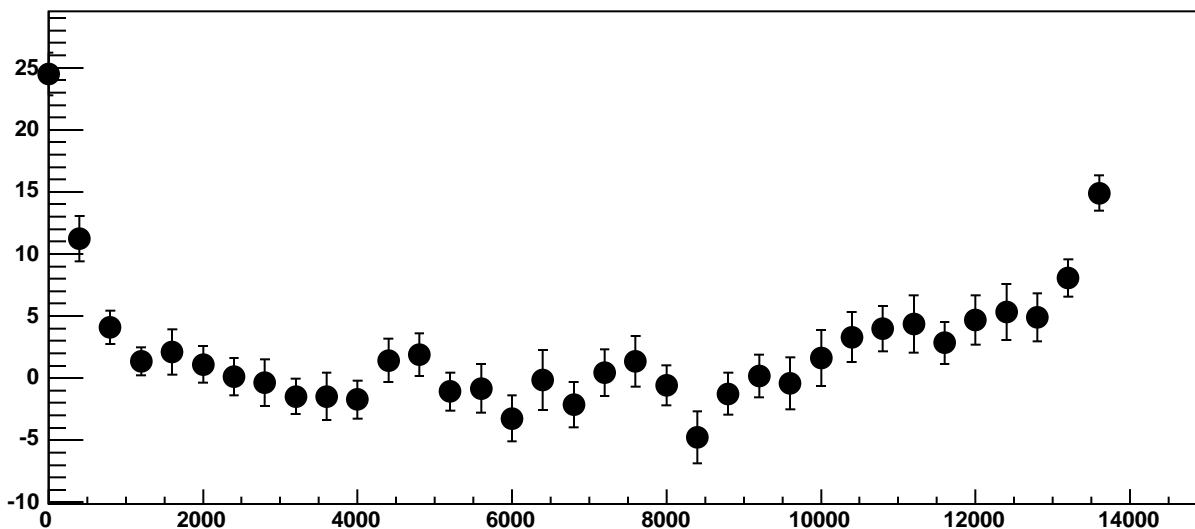


χ^2 / ndf 27.74 / 23
p0 62.23 ± 0.7183
p1 0.02381 ± 0.0001169

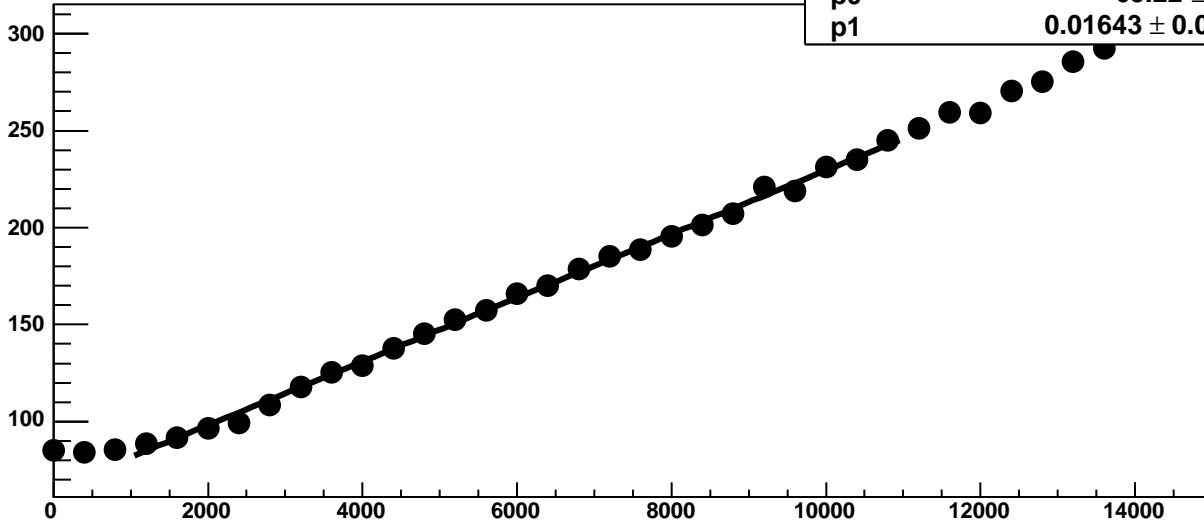
Chip 0, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



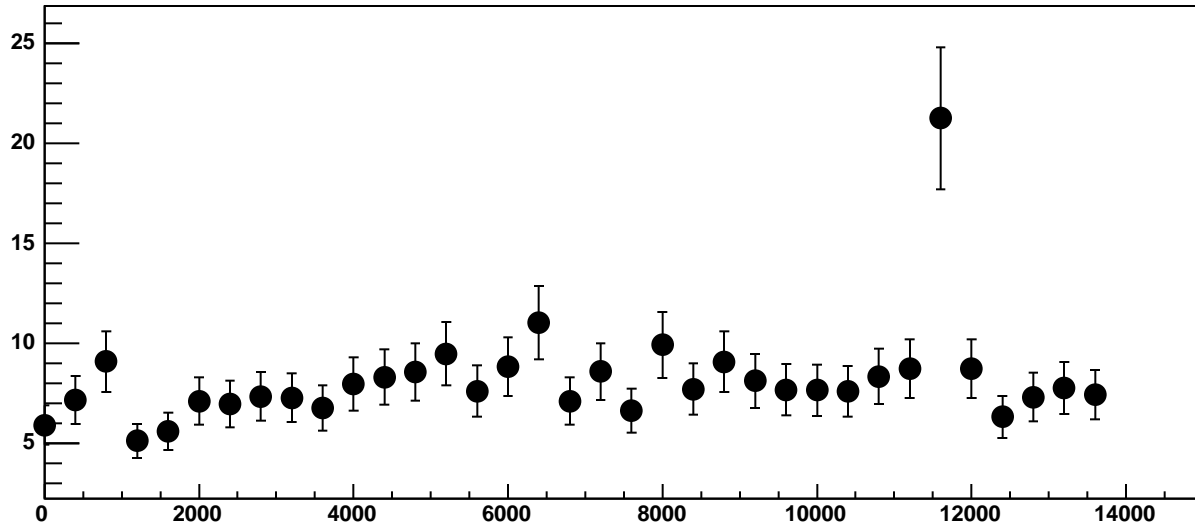
Chip 0, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



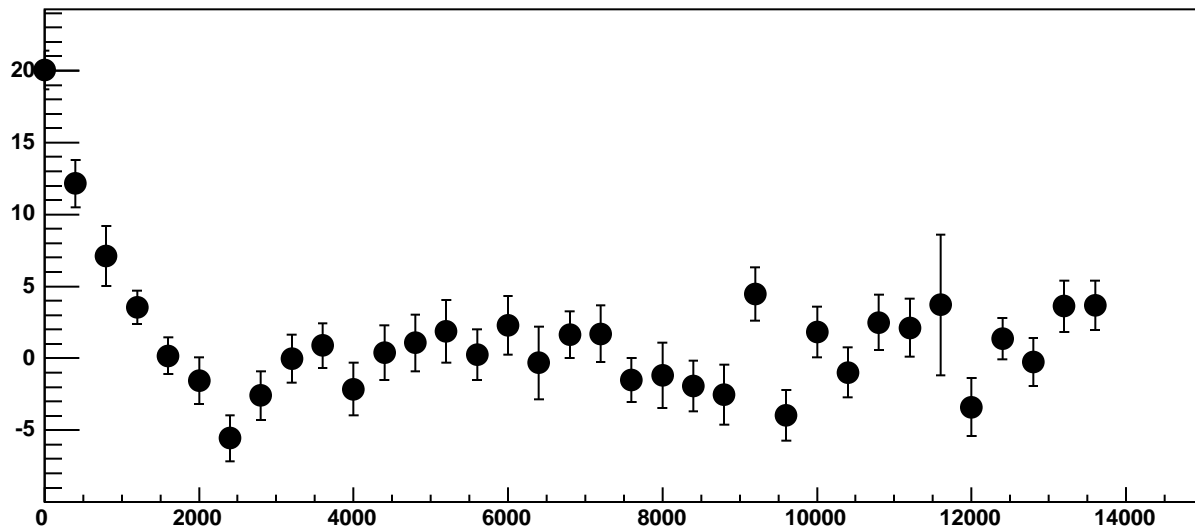
Chip 0, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



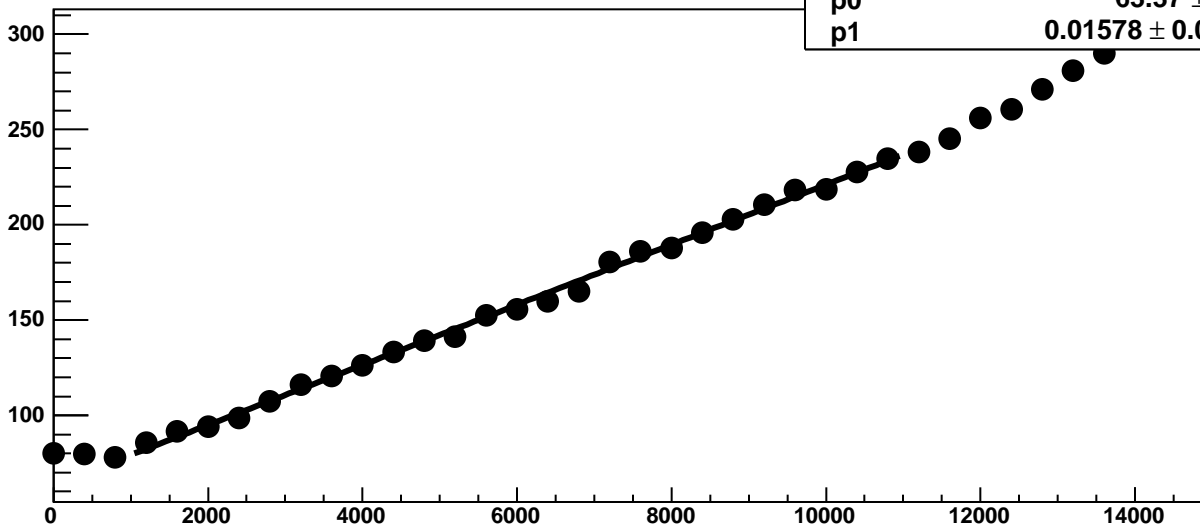
Chip 0, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

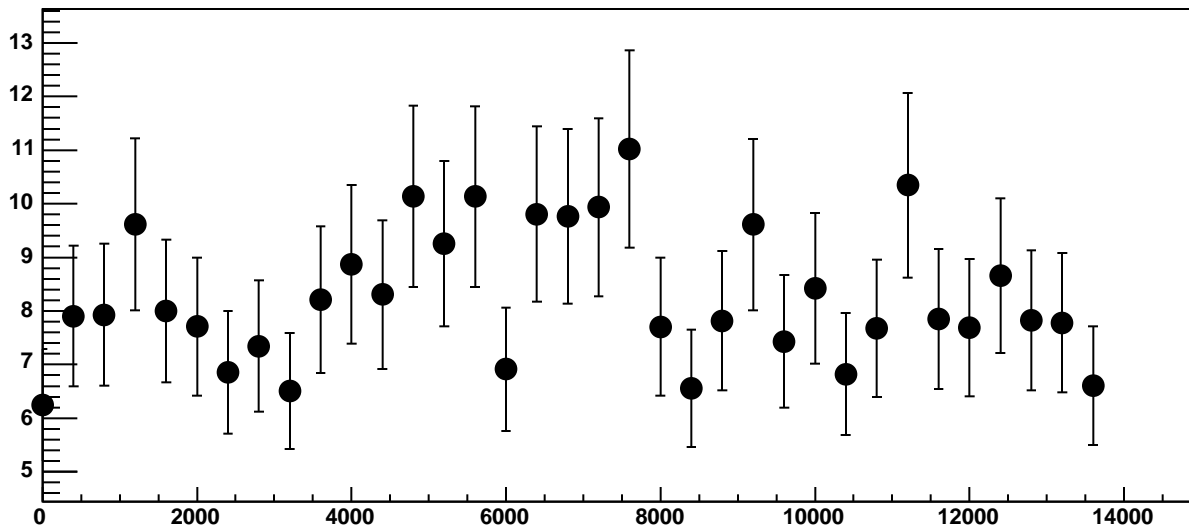


Chip 0, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

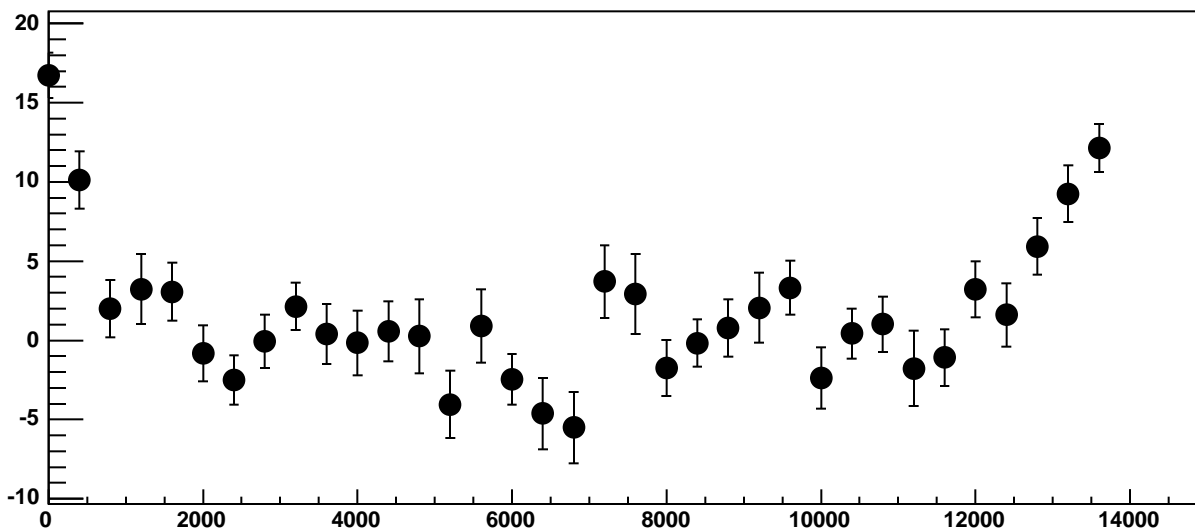


χ^2 / ndf 38.05 / 23
p0 63.37 ± 0.8345
p1 0.01578 ± 0.0001241

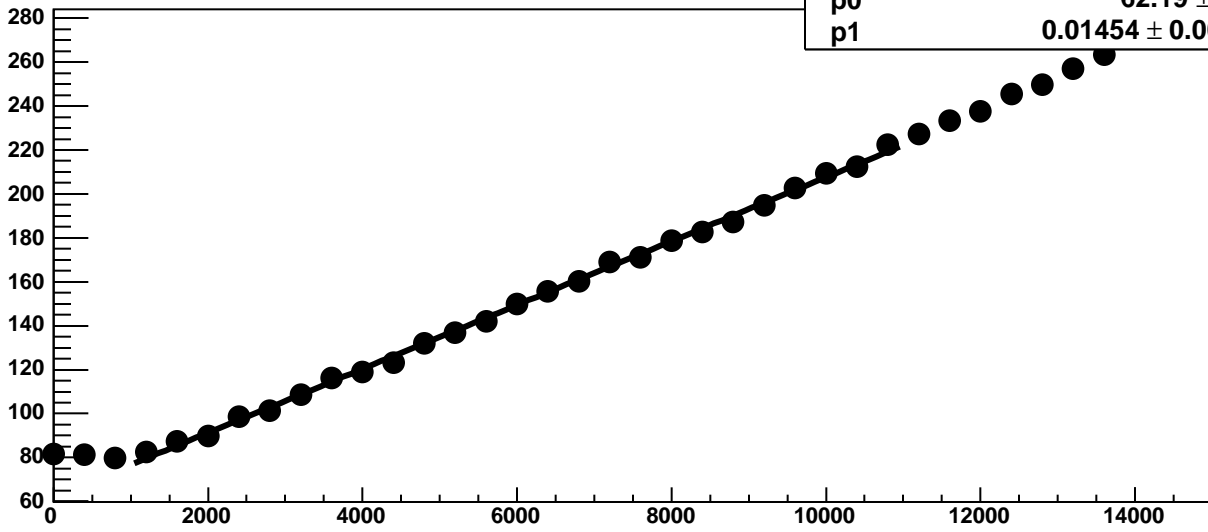
Chip 0, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

21.24 / 23

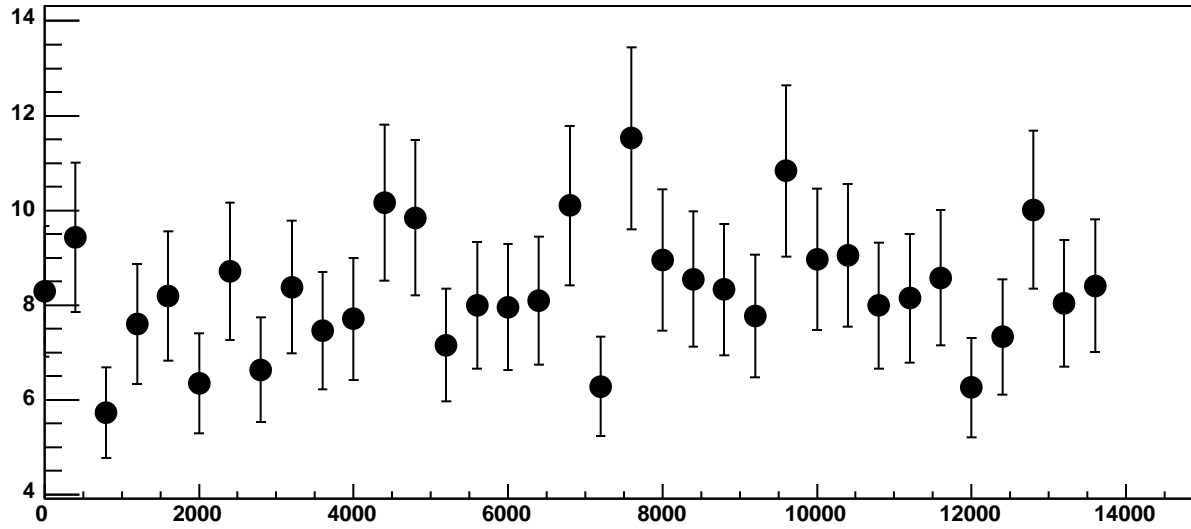
p0

62.19 ± 0.8221

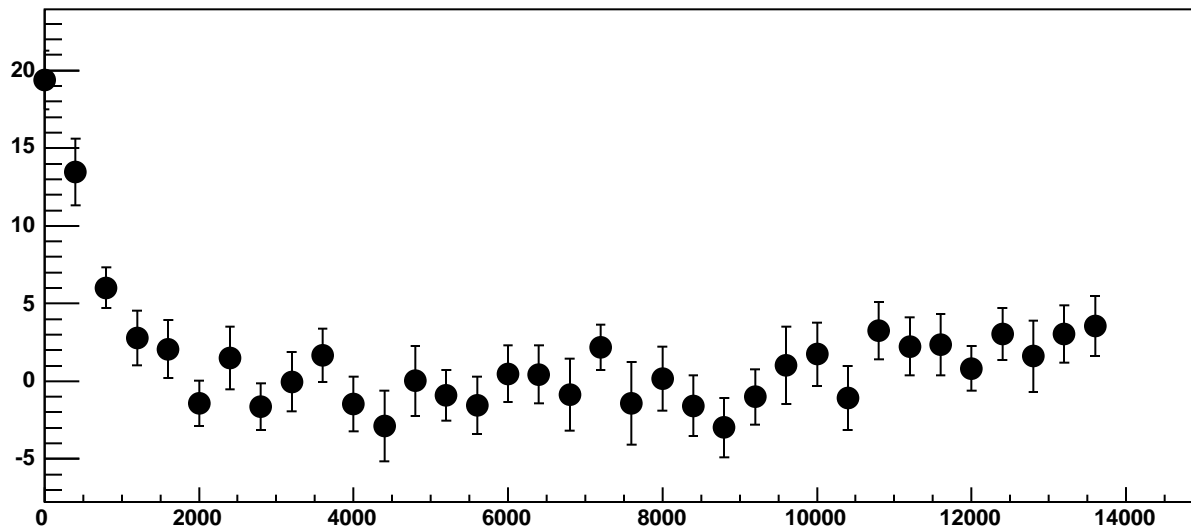
p1

0.01454 ± 0.0001288

Chip 0, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

χ^2 / ndf

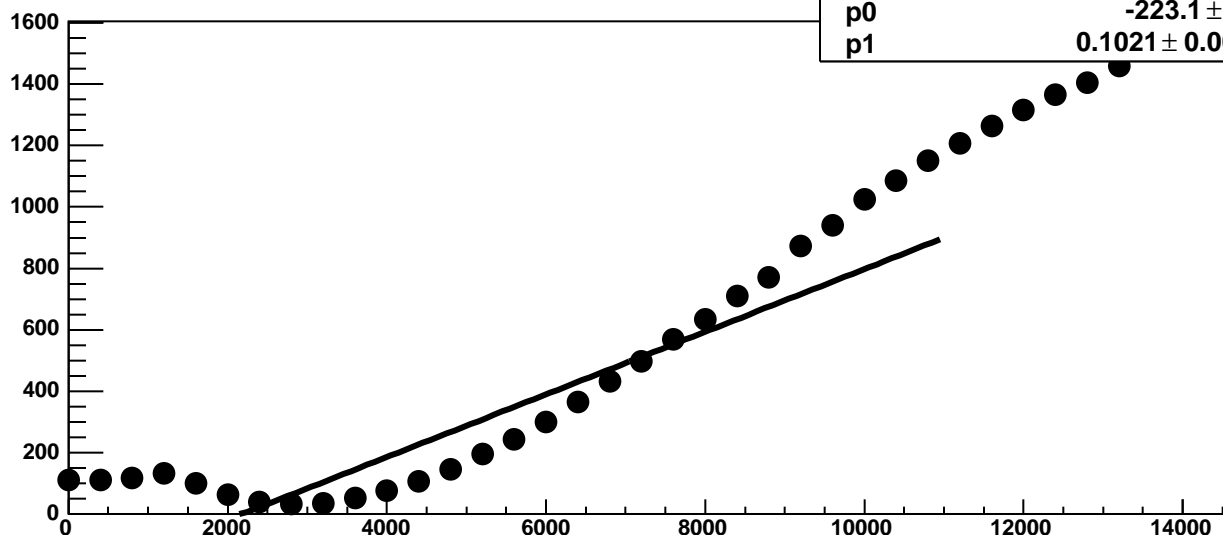
7.564e+04 / 23

p0

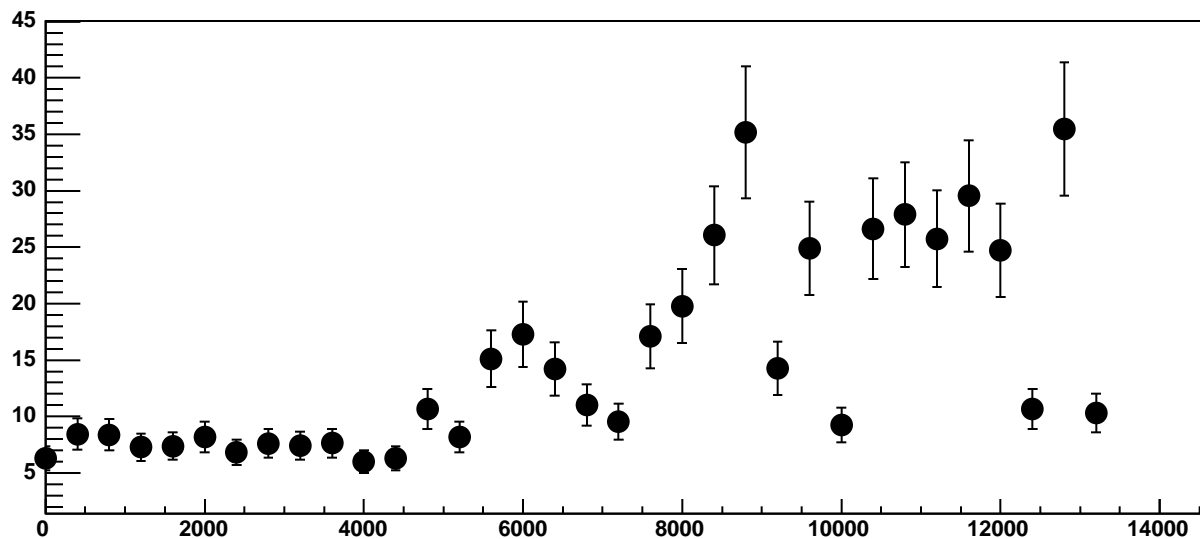
-223.1 ± 0.9147

p1

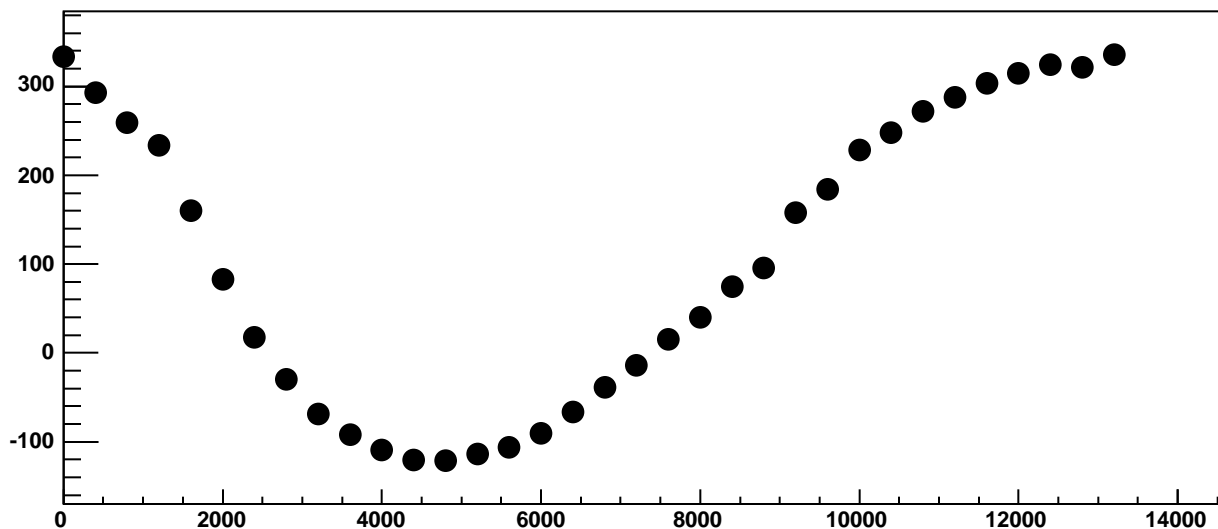
0.1021 ± 0.0001869



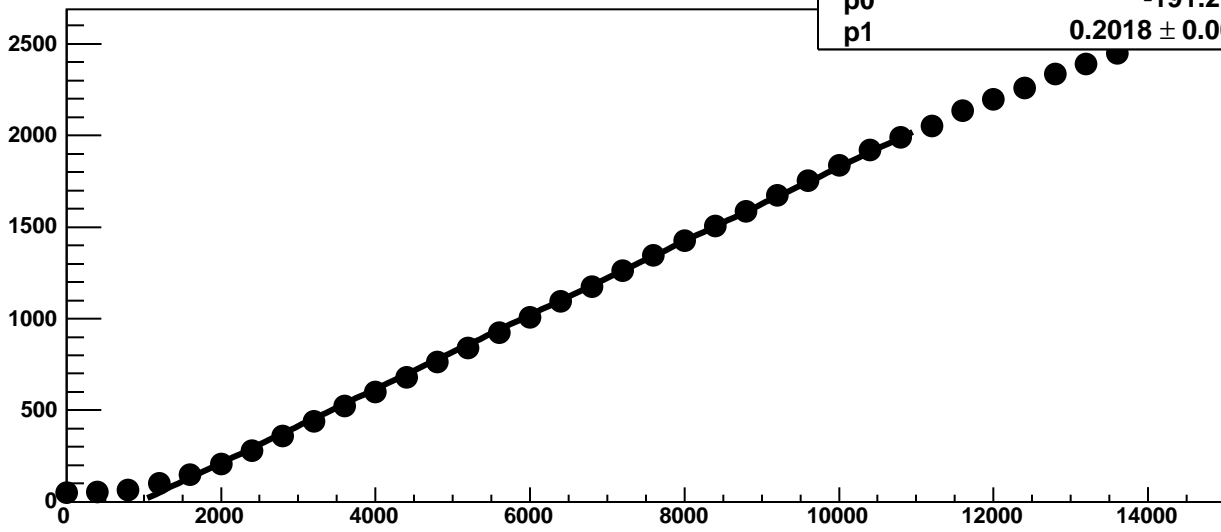
Chip 0, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

631.4 / 23

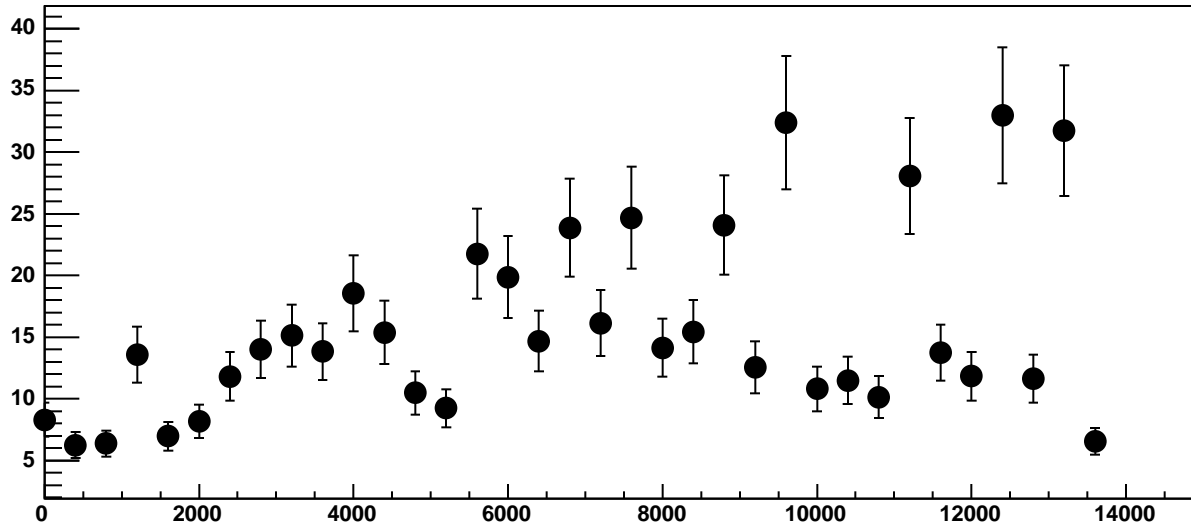
p0

-191.2 ± 1.13

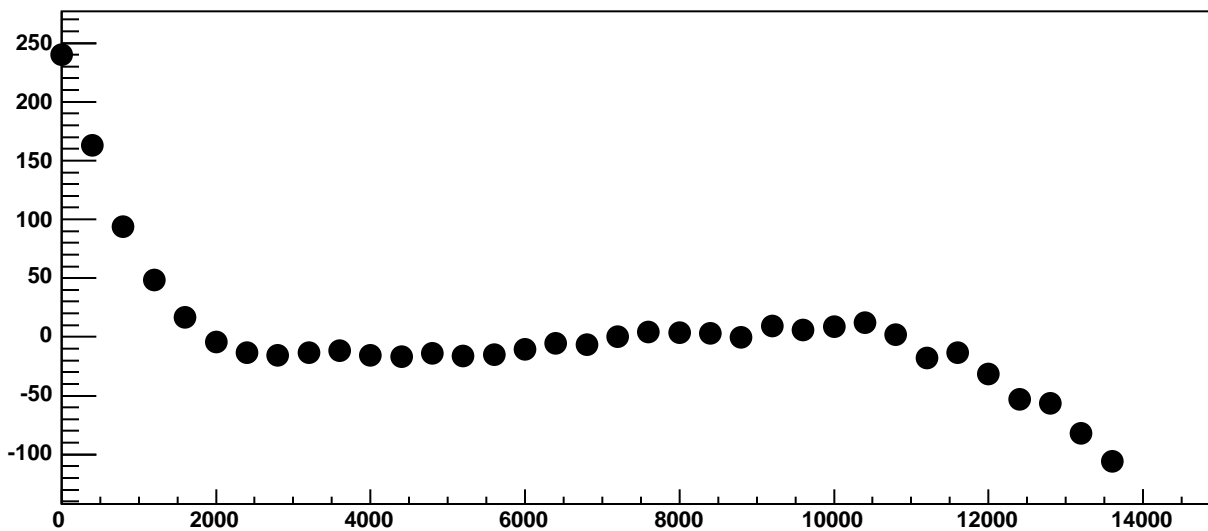
p1

0.2018 ± 0.0001813

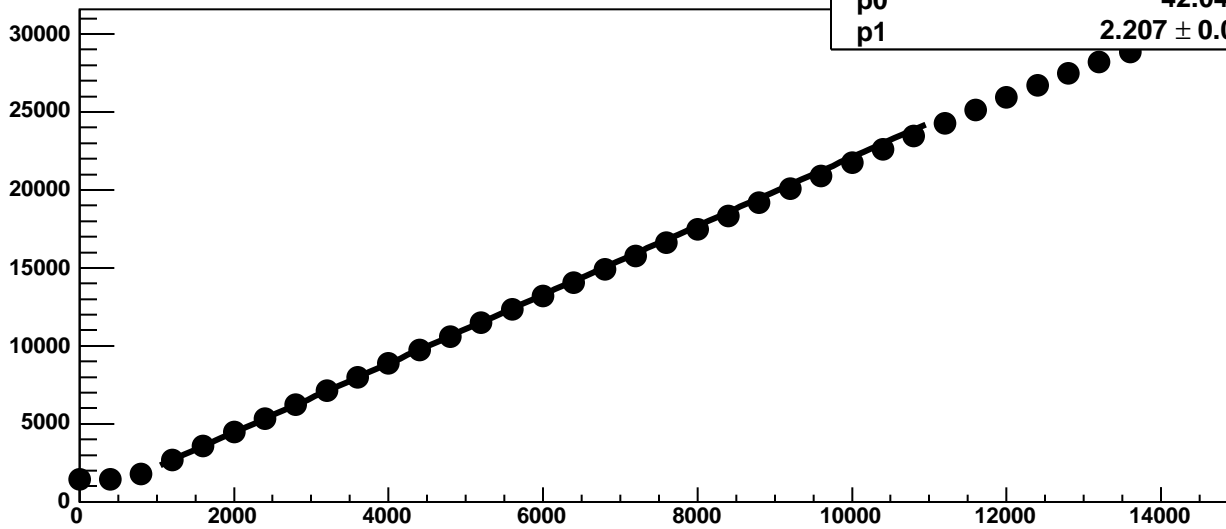
Chip 0, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



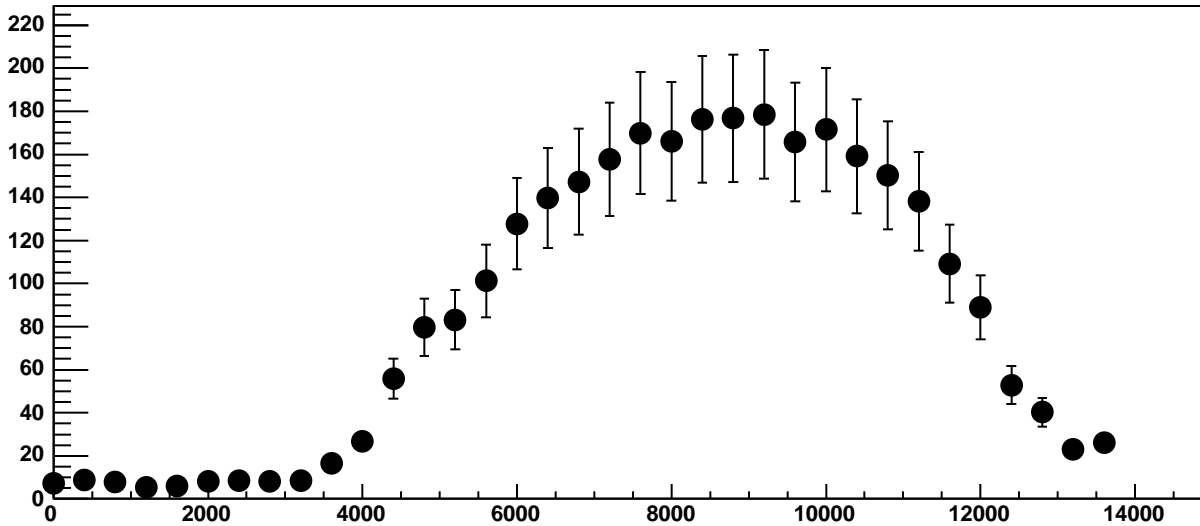
Chip 0, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



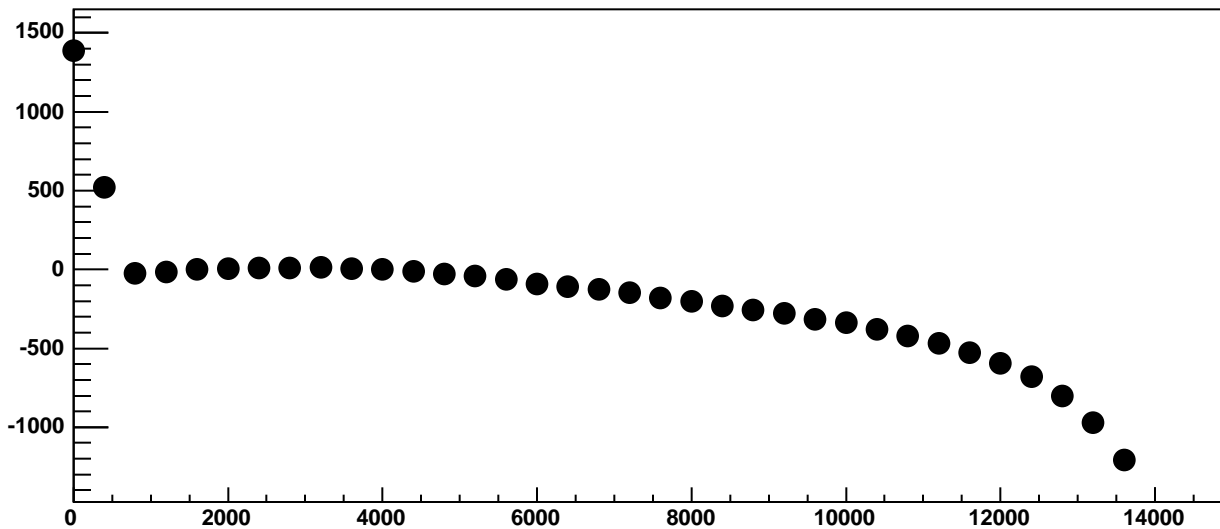
Chip 0, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC



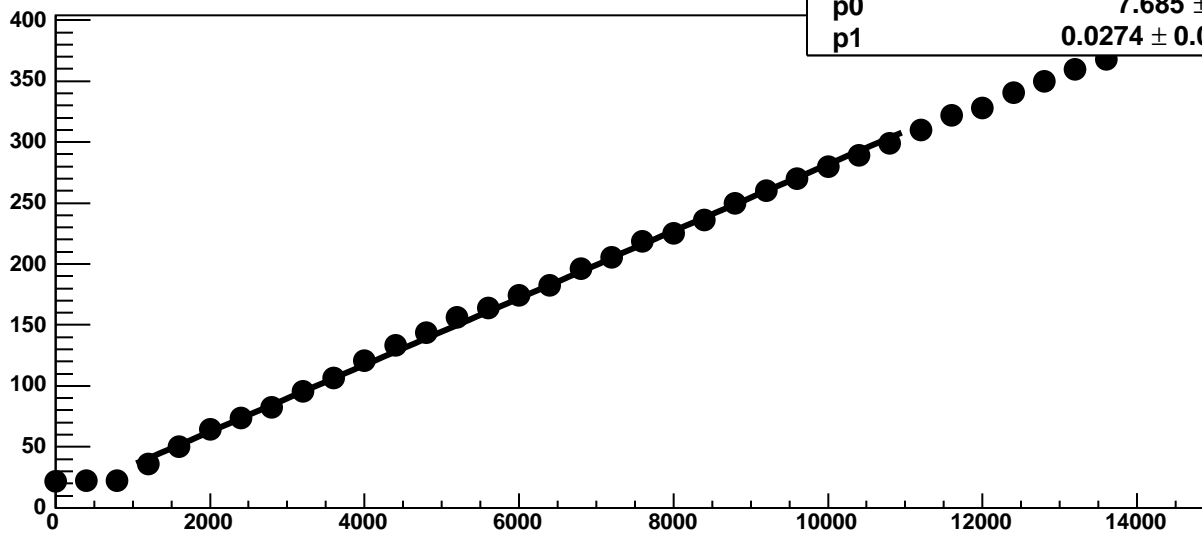
Chip 0, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC

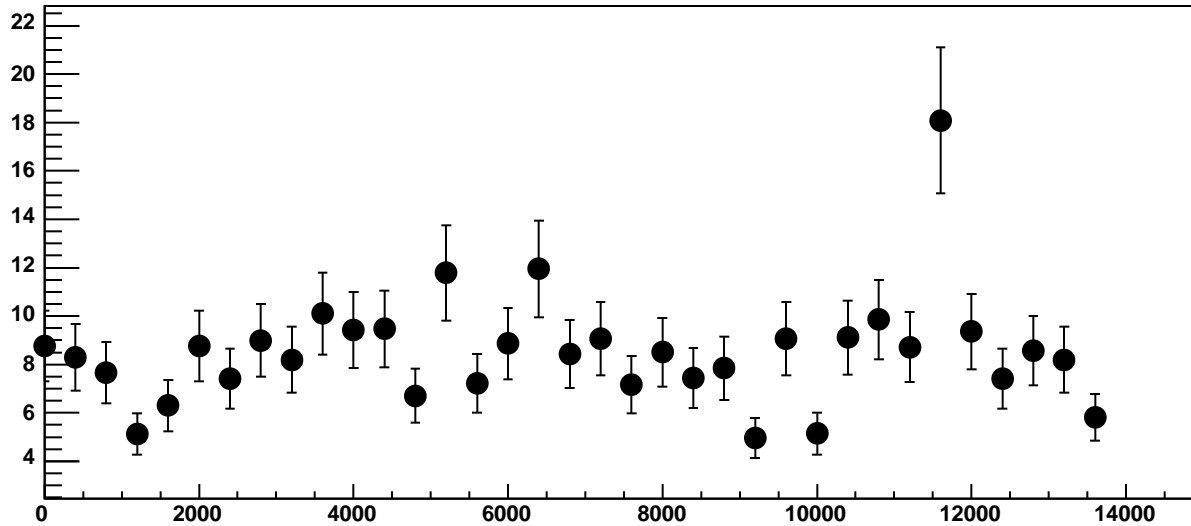


Chip 0, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

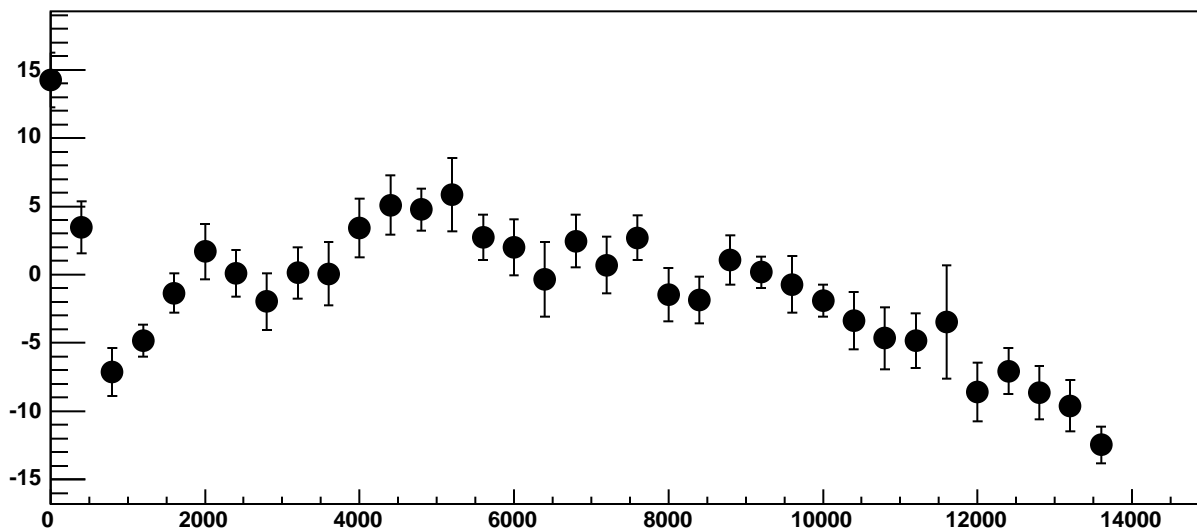


χ^2 / ndf 61.43 / 23
p0 7.685 ± 0.7569
p1 0.0274 ± 0.0001115

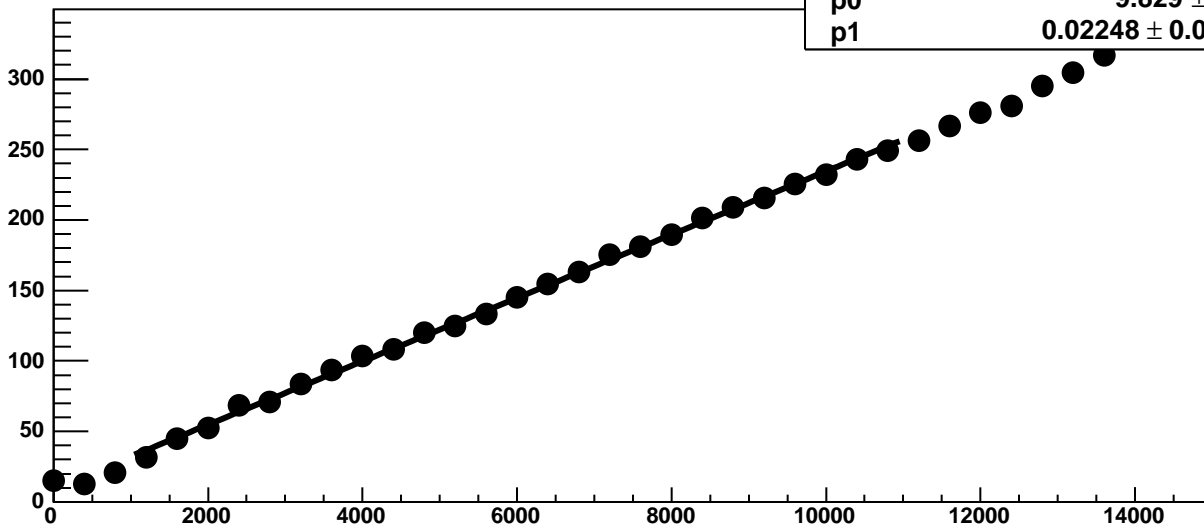
Chip 0, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

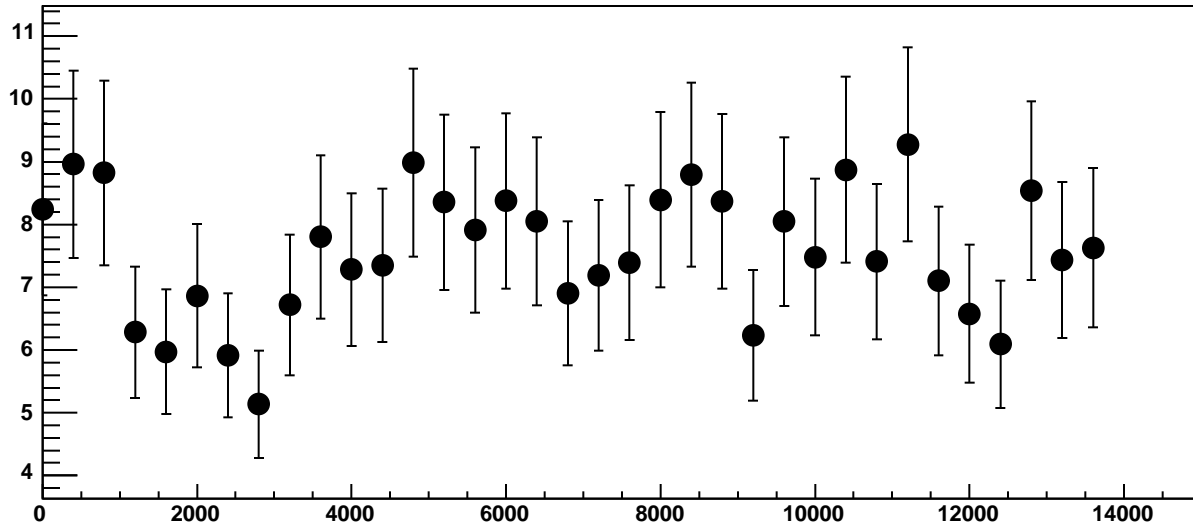


Chip 0, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

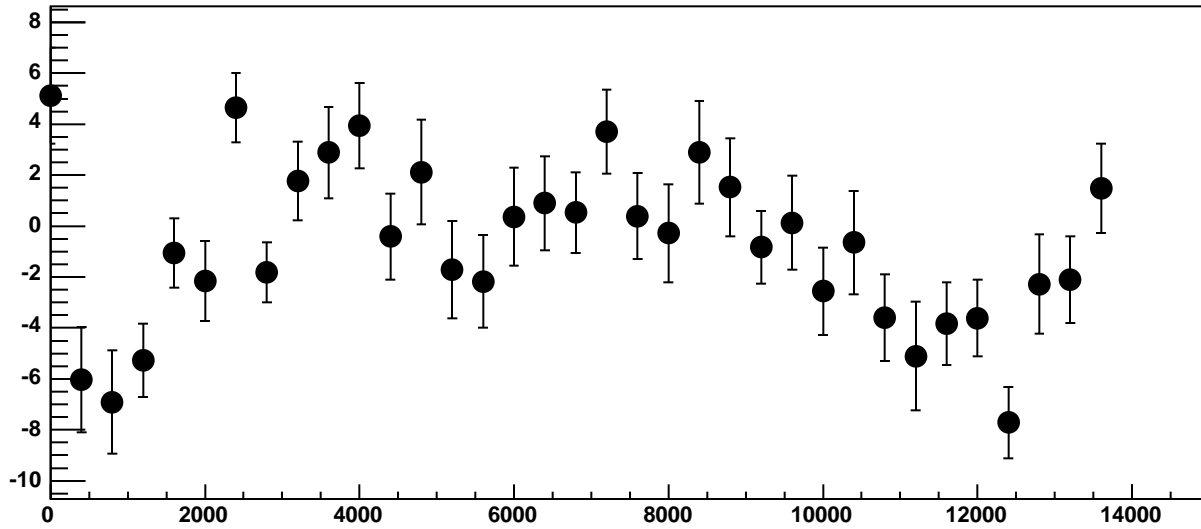


χ^2 / ndf 58.07 / 23
p0 9.829 ± 0.6962
p1 0.02248 ± 0.0001108

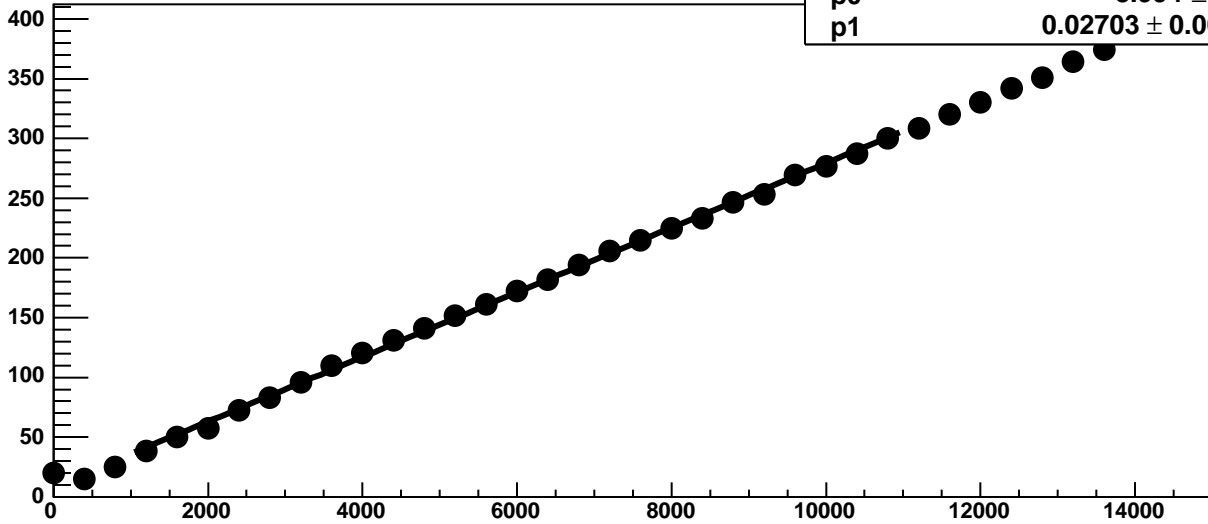
Chip 0, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

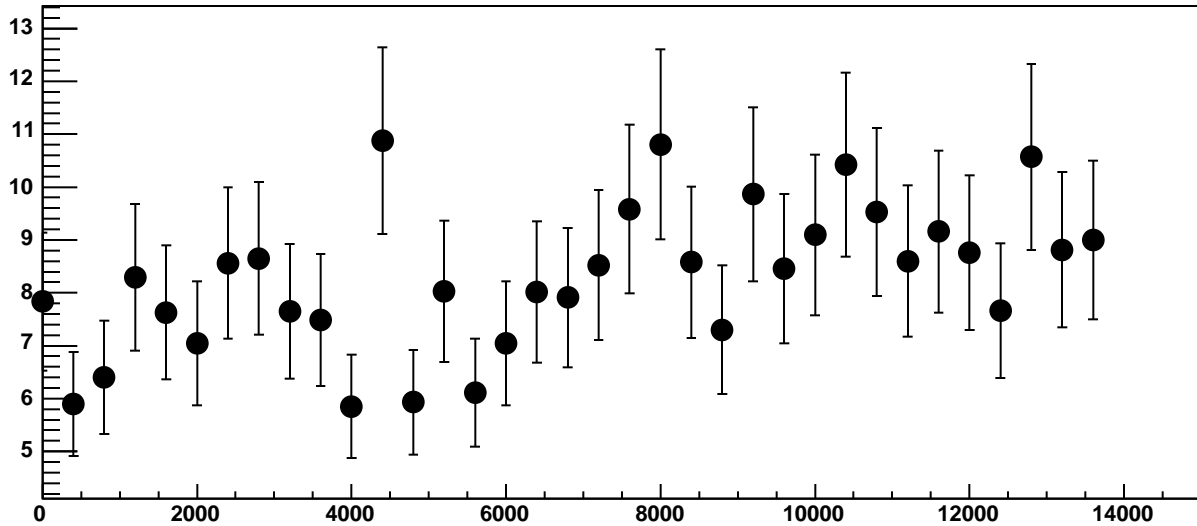


Chip 0, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

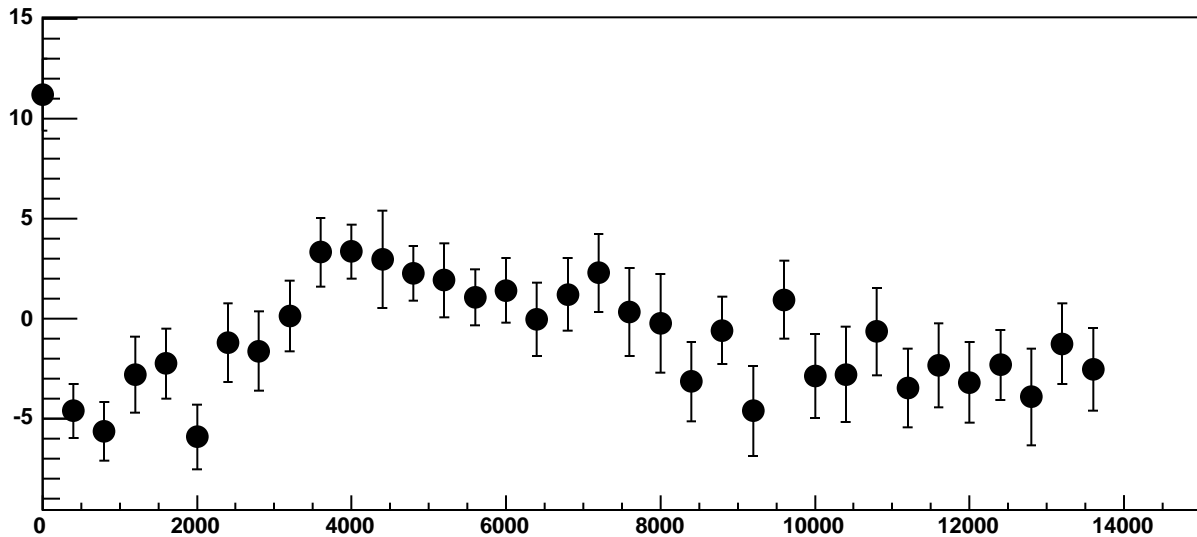


χ^2 / ndf 47.12 / 23
p0 8.994 ± 0.8375
p1 0.02703 ± 0.0001346

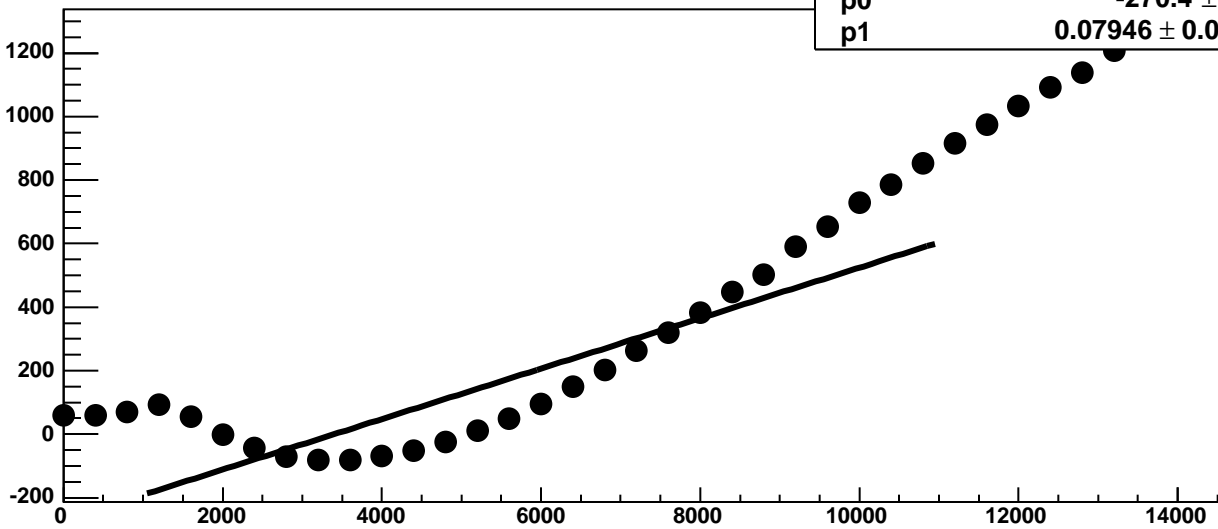
Chip 0, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC

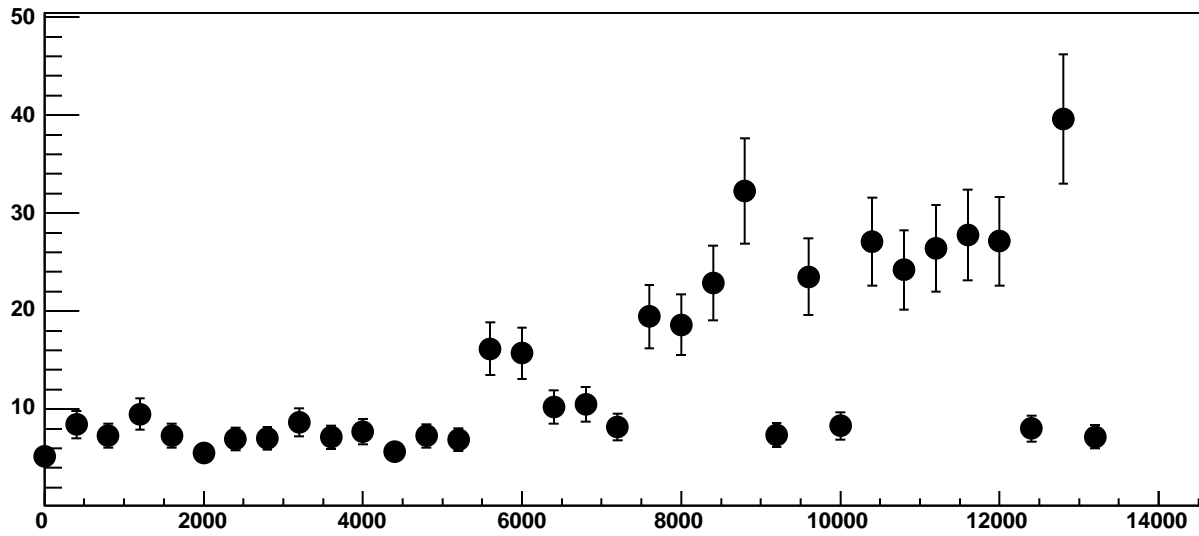


Chip 0, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

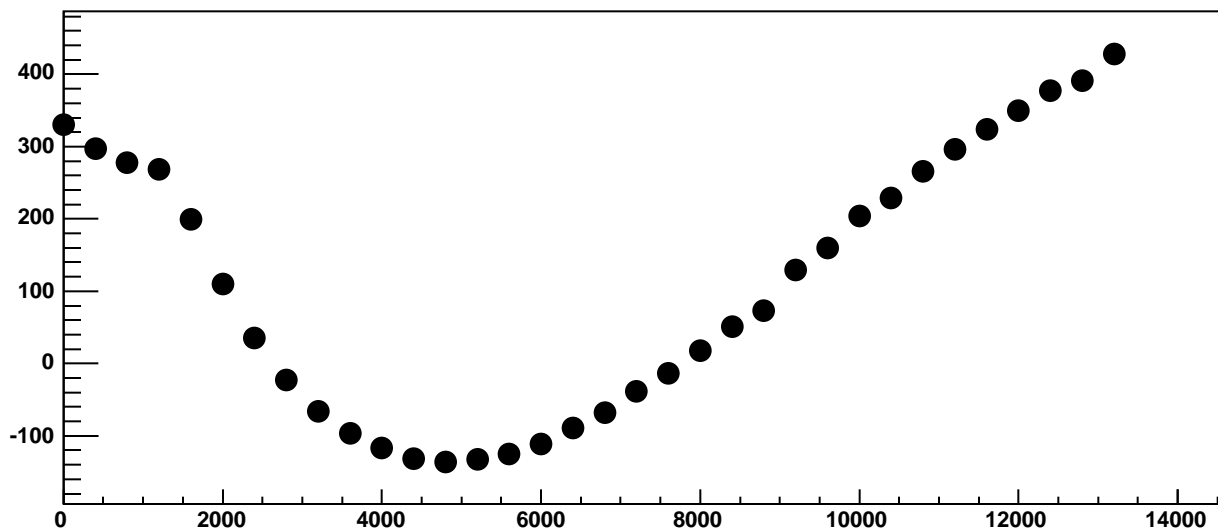


χ^2 / ndf 9.712e+04 / 23
p0 -270.4 ± 0.8547
p1 0.07946 ± 0.0001621

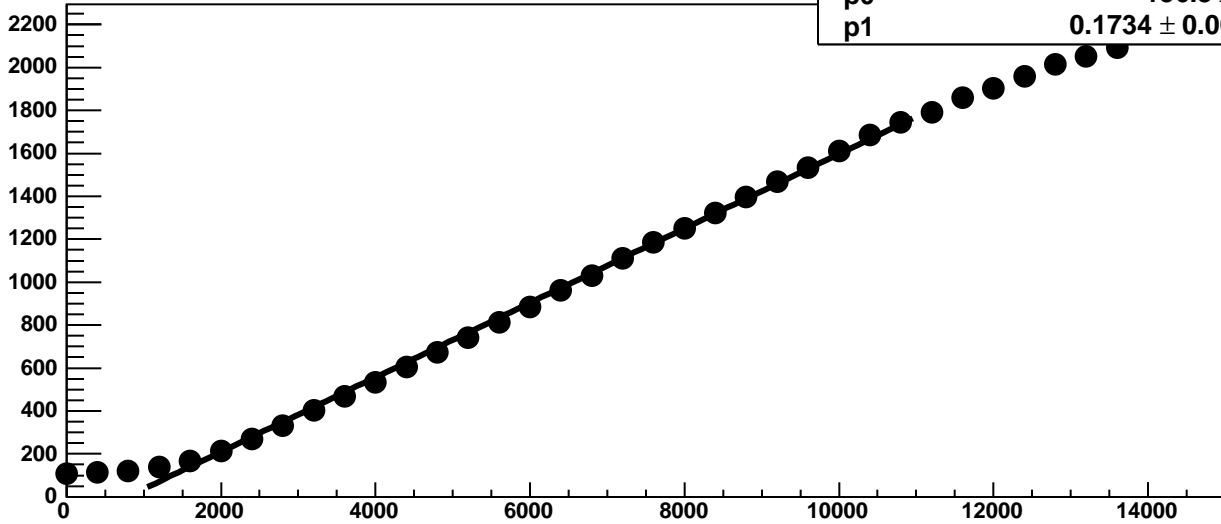
Chip 0, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



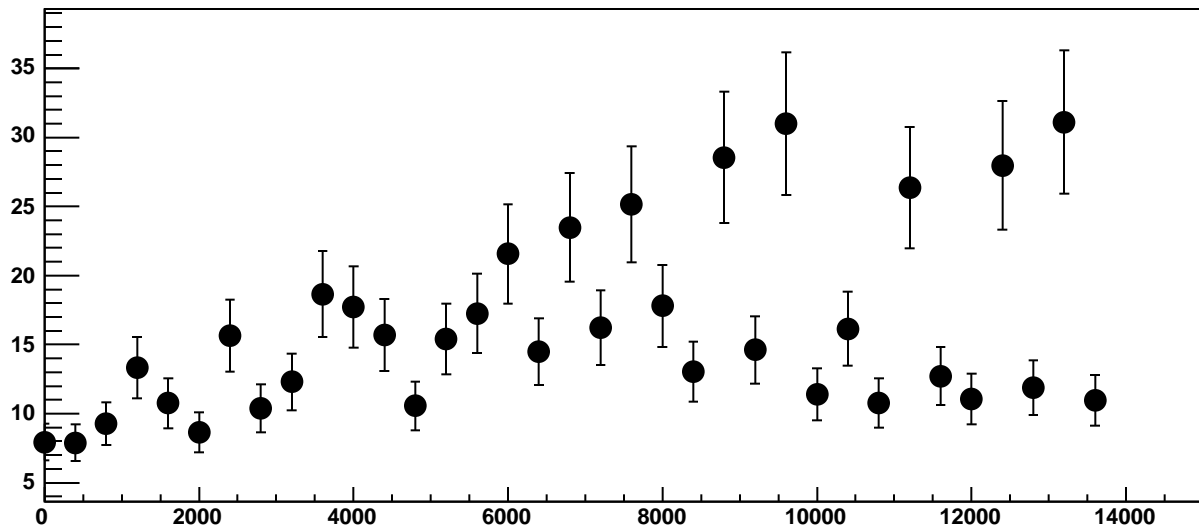
Chip 0, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



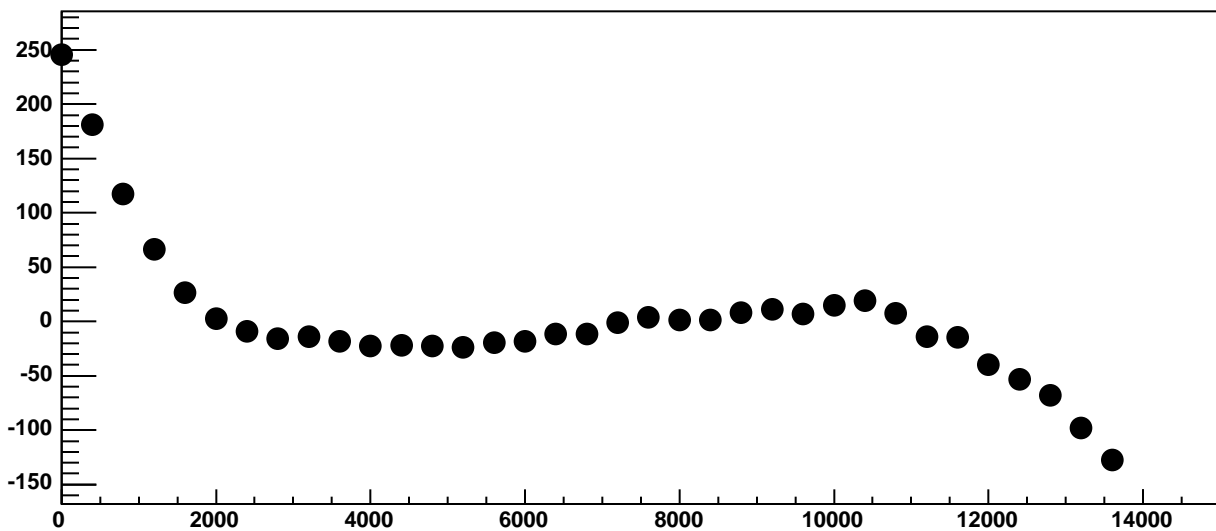
Chip 0, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



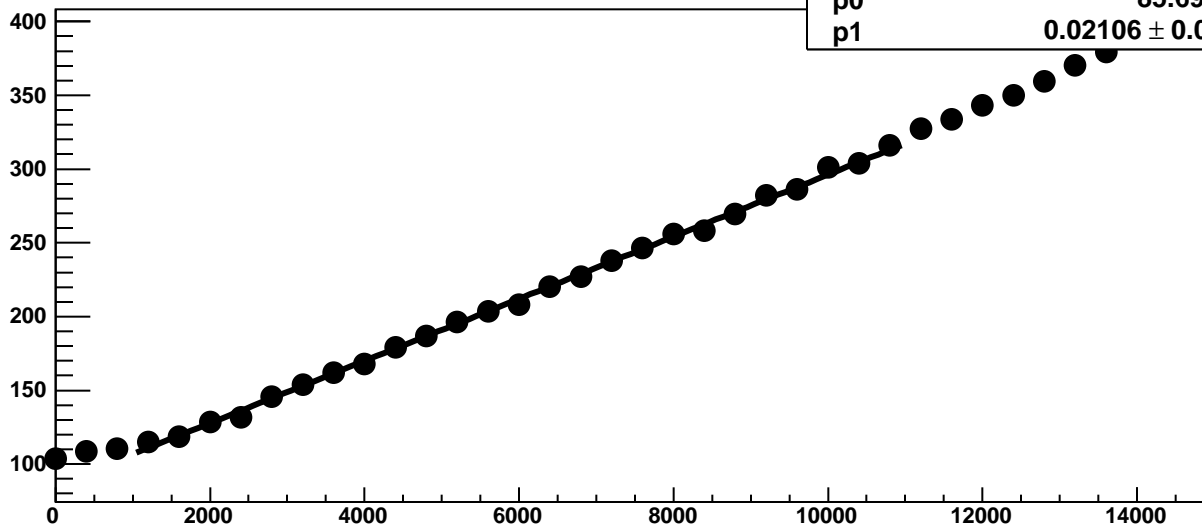
Chip 0, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

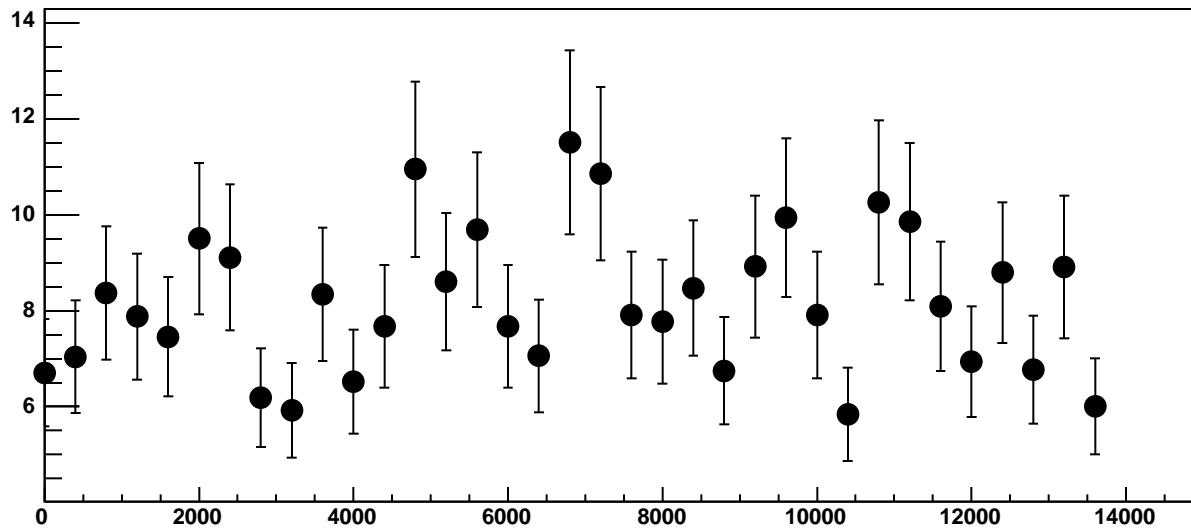


Chip 0, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

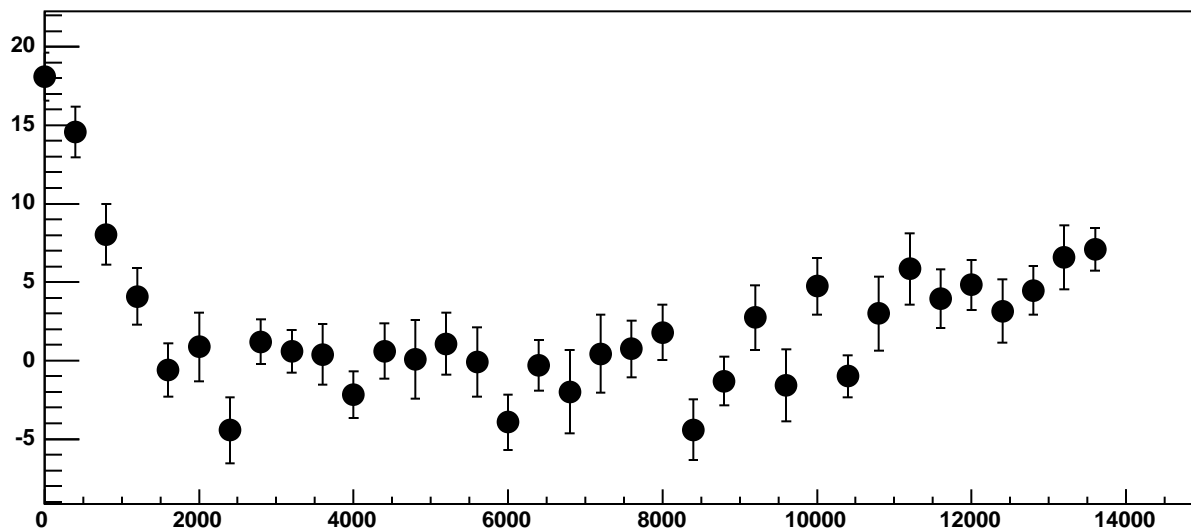


χ^2 / ndf 37.33 / 23
p0 85.69 ± 0.811
p1 0.02106 ± 0.0001233

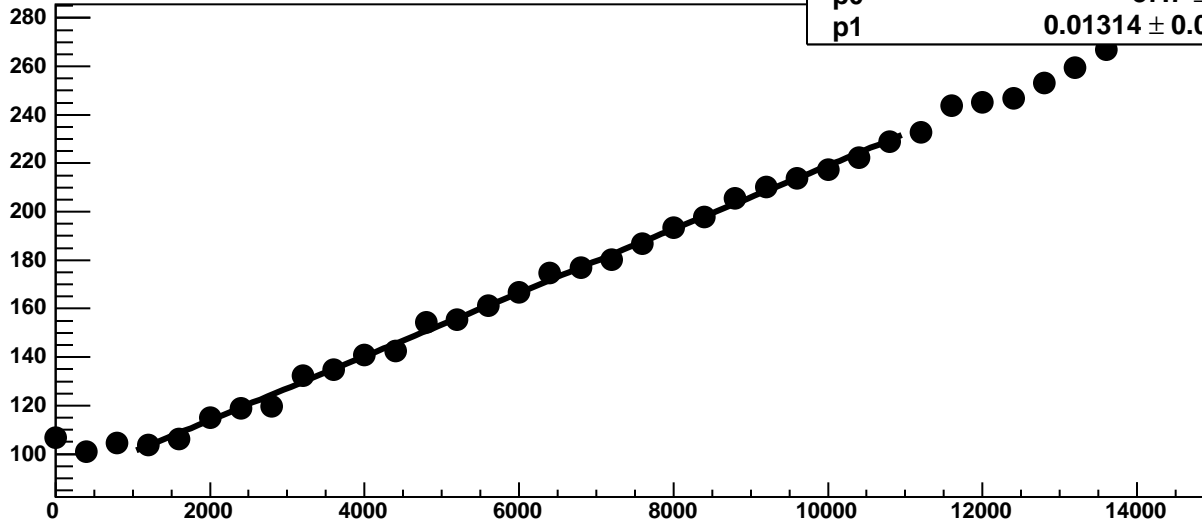
Chip 0, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

27.05 / 23

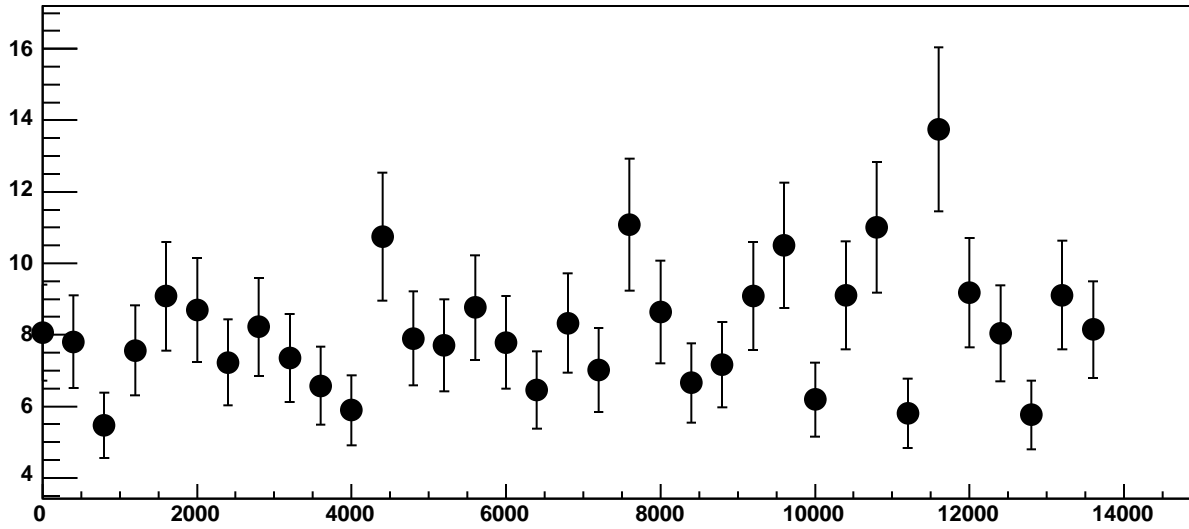
p0

87.7 ± 0.8348

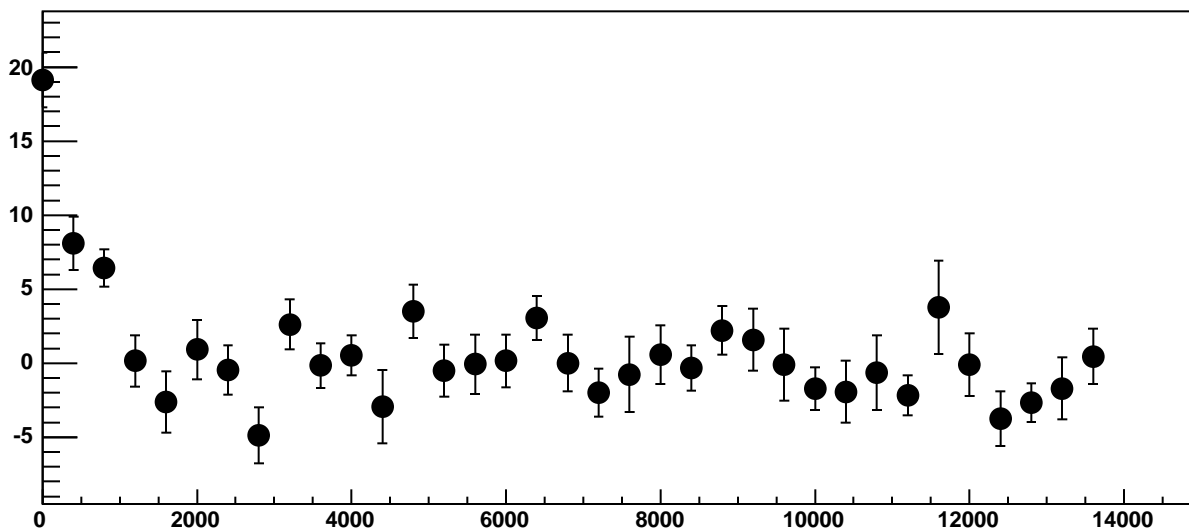
p1

0.01314 ± 0.0001288

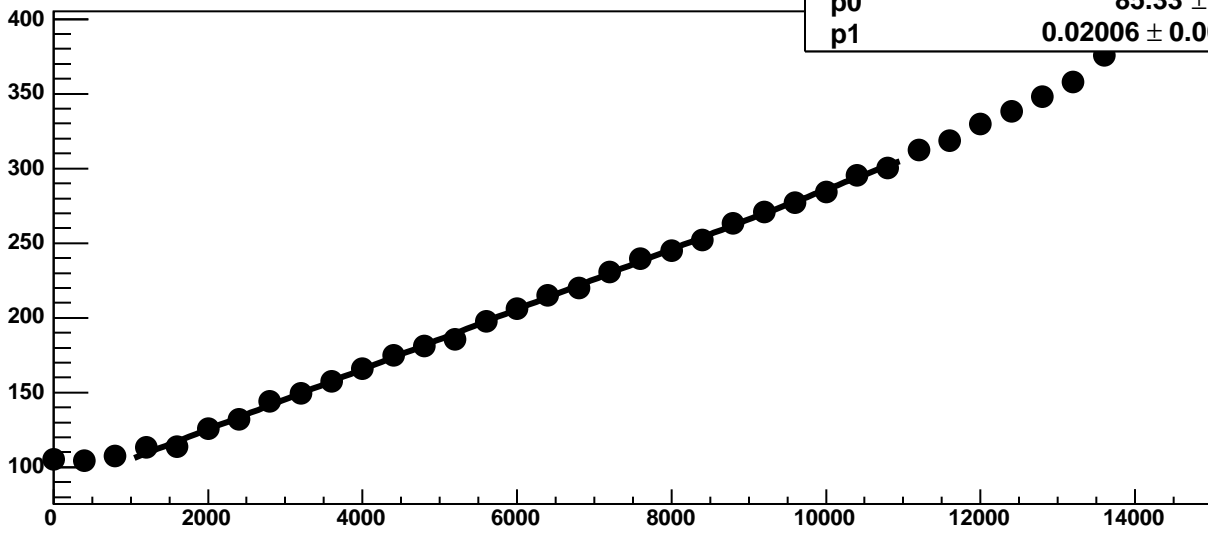
Chip 0, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

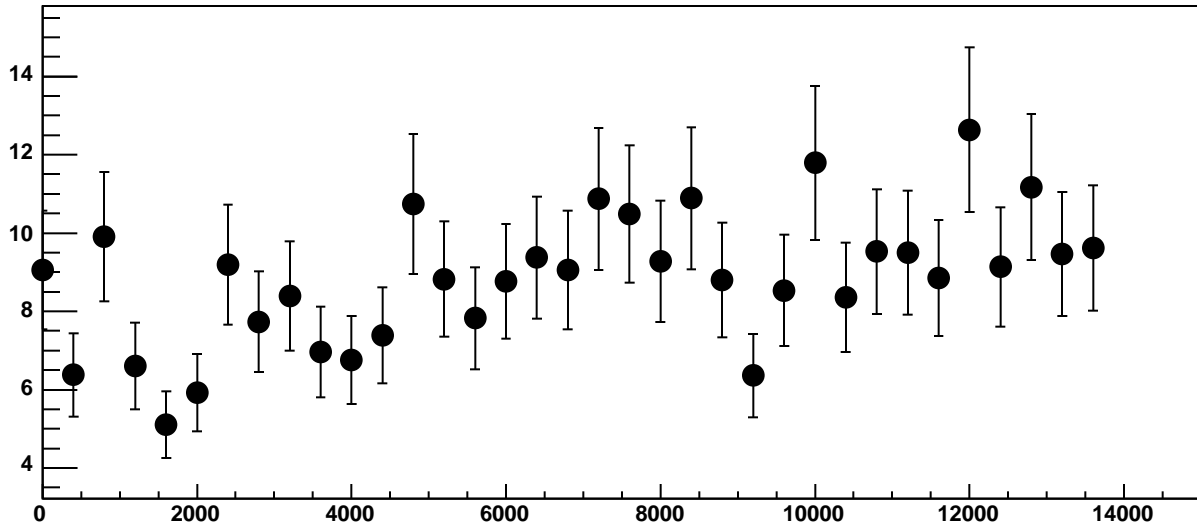


Chip 0, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

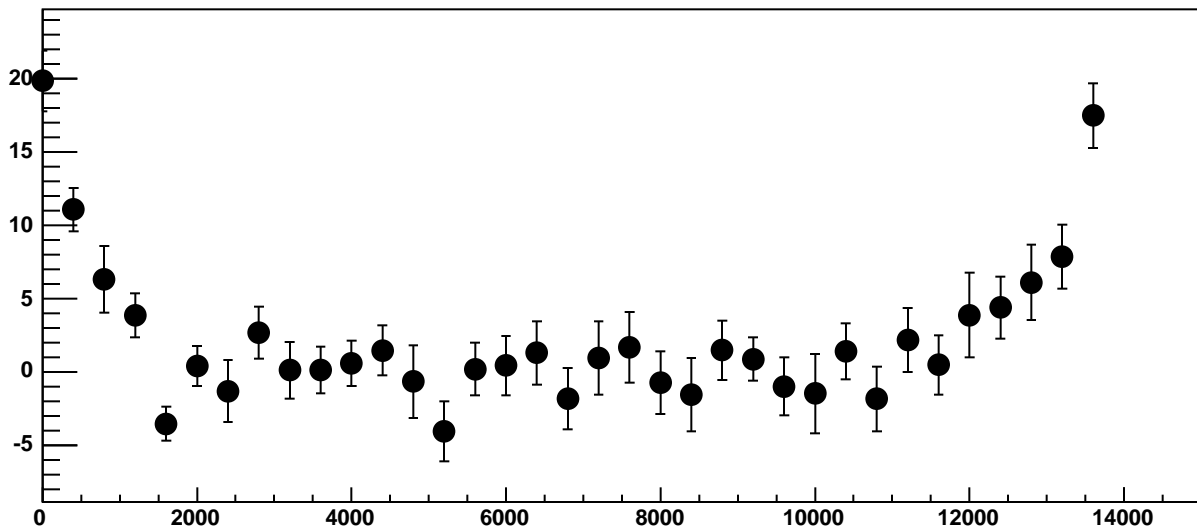


χ^2 / ndf 28.29 / 23
p0 85.33 ± 0.7394
p1 0.02006 ± 0.0001218

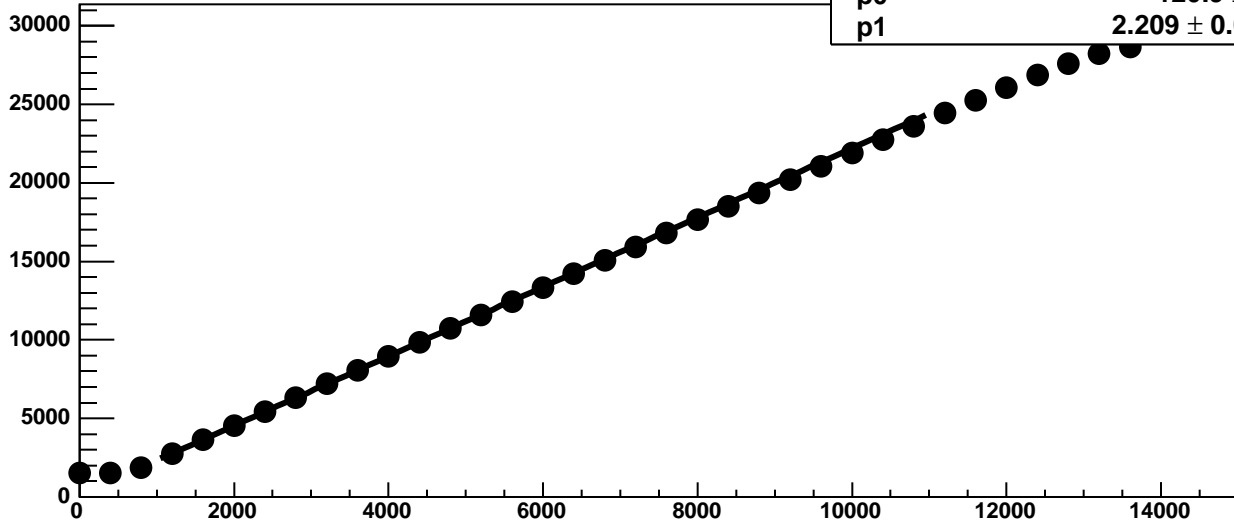
Chip 0, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

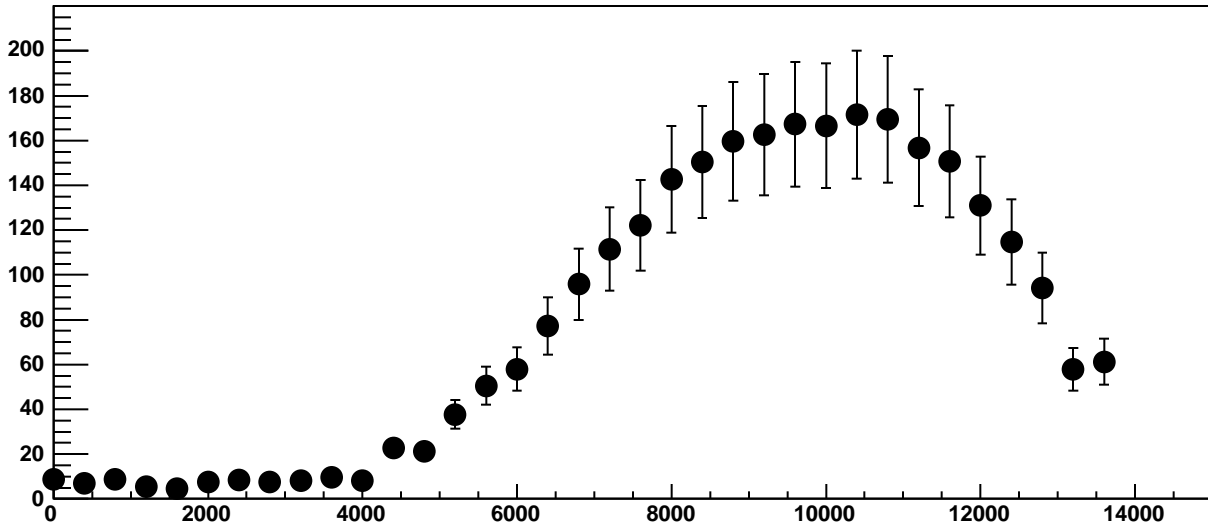


Chip 0, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC

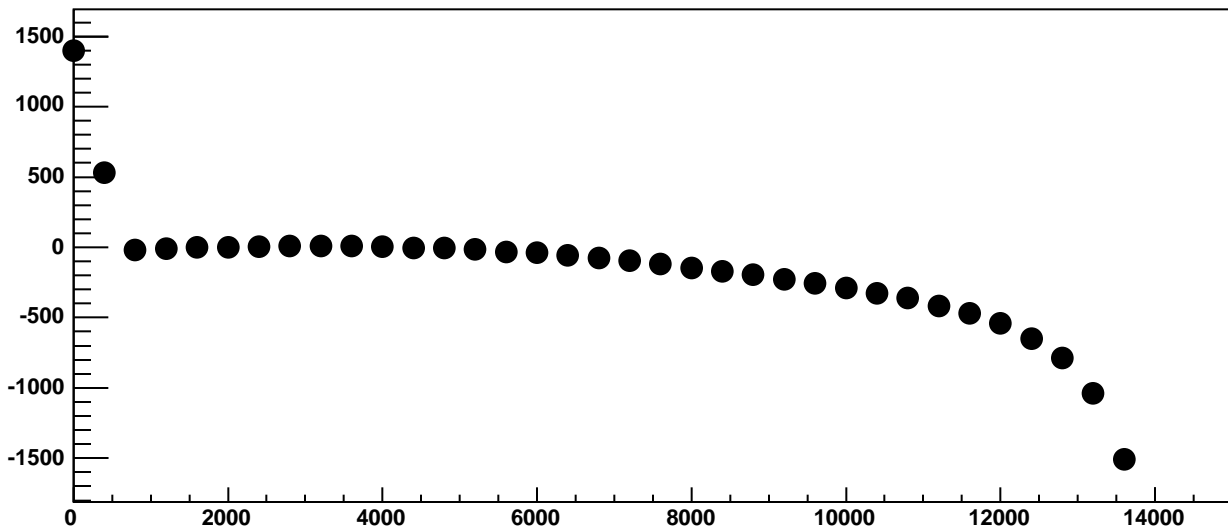


χ^2 / ndf	581.2 / 23
p0	120.9 ± 1.295
p1	2.209 ± 0.000507

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC

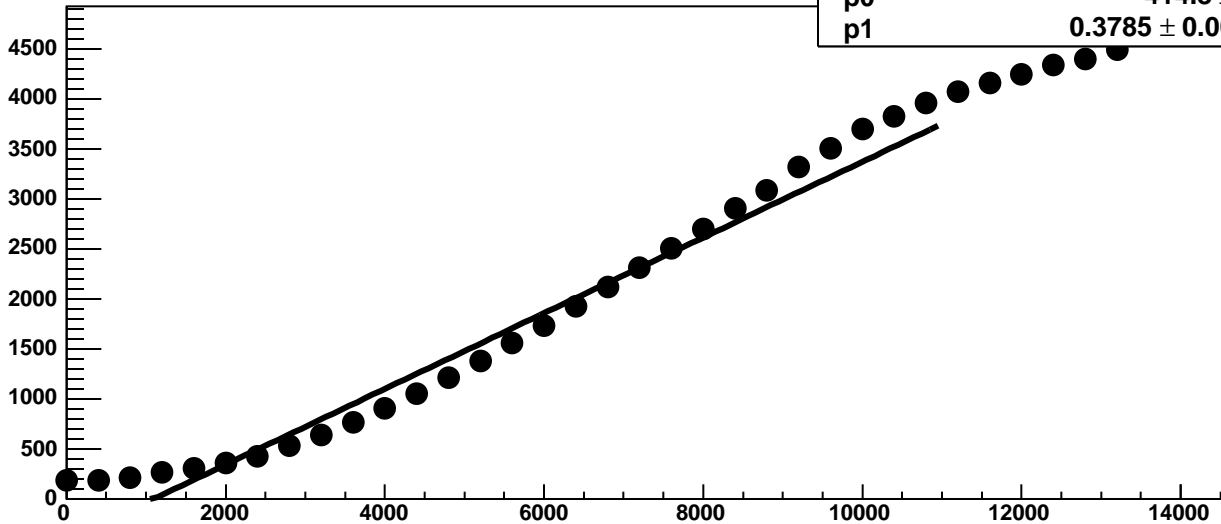


Chip 0, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC

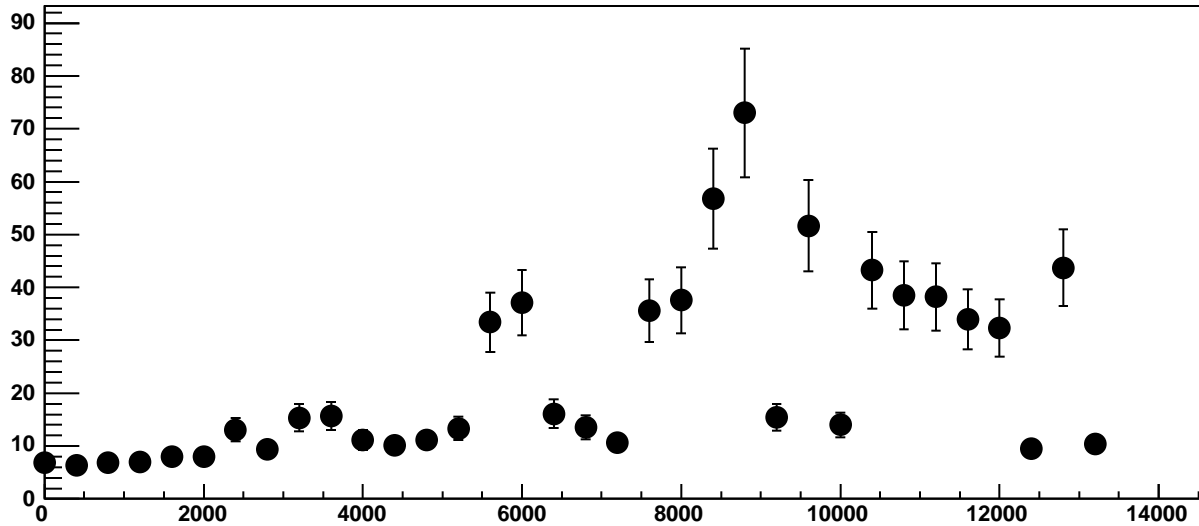


Chip 0, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

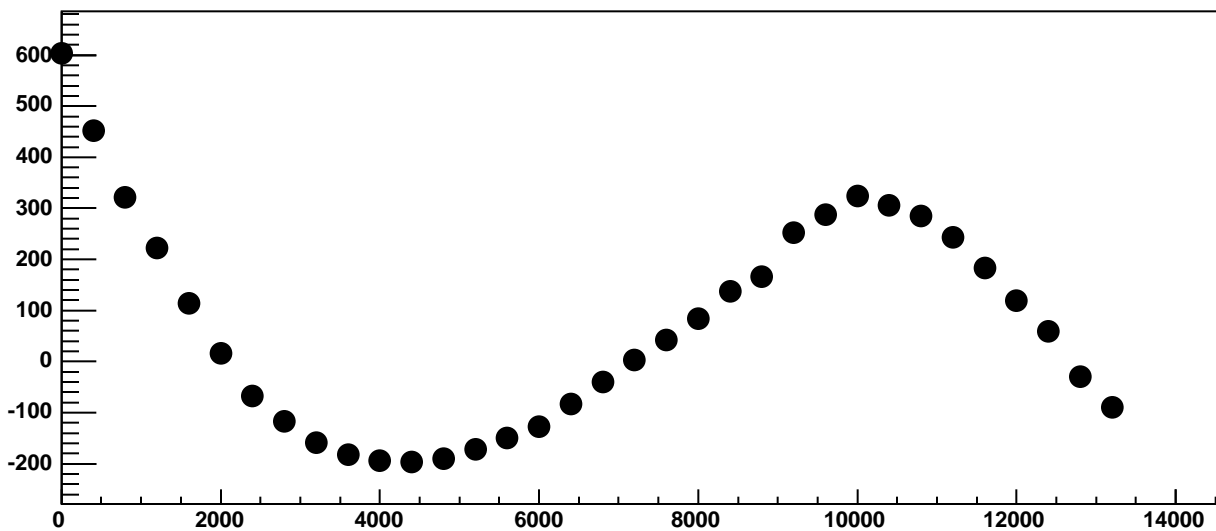
χ^2 / ndf 7.192e+04 / 23
p0 -414.8 ± 1.124
p1 0.3785 ± 0.0002402



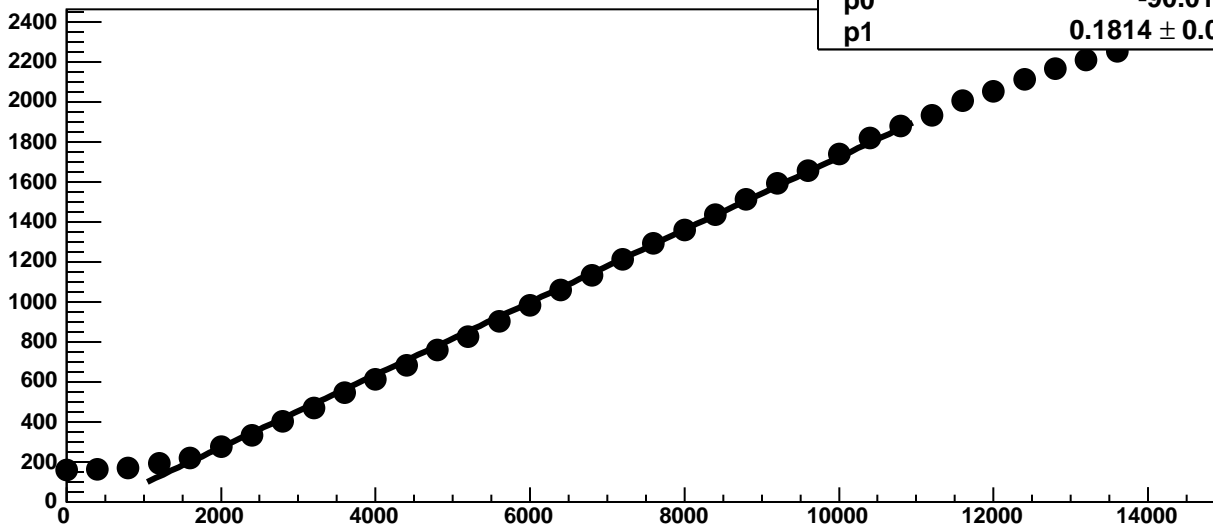
Chip 0, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1103 / 23

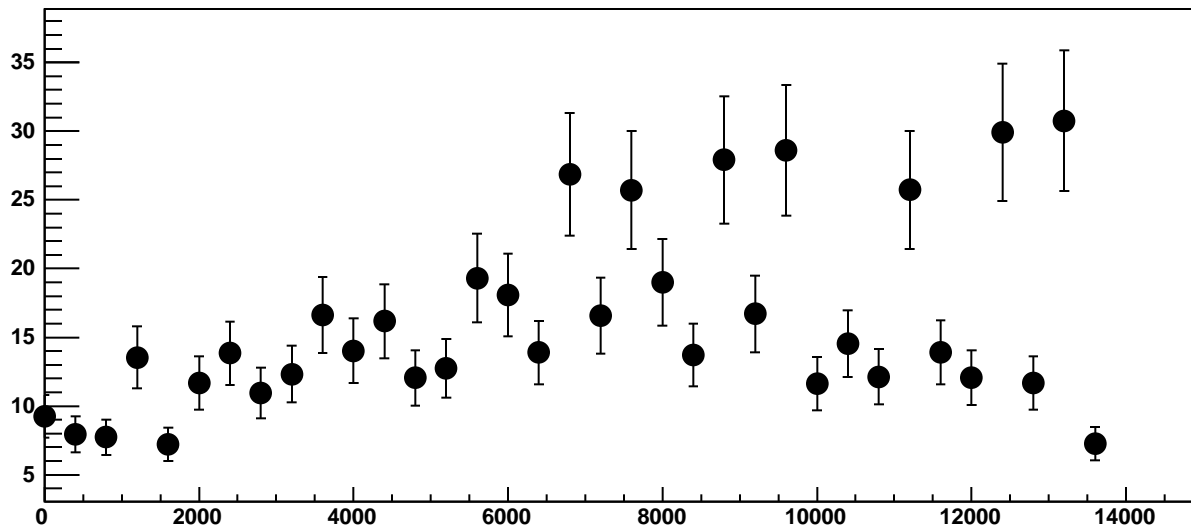
p0

-90.01 ± 1.224

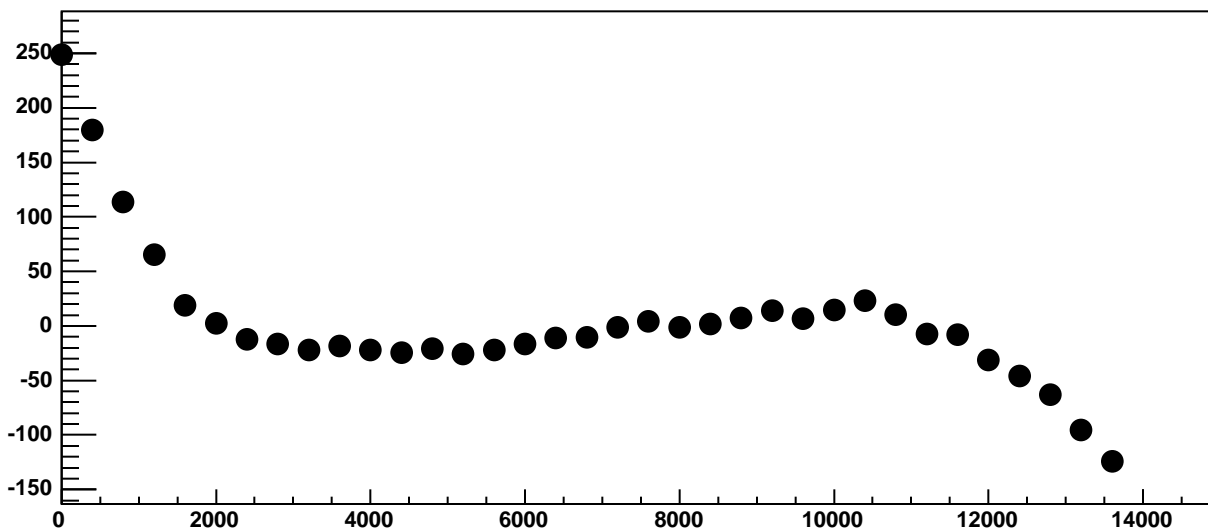
p1

0.1814 ± 0.0002037

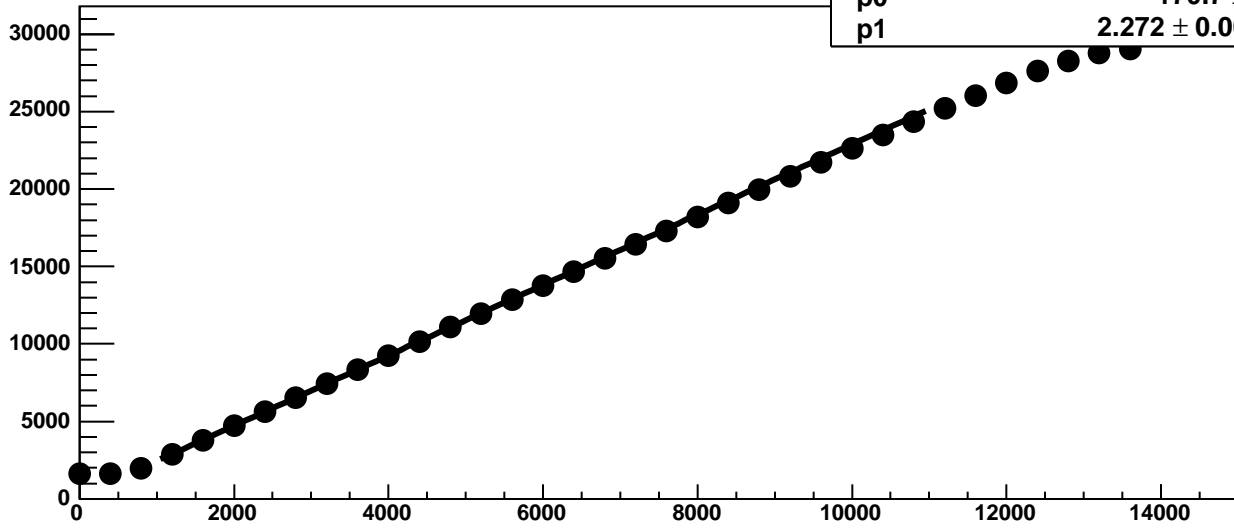
Chip 0, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

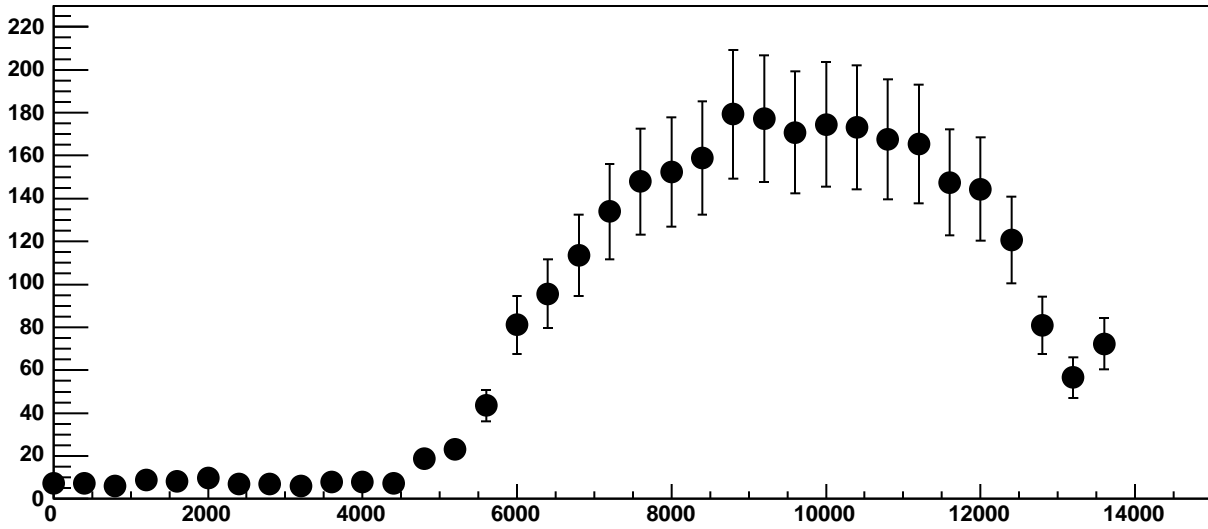


Chip 0, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC

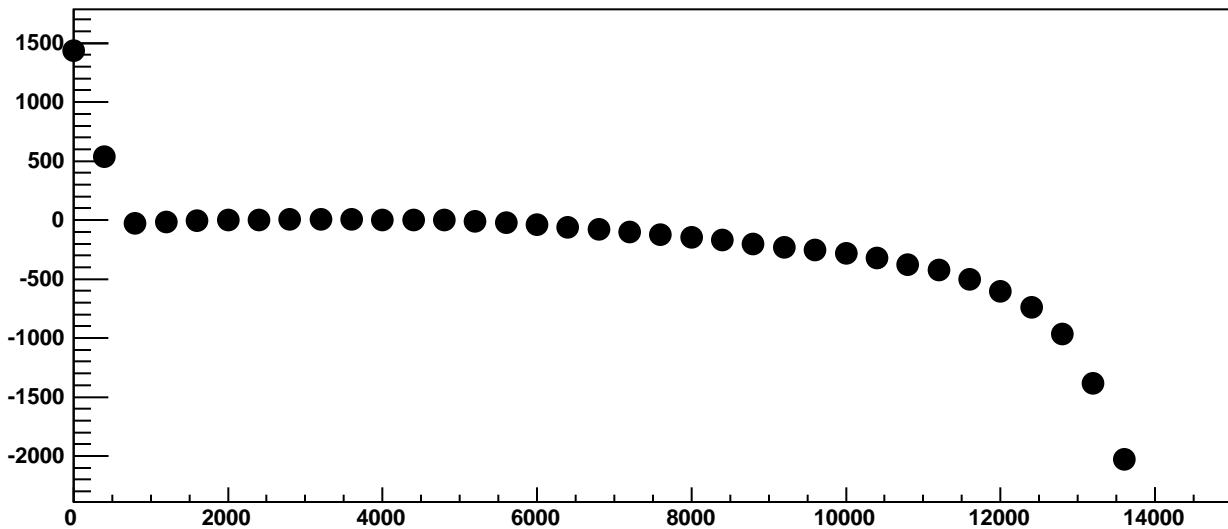


χ^2 / ndf 530.6 / 23
p0 170.7 ± 1.683
p1 2.272 ± 0.0005286

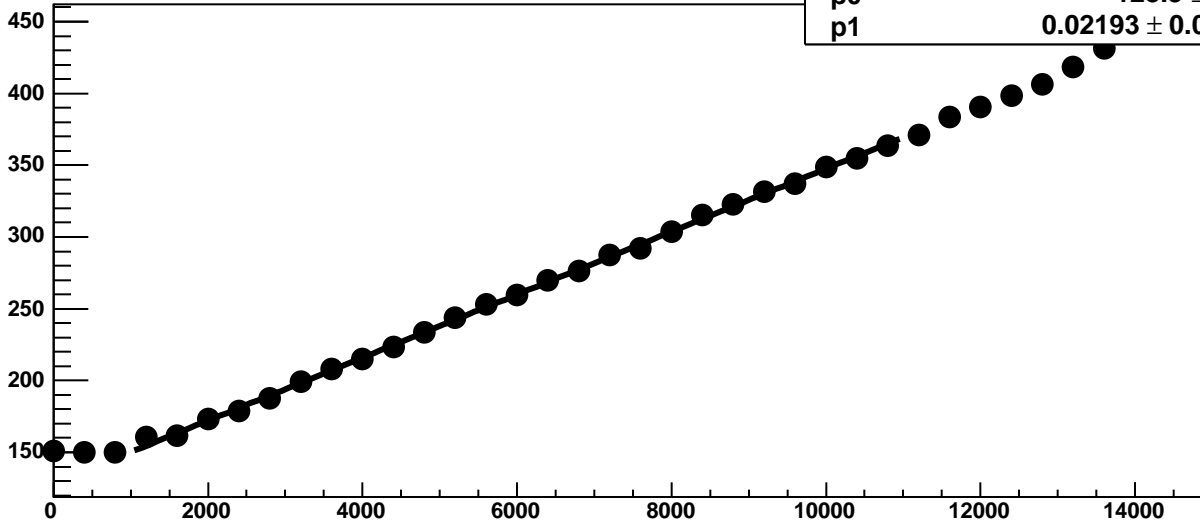
Chip 0, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



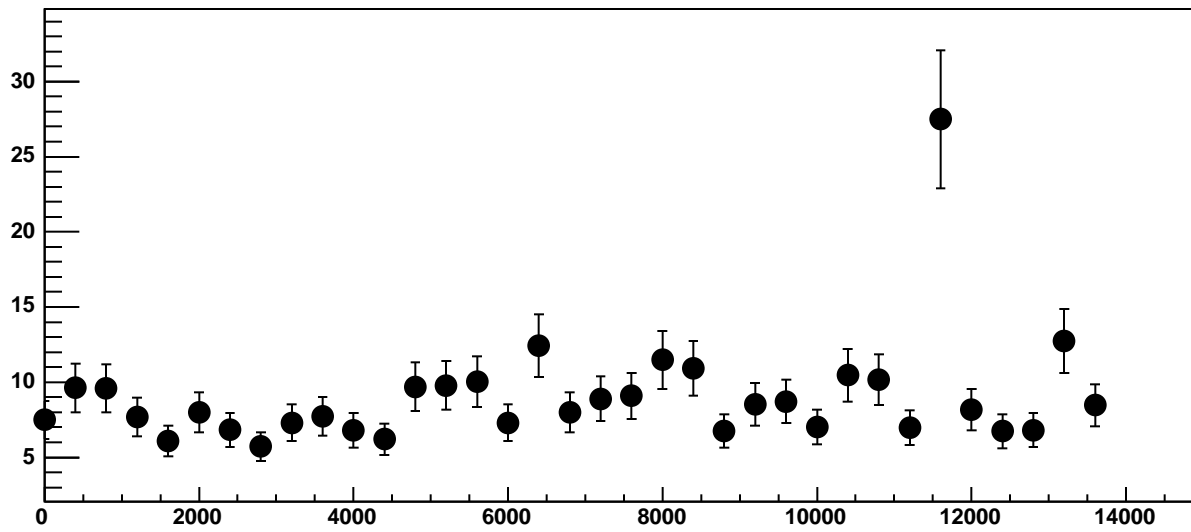
Chip 0, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC



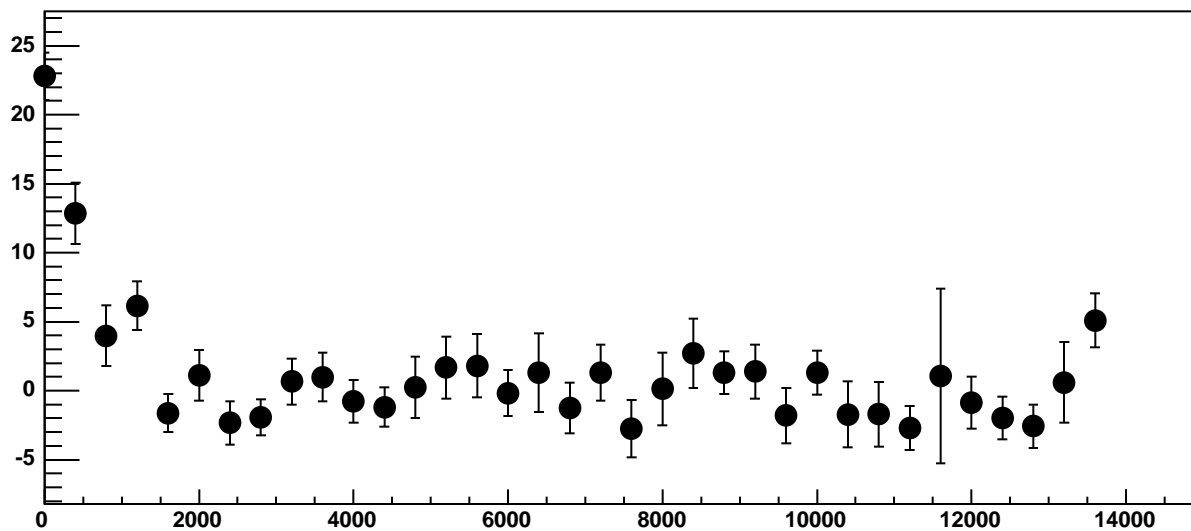
Chip 0, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



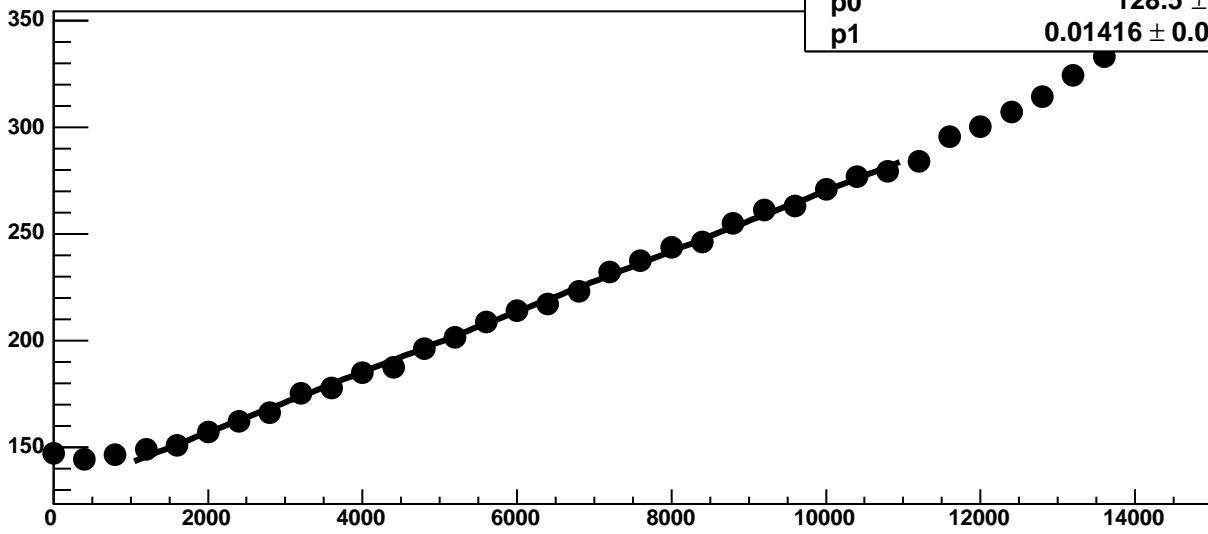
Chip 0, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

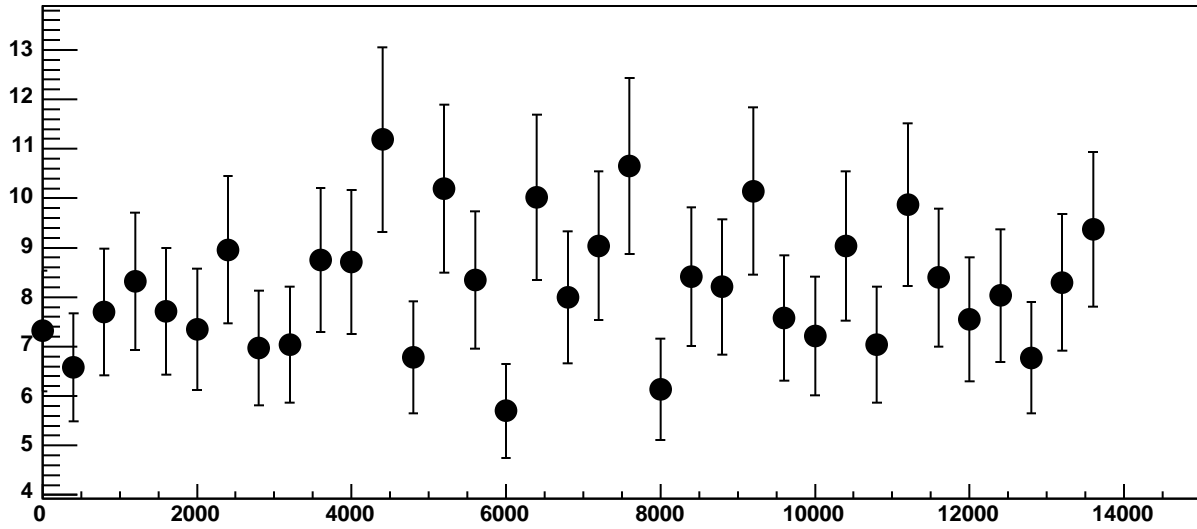


Chip 0, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

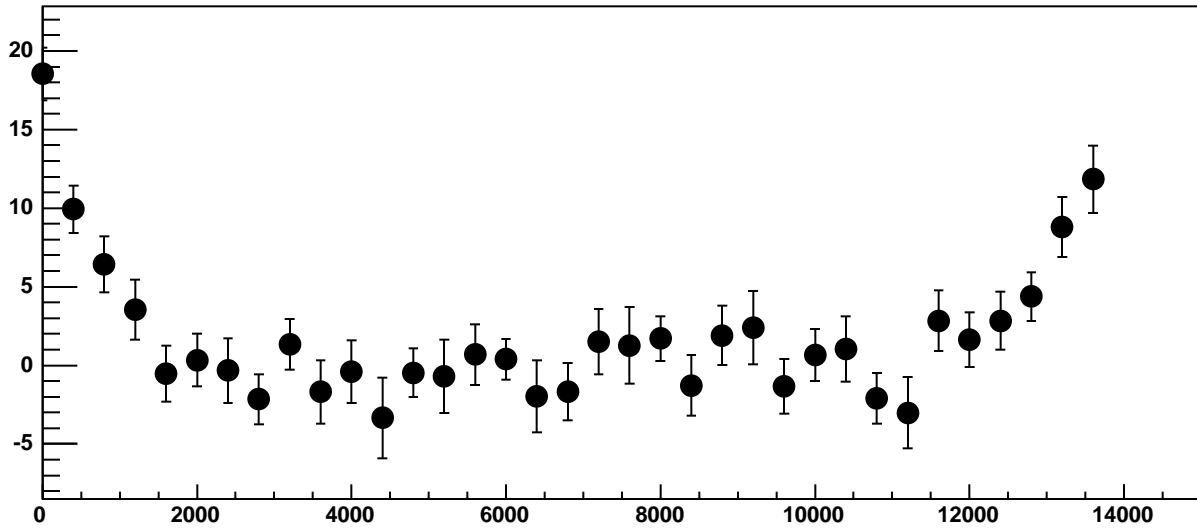


χ^2 / ndf 17.95 / 23
p0 128.5 ± 0.8347
p1 0.01416 ± 0.0001251

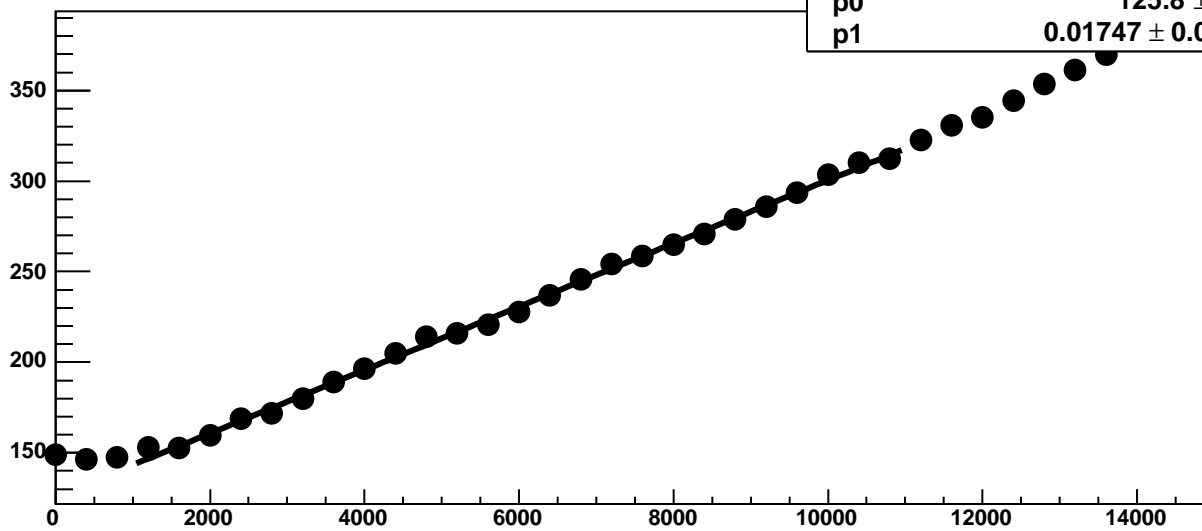
Chip 0, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

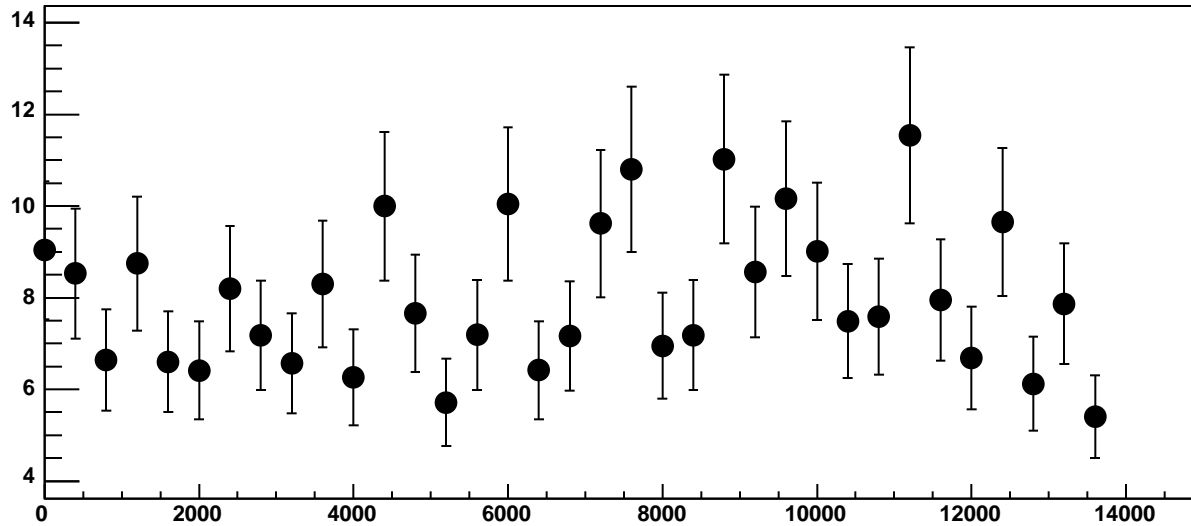


Chip 0, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

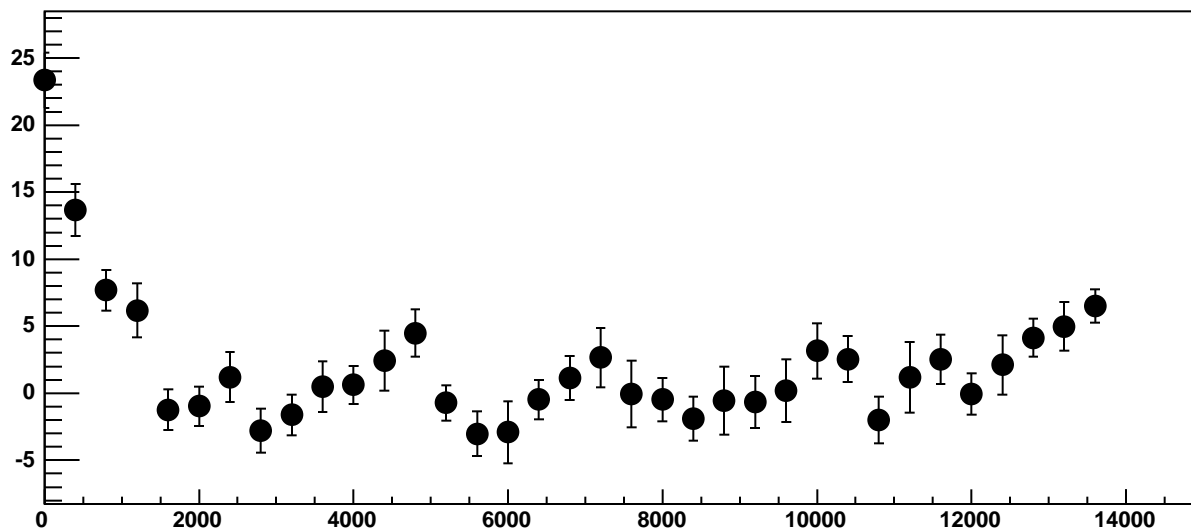


χ^2 / ndf 37.55 / 23
p0 125.8 ± 0.7834
p1 0.01747 ± 0.0001237

Chip 0, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC

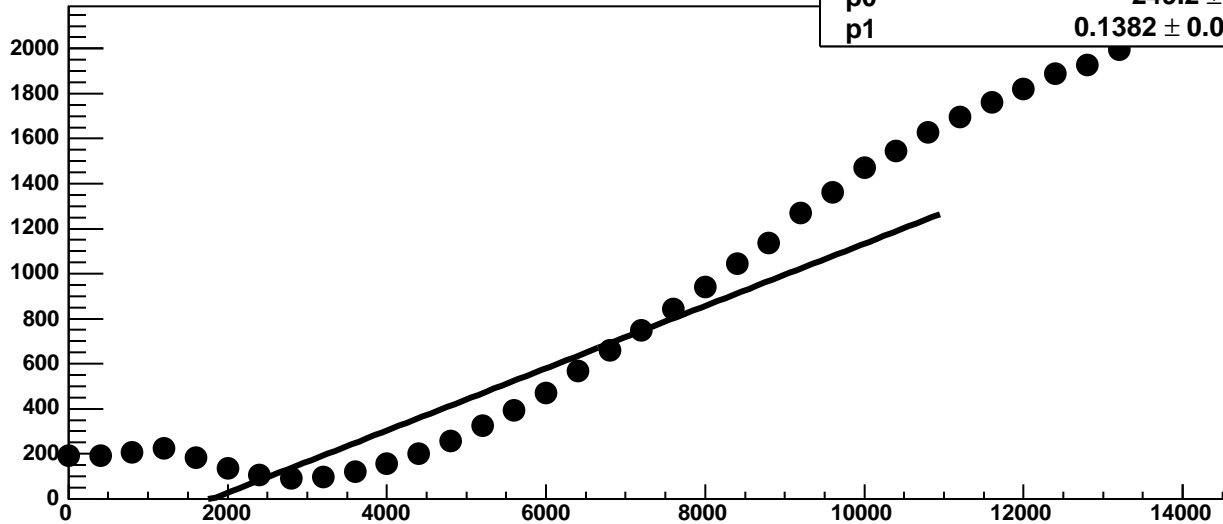


Chip 0, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC

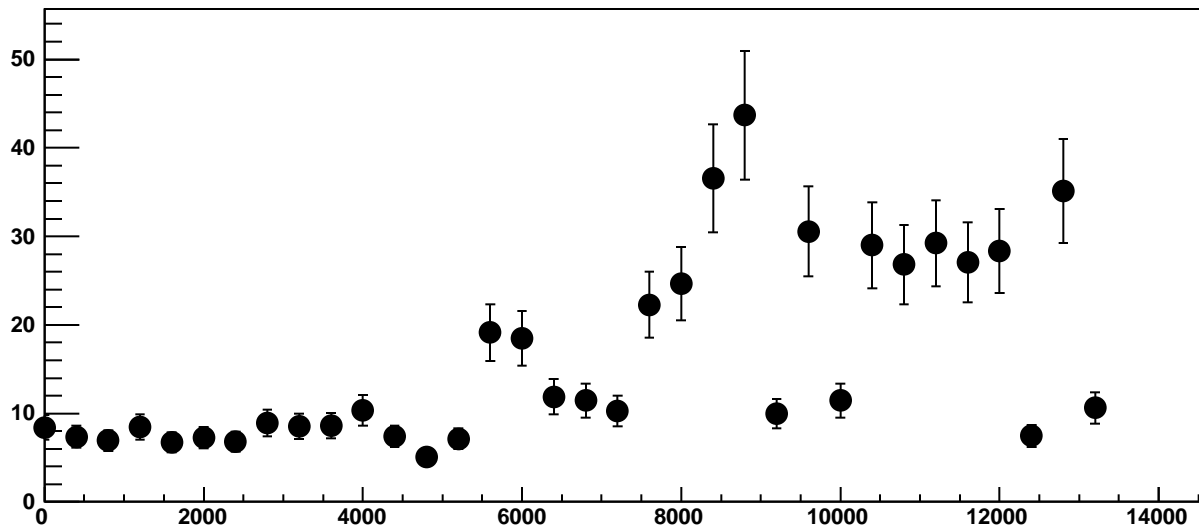


Chip 0, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

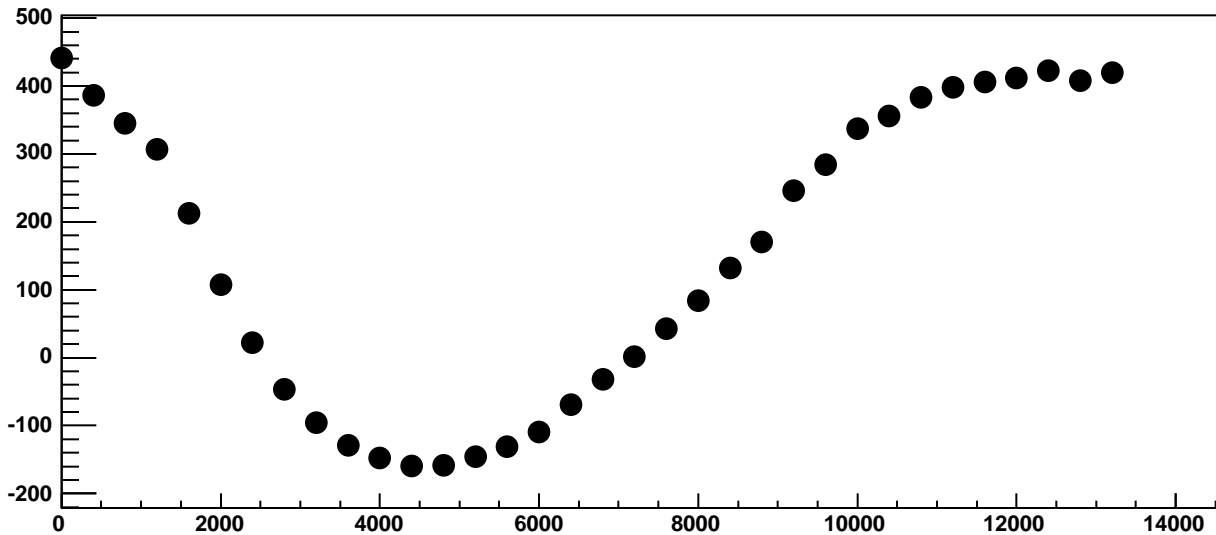
χ^2 / ndf 1.335e+05 / 23
p0 -249.2 ± 0.9503
p1 0.1382 ± 0.0001931



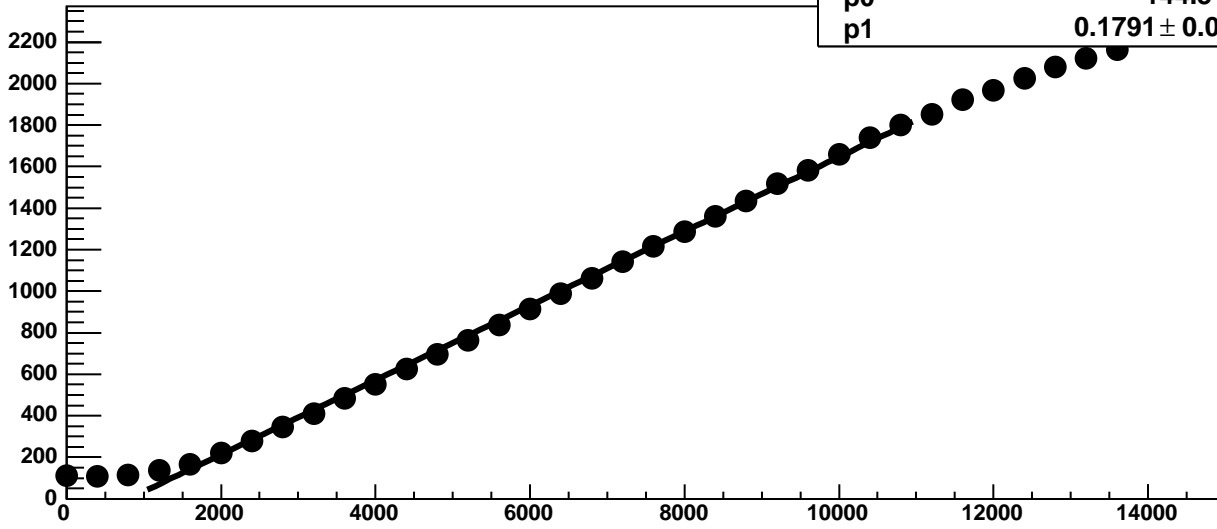
Chip 0, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



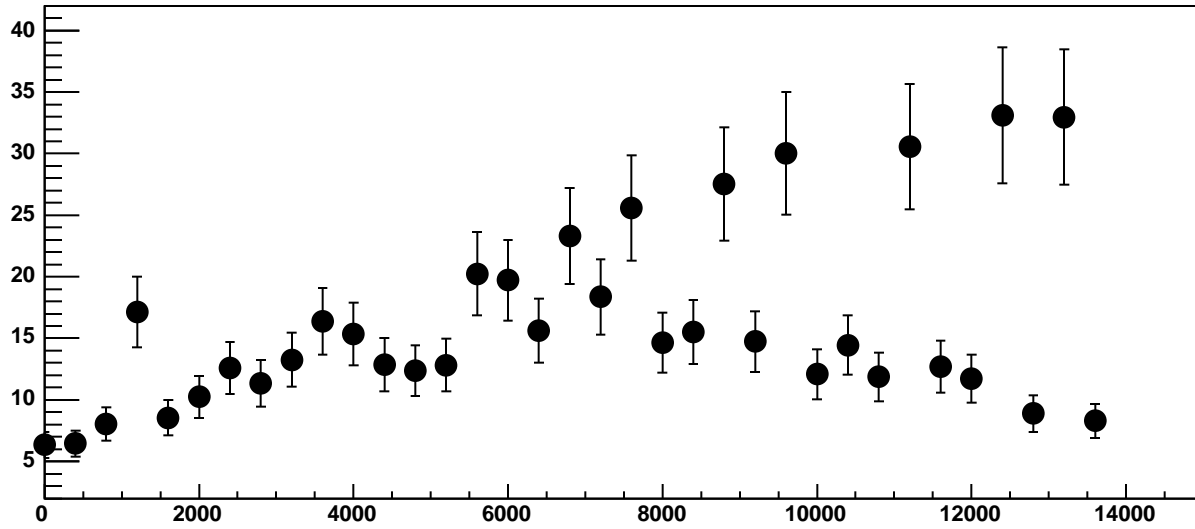
Chip 0, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



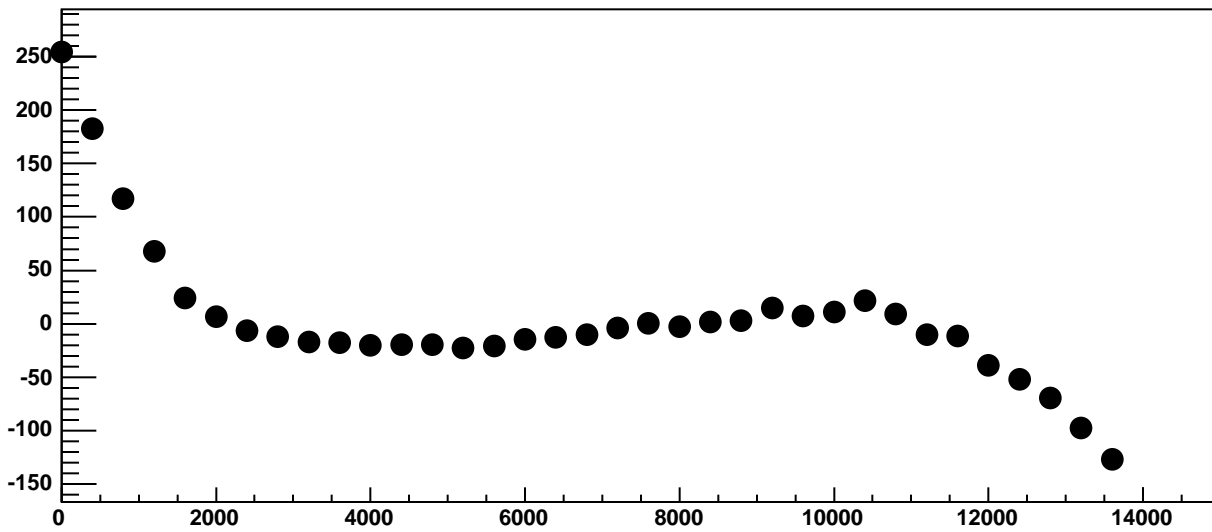
Chip 0, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



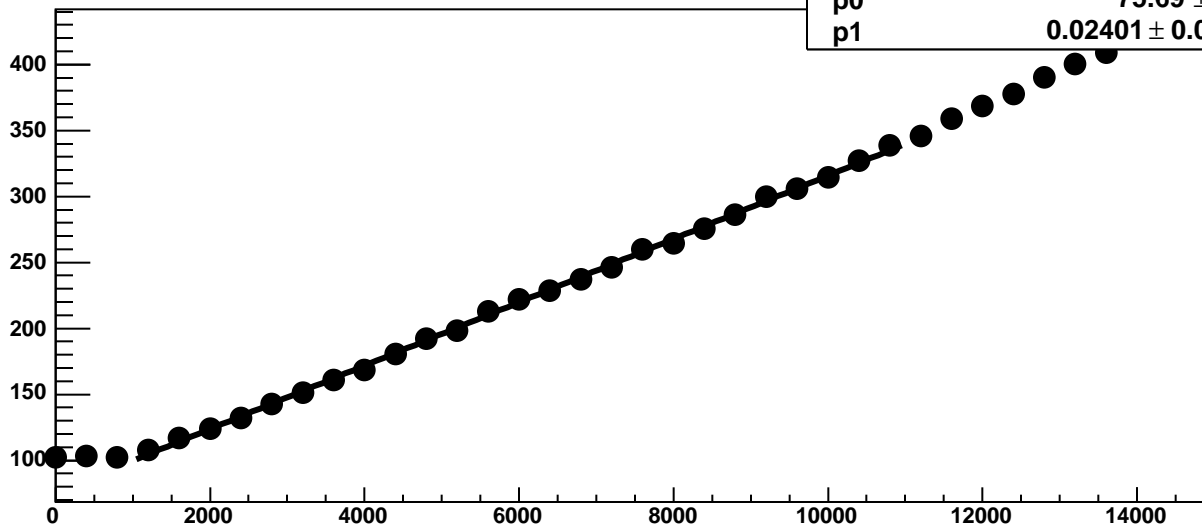
Chip 0, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

29.13 / 23

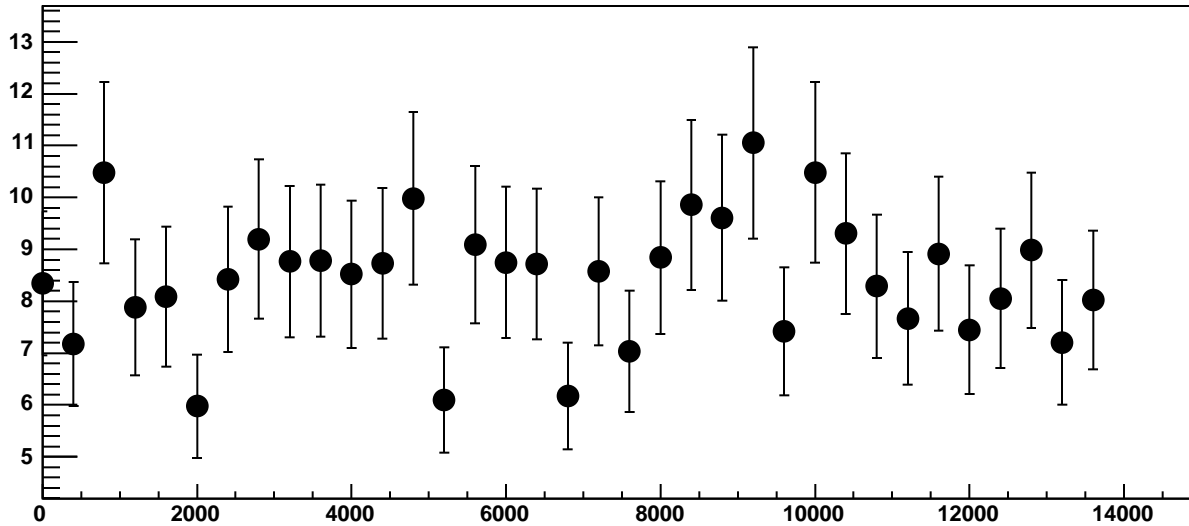
p0

75.69 ± 0.8488

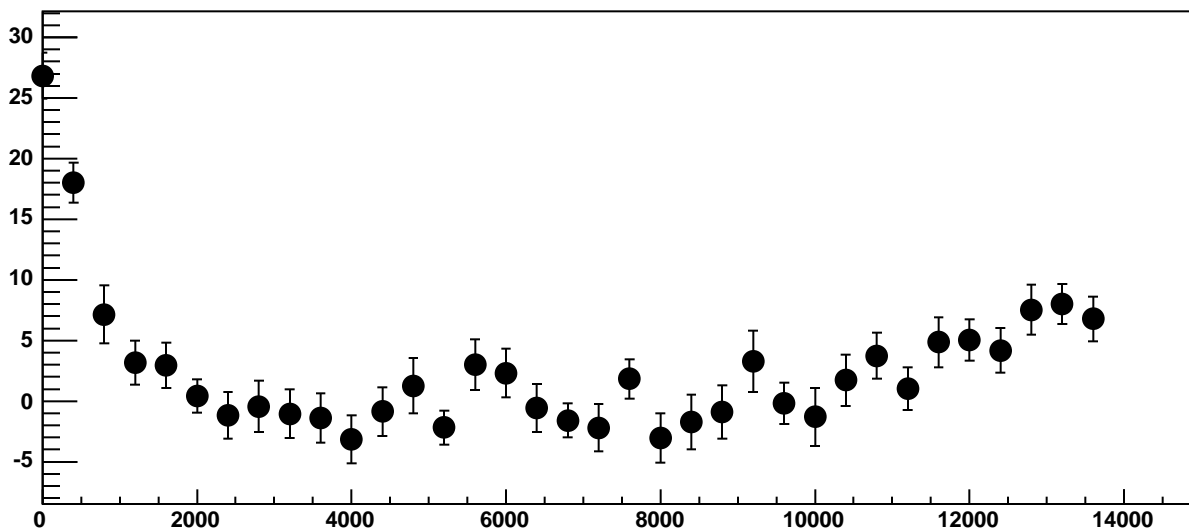
p1

0.02401 ± 0.0001324

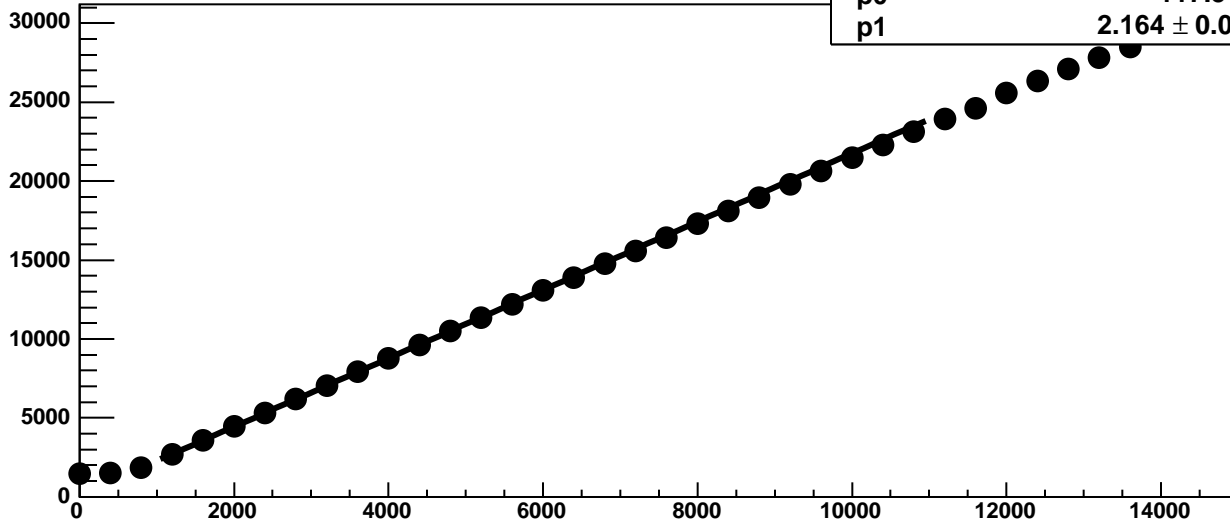
Chip 0, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

507.3 / 23

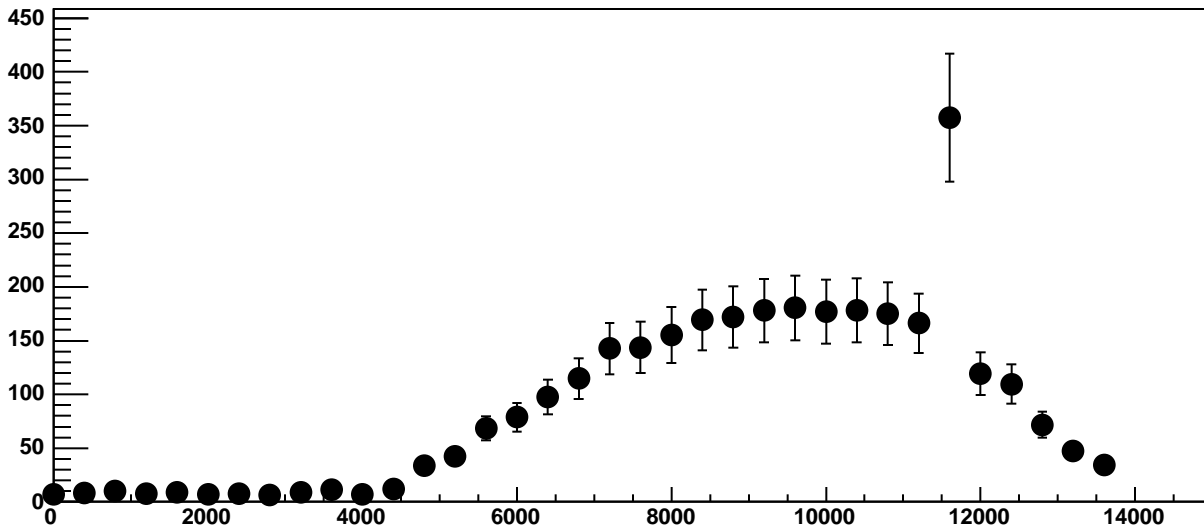
p0

117.5 ± 1.727

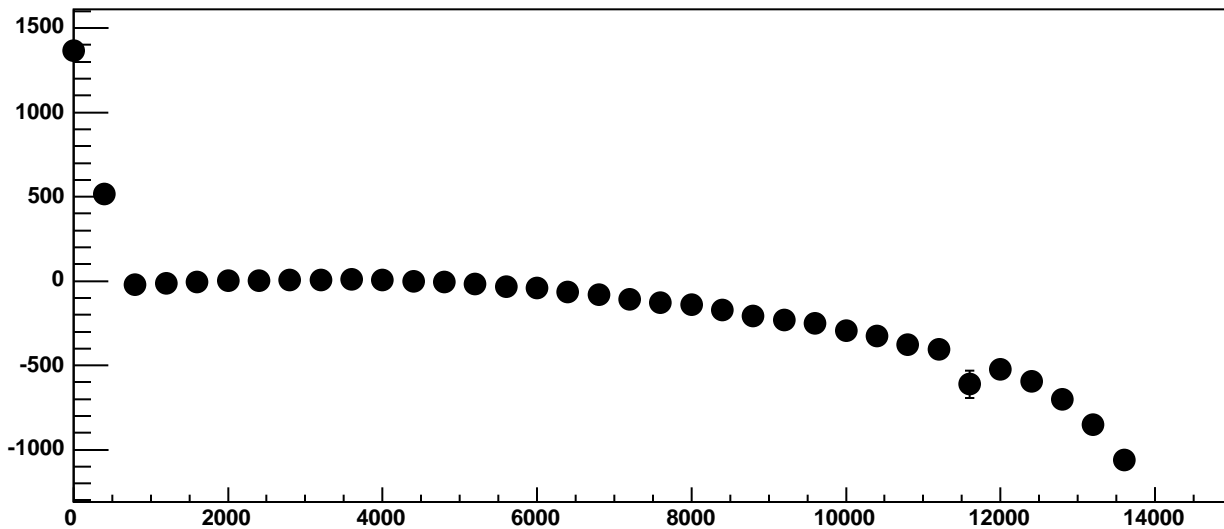
p1

2.164 ± 0.0005915

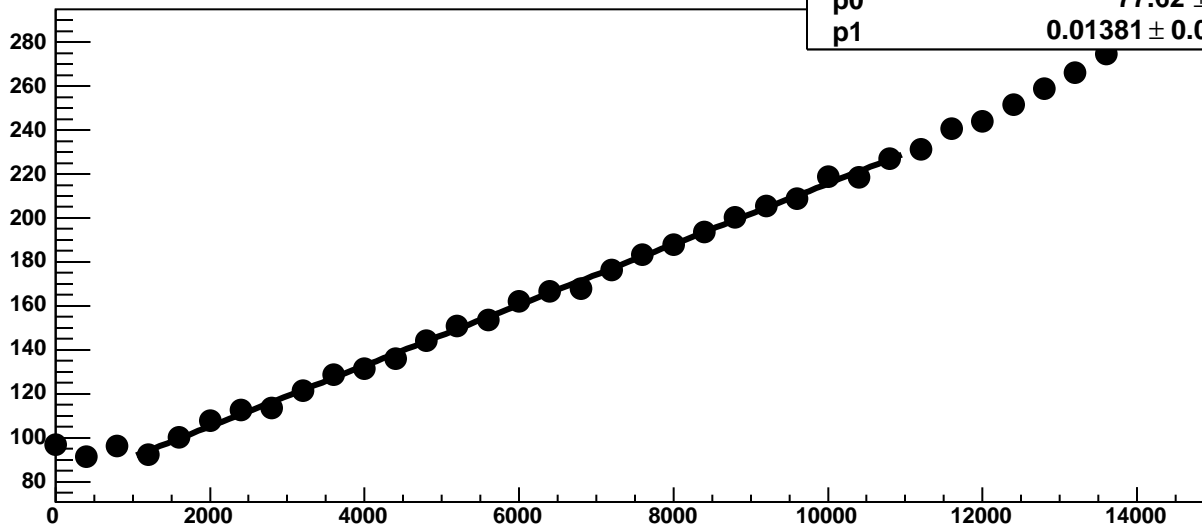
Chip 0, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

22.32 / 23

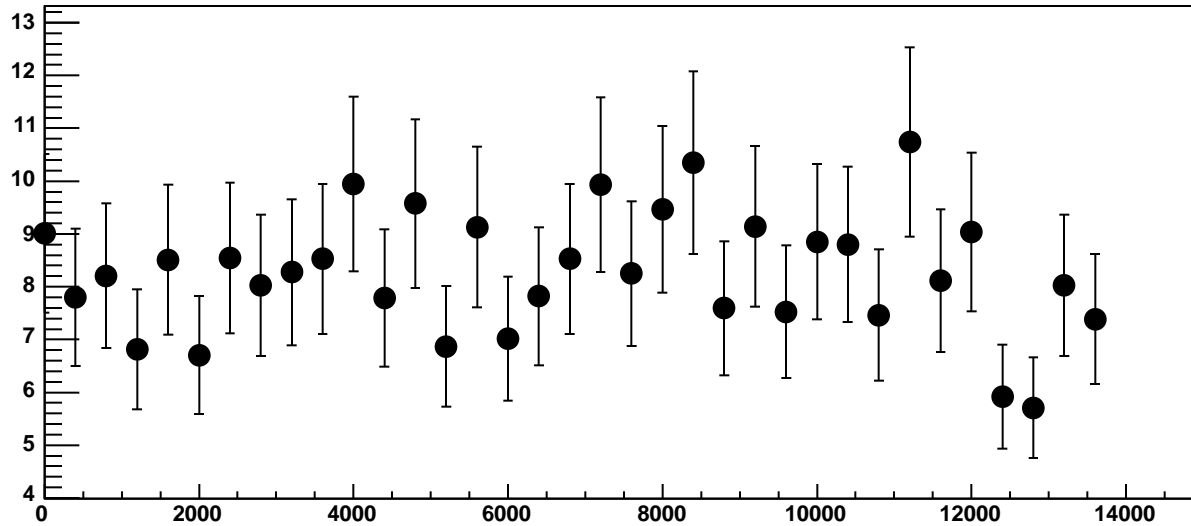
p0

77.62 ± 0.8294

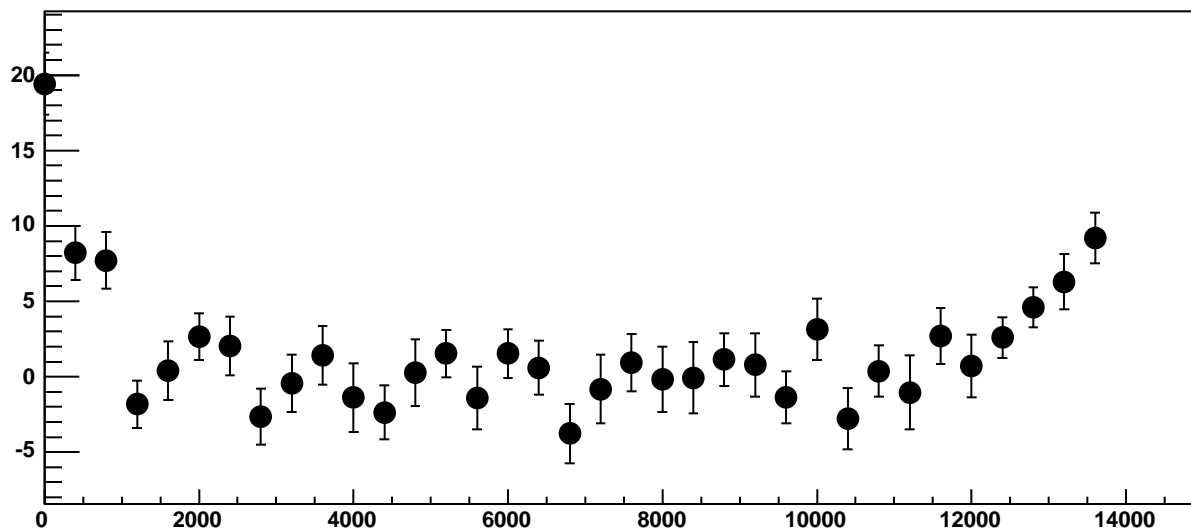
p1

0.01381 ± 0.0001269

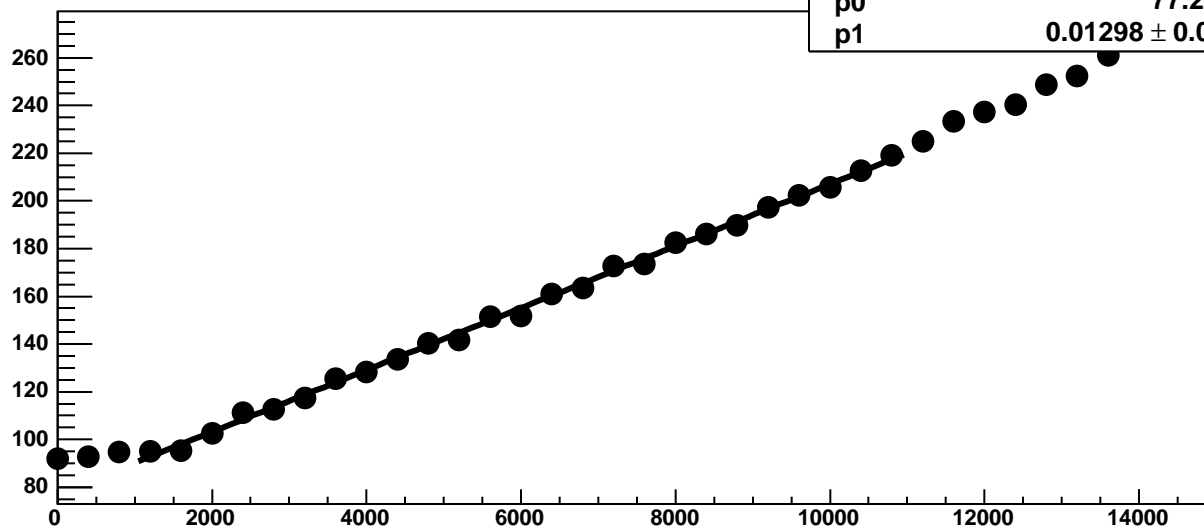
Chip 0, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

20.06 / 23

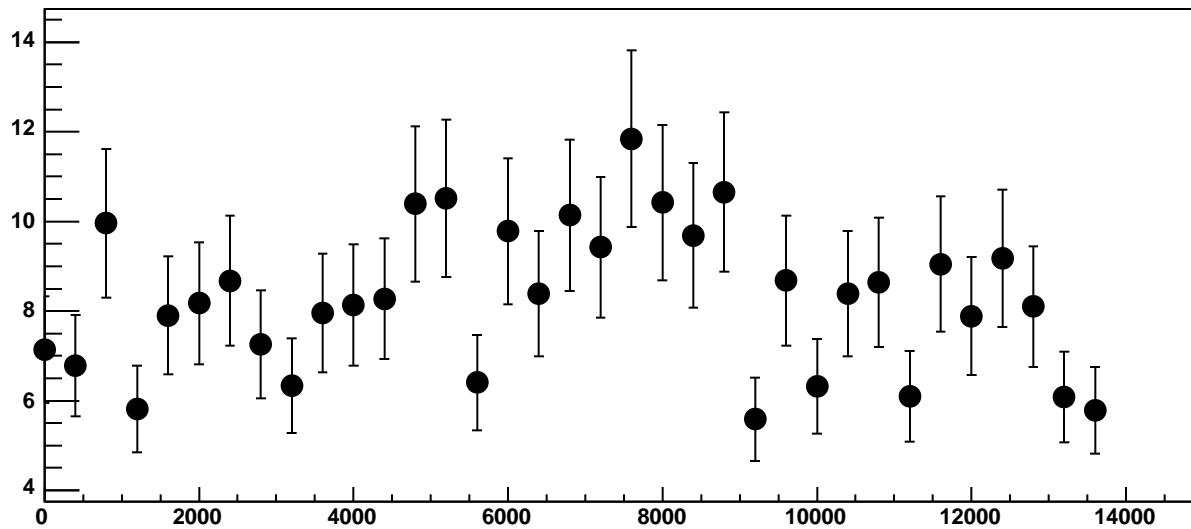
p0

77.2 ± 0.783

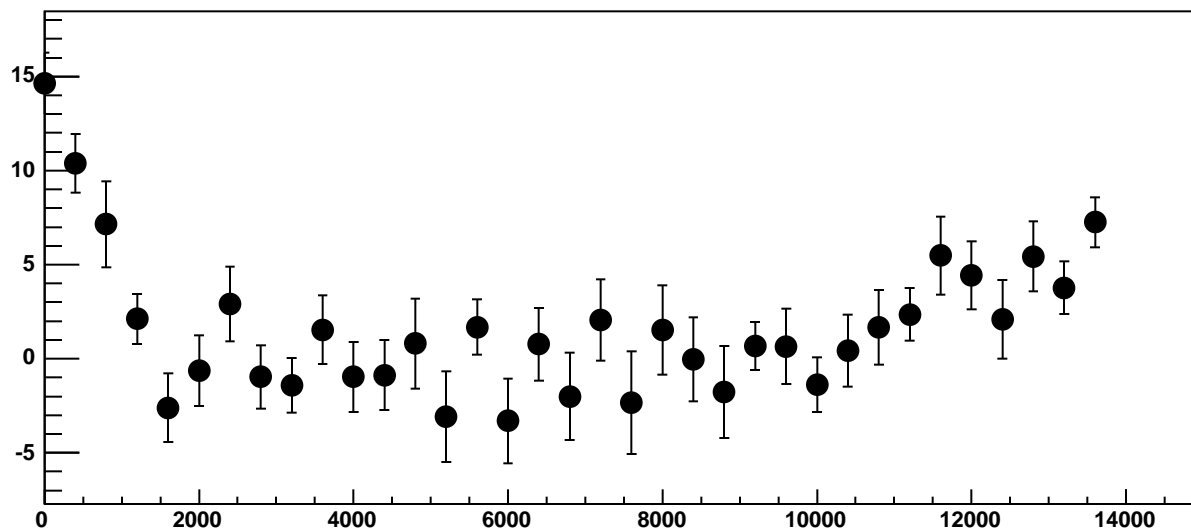
p1

0.01298 ± 0.0001196

Chip 0, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

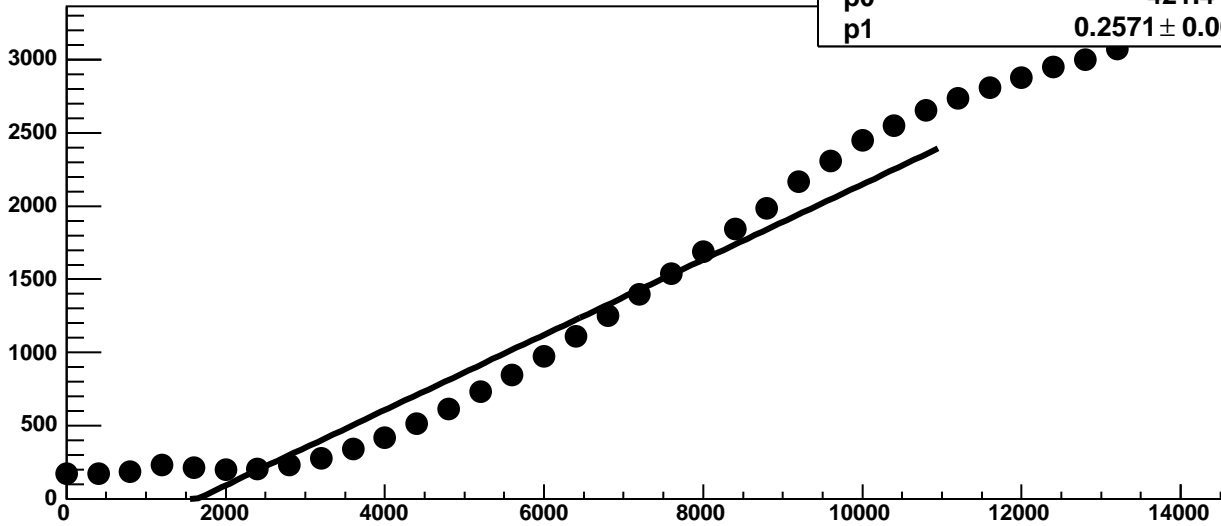


Chip 0, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

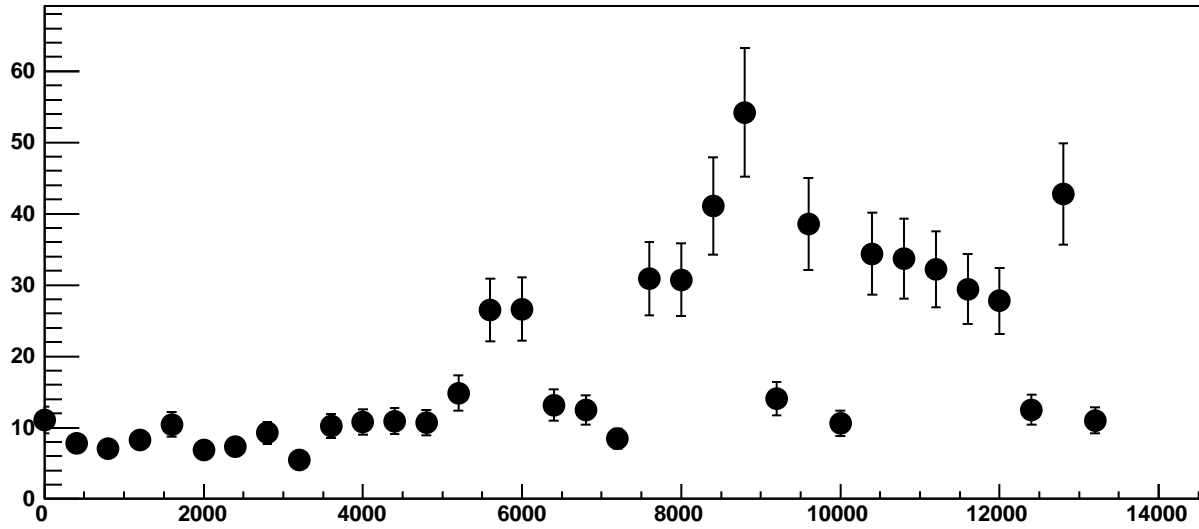


Chip 0, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC

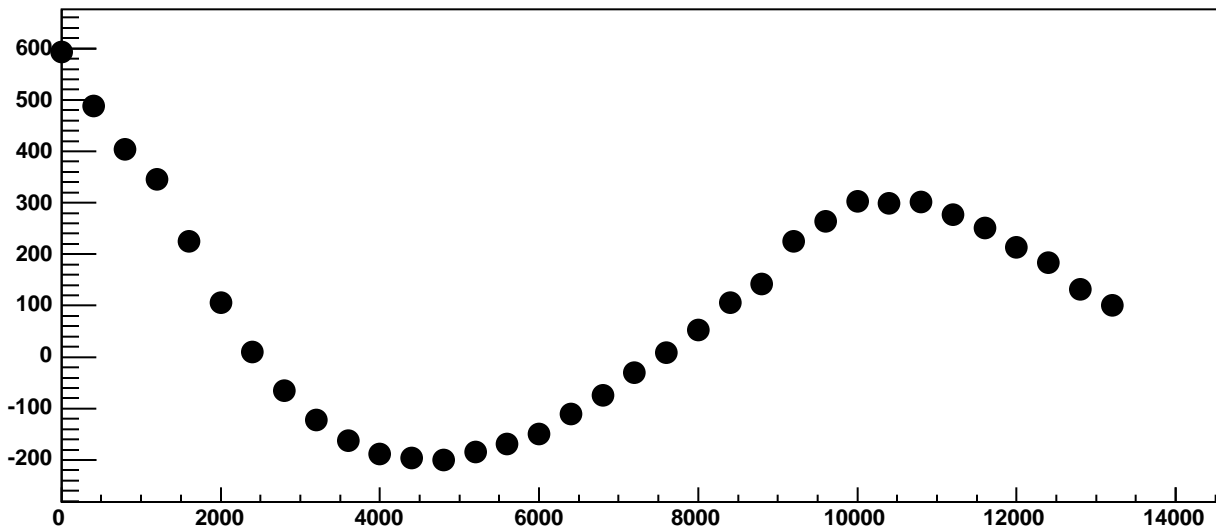
χ^2 / ndf 1.118e+05 / 23
p0 -421.4 ± 1.011
p1 0.2571 ± 0.0002104



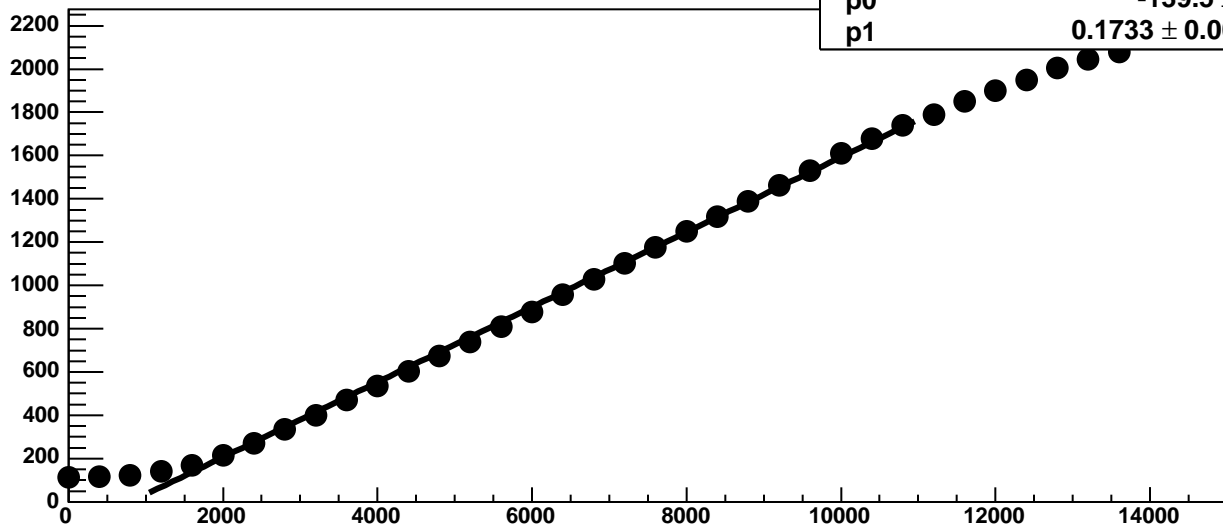
Chip 0, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



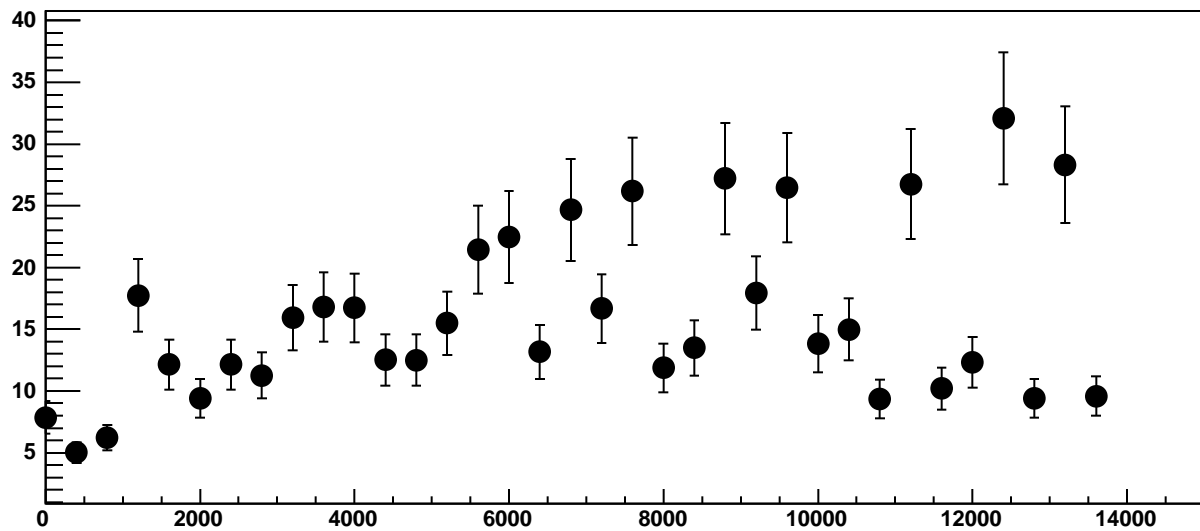
Chip 0, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC



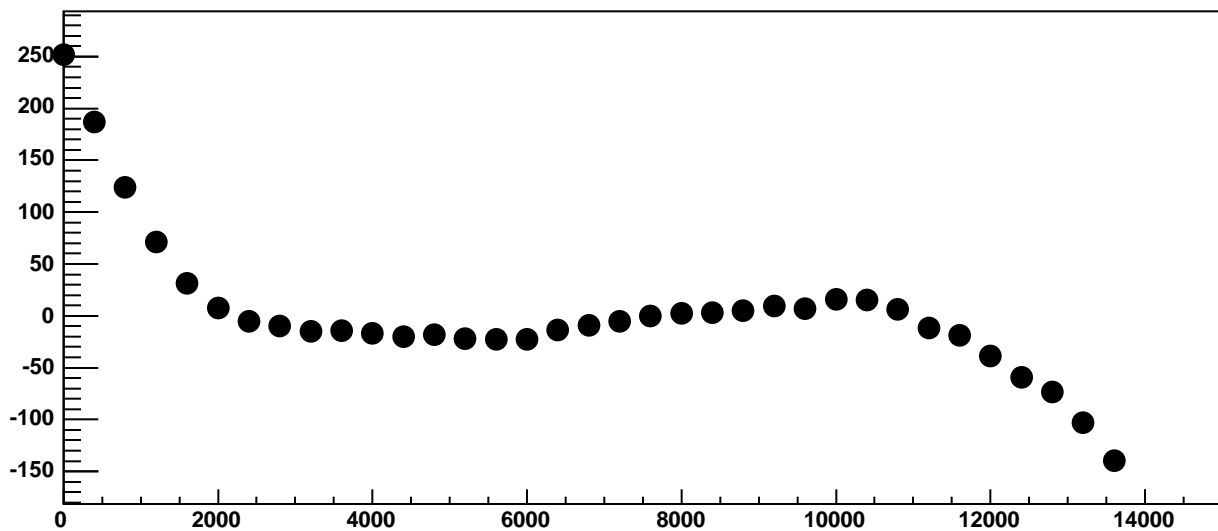
Chip 0, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



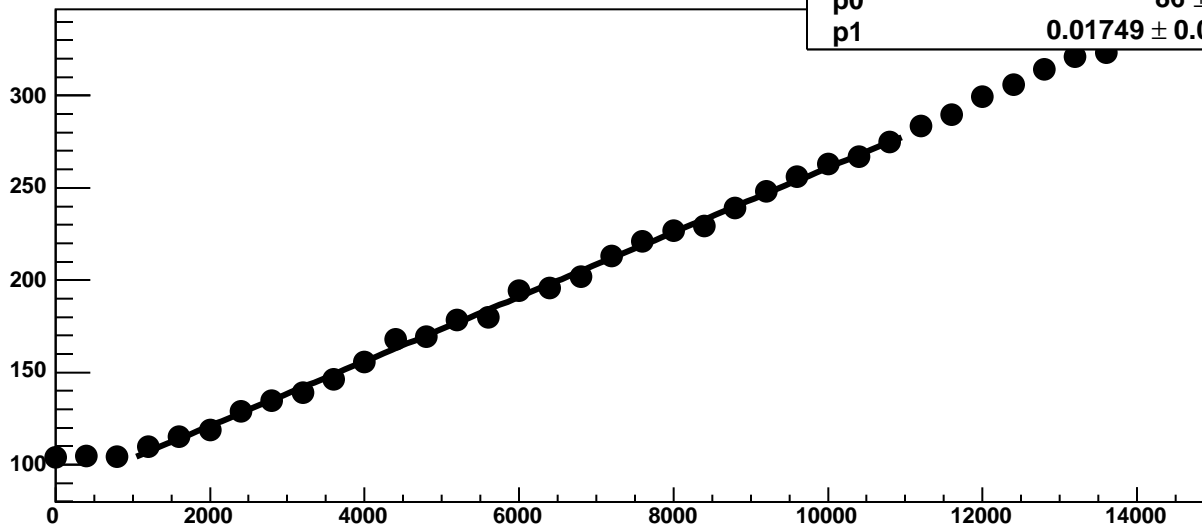
Chip 0, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

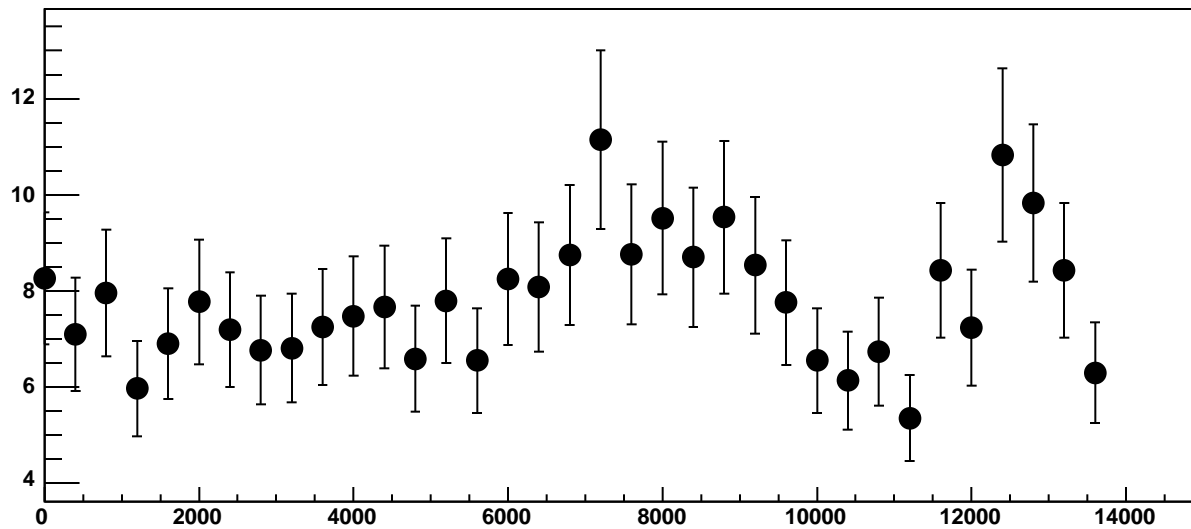


Chip 0, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

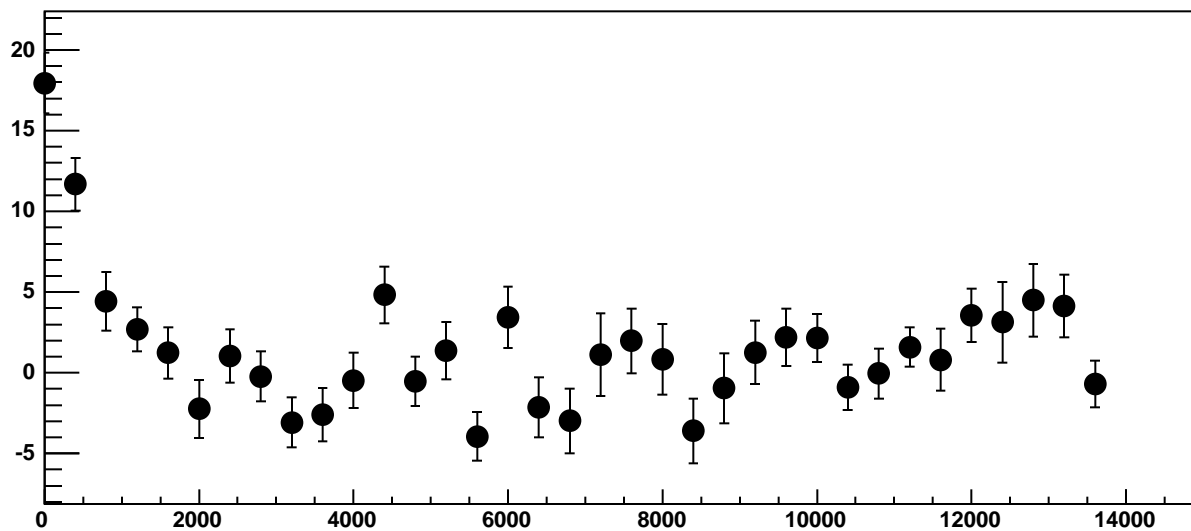


χ^2 / ndf 44.01 / 23
p0 86 ± 0.7299
p1 0.01749 ± 0.0001116

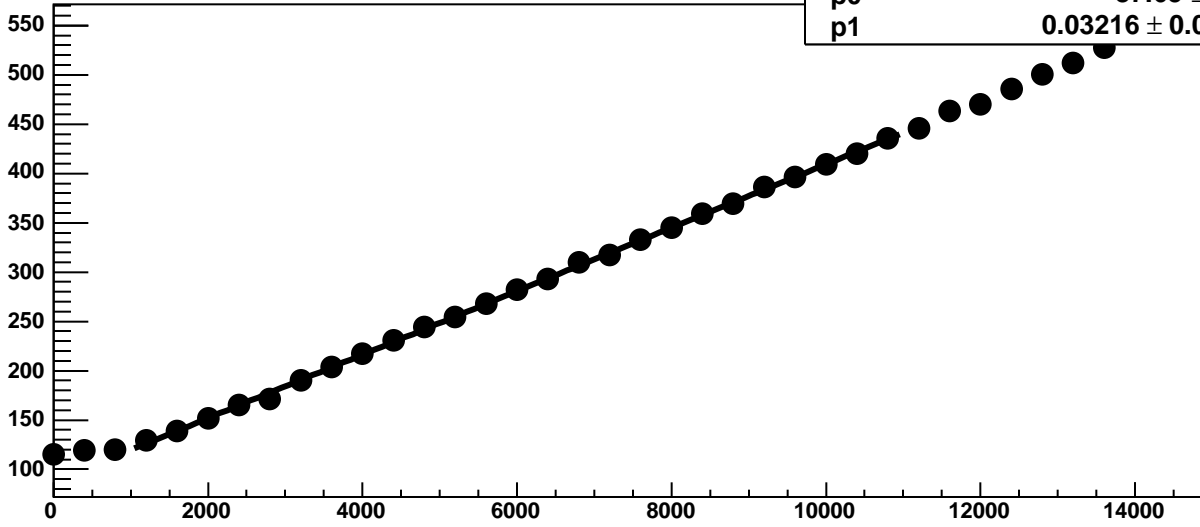
Chip 0, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

26.43 / 23

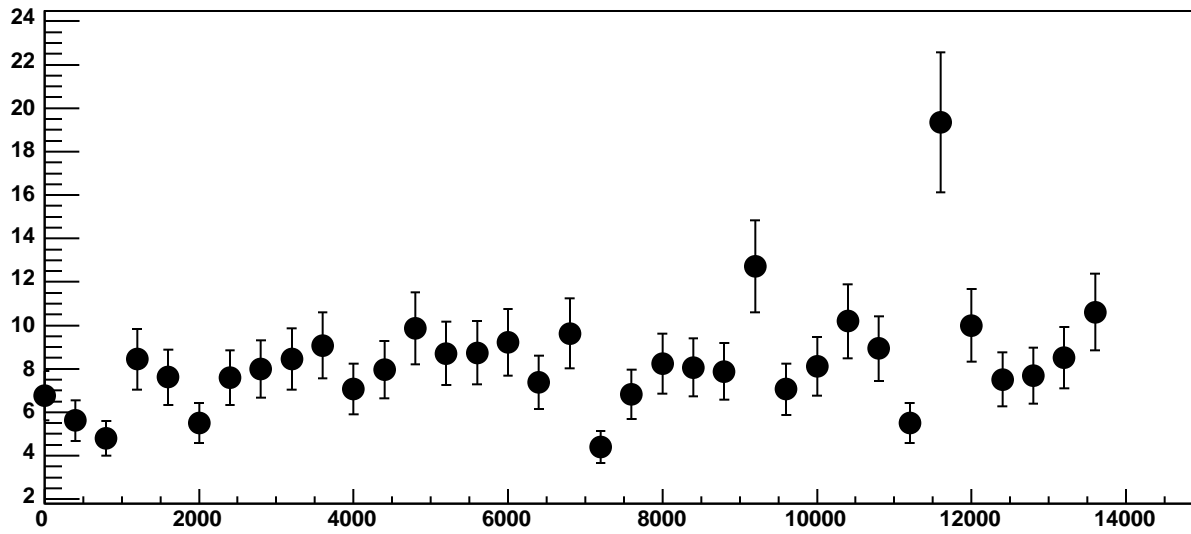
p0

87.65 ± 0.8137

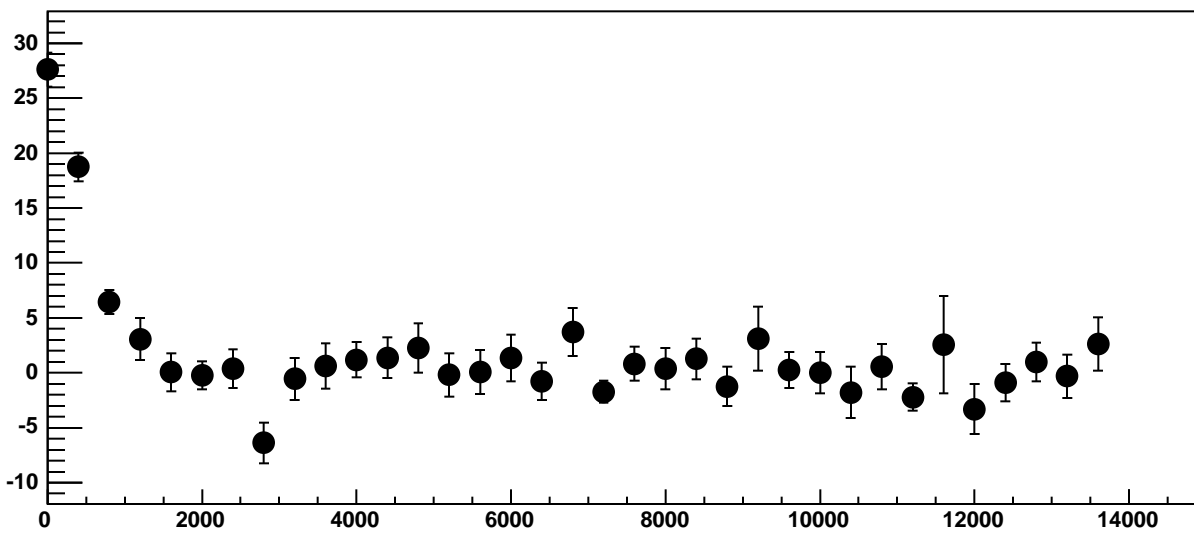
p1

0.03216 ± 0.0001256

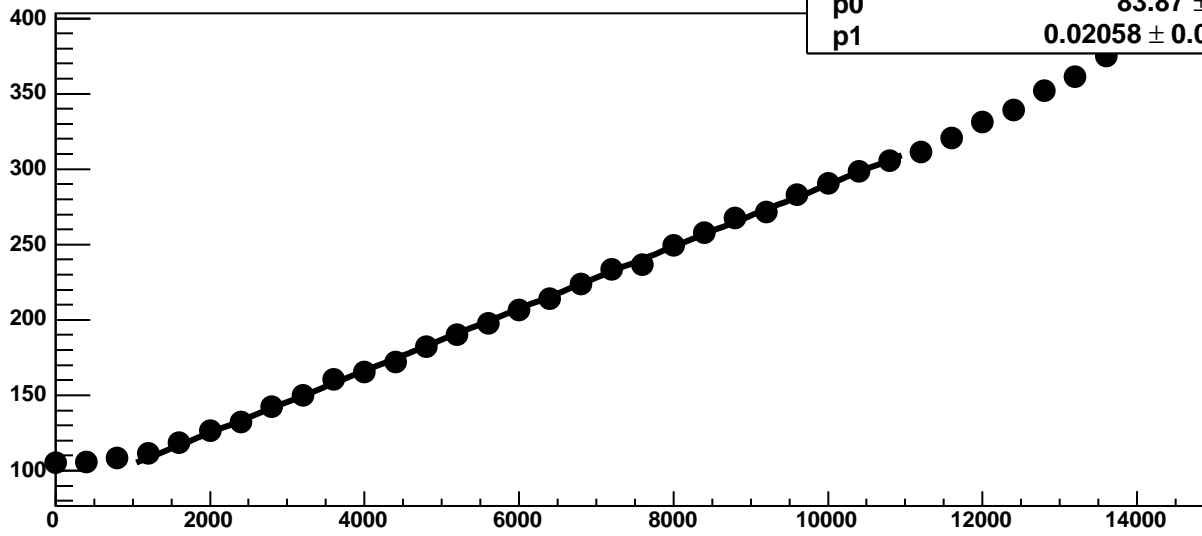
Chip 0, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

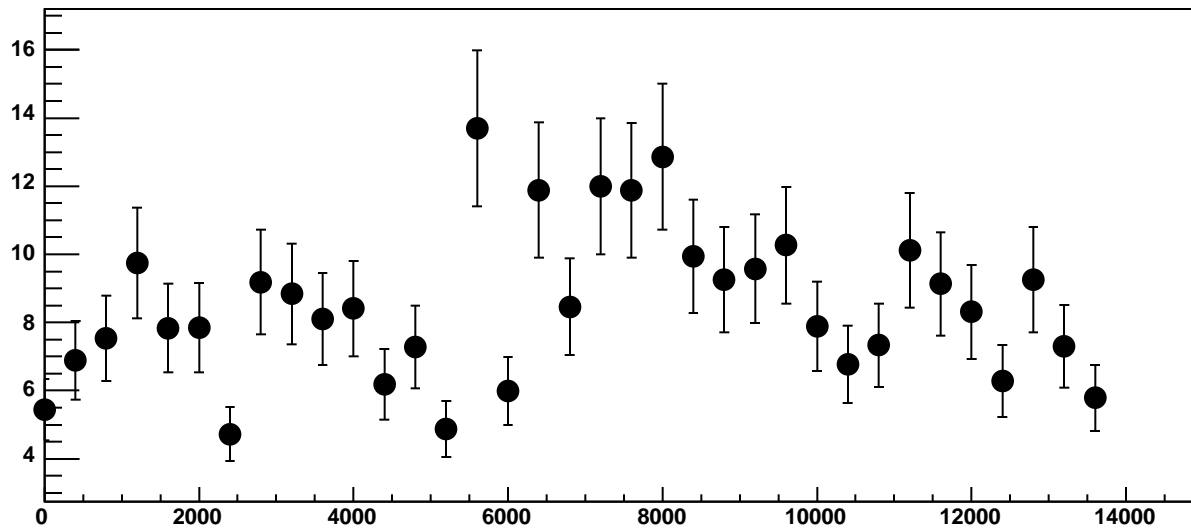


Chip 0, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

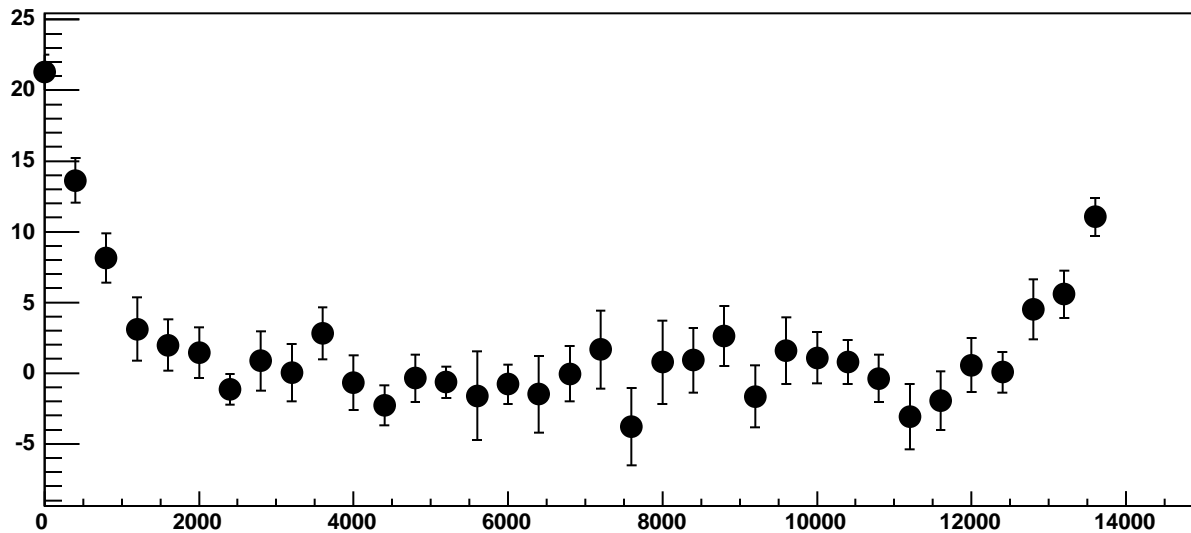


χ^2 / ndf 17.14 / 23
p0 83.87 ± 0.7863
p1 0.02058 ± 0.0001249

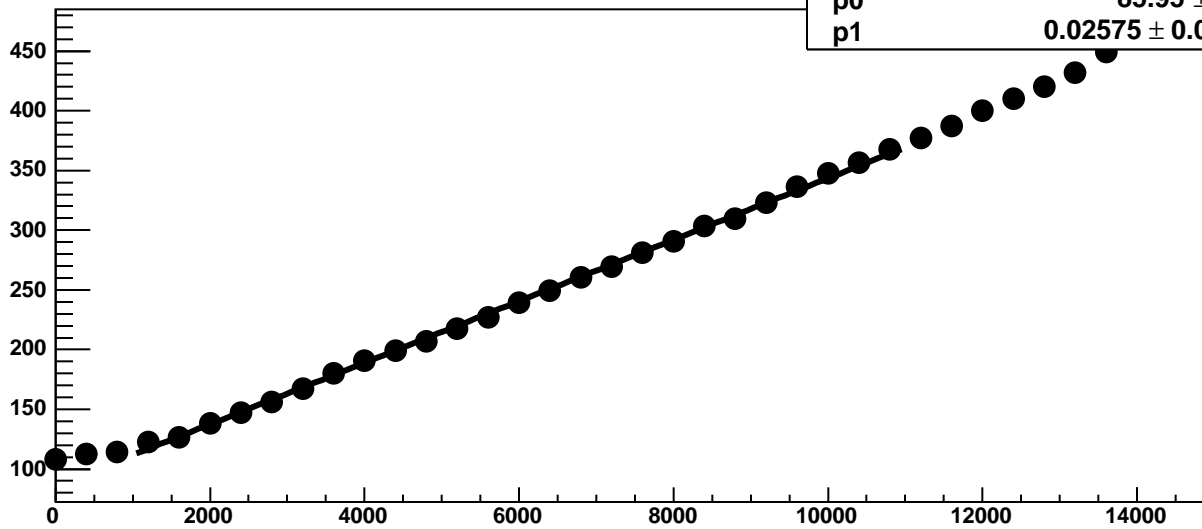
Chip 0, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

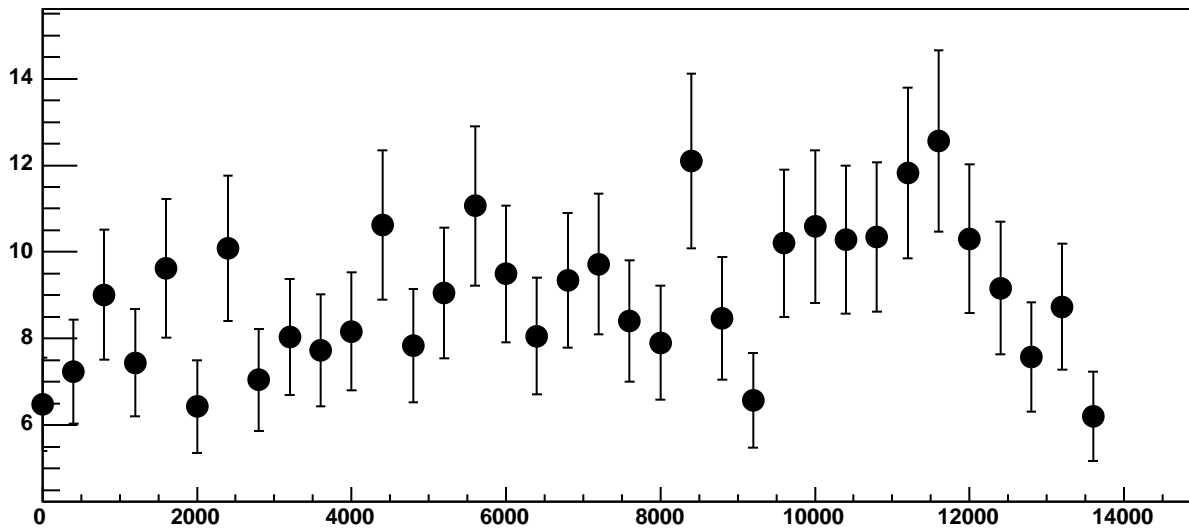


Chip 0, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

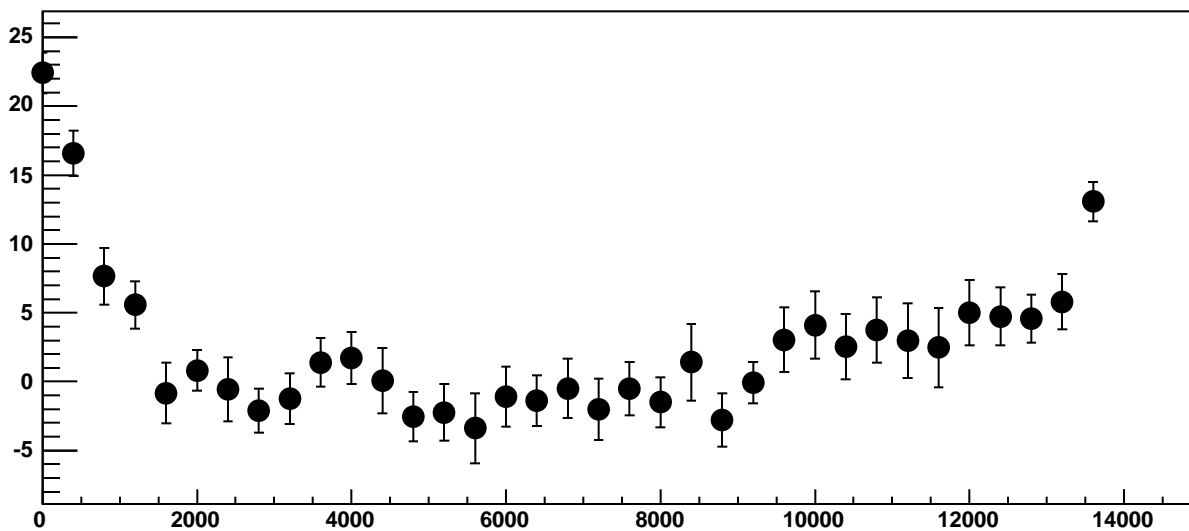


χ^2 / ndf 32.68 / 23
p0 85.95 ± 0.8663
p1 0.02575 ± 0.0001368

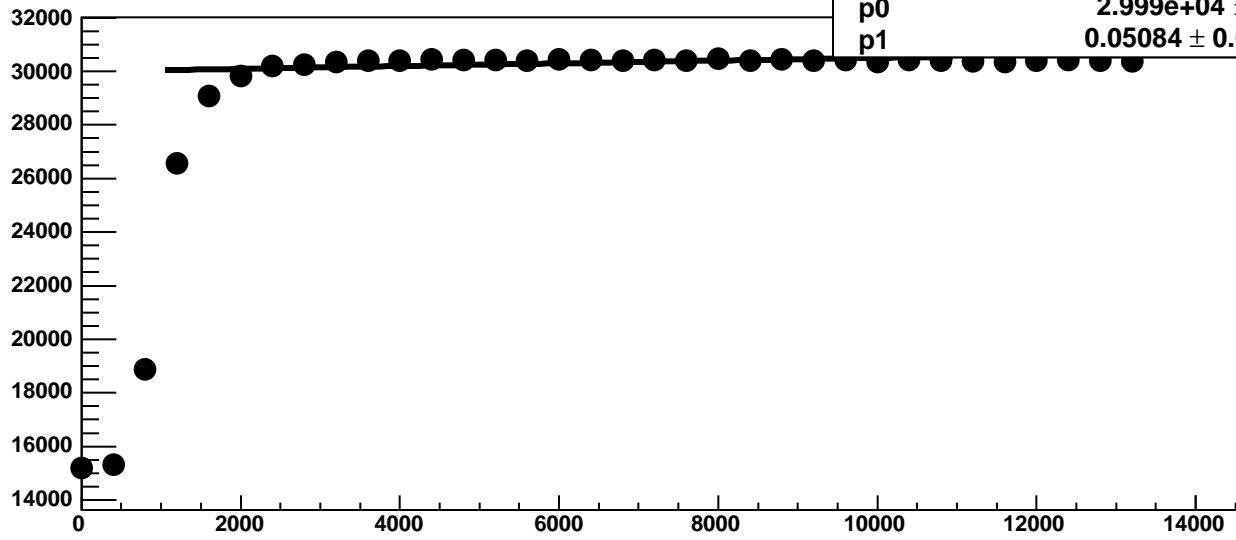
Chip 0, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



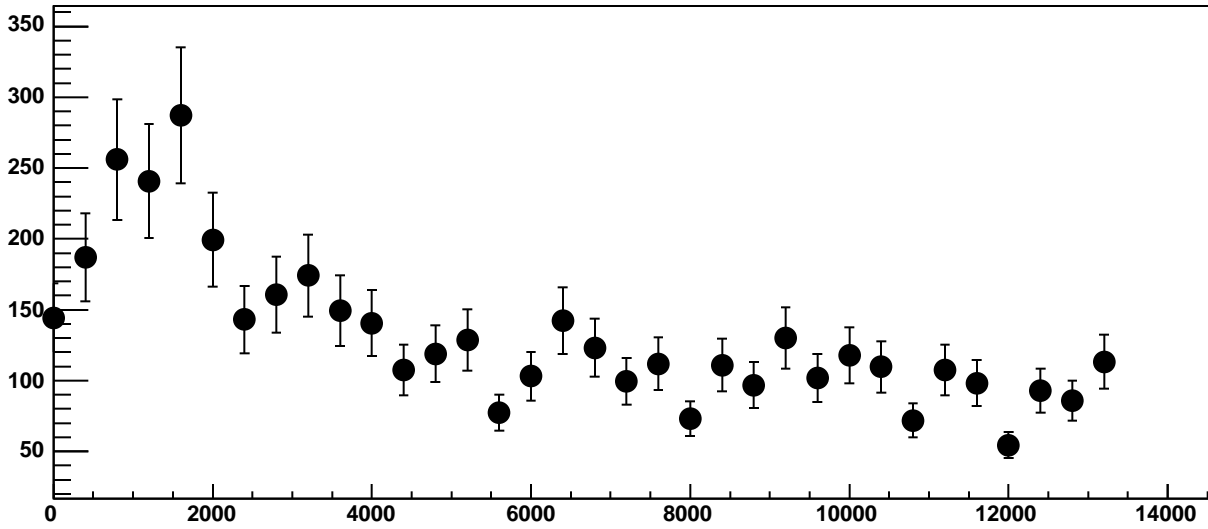
Chip 0, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



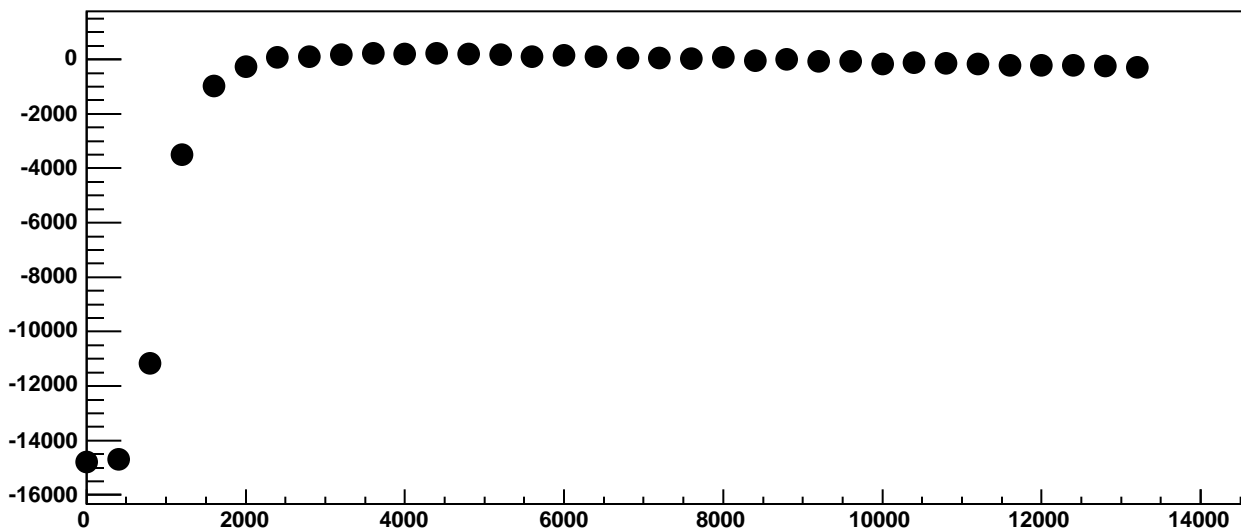
Chip 0, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC



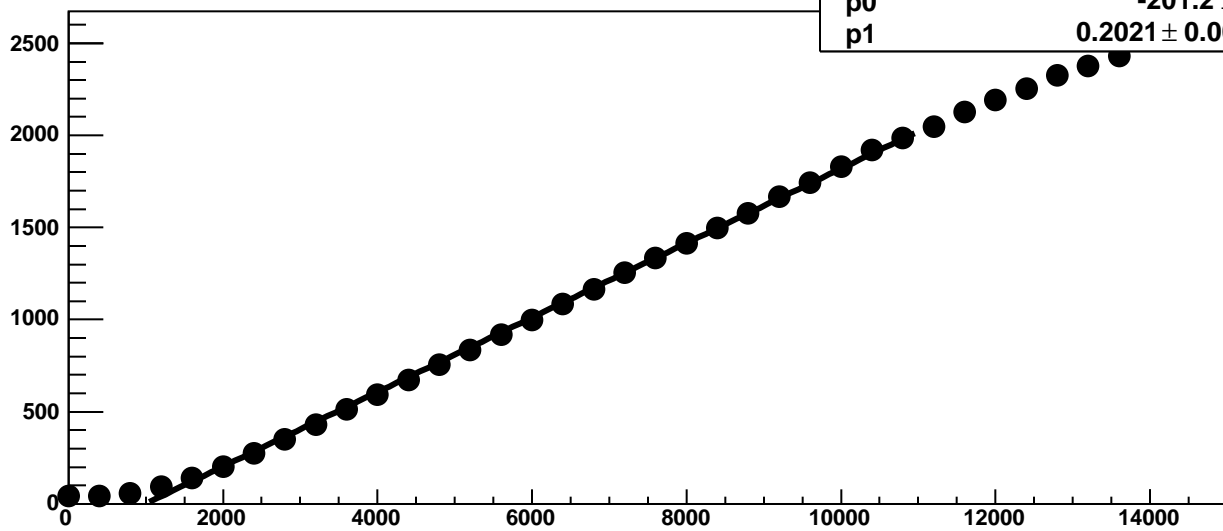
Chip 0, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC

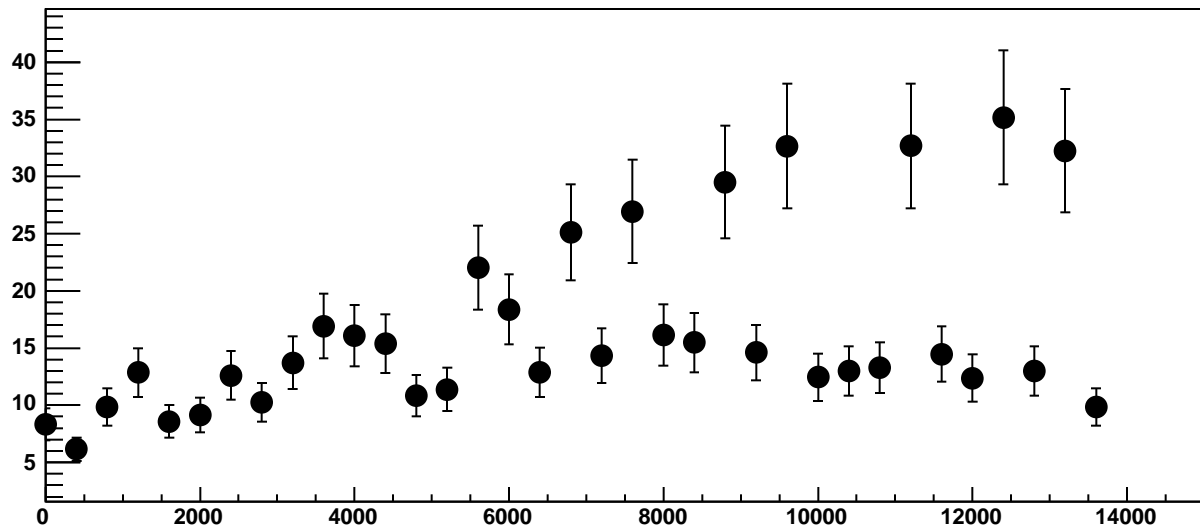


Chip 0, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

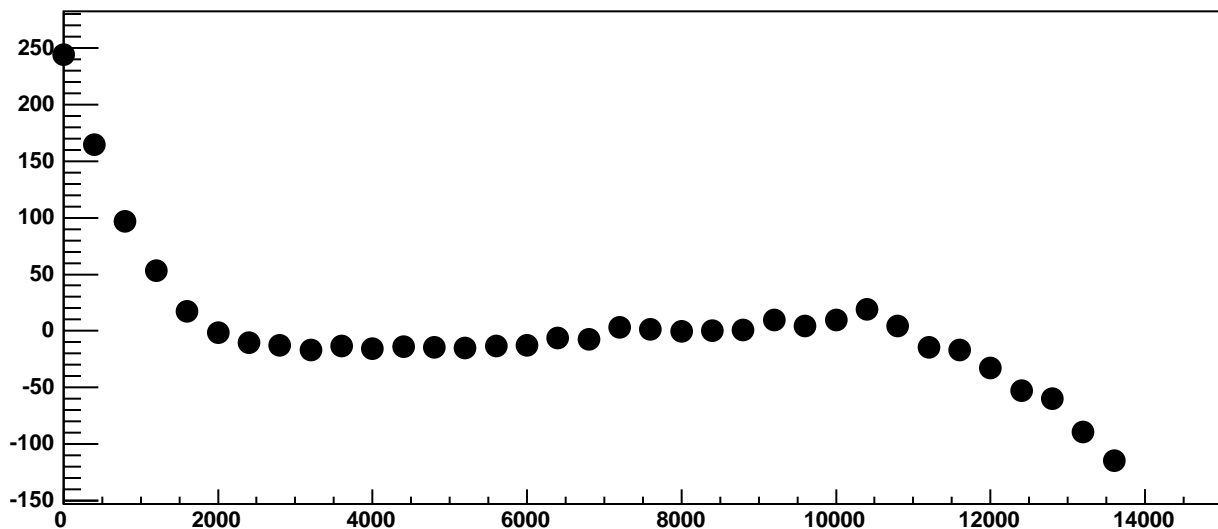


χ^2 / ndf 674.3 / 23
p0 -201.2 ± 1.227
p1 0.2021 ± 0.0002052

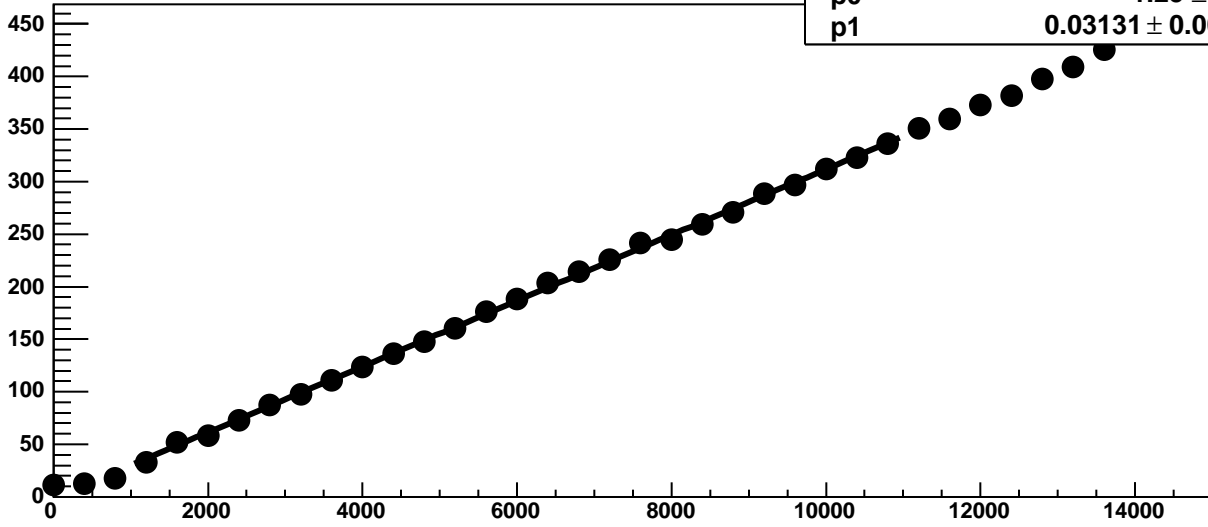
Chip 0, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

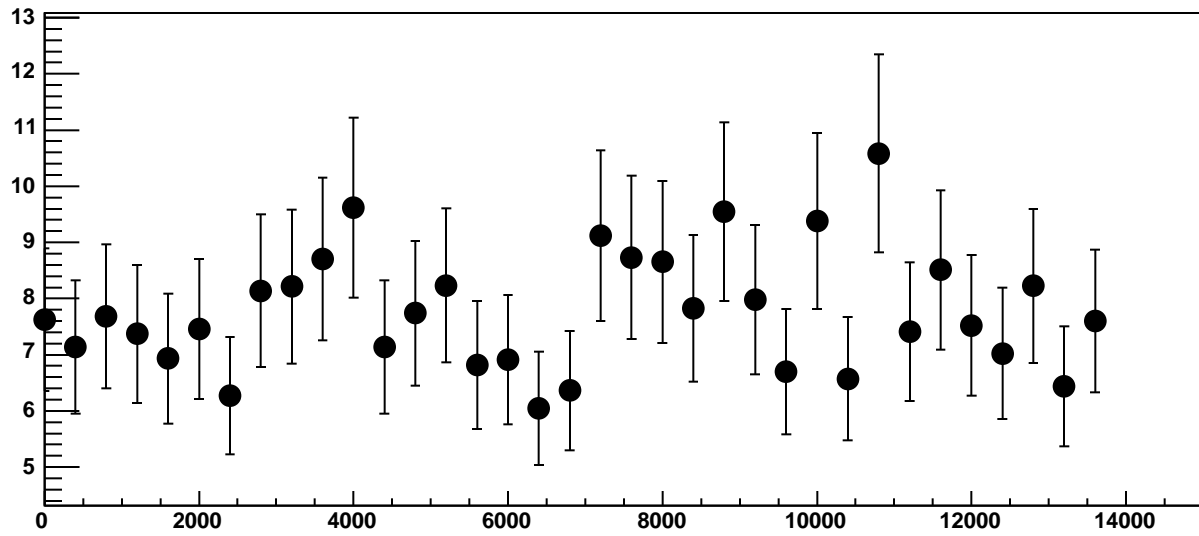


Chip 0, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

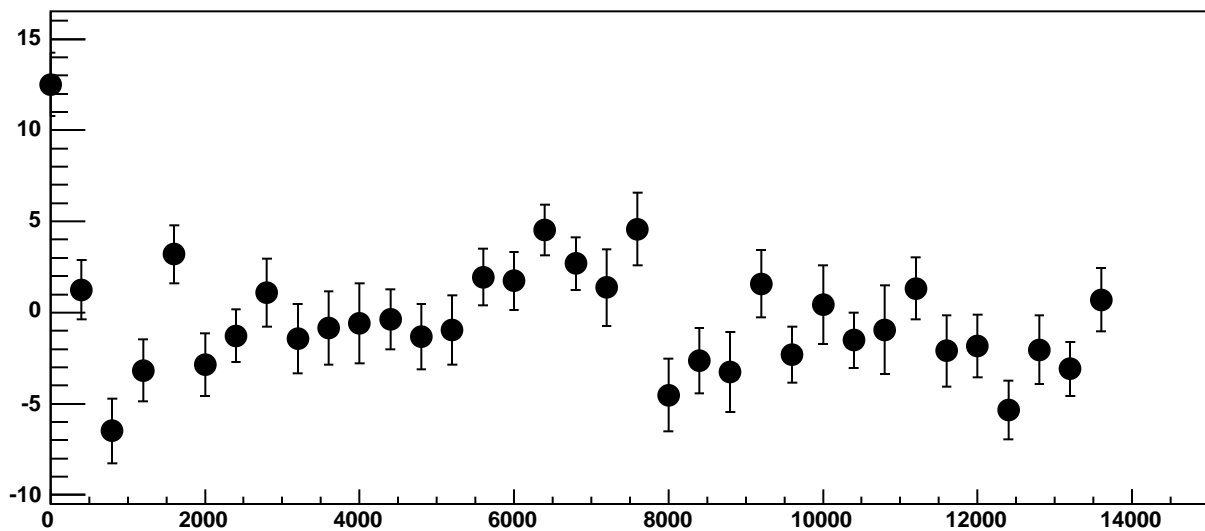


χ^2 / ndf 49.34 / 23
p0 -1.25 ± 0.7958
p1 0.03131 ± 0.0001227

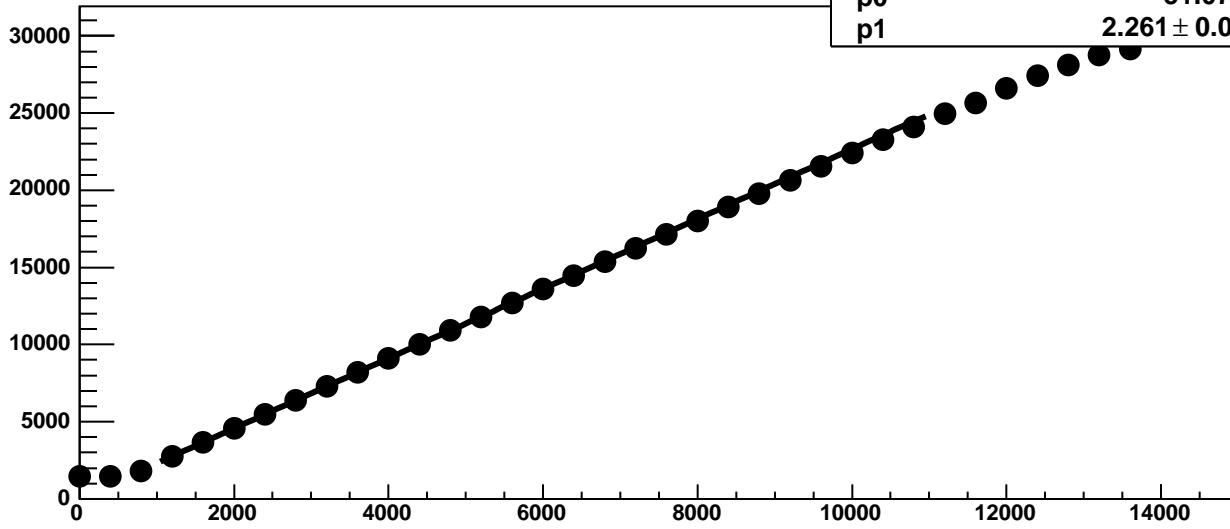
Chip 0, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

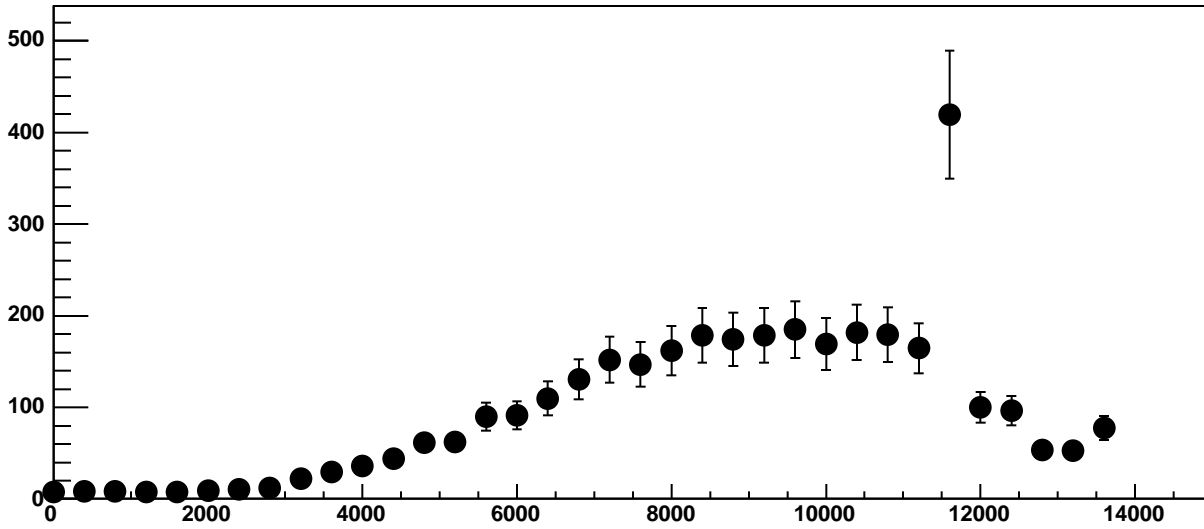


Chip 0, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC

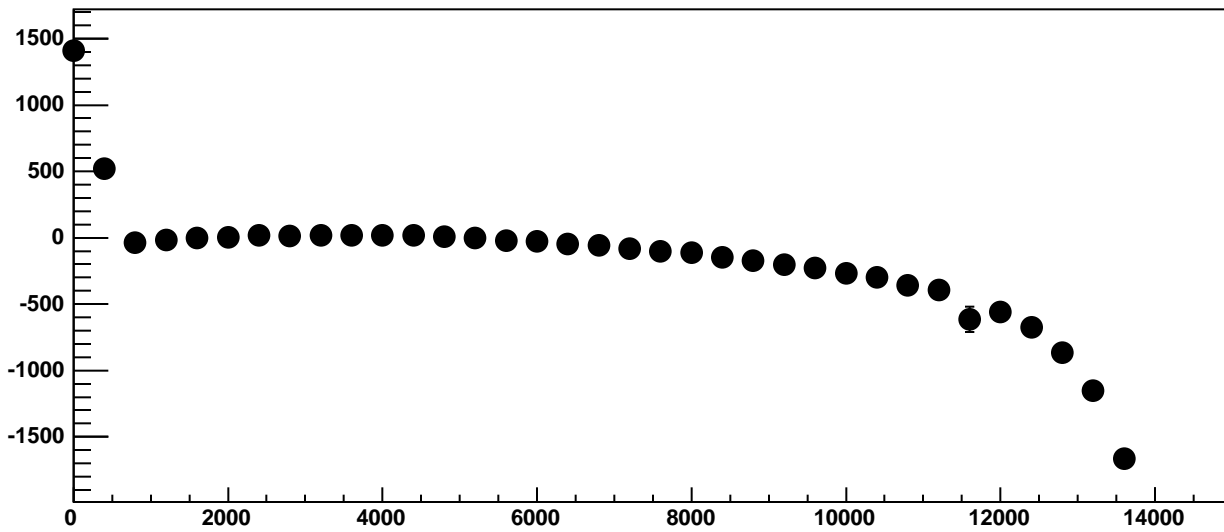


χ^2 / ndf 524.9 / 23
p0 51.07 ± 2.08
p1 2.261 ± 0.0009374

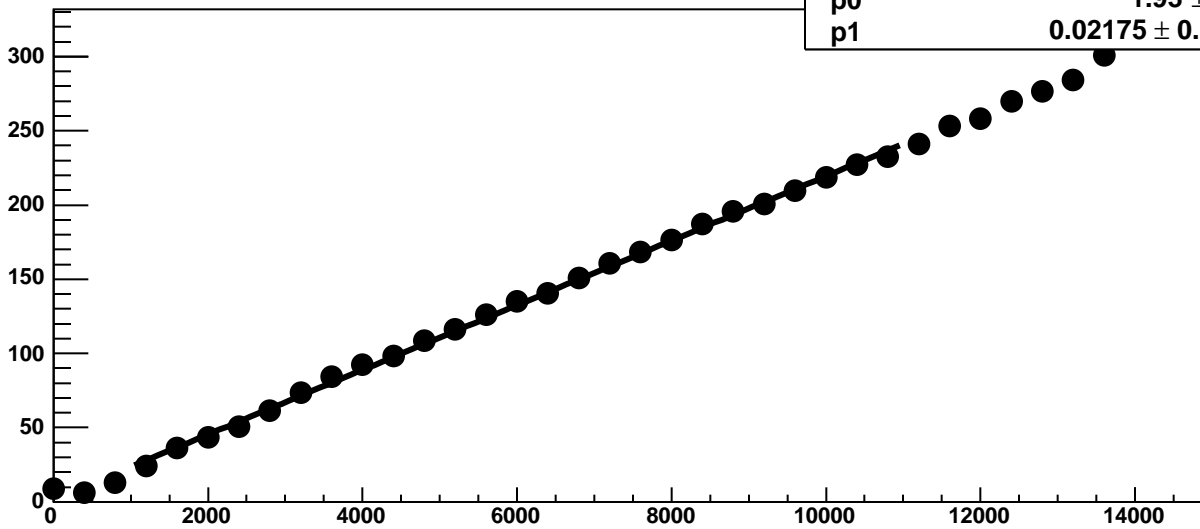
Chip 0, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC

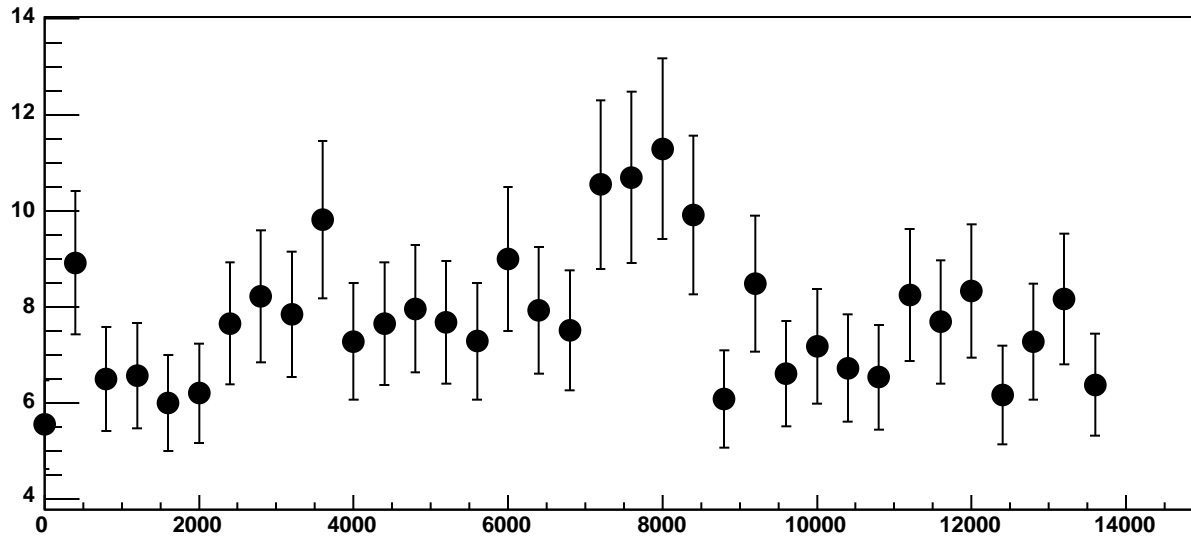


Chip 0, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

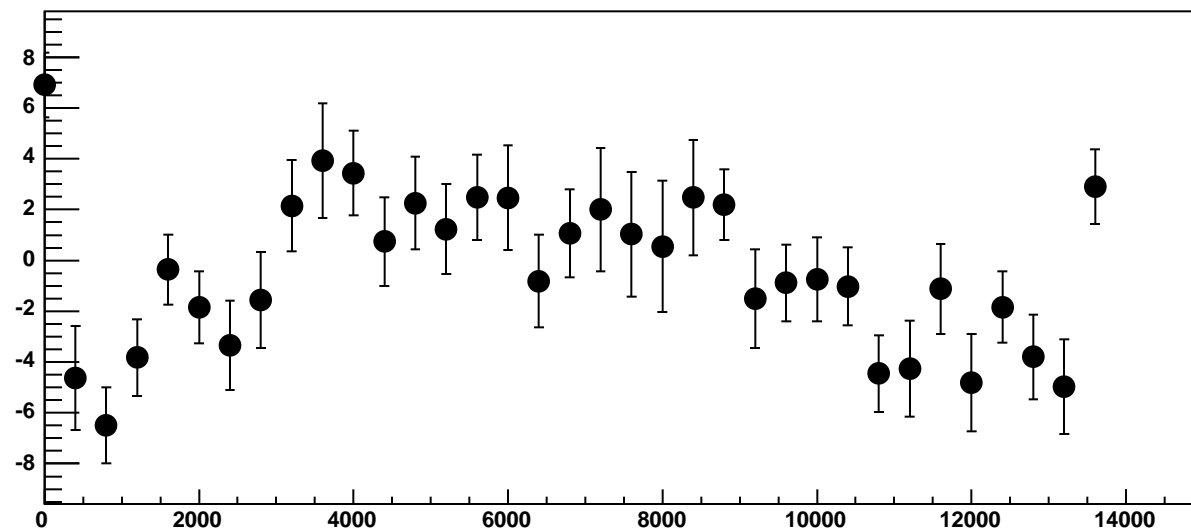


χ^2 / ndf 42.61 / 23
p0 1.93 ± 0.7413
p1 0.02175 ± 0.0001111

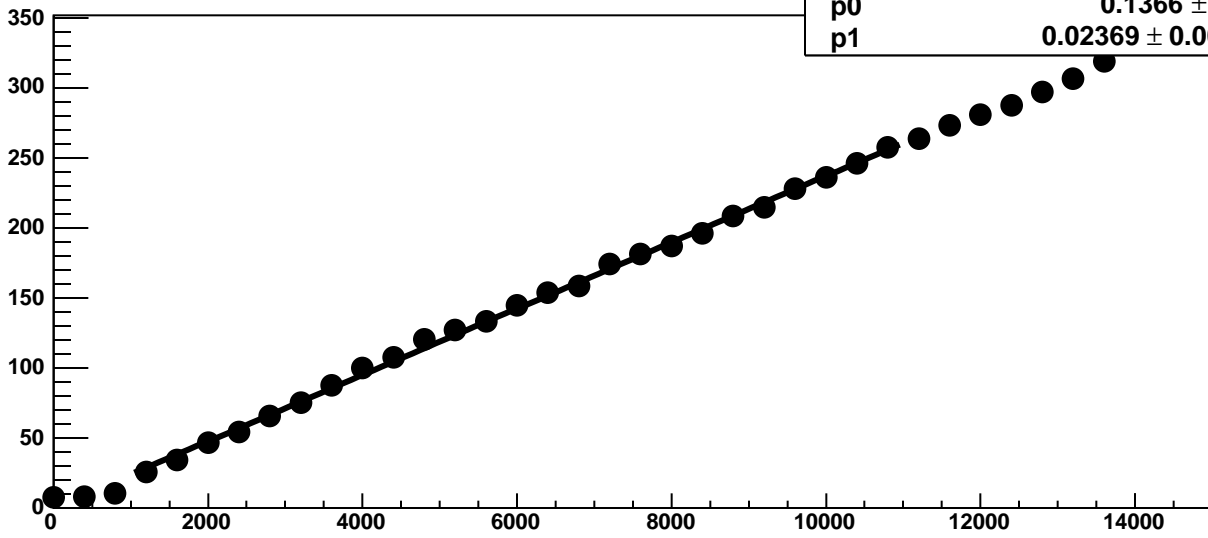
Chip 0, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

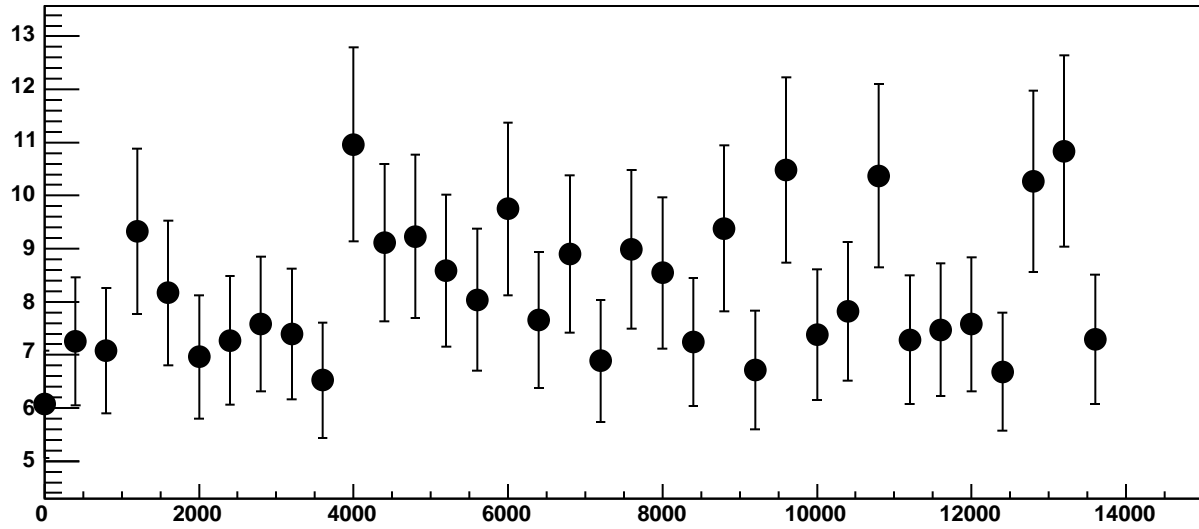


Chip 0, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

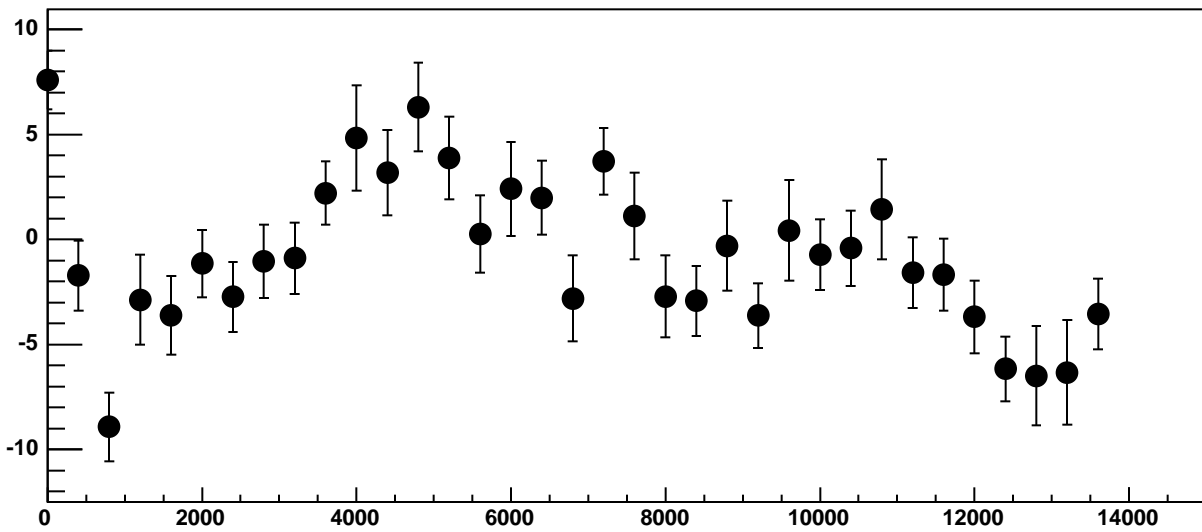


χ^2 / ndf 51.79 / 23
p0 0.1366 ± 0.8448
p1 0.02369 ± 0.0001288

Chip 0, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC

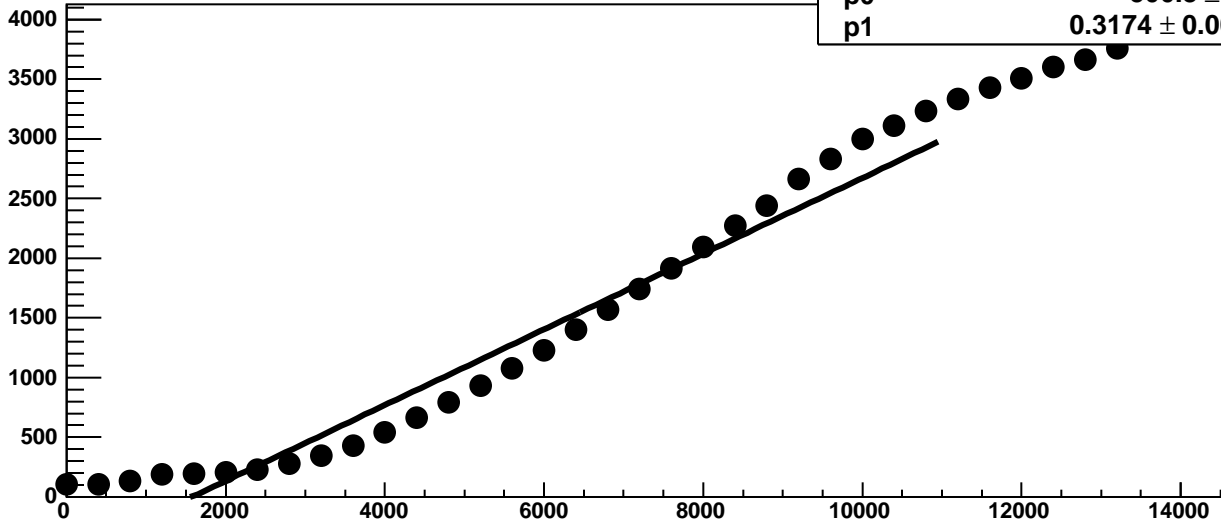


Chip 0, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

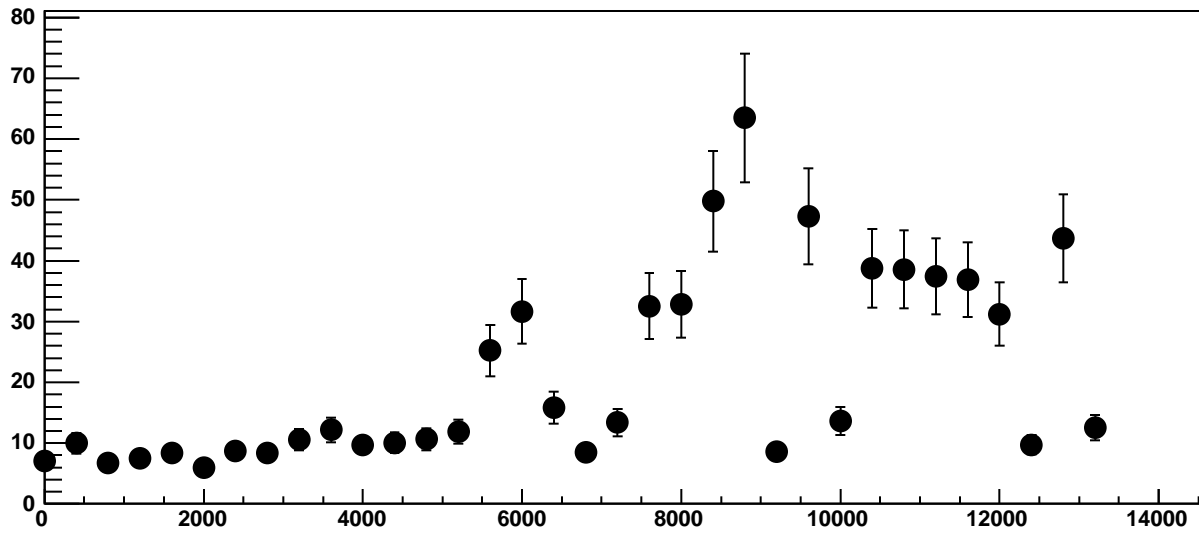


Chip 0, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC

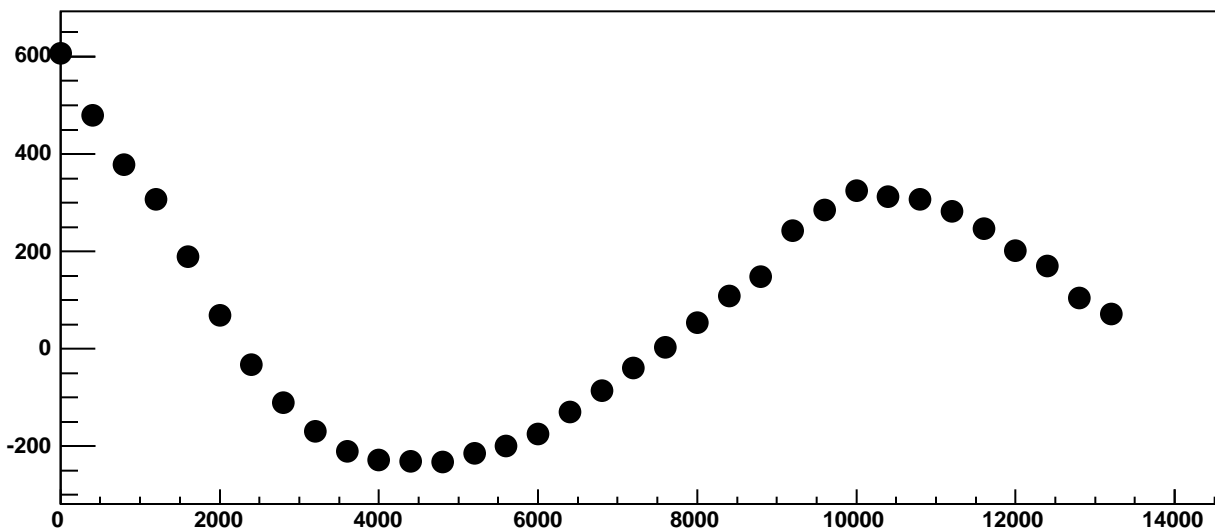
χ^2 / ndf 1.282e+05 / 23
p0 -500.8 ± 0.9949
p1 0.3174 ± 0.0002032



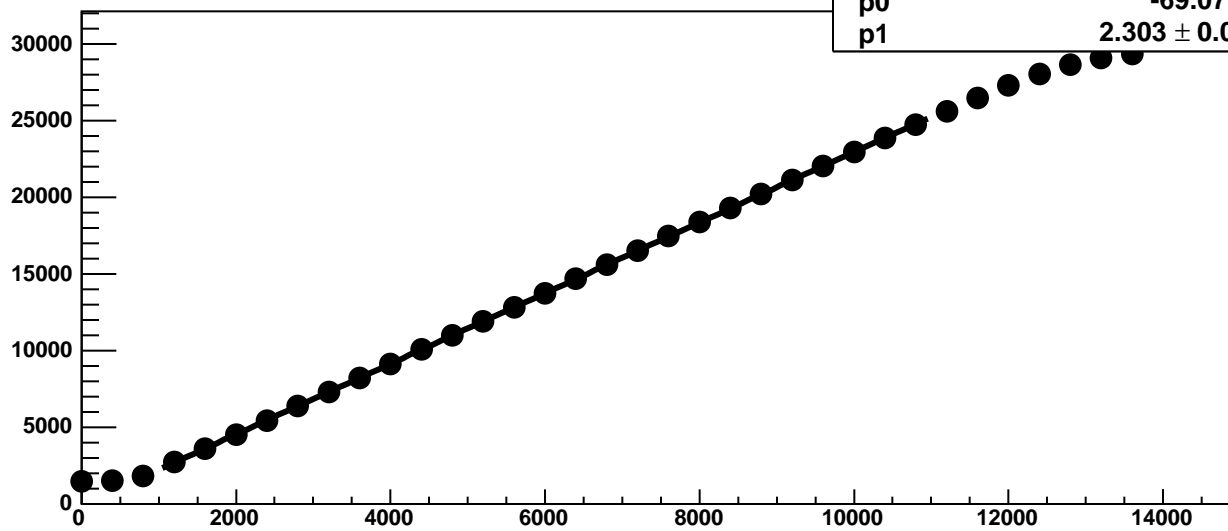
Chip 0, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



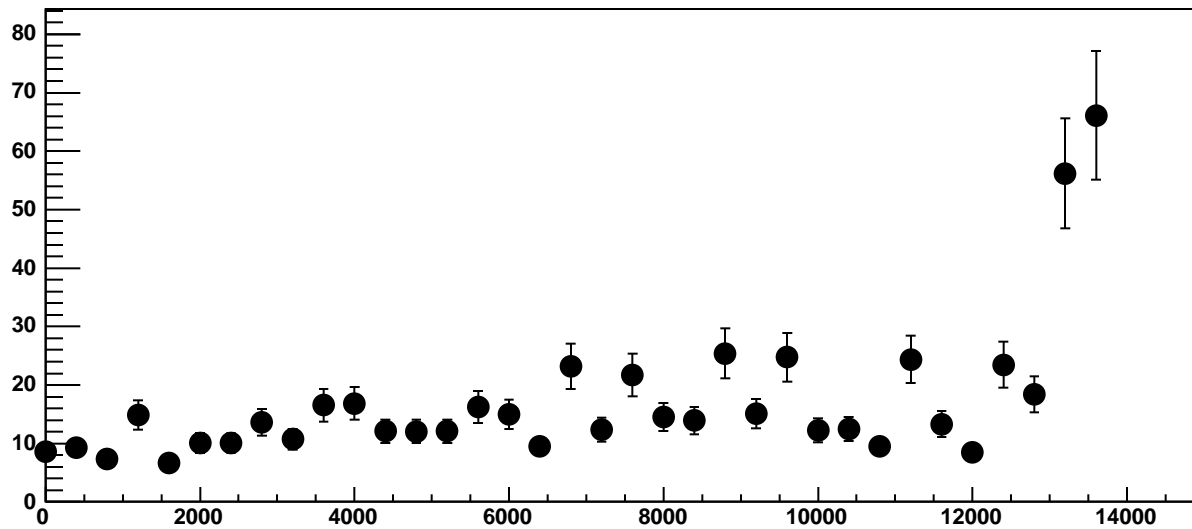
Chip 0, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



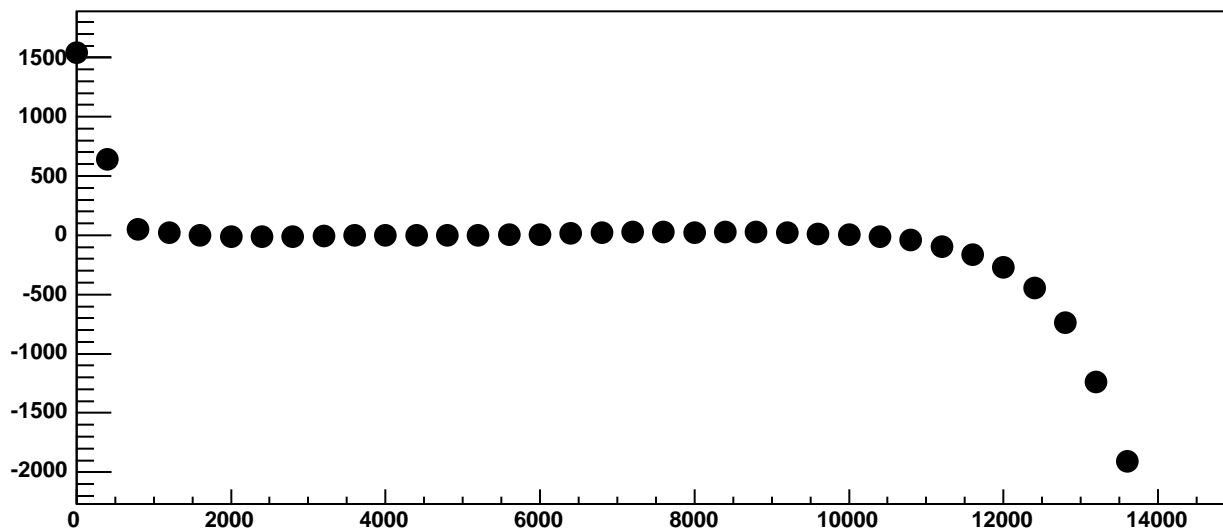
Chip 0, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



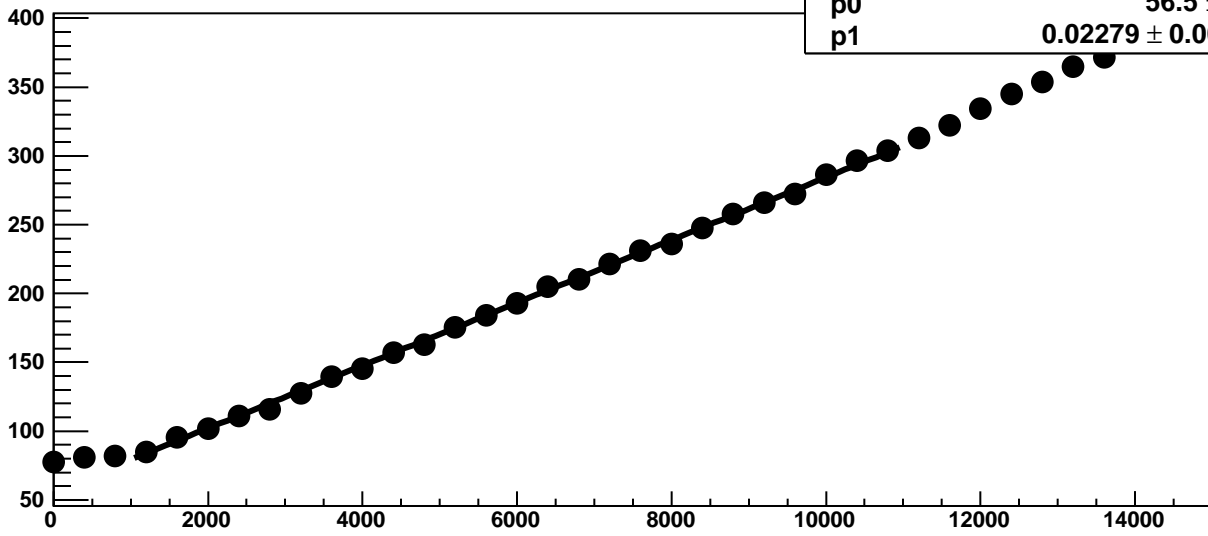
Chip 0, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC

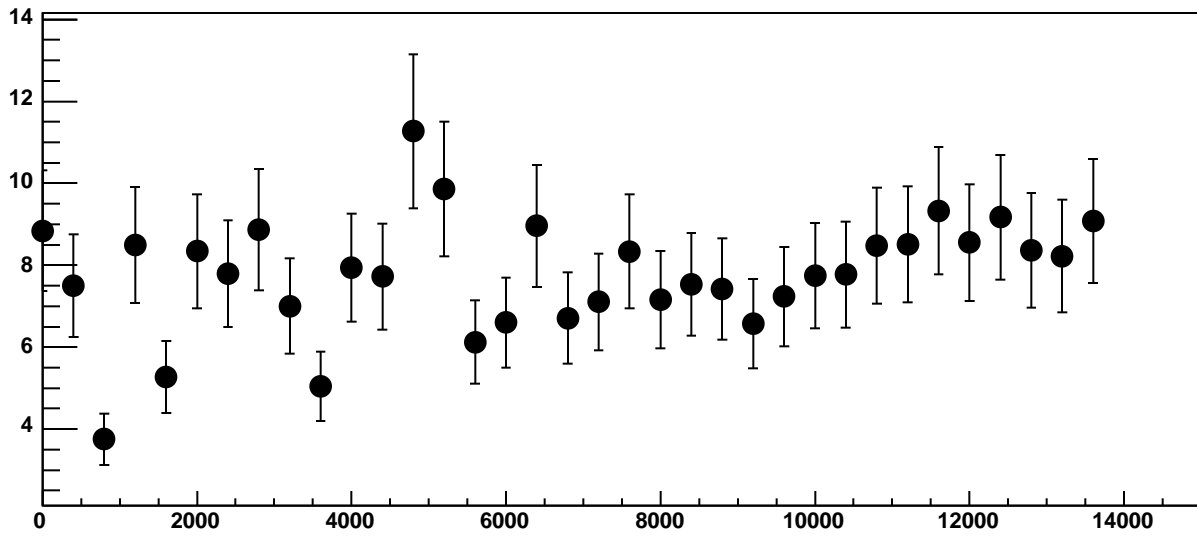


Chip 0, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

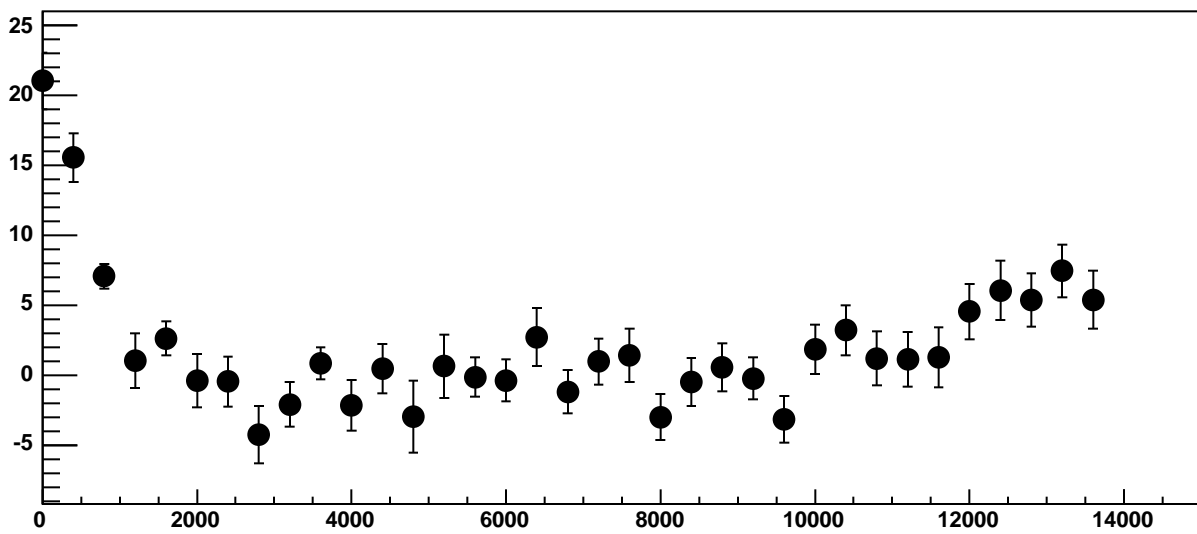


χ^2 / ndf 29.73 / 23
p0 56.5 ± 0.759
p1 0.02279 ± 0.0001163

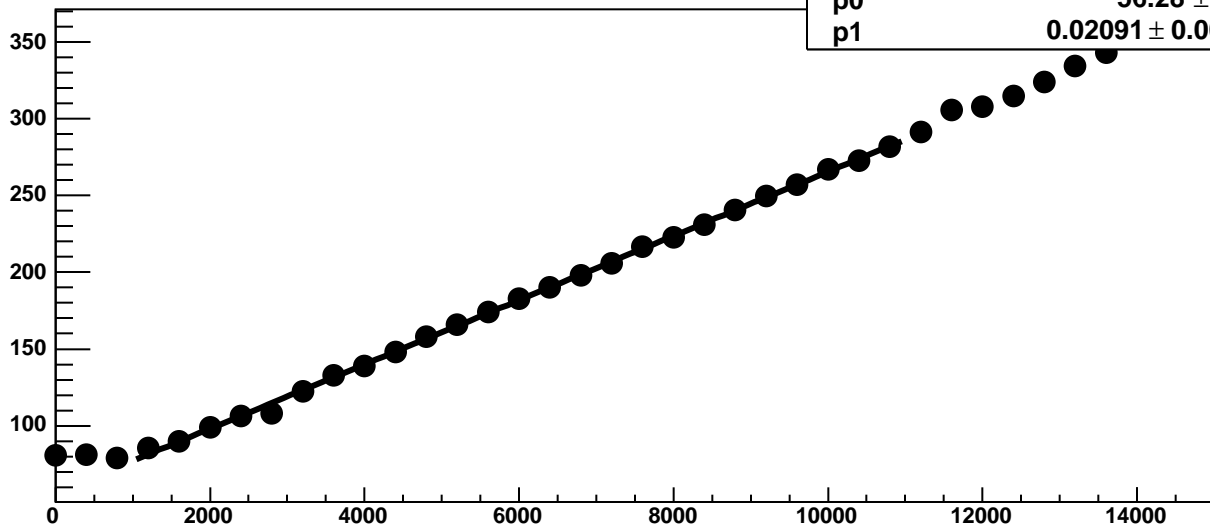
Chip 0, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

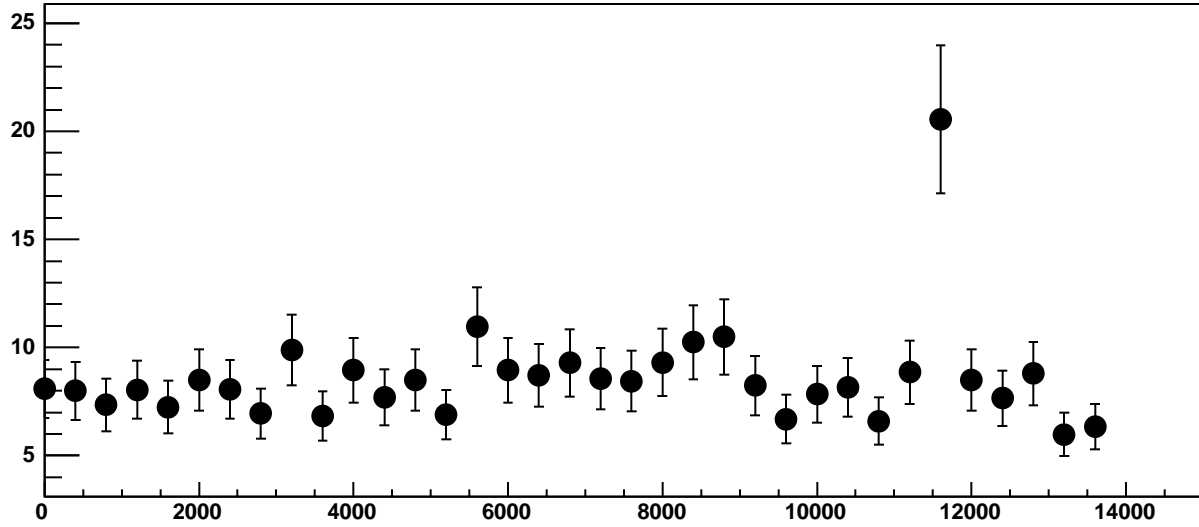


Chip 0, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

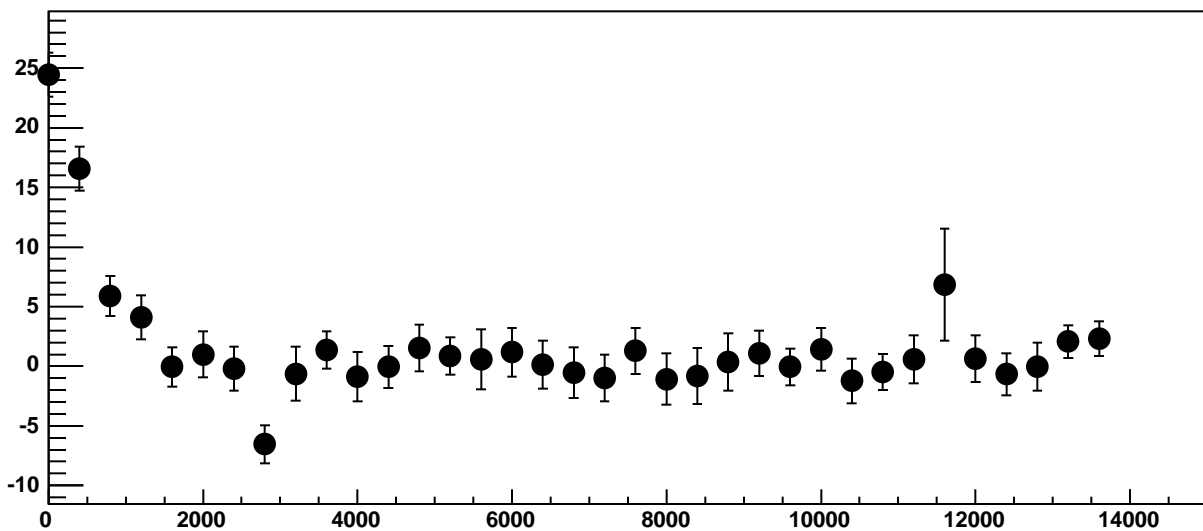


χ^2 / ndf 26.93 / 23
p0 56.28 ± 0.8279
p1 0.02091 ± 0.0001236

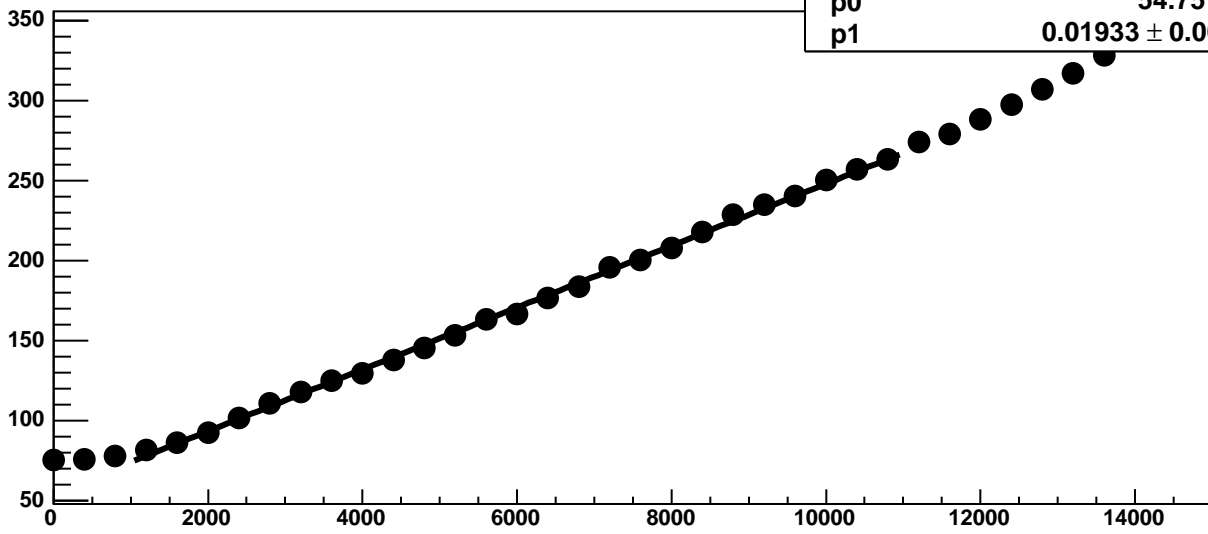
Chip 0, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

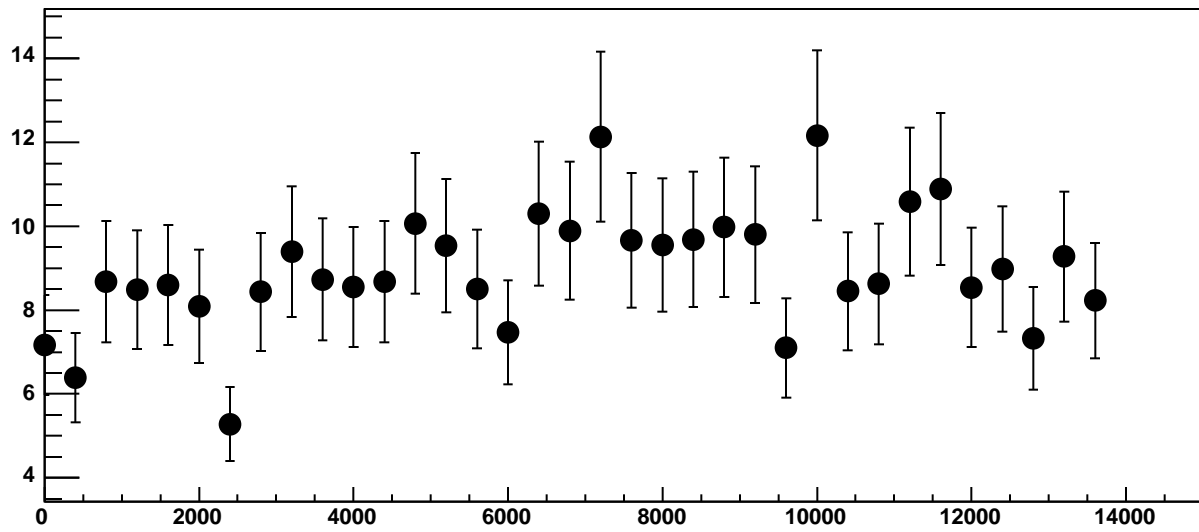


Chip 0, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

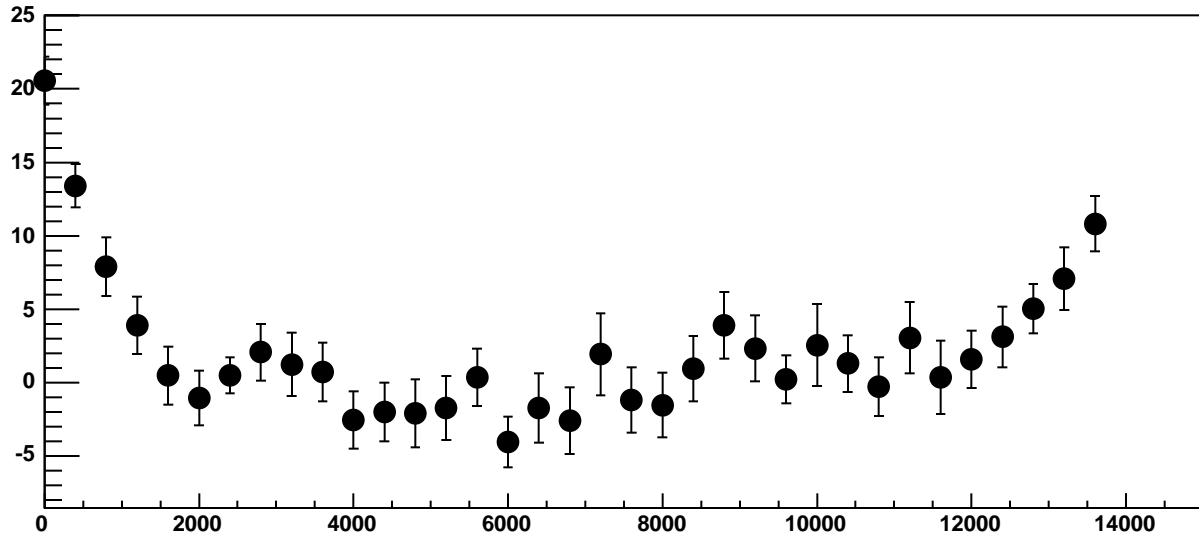


χ^2 / ndf 24.44 / 23
p0 54.75 ± 0.85
p1 0.01933 ± 0.0001337

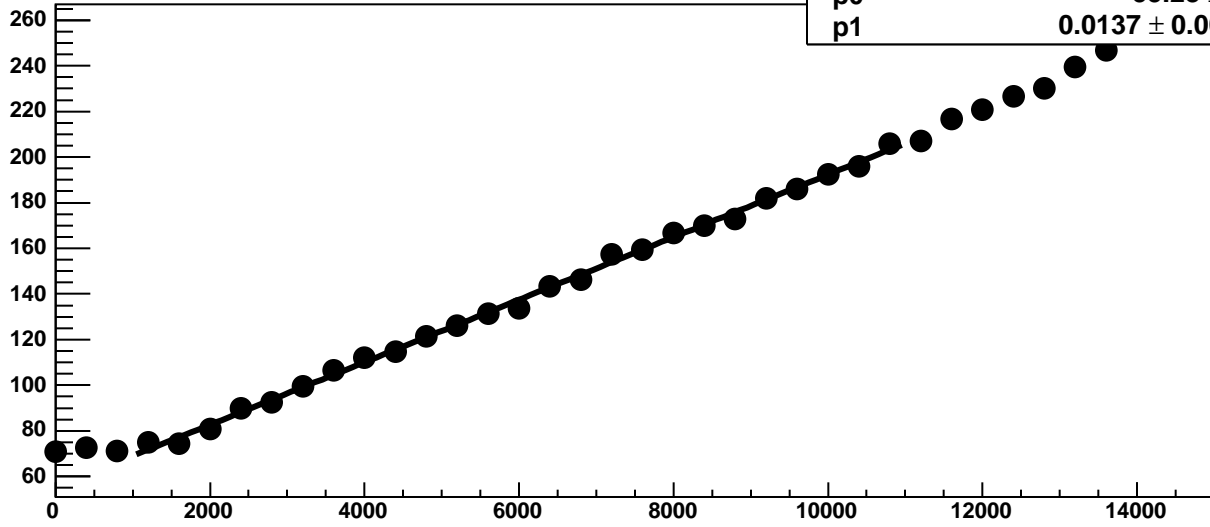
Chip 0, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

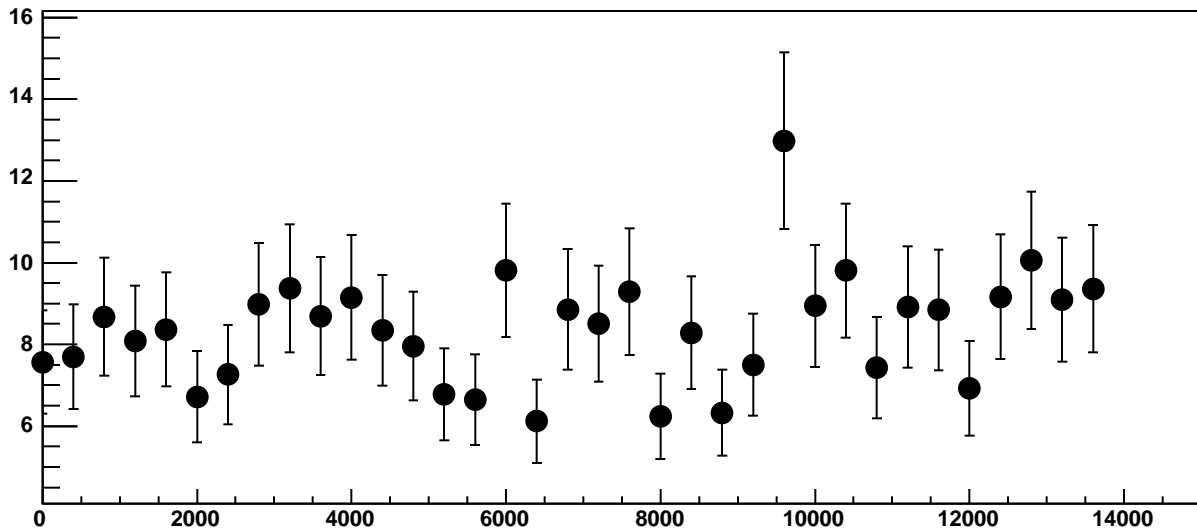


Chip 0, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

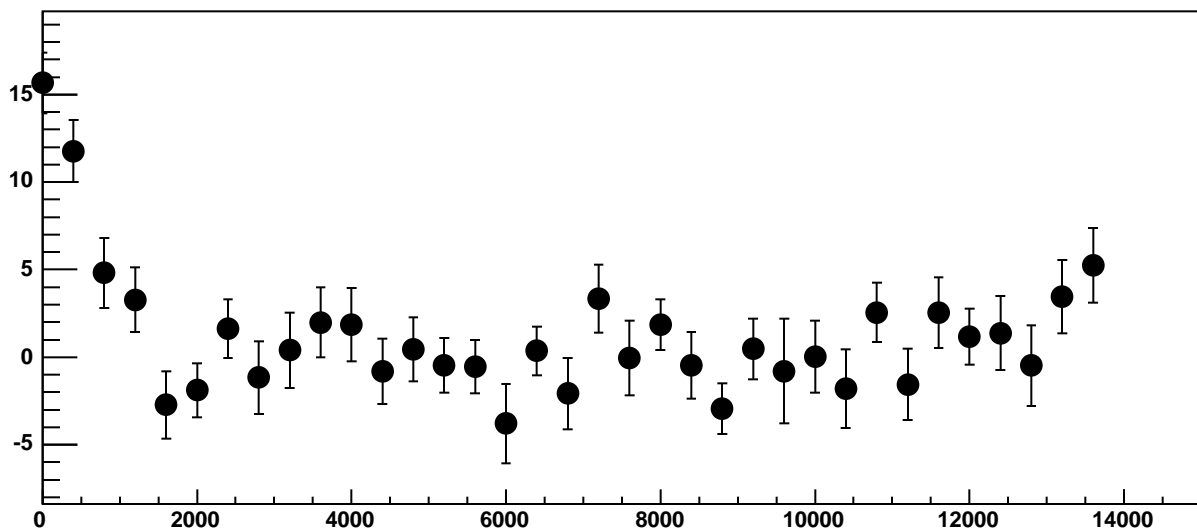


χ^2 / ndf 25.97 / 23
p0 55.28 ± 0.849
p1 0.0137 ± 0.0001288

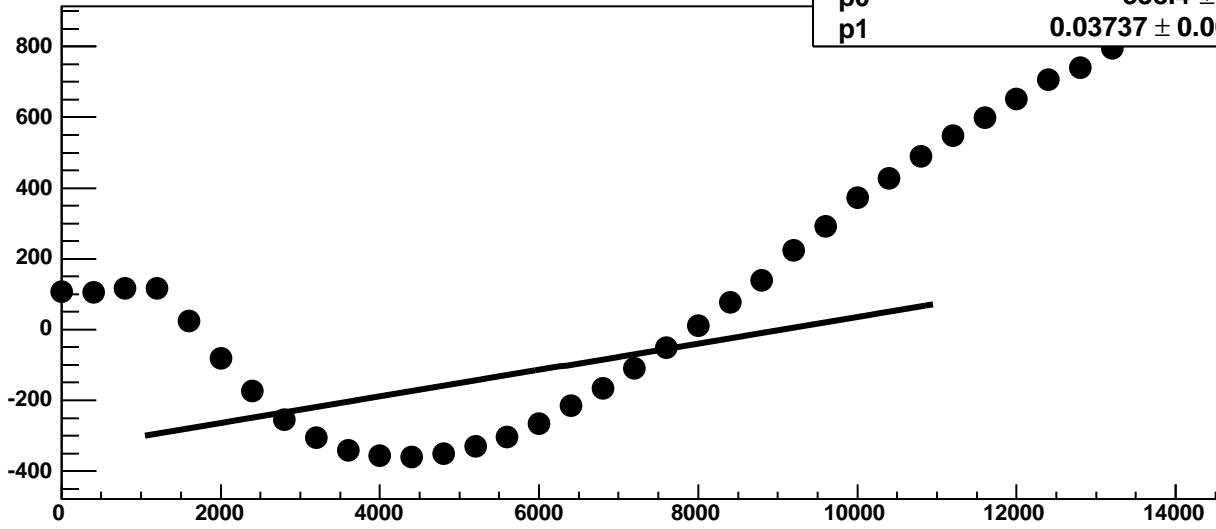
Chip 0, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

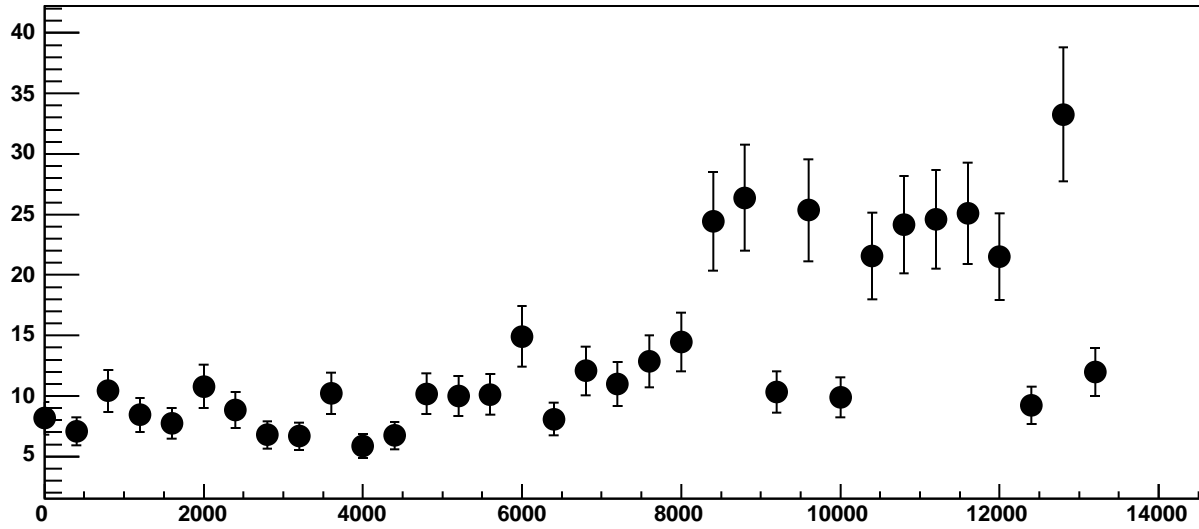


Chip 0, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

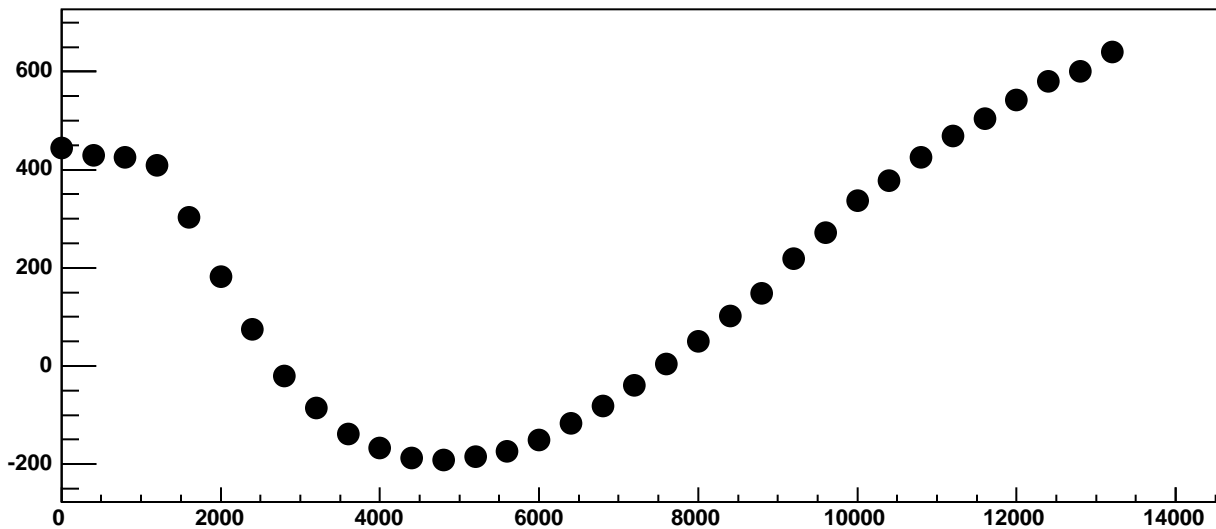


χ^2 / ndf 1.889e+05 / 23
p0 -338.4 ± 0.9654
p1 0.03737 ± 0.0001836

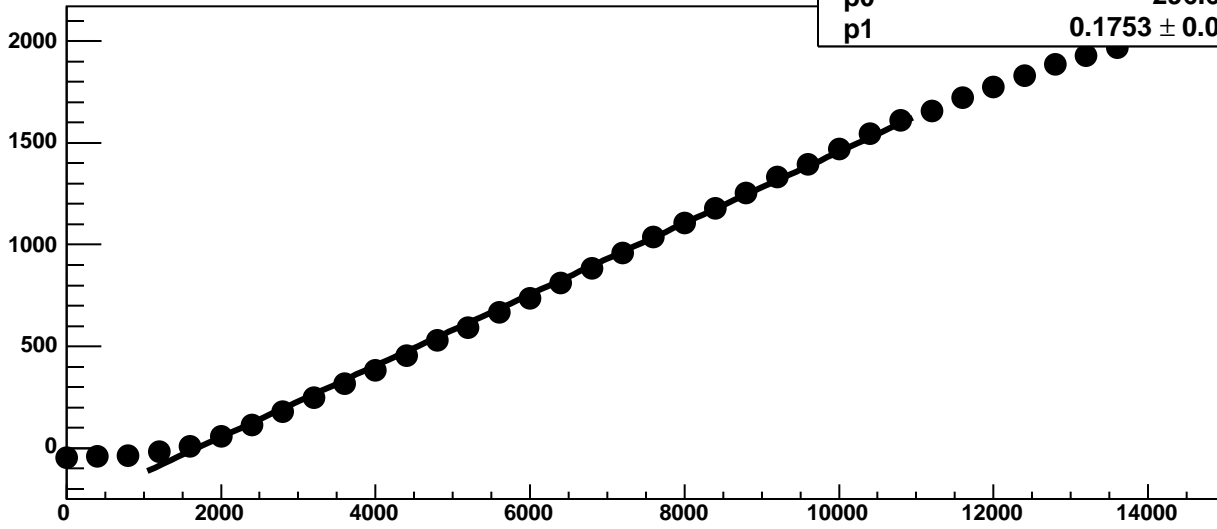
Chip 0, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

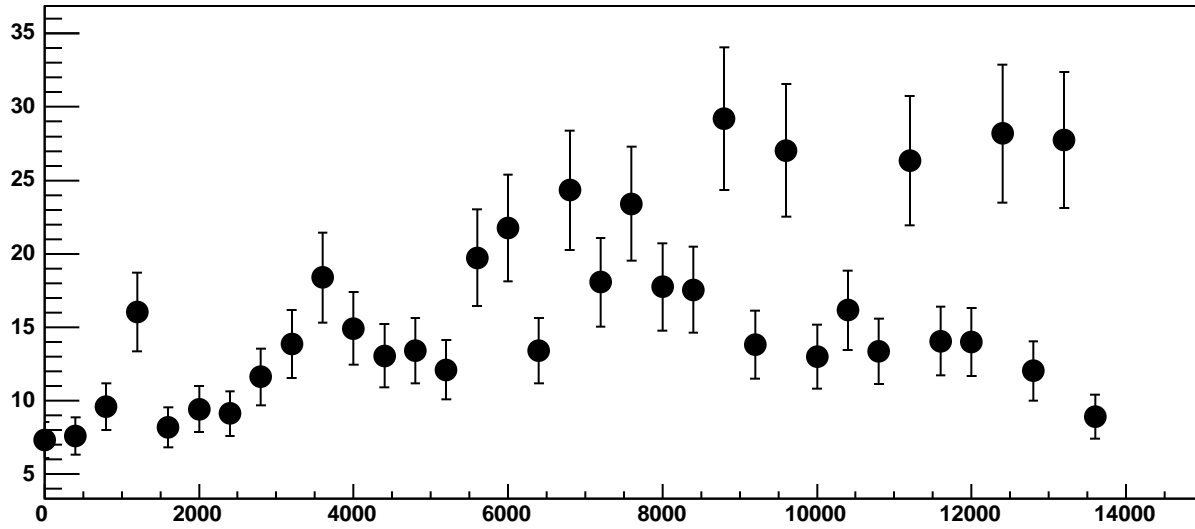


Chip 0, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC

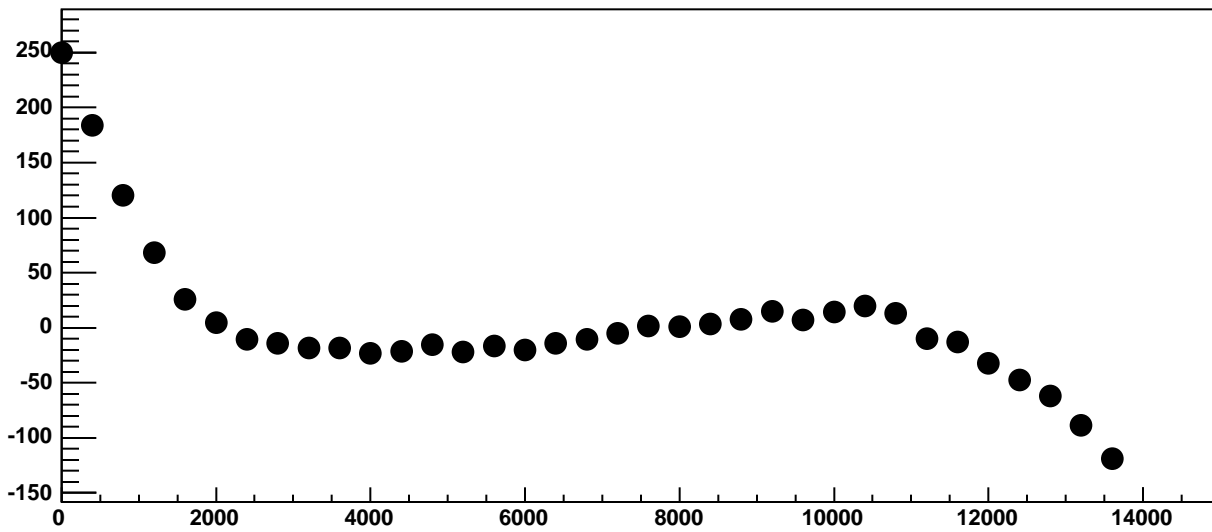


χ^2 / ndf 978.2 / 23
p0 -296.6 ± 1.23
p1 0.1753 ± 0.0002116

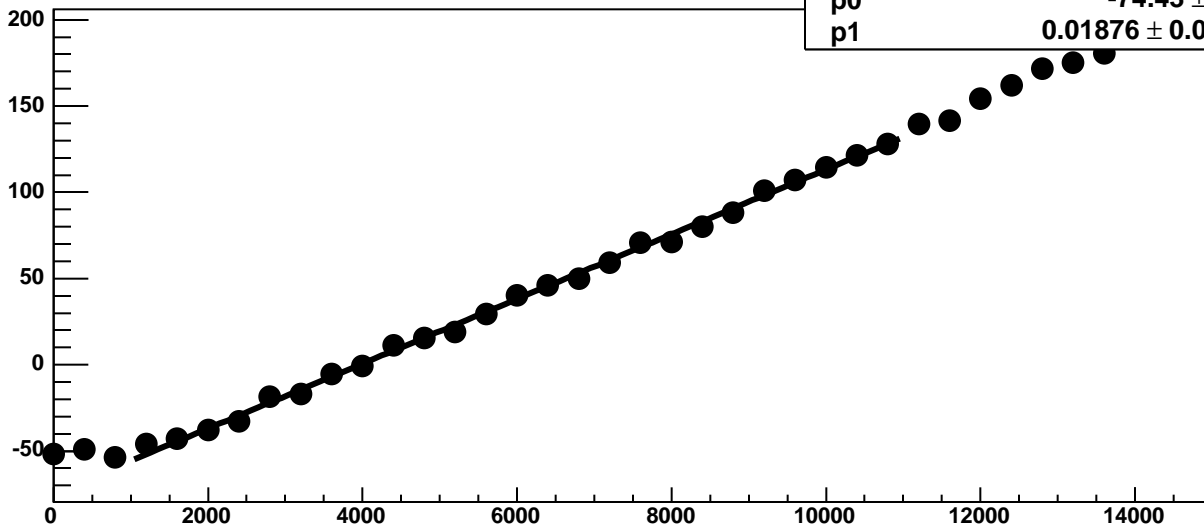
Chip 0, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



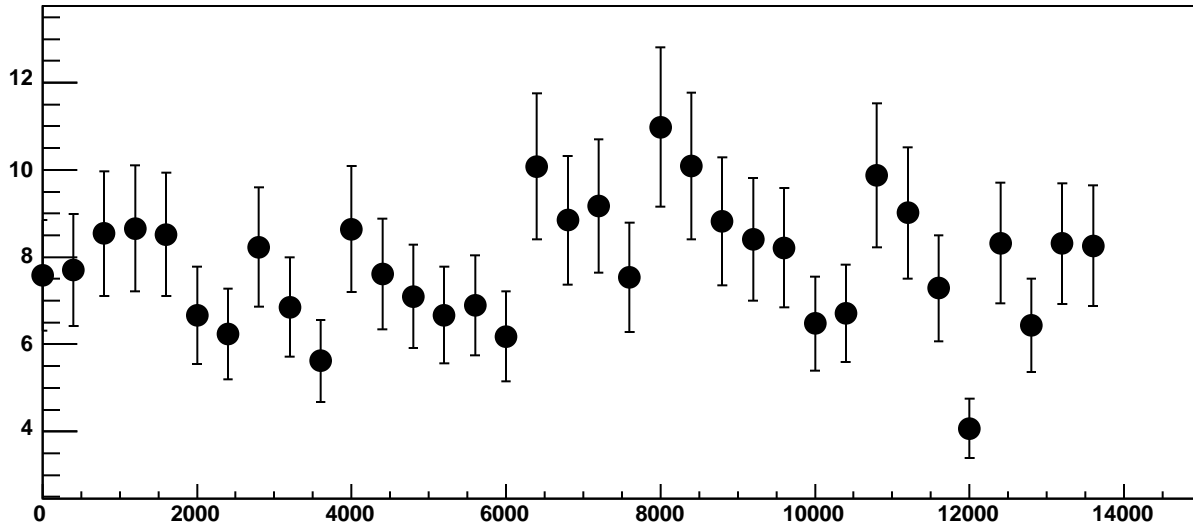
Chip 0, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC



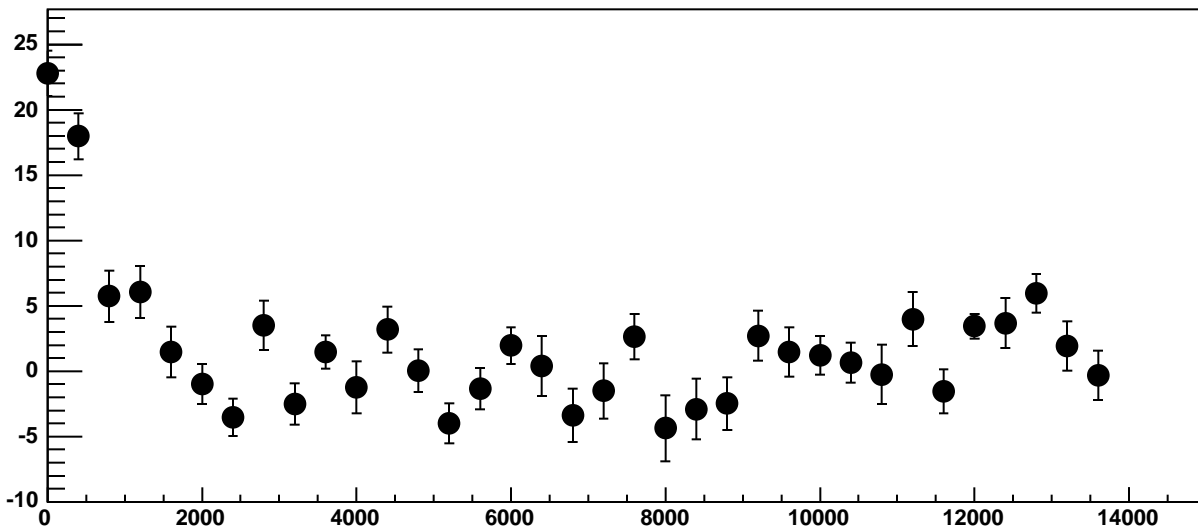
Chip 0, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



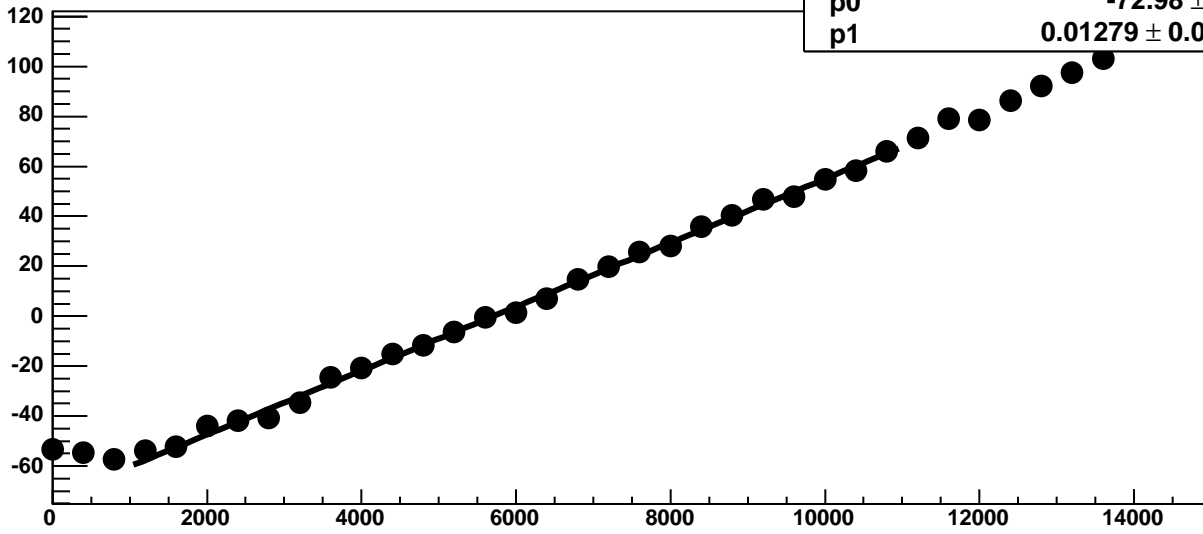
Chip 0, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



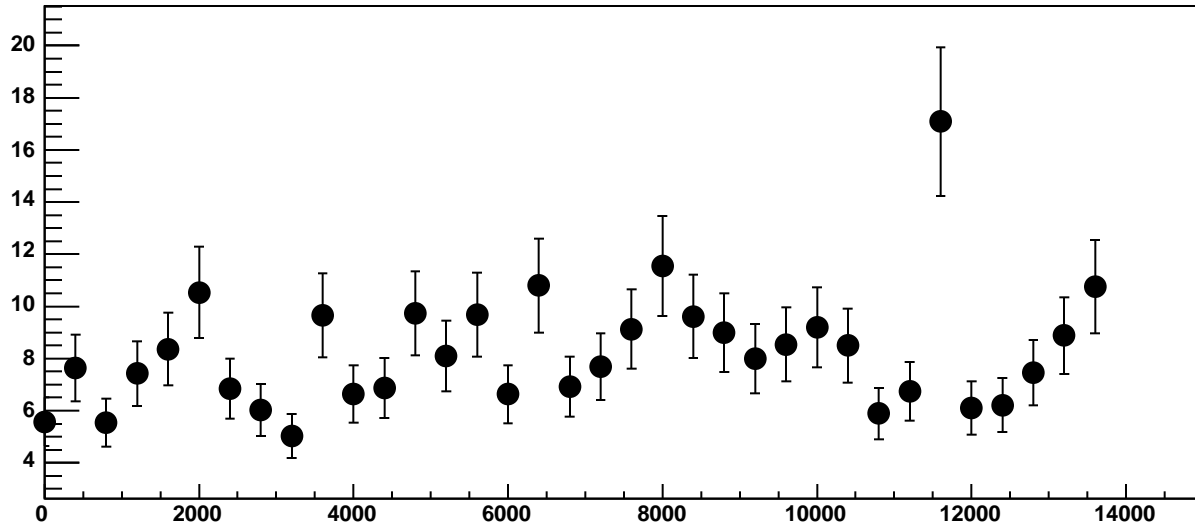
Chip 0, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



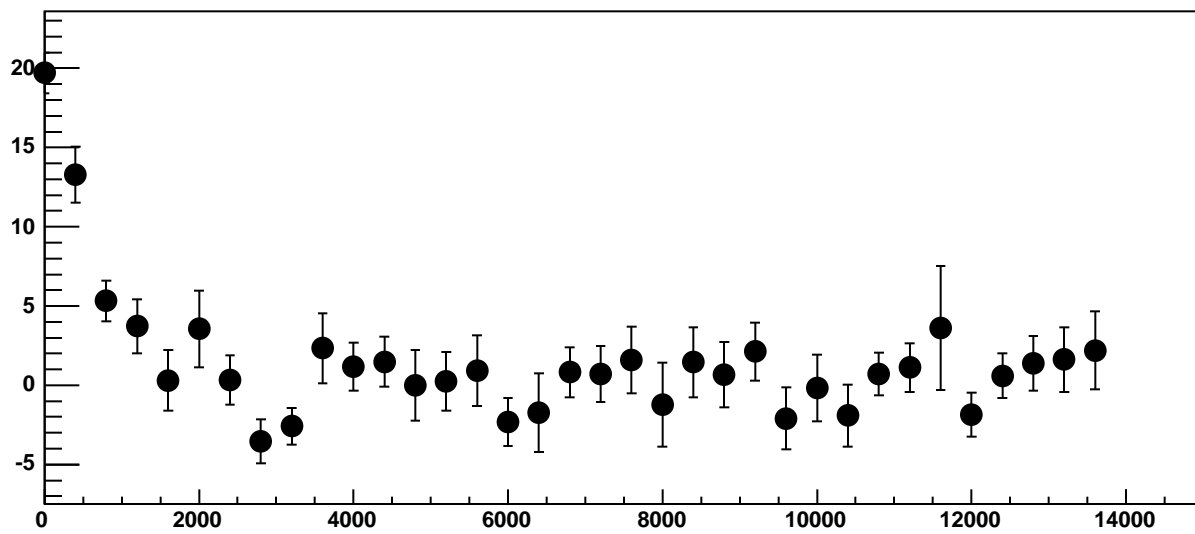
Chip 0, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



Chip 0, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

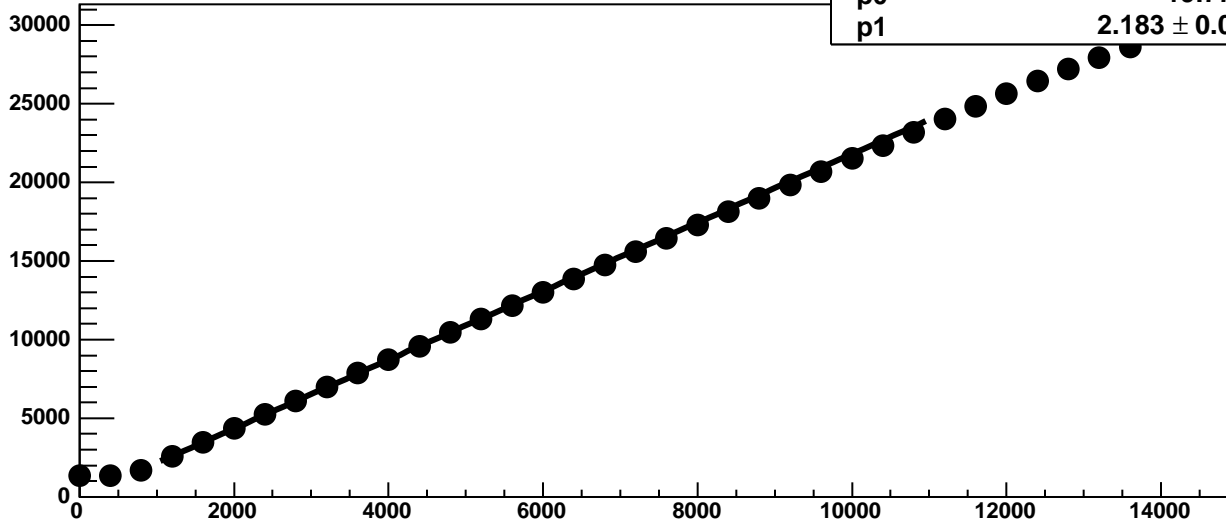


Chip 0, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

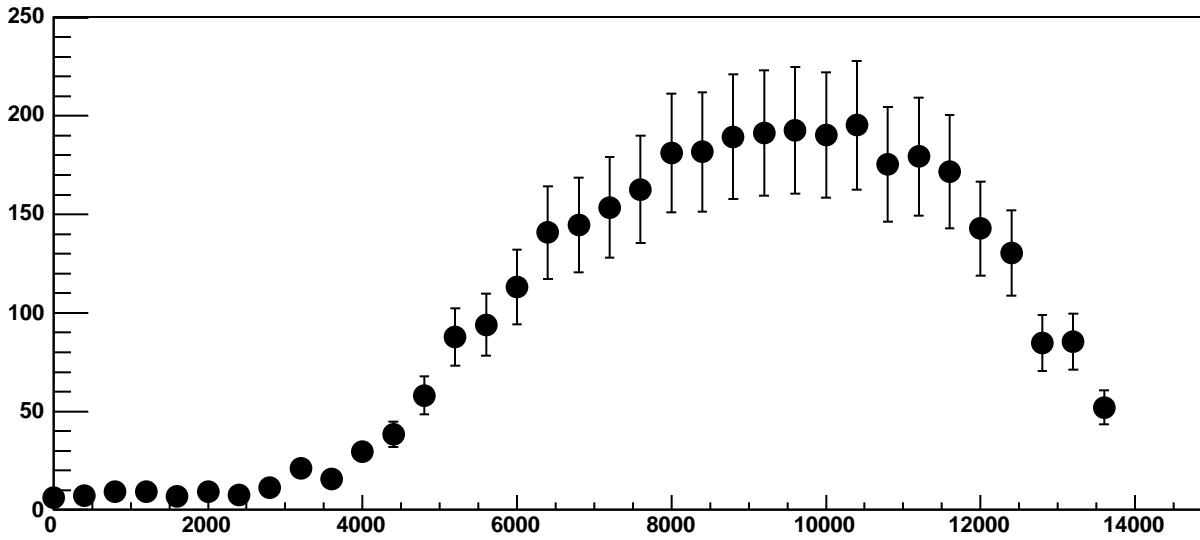


Chip 0, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC

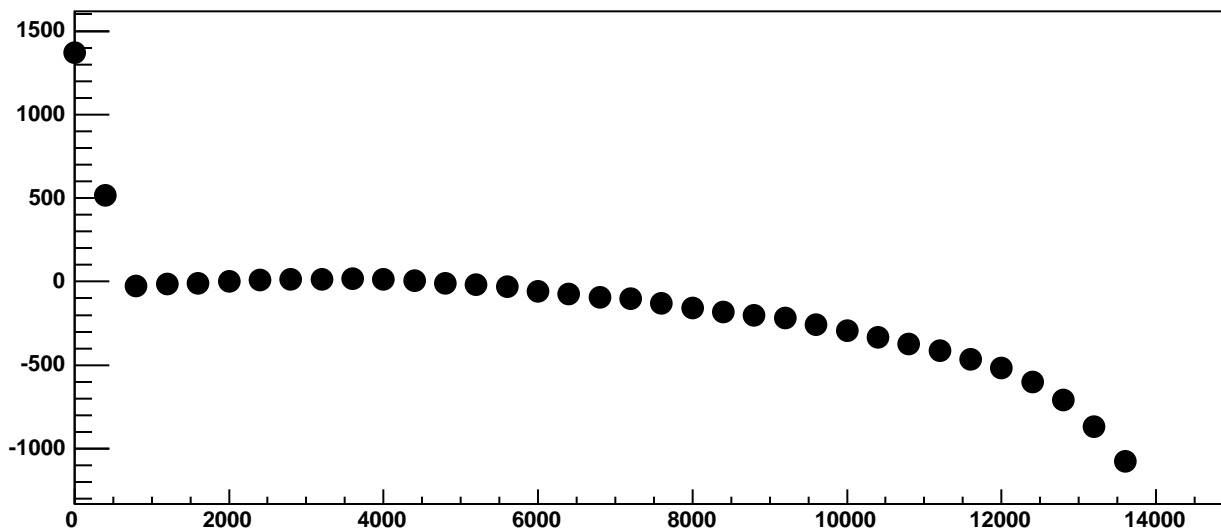
χ^2 / ndf 533.5 / 23
p0 -19.77 ± 2.17
p1 2.183 ± 0.0009249



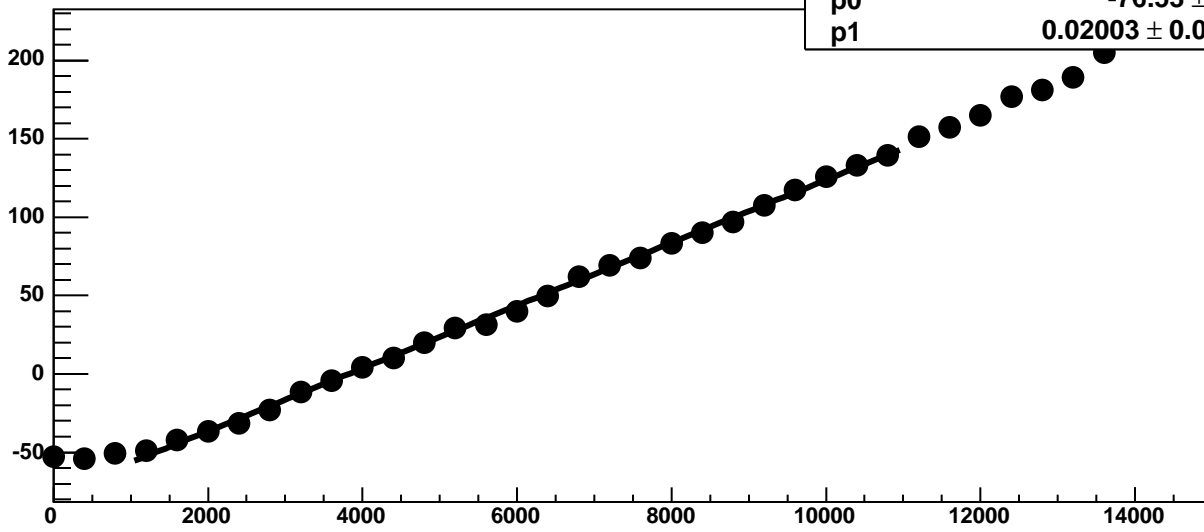
Chip 0, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC

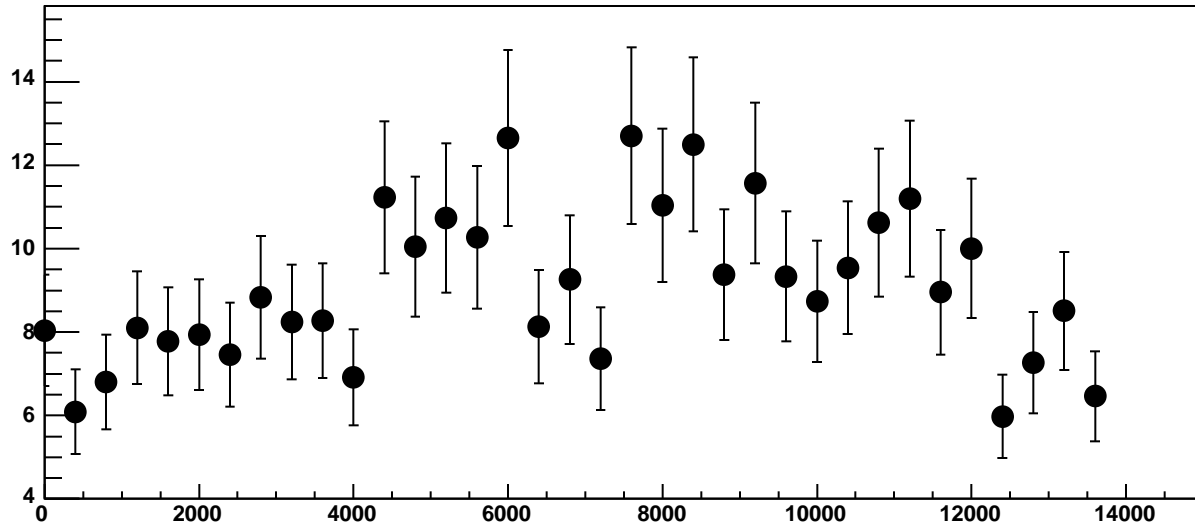


Chip 0, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

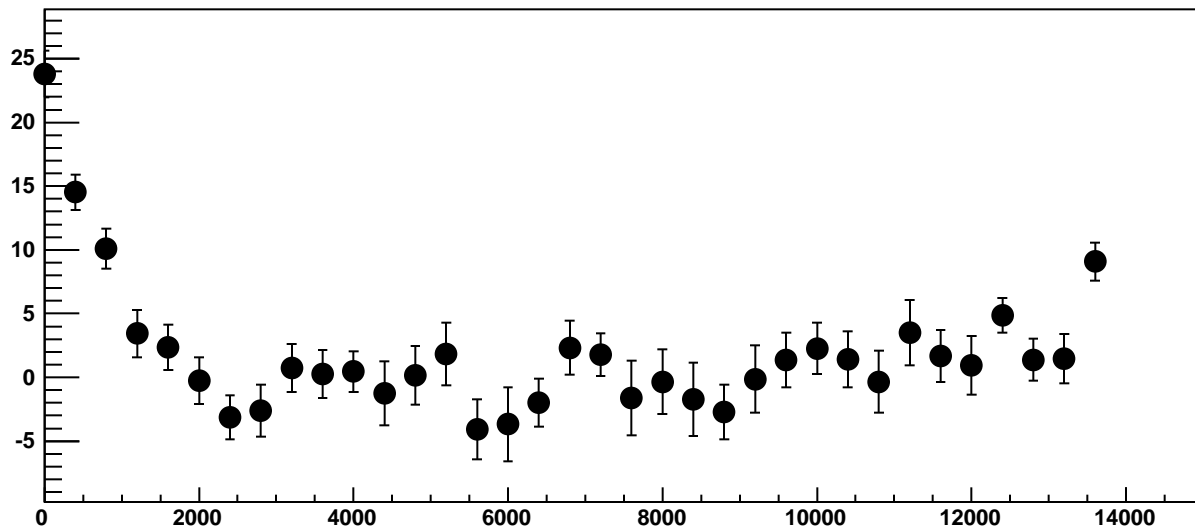


χ^2 / ndf 23.67 / 23
p0 -76.53 ± 0.8872
p1 0.02003 ± 0.0001422

Chip 0, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

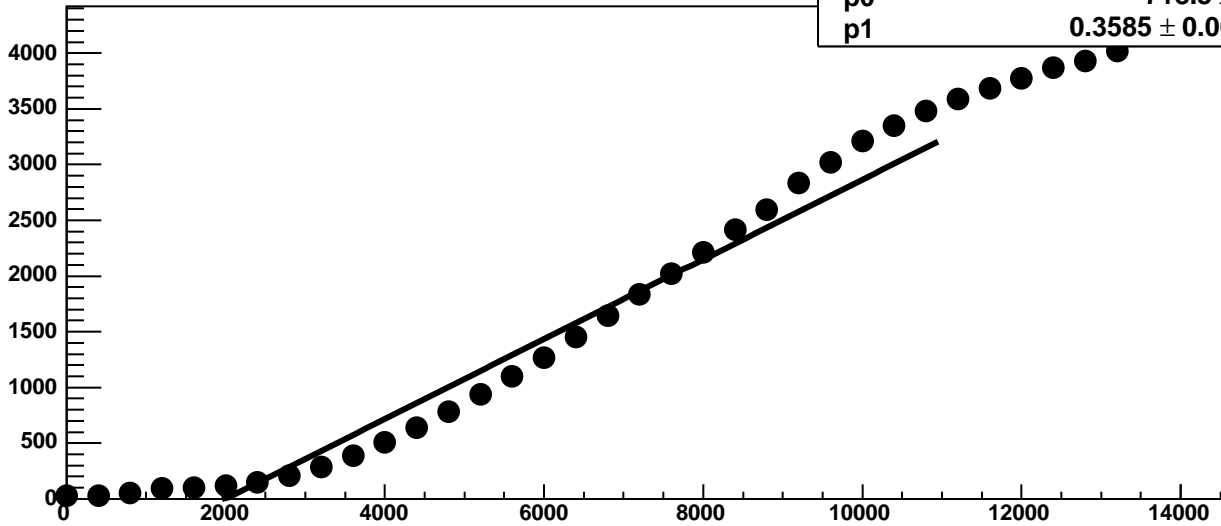


Chip 0, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

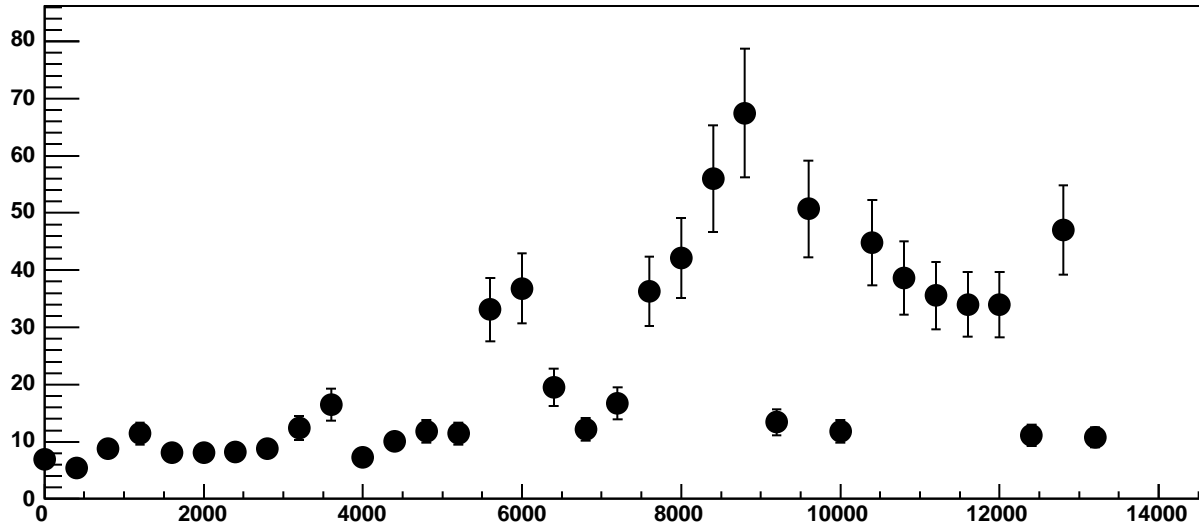


Chip 0, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

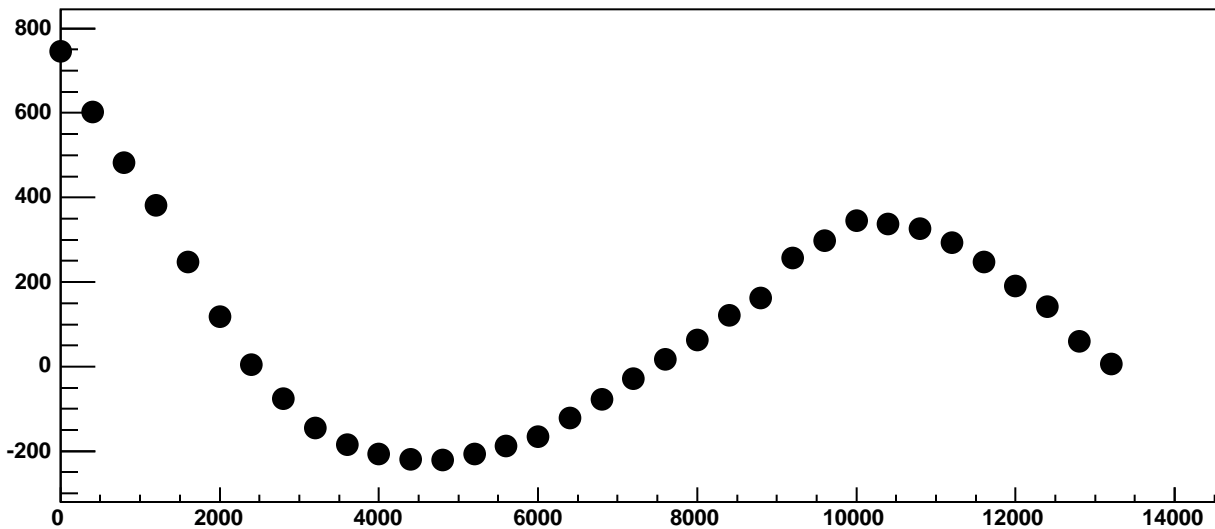
χ^2 / ndf 1.158e+05 / 23
p0 -718.3 ± 1.152
p1 0.3585 ± 0.0002423



Chip 0, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

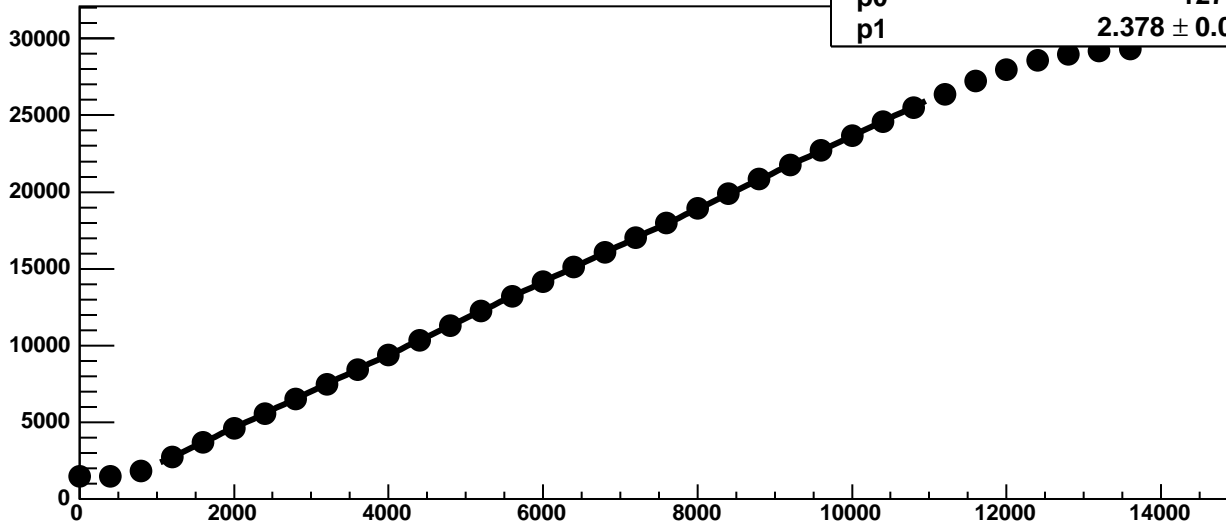


Chip 0, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

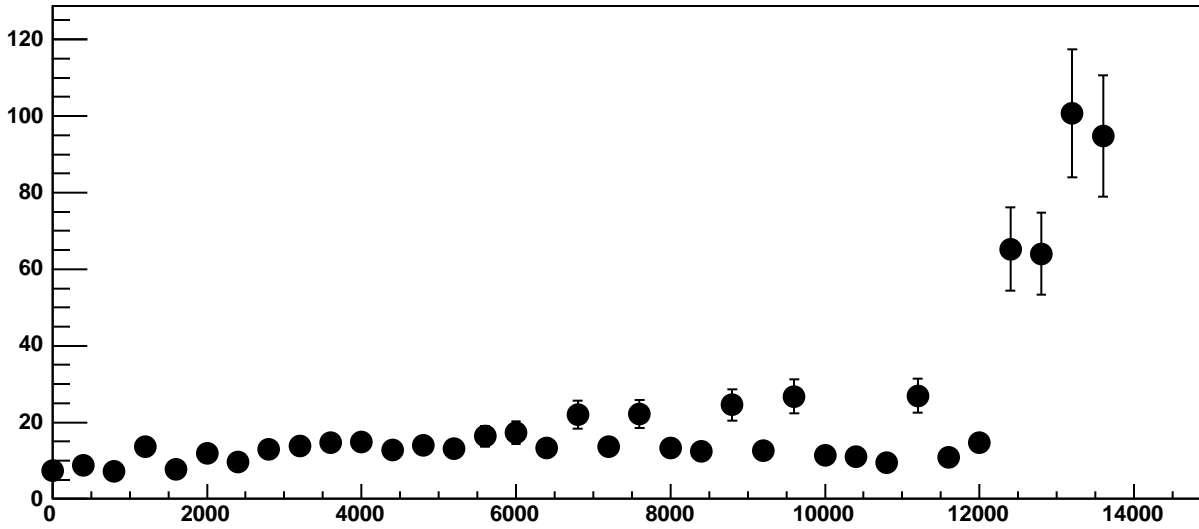


Chip 0, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC

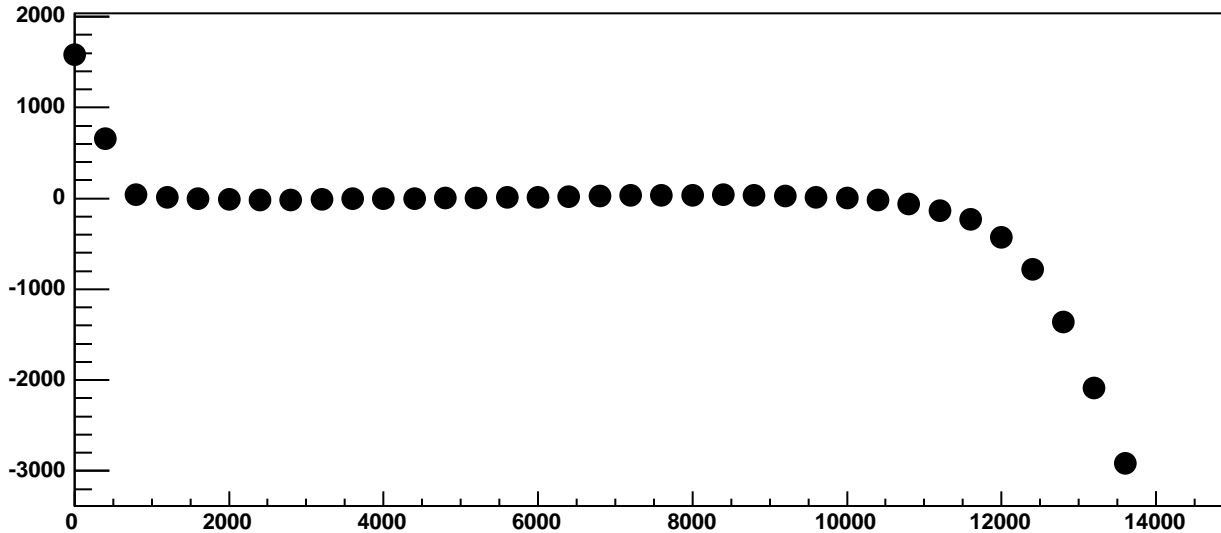
χ^2 / ndf 1698 / 23
p0 -127 ± 1.194
p1 2.378 ± 0.0001832



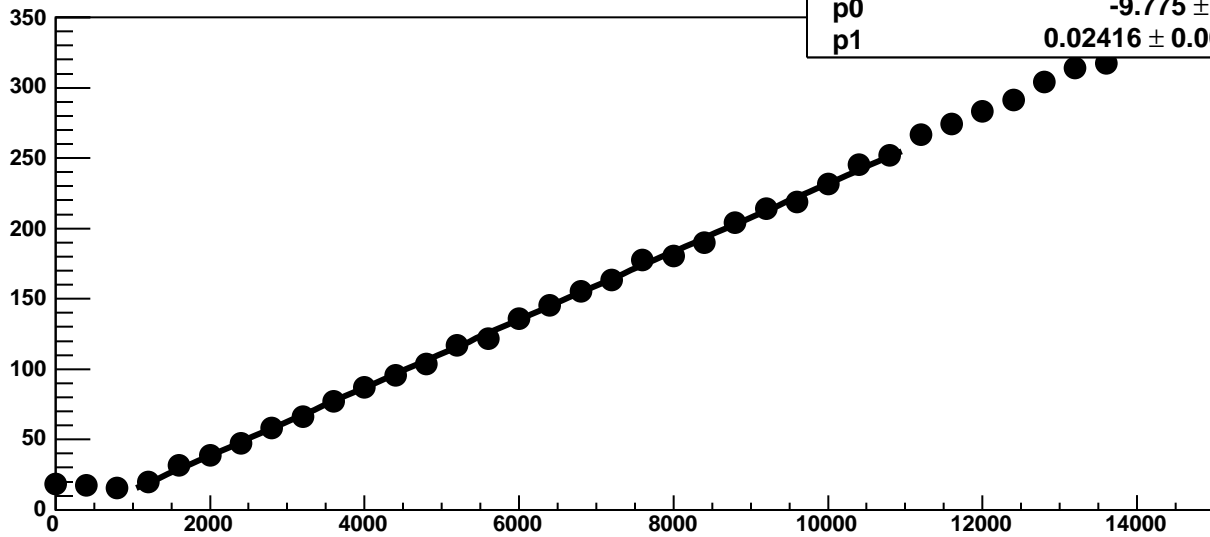
Chip 0, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC

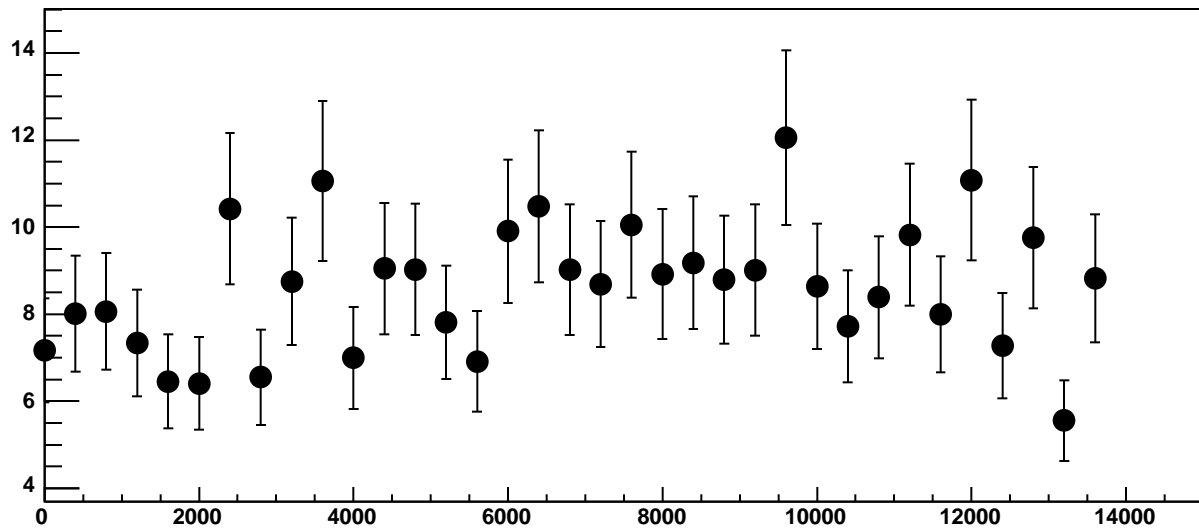


Chip 0, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

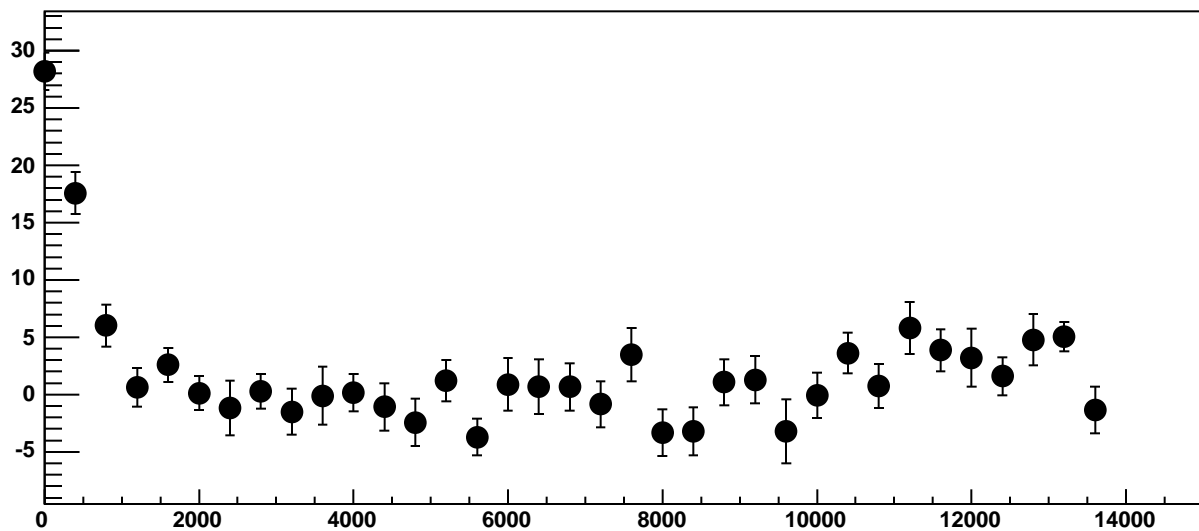


χ^2 / ndf 25.73 / 23
p0 -9.775 ± 0.8077
p1 0.02416 ± 0.0001279

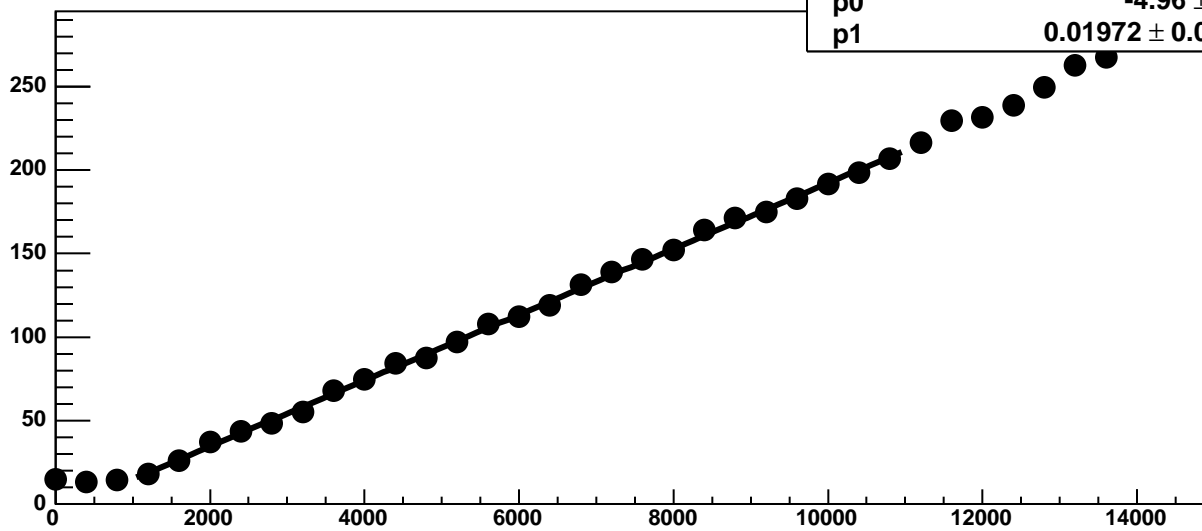
Chip 0, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



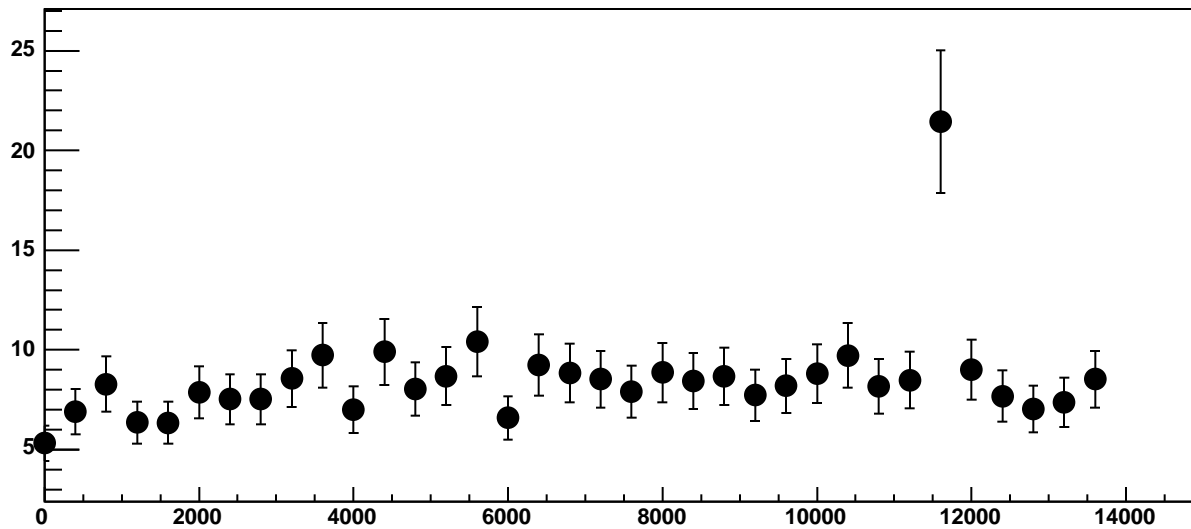
Chip 0, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



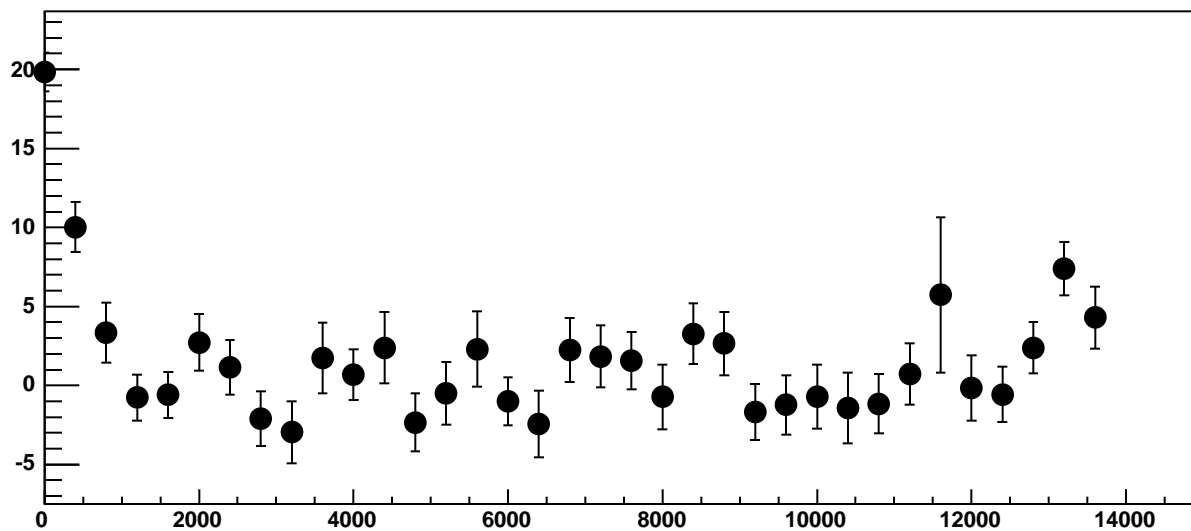
Chip 0, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



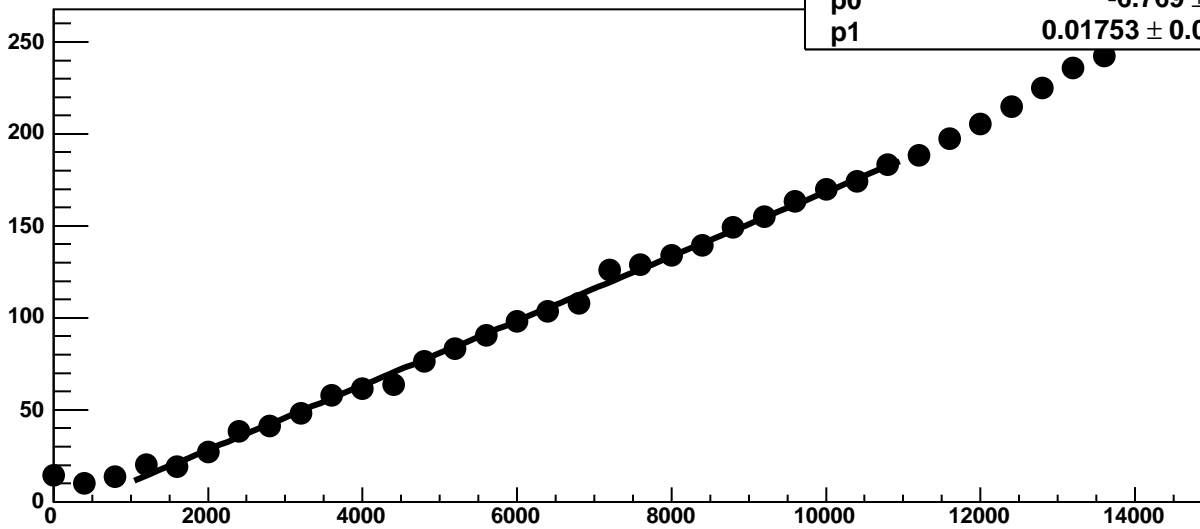
Chip 0, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

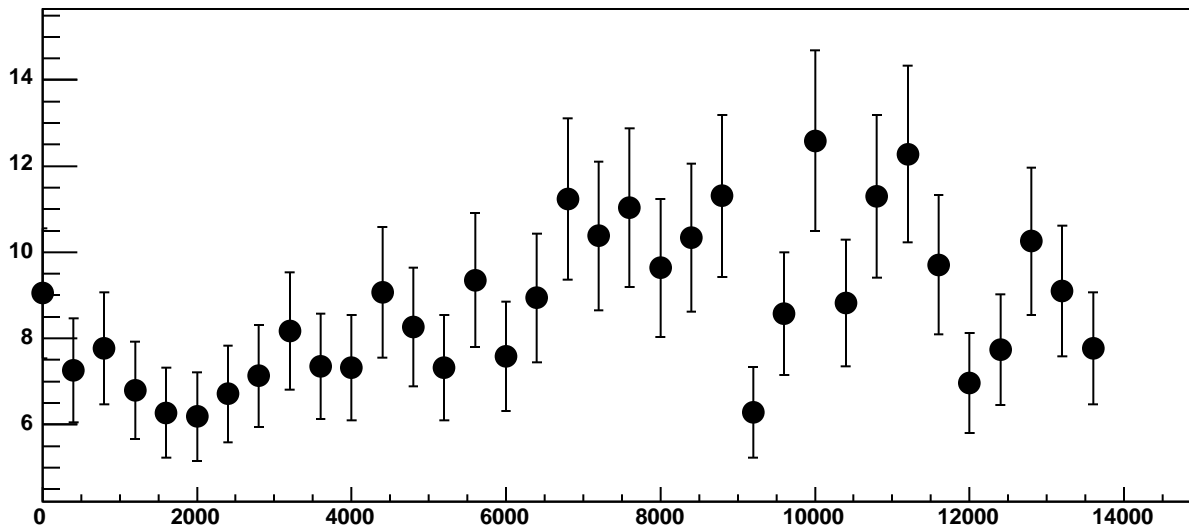


Chip 0, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

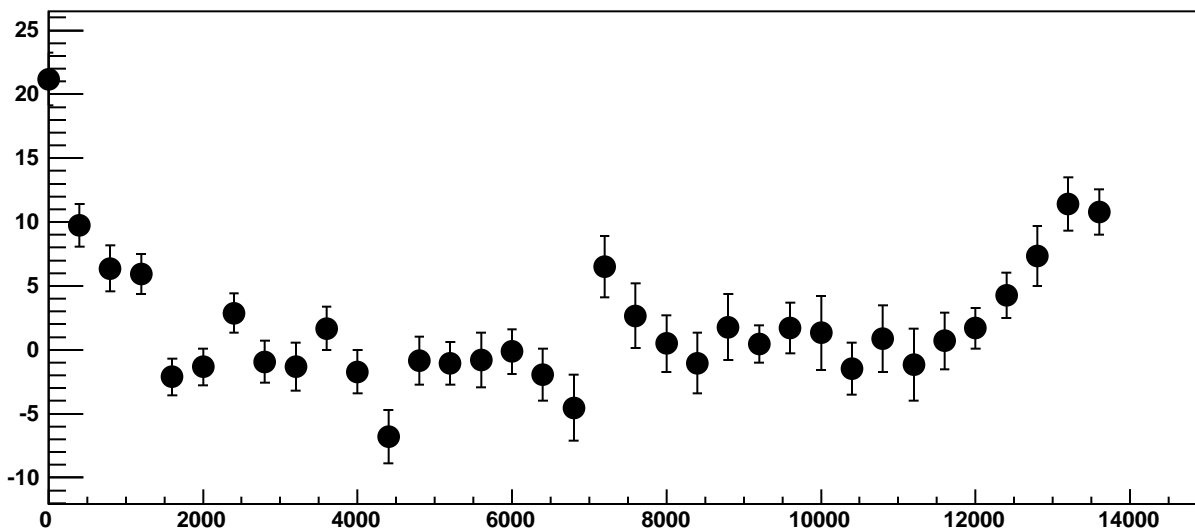


χ^2 / ndf 50.24 / 23
p0 -6.769 ± 0.7673
p1 0.01753 ± 0.0001283

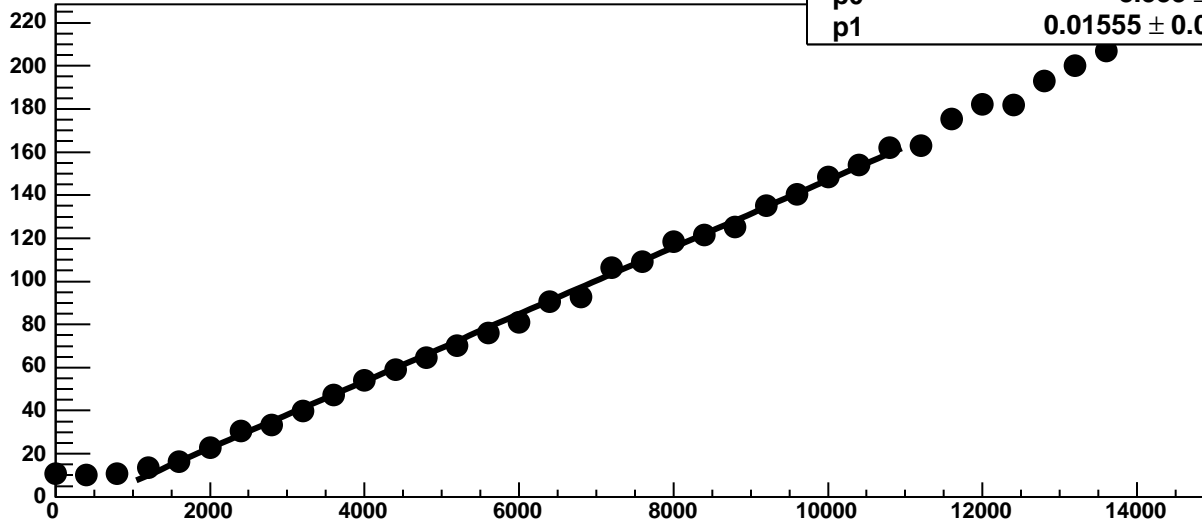
Chip 0, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

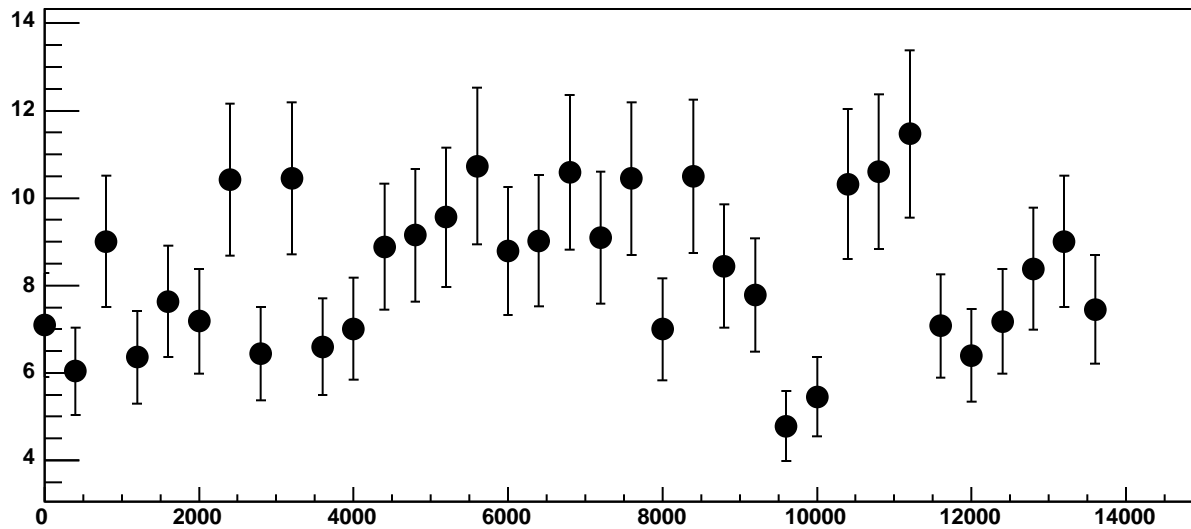


Chip 0, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

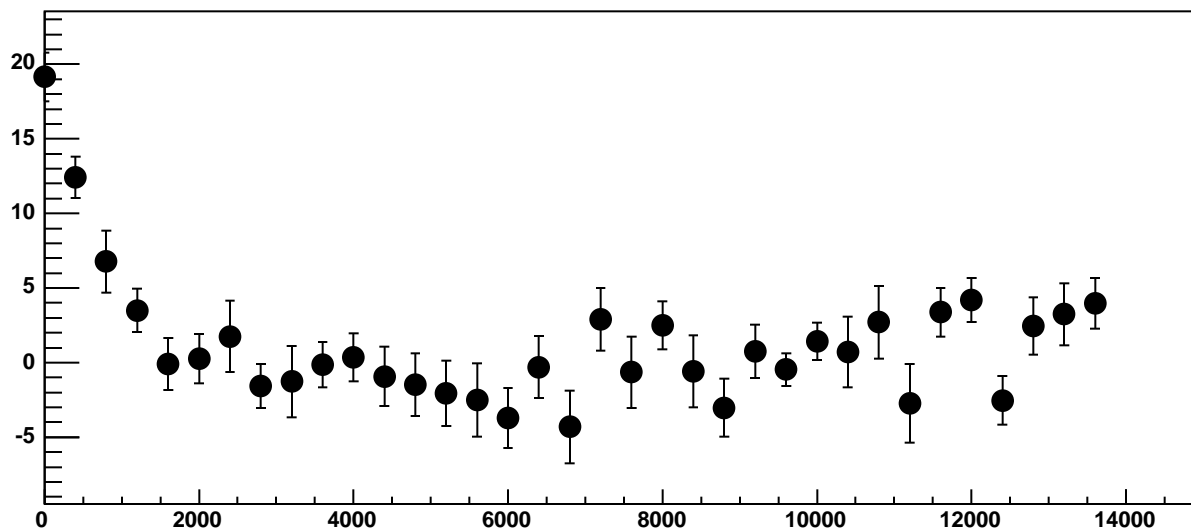


χ^2 / ndf 26.9 / 23
p0 -8.533 ± 0.7924
p1 0.01555 ± 0.0001165

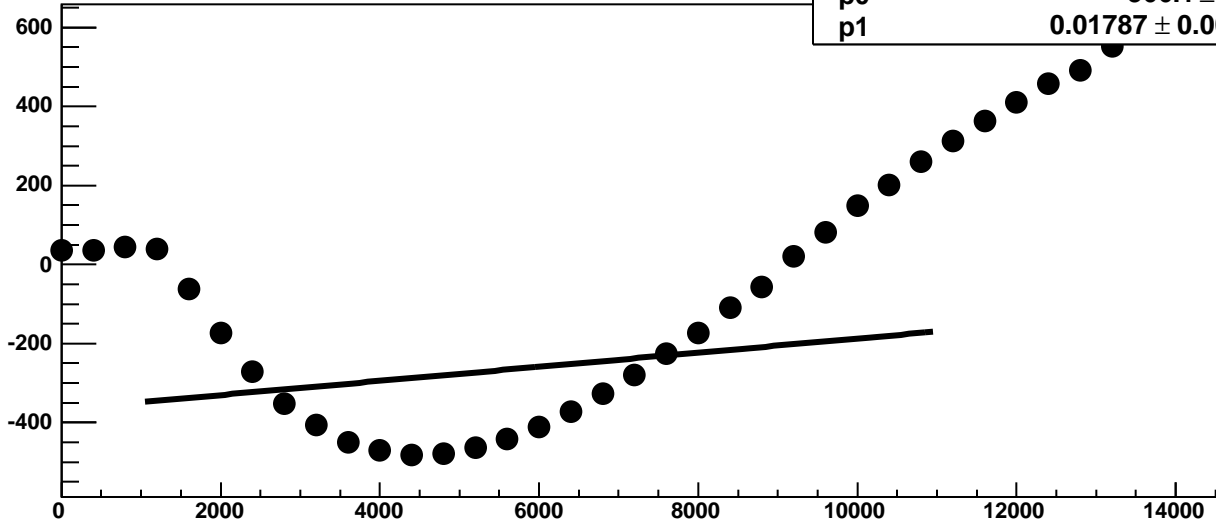
Chip 0, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

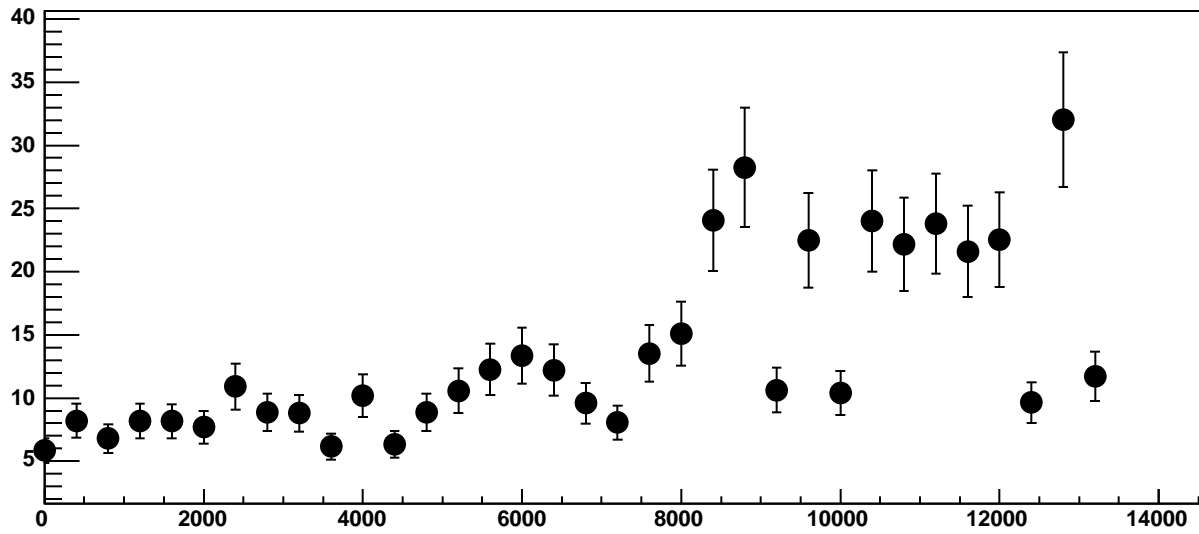


Chip 0, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

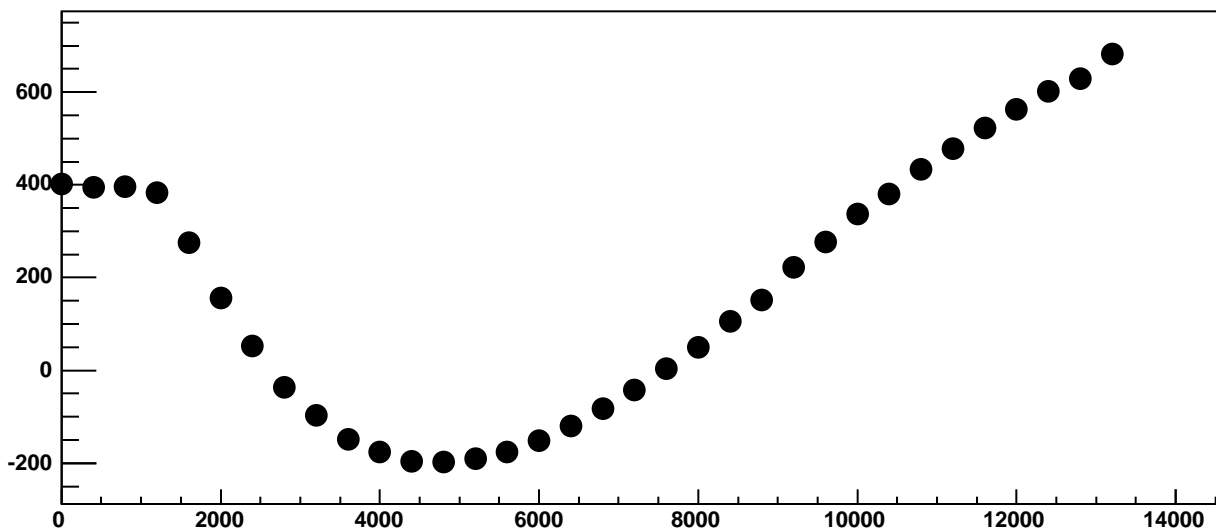


χ^2 / ndf 1.793e+05 / 23
p0 -366.1 ± 0.9857
p1 0.01787 ± 0.0001844

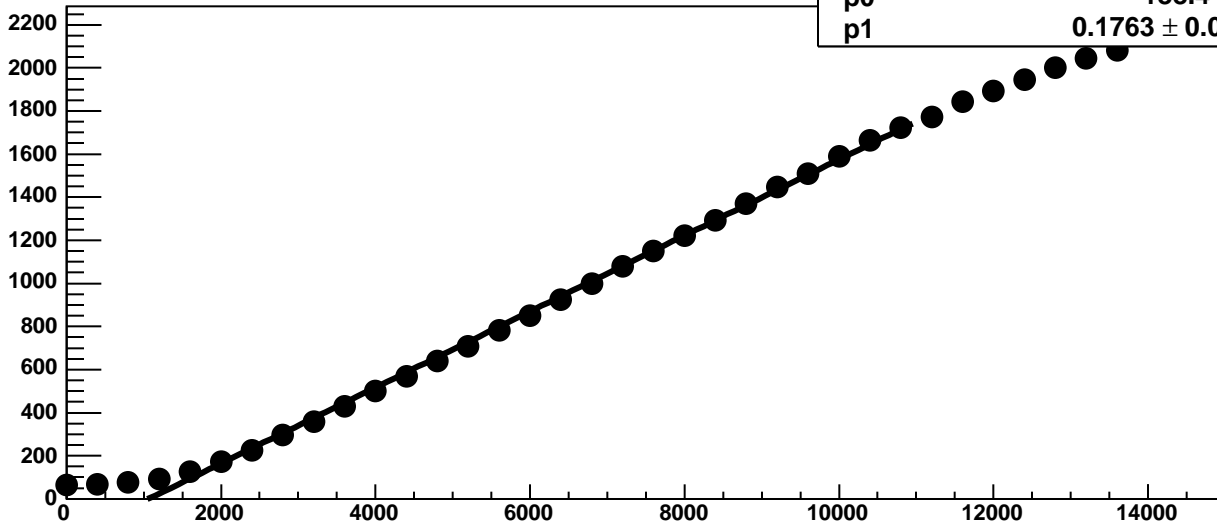
Chip 0, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

870.9 / 23

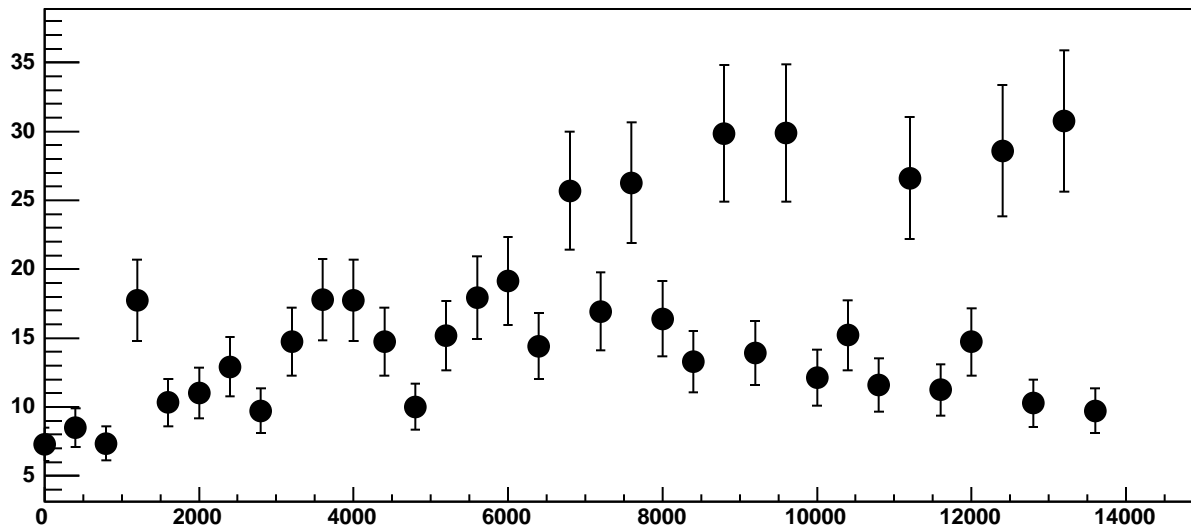
p0

-188.4 ± 1.357

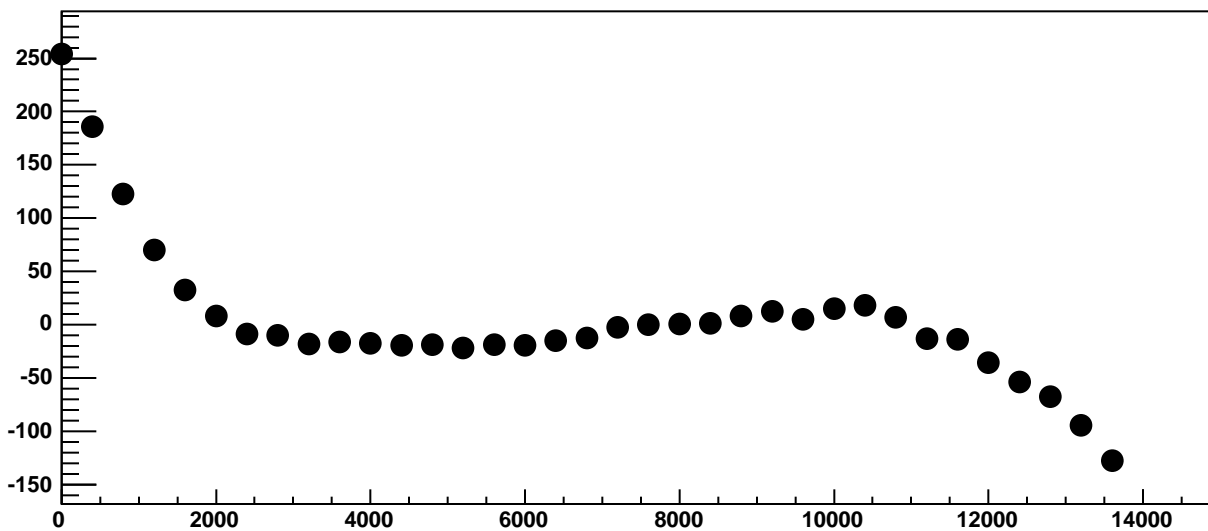
p1

0.1763 ± 0.0002151

Chip 0, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

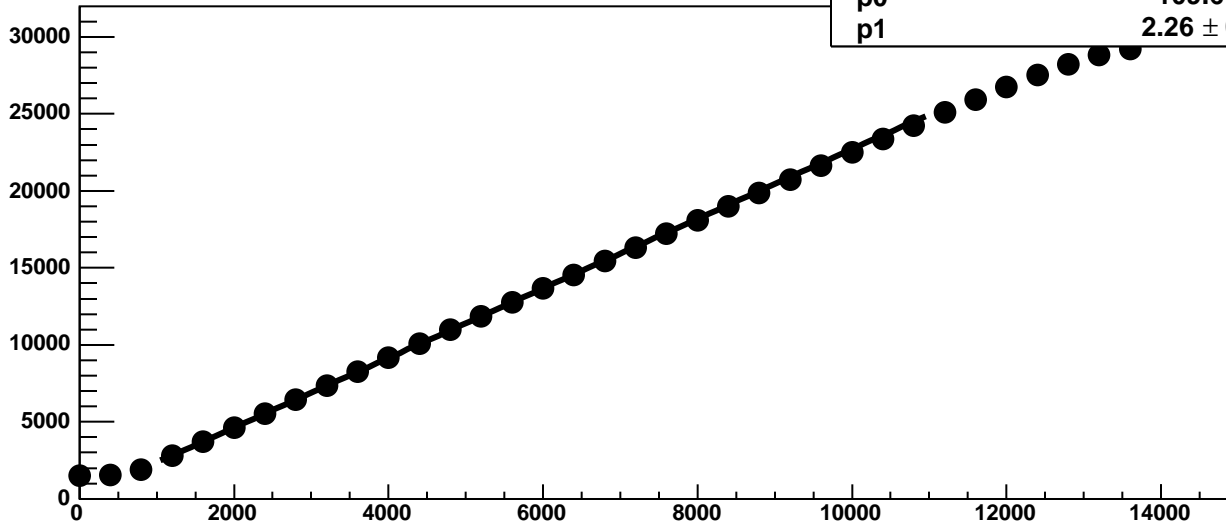


Chip 0, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

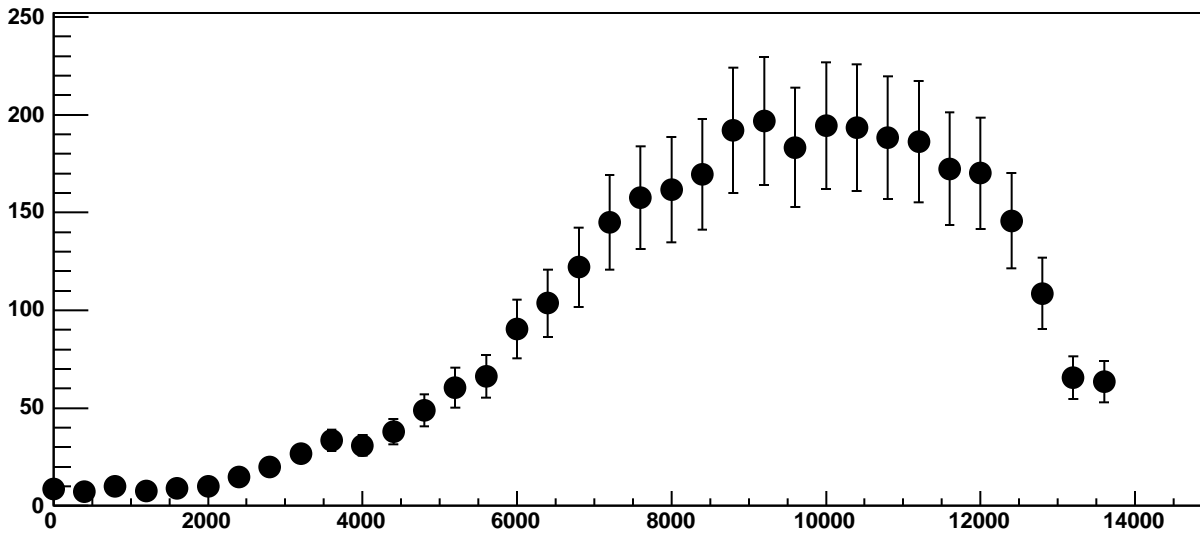


Chip 0, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC

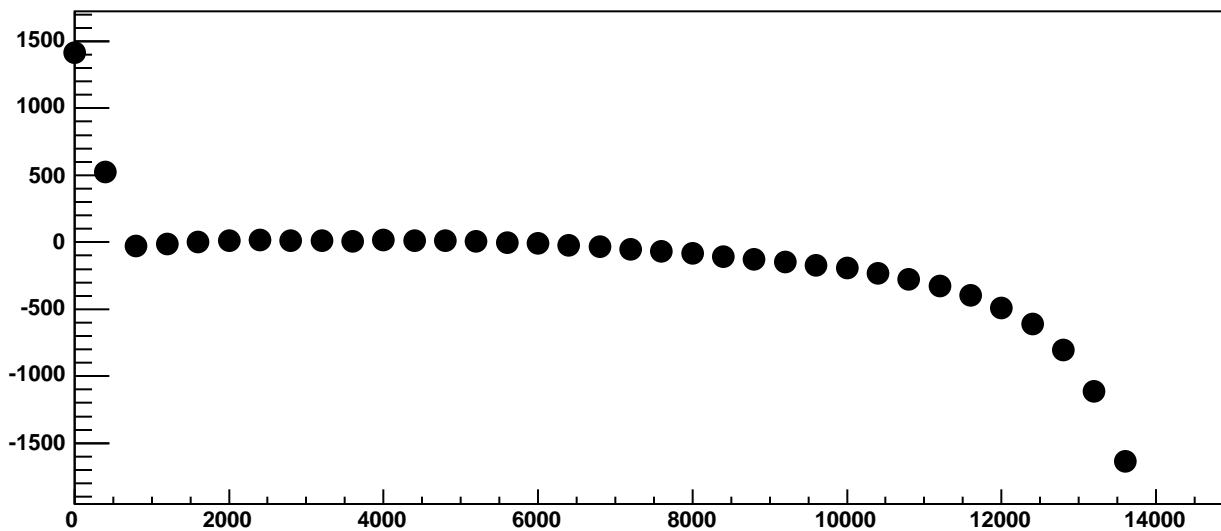
χ^2 / ndf 285 / 23
p0 109.6 ± 2.152
p1 2.26 ± 0.00096



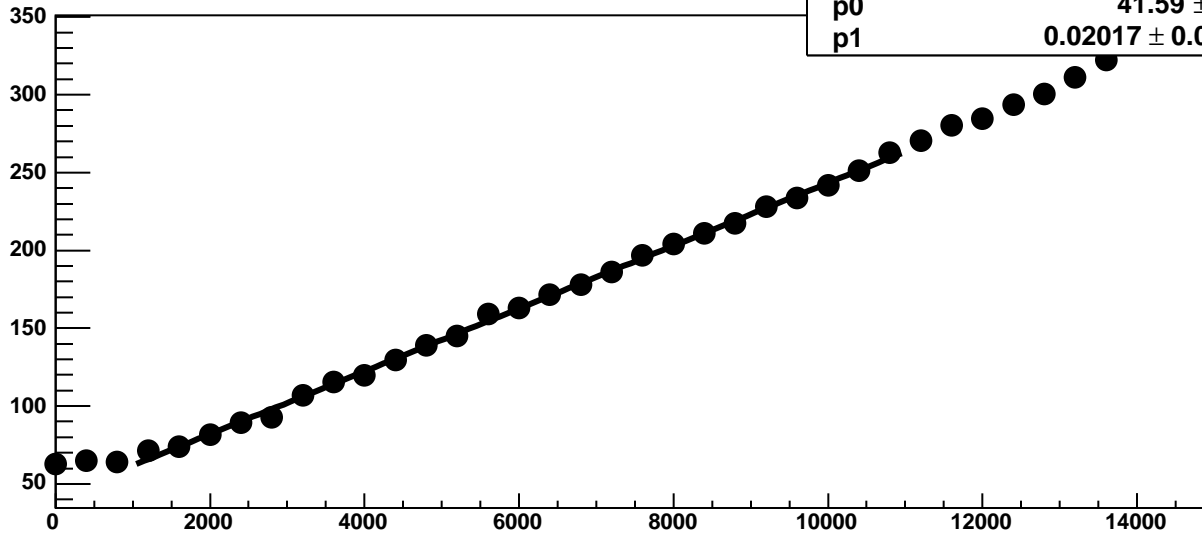
Chip 0, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



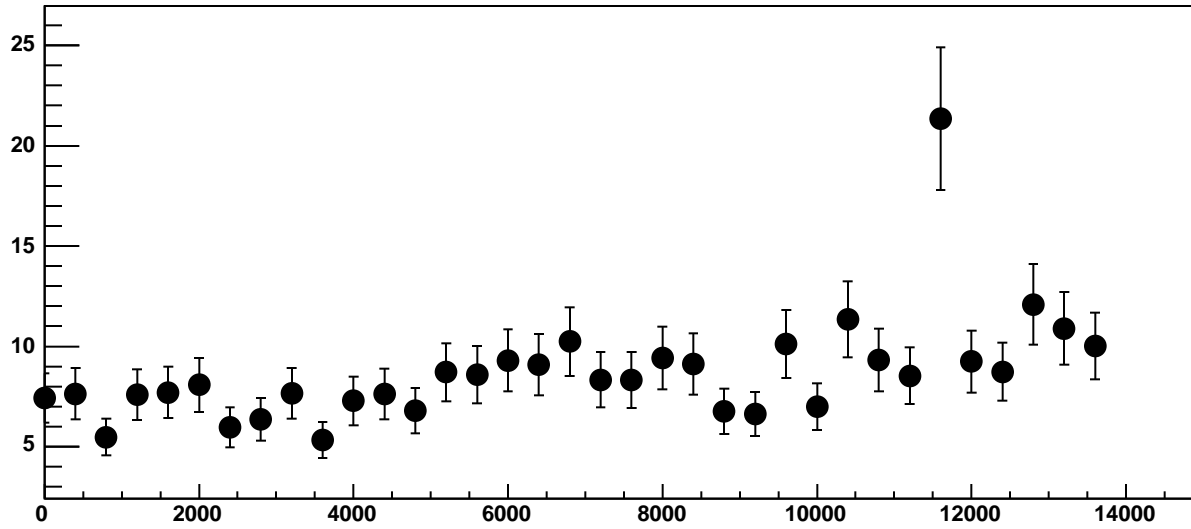
Chip 0, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC



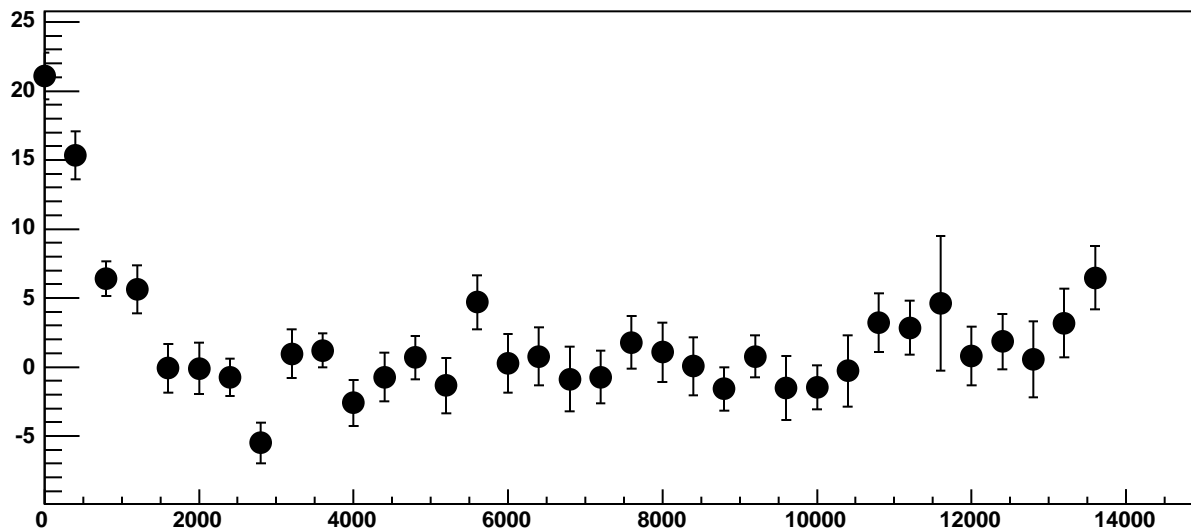
Chip 0, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



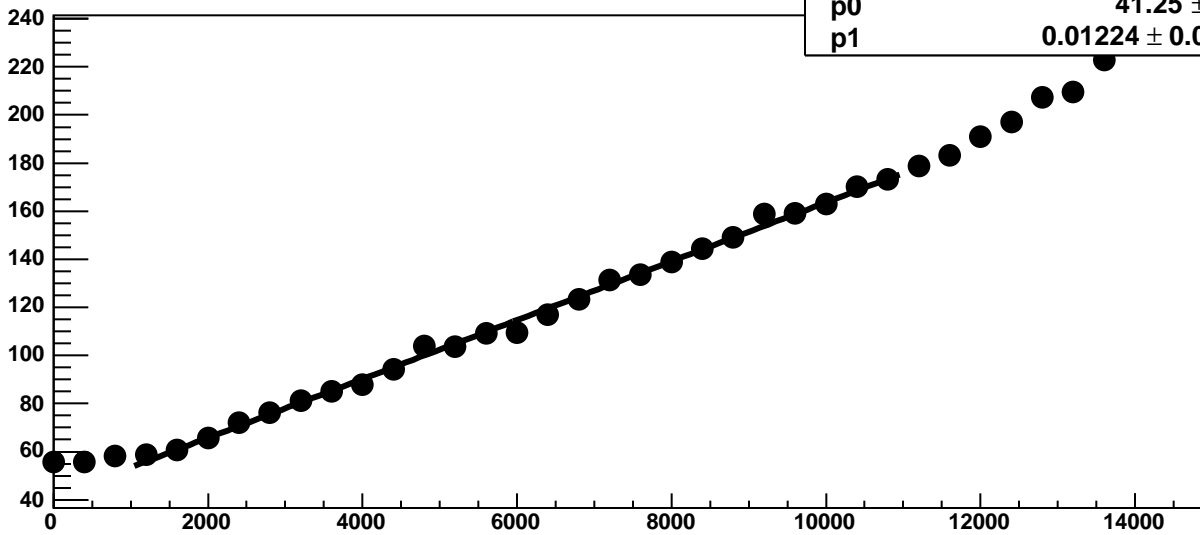
Chip 0, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

25.61 / 23

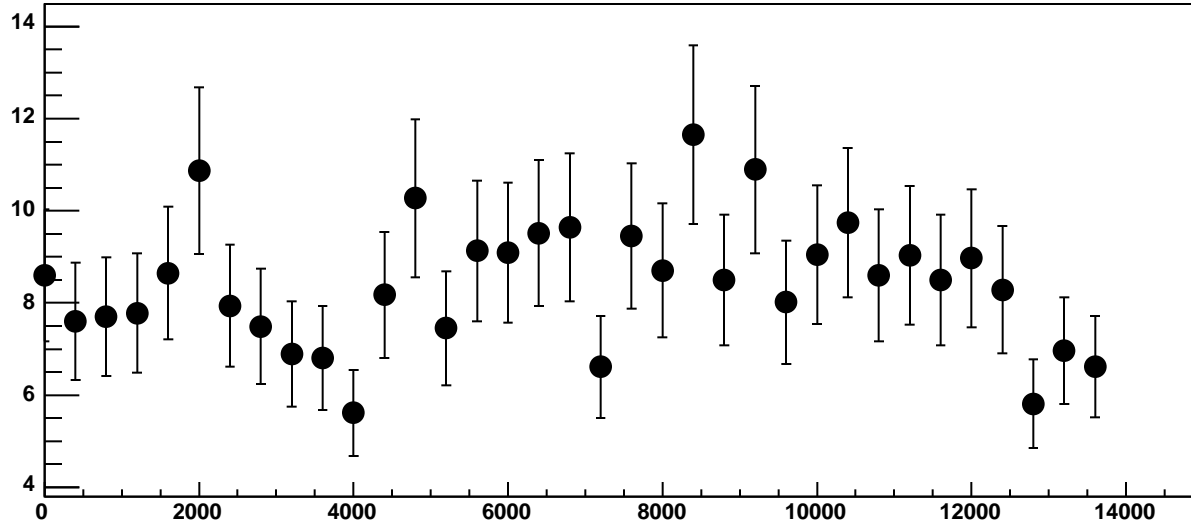
p0

41.25 ± 0.8532

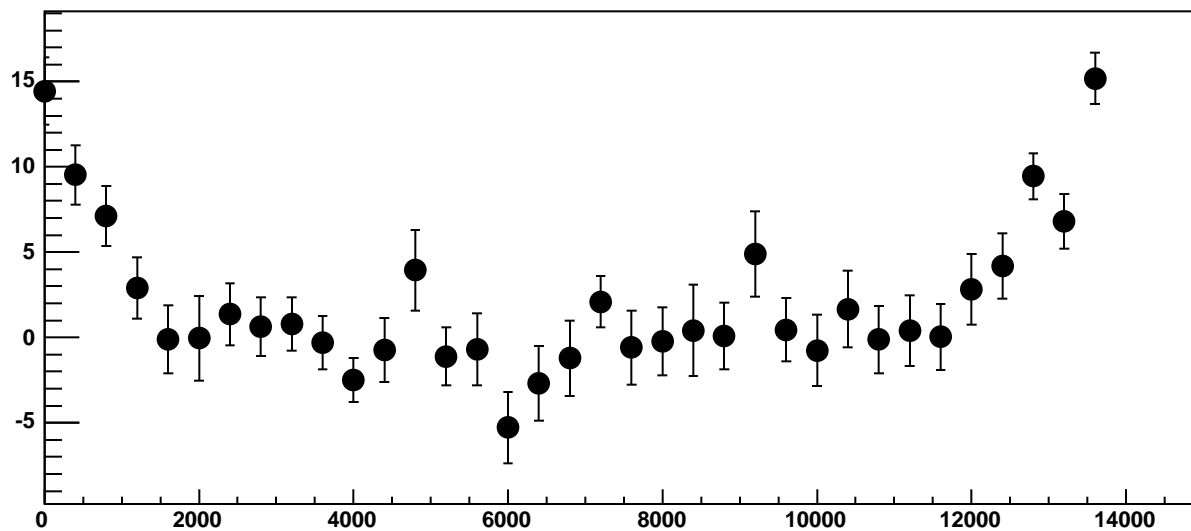
p1

0.01224 ± 0.0001354

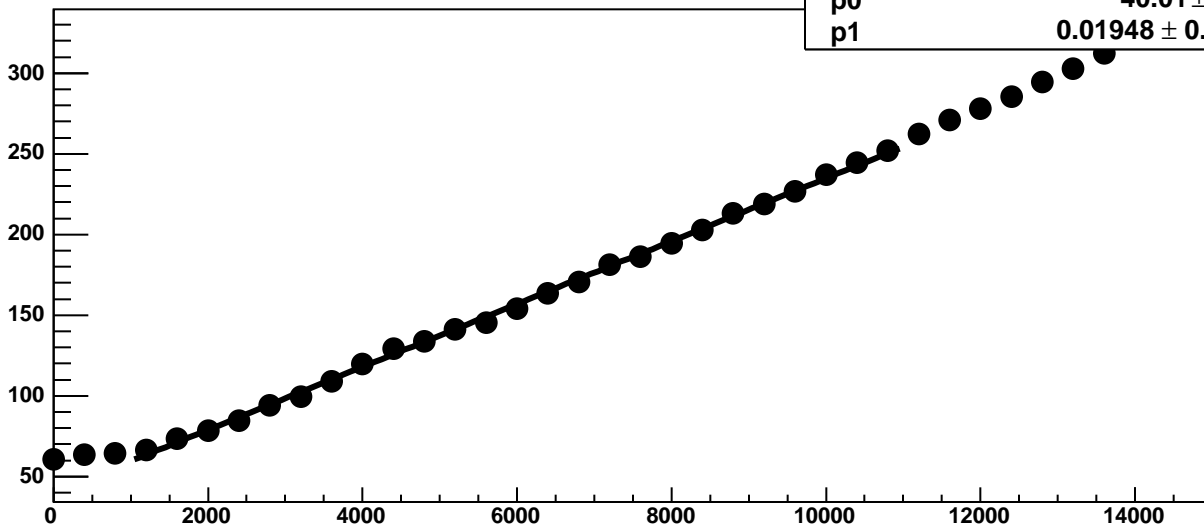
Chip 0, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

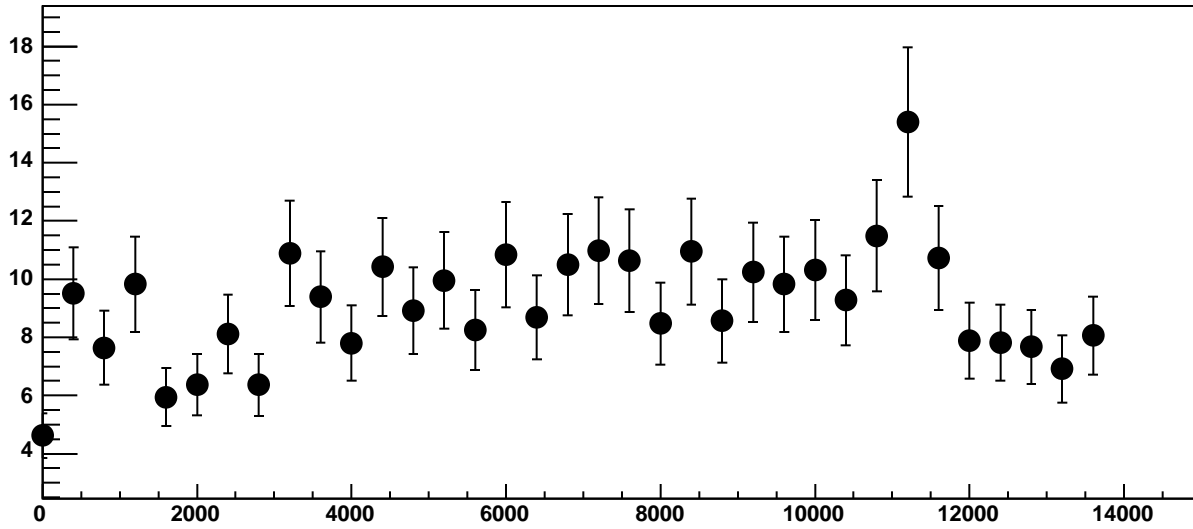


Chip 0, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC

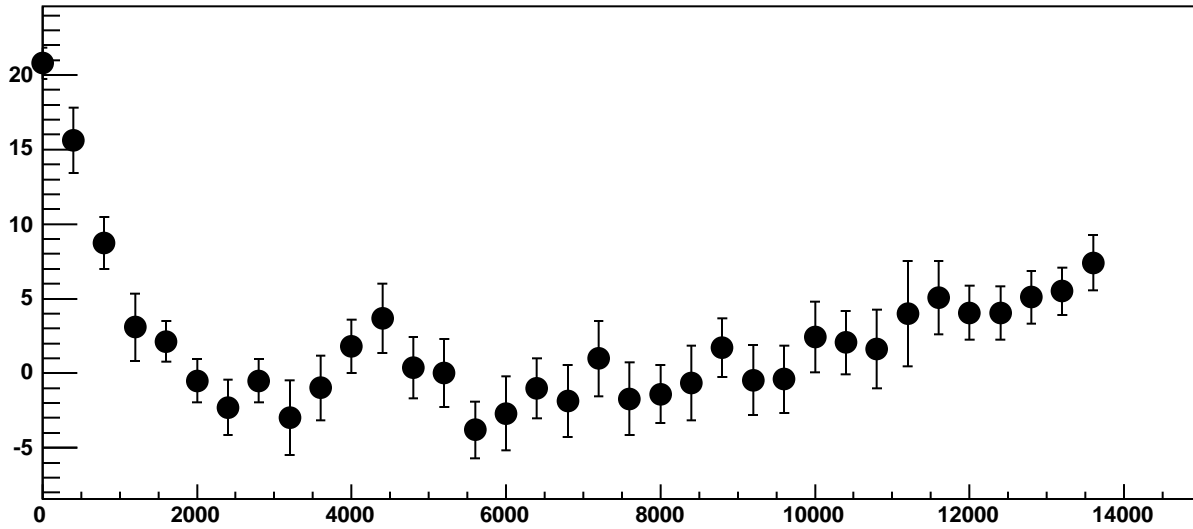


χ^2 / ndf 21.7 / 23
p0 40.01 ± 0.8383
p1 0.01948 ± 0.000138

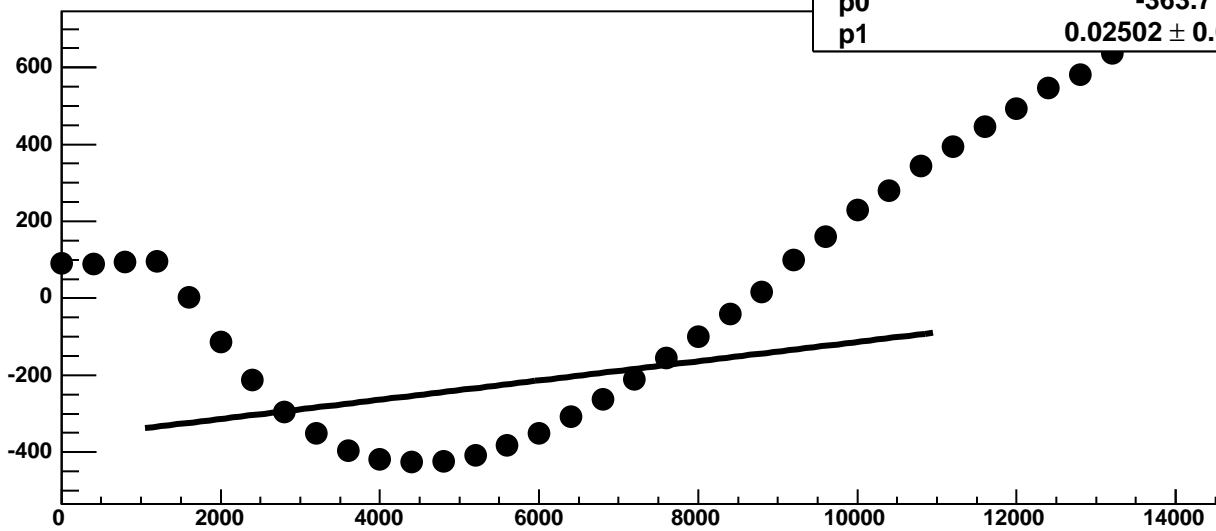
Chip 0, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC

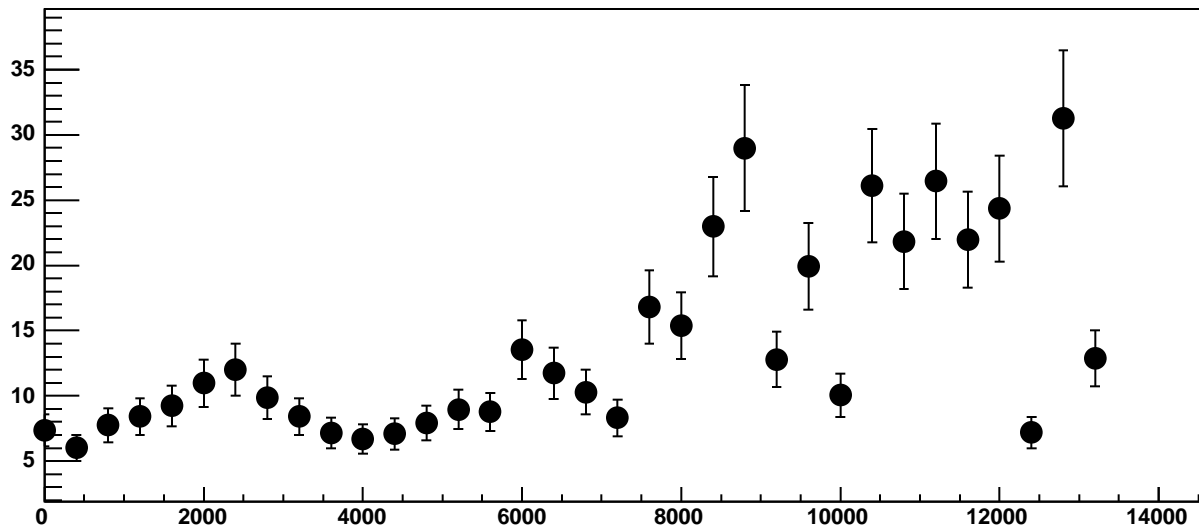


Chip 0, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

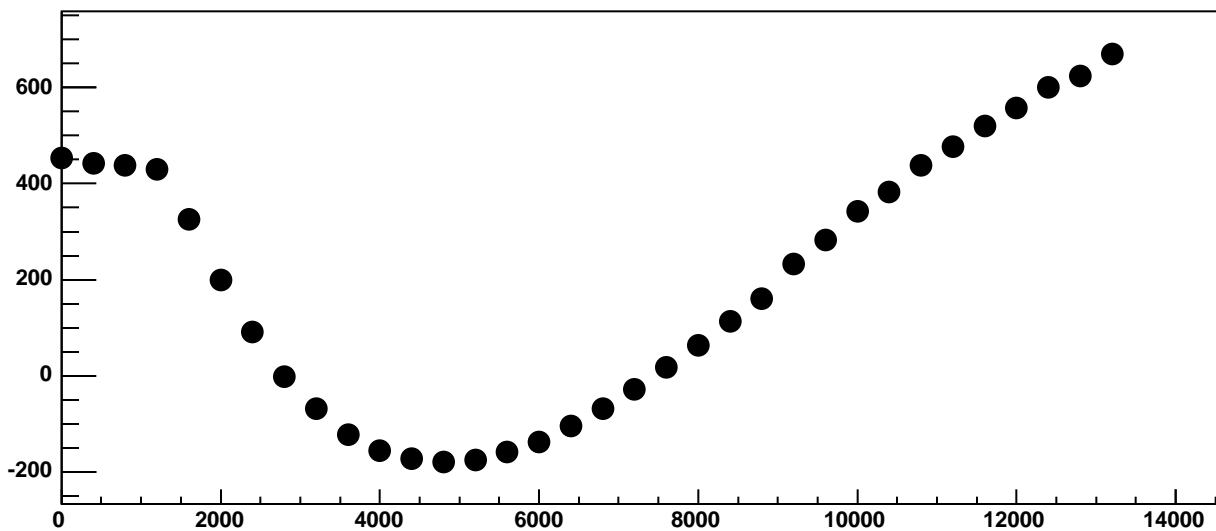


χ^2 / ndf 1.82e+05 / 23
p0 -363.7 \pm 1.061
p1 0.02502 \pm 0.000196

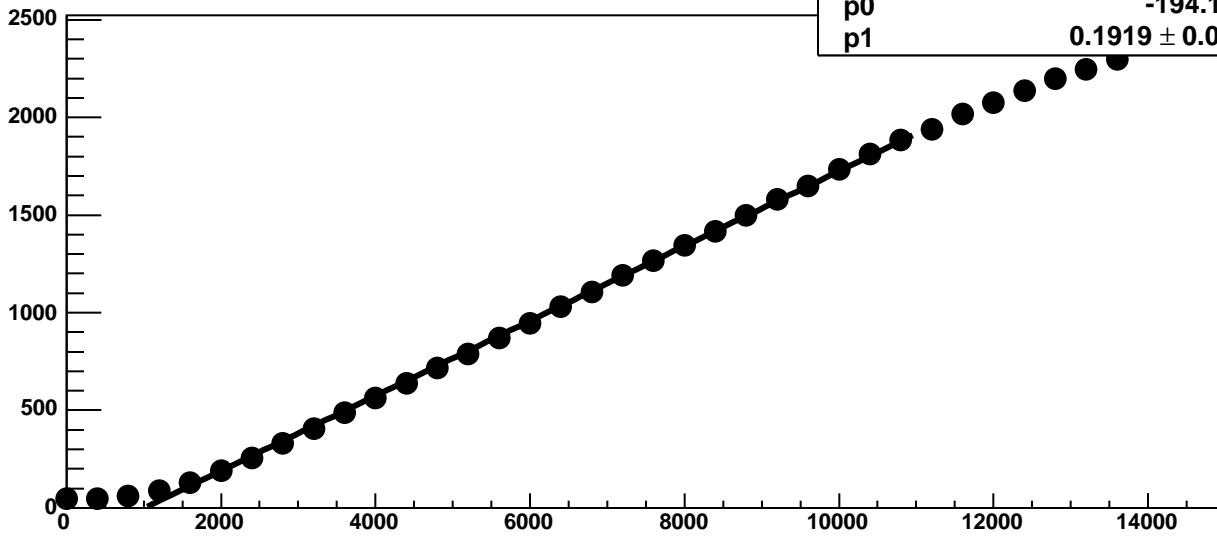
Chip 0, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



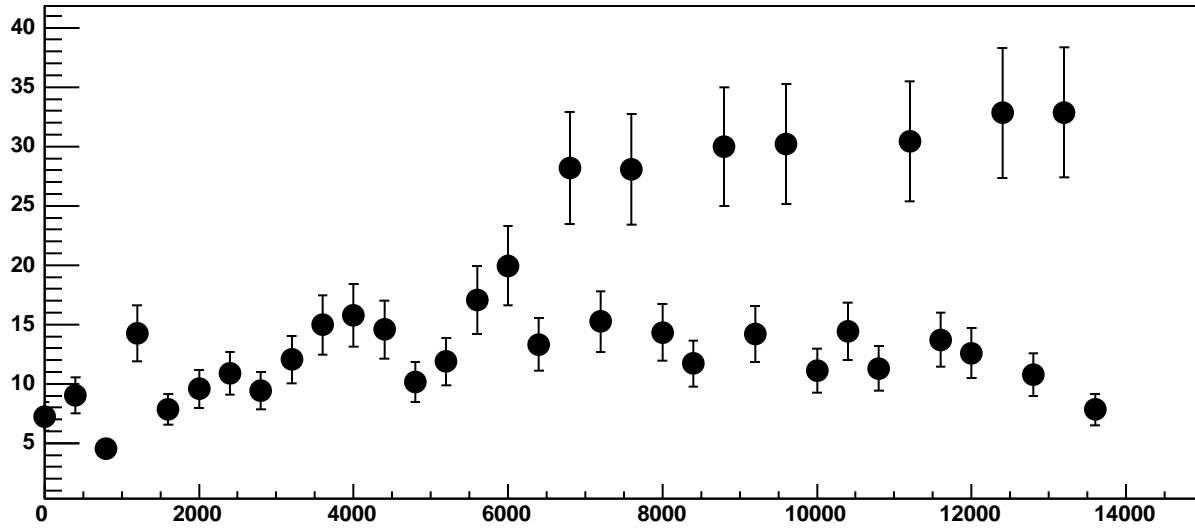
Chip 0, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC



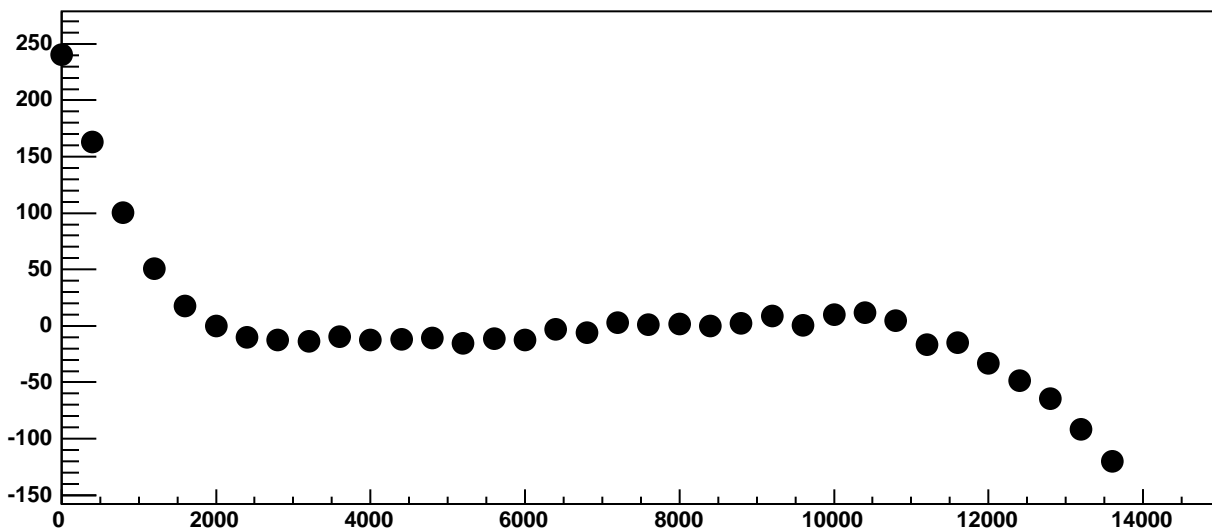
Chip 0, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



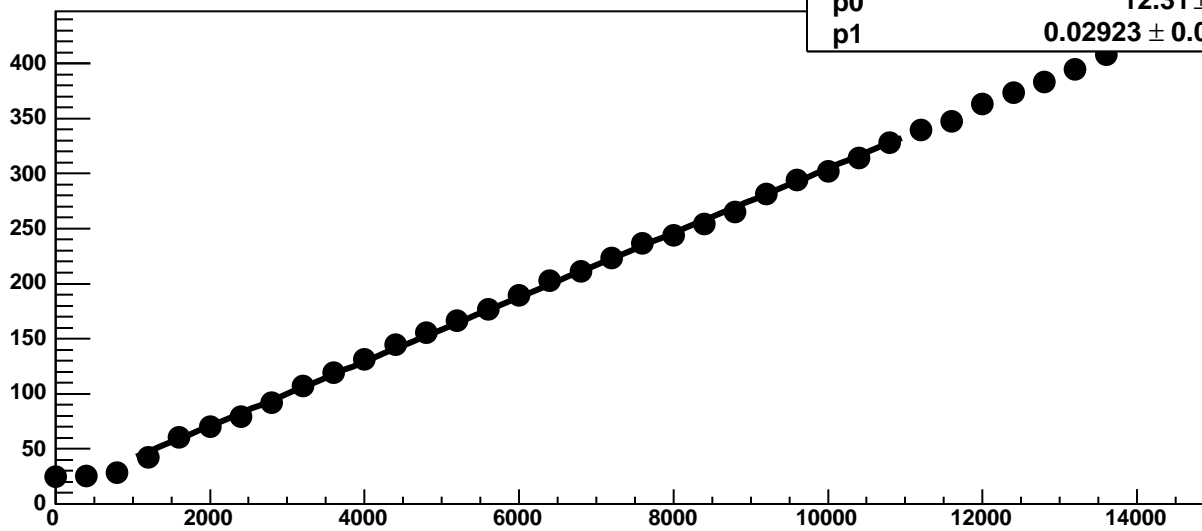
Chip 0, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

49.53 / 23

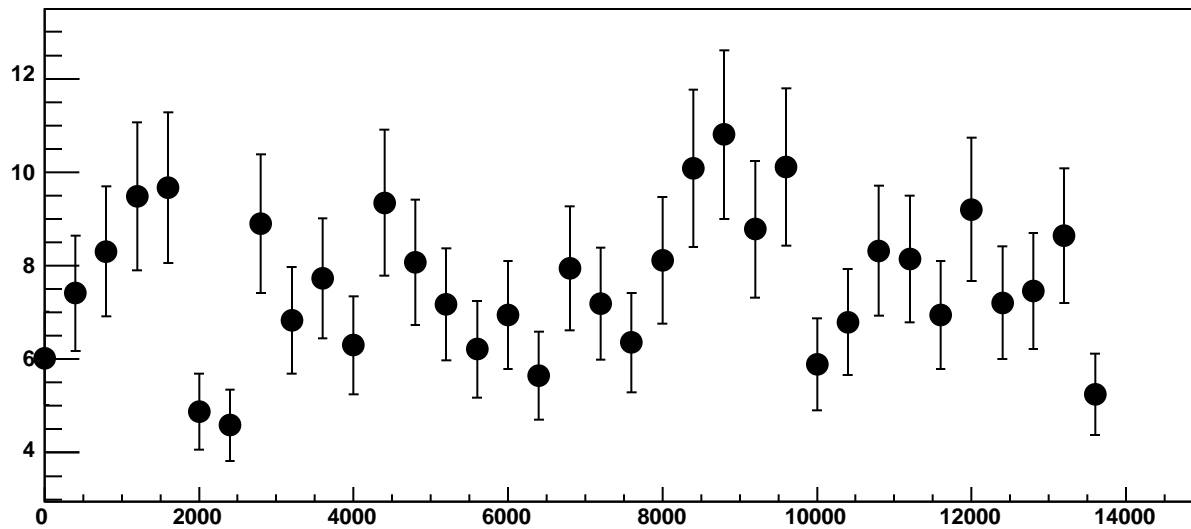
p0

12.31 ± 0.7246

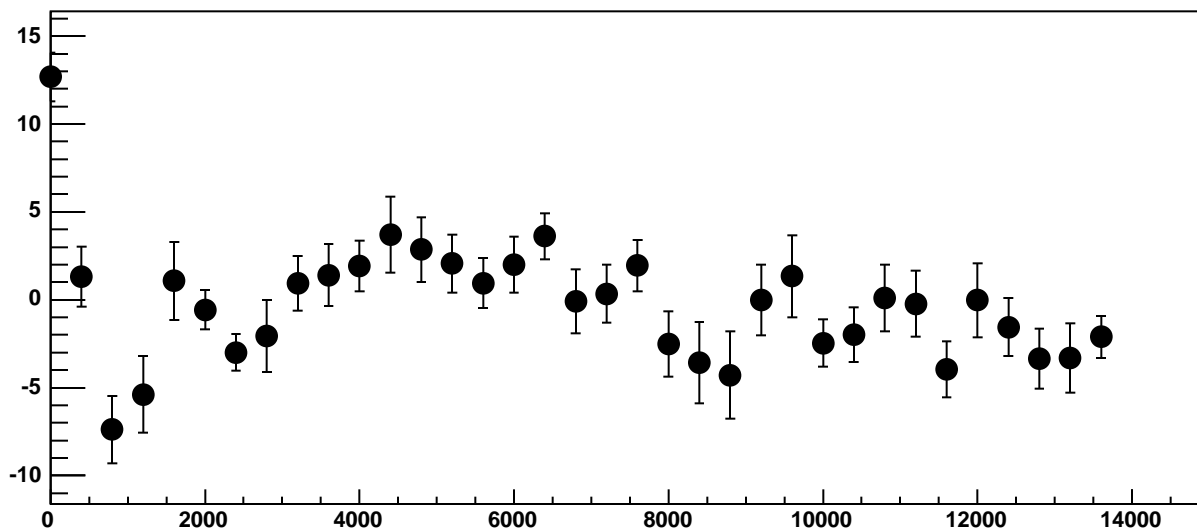
p1

0.02923 ± 0.0001144

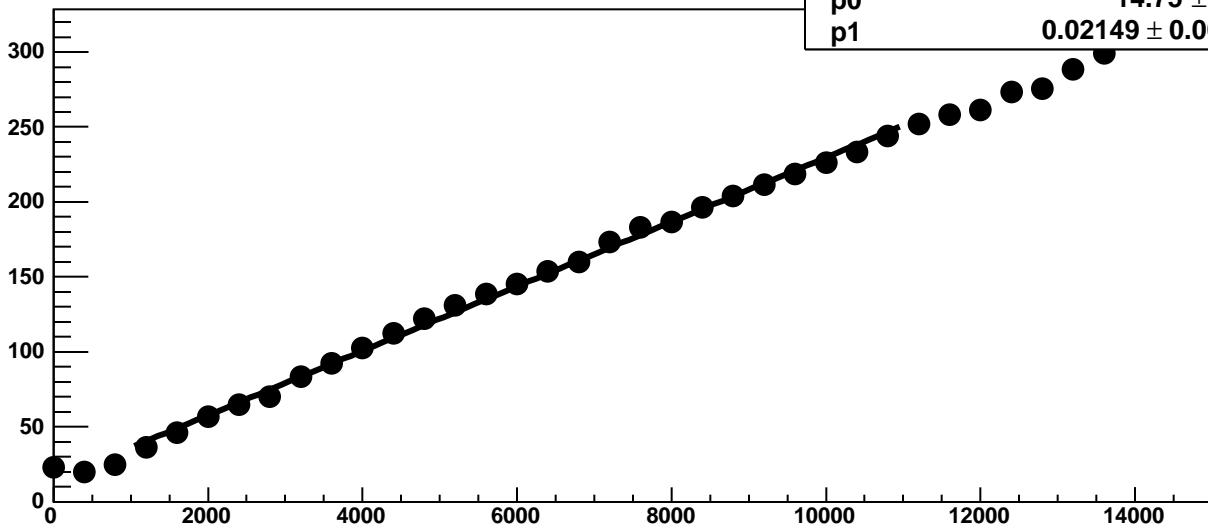
Chip 0, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC

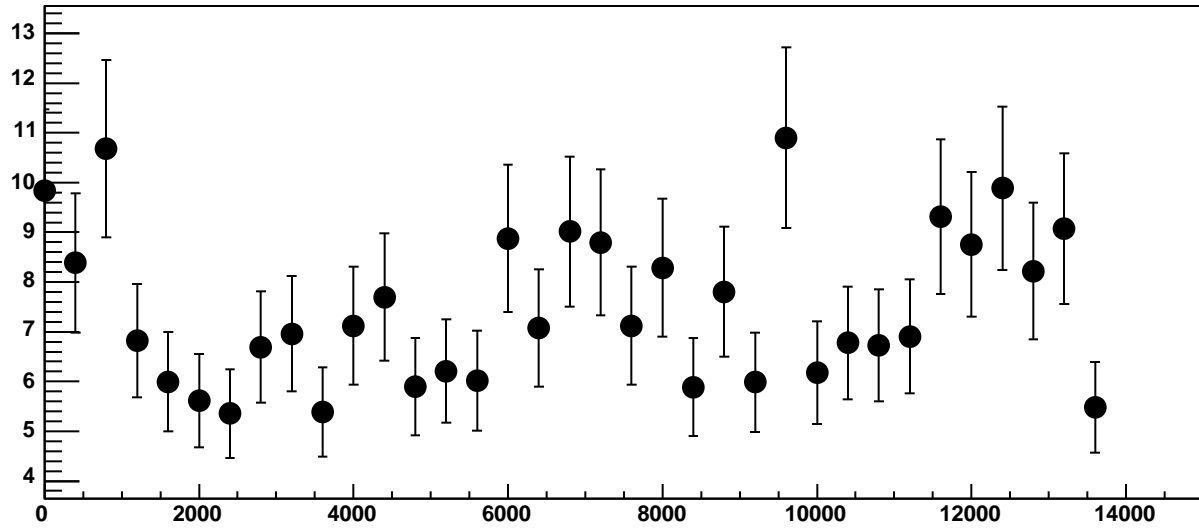


Chip 0, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

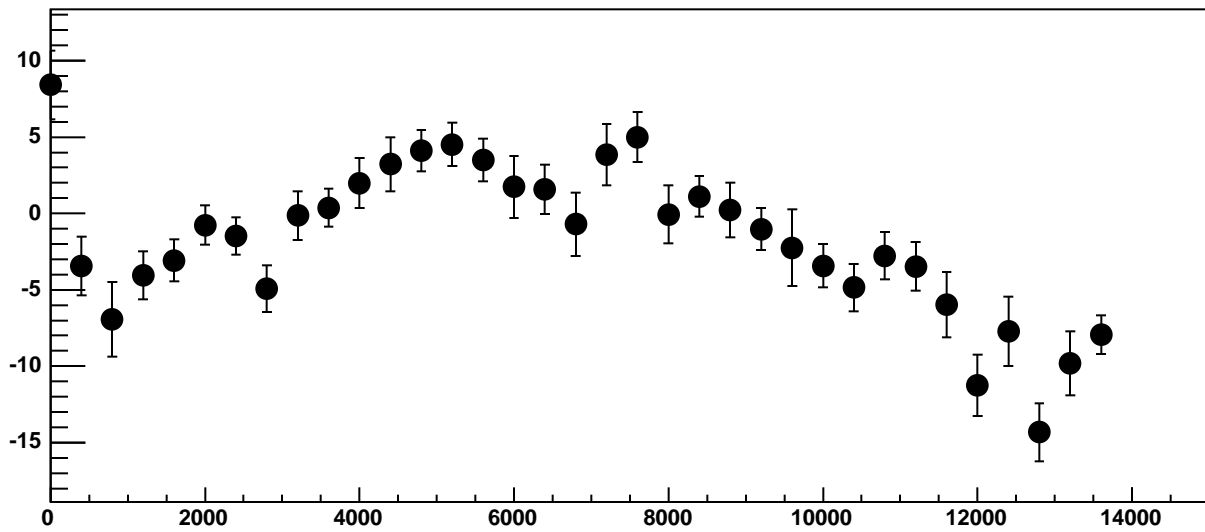


χ^2 / ndf 90 / 23
p0 14.75 ± 0.6637
p1 0.02149 ± 0.0001038

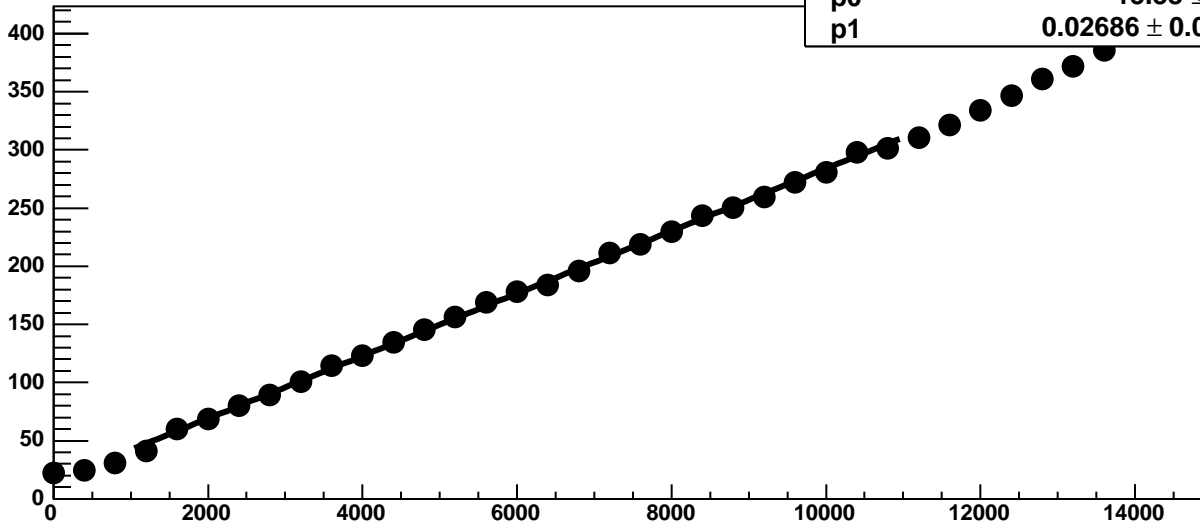
Chip 0, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

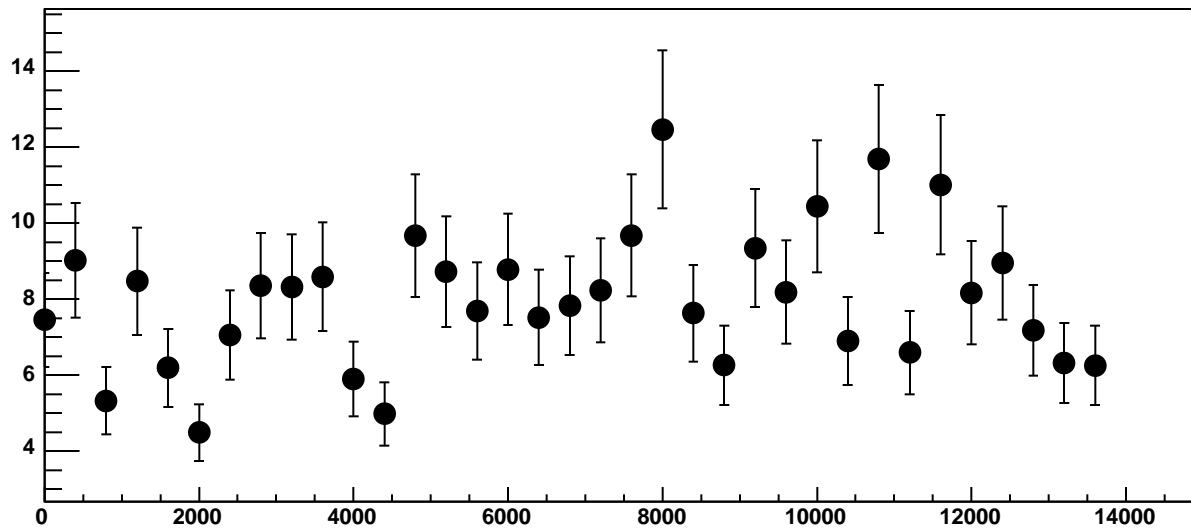


Chip 0, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

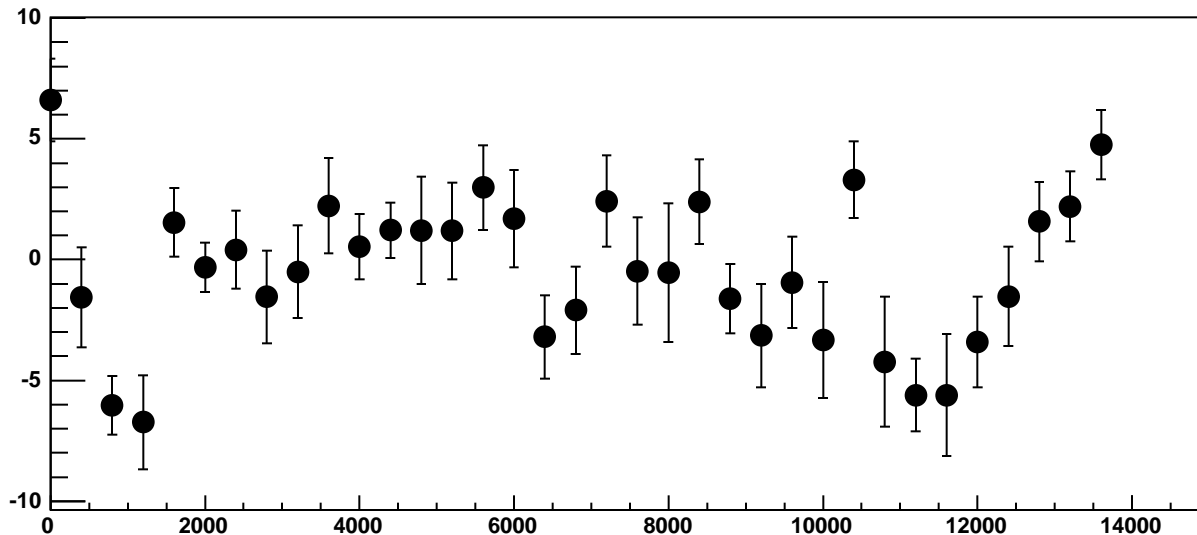


χ^2 / ndf 41.62 / 23
p0 15.38 ± 0.7254
p1 0.02686 ± 0.0001193

Chip 0, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

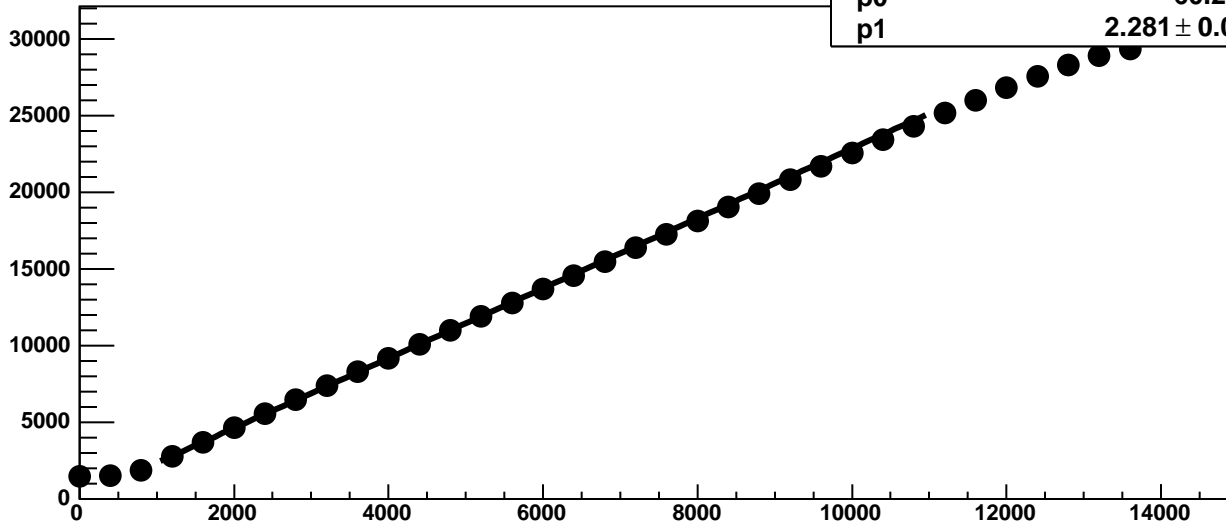


Chip 0, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

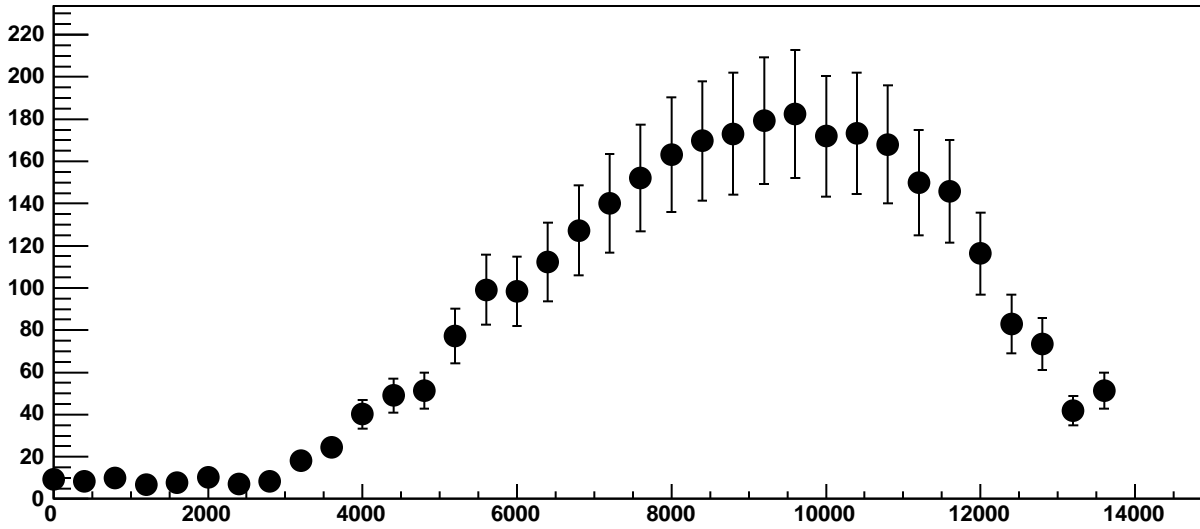


Chip 0, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

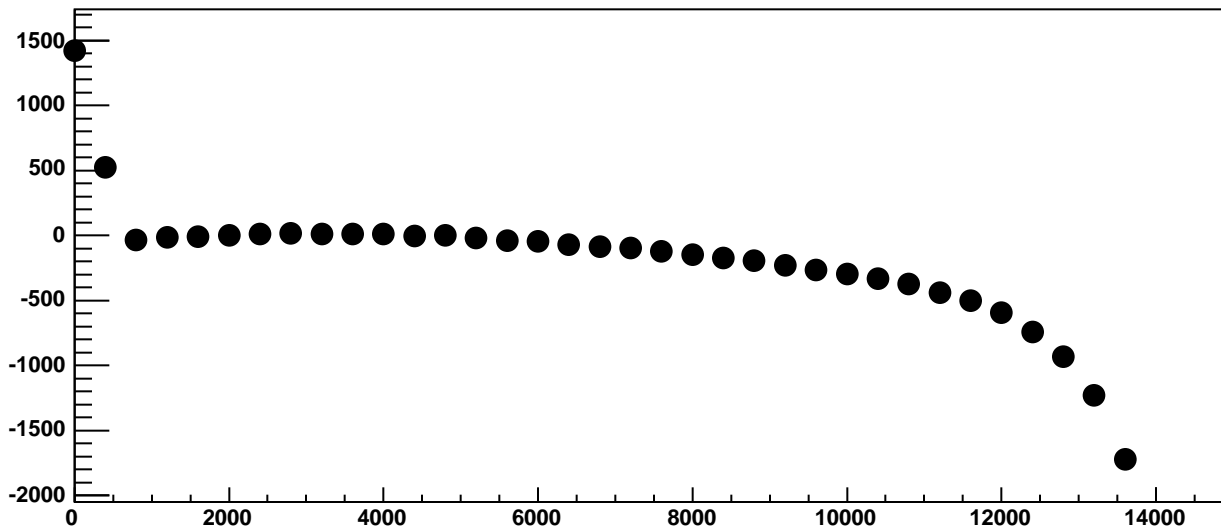
χ^2 / ndf 637.2 / 23
p0 60.2 ± 2.003
p1 2.281 ± 0.0008731



Chip 0, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC

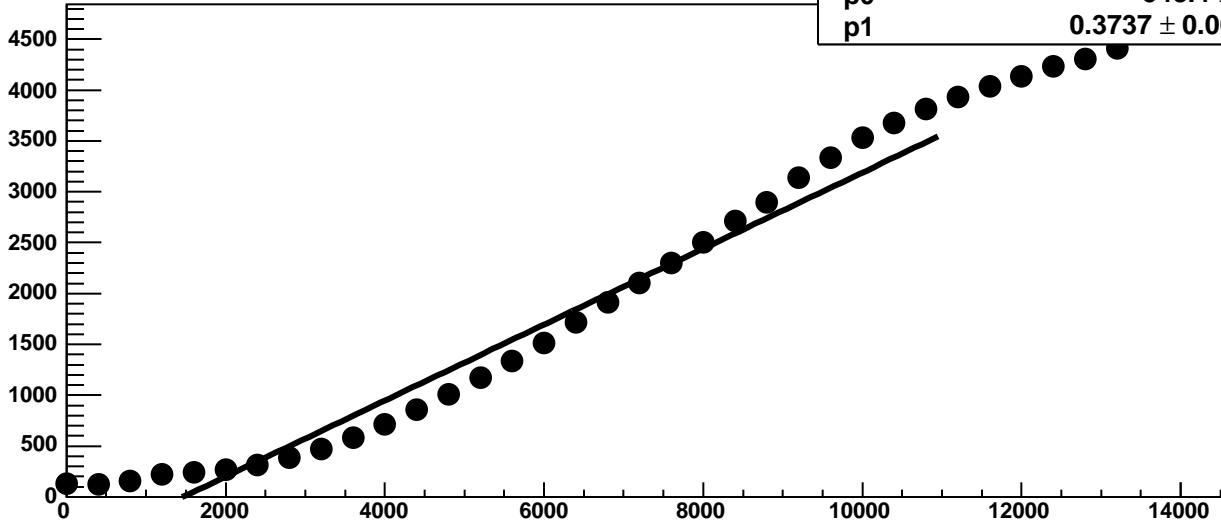


Chip 0, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC

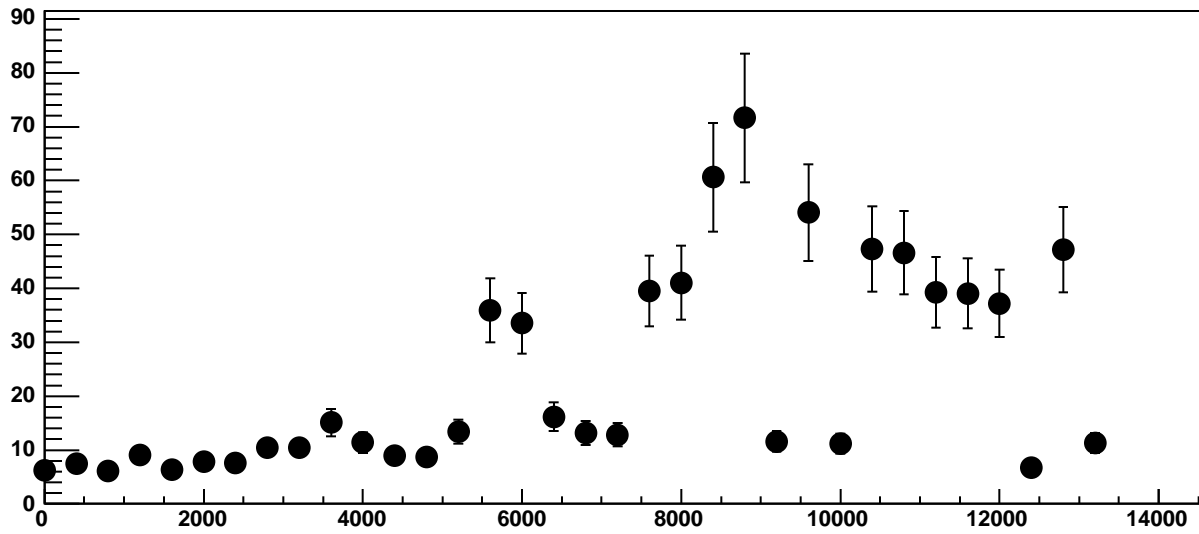


Chip 0, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

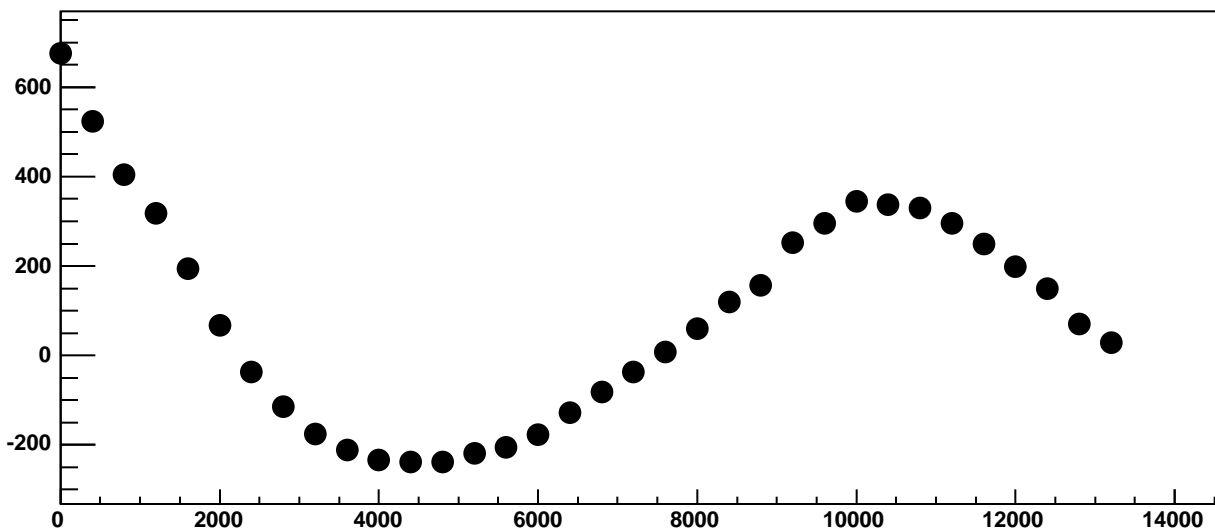
χ^2 / ndf 1.276e+05 / 23
p0 -548.4 ± 1.043
p1 0.3737 ± 0.0002202



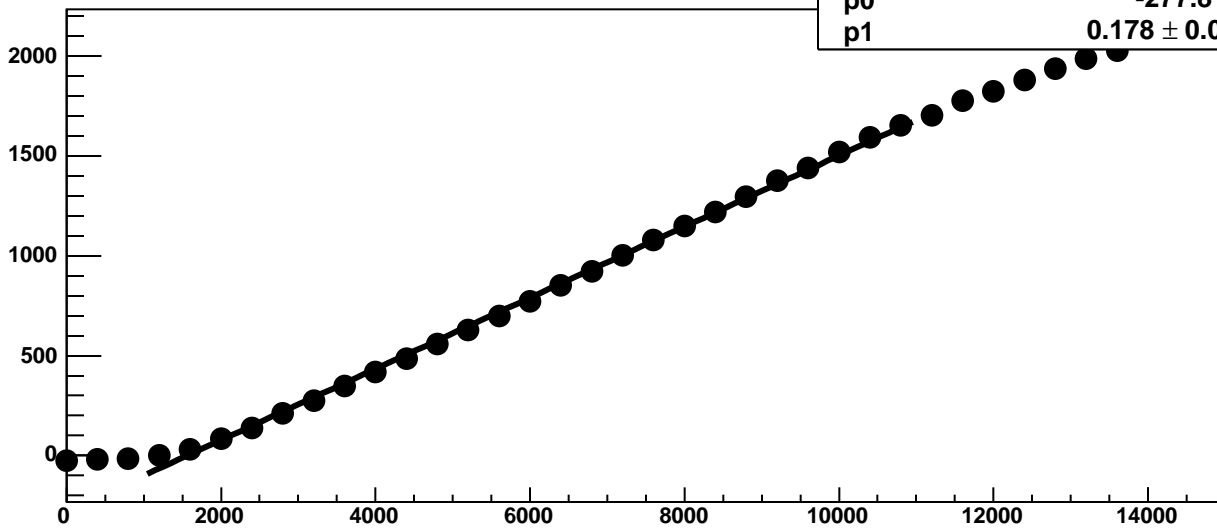
Chip 0, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



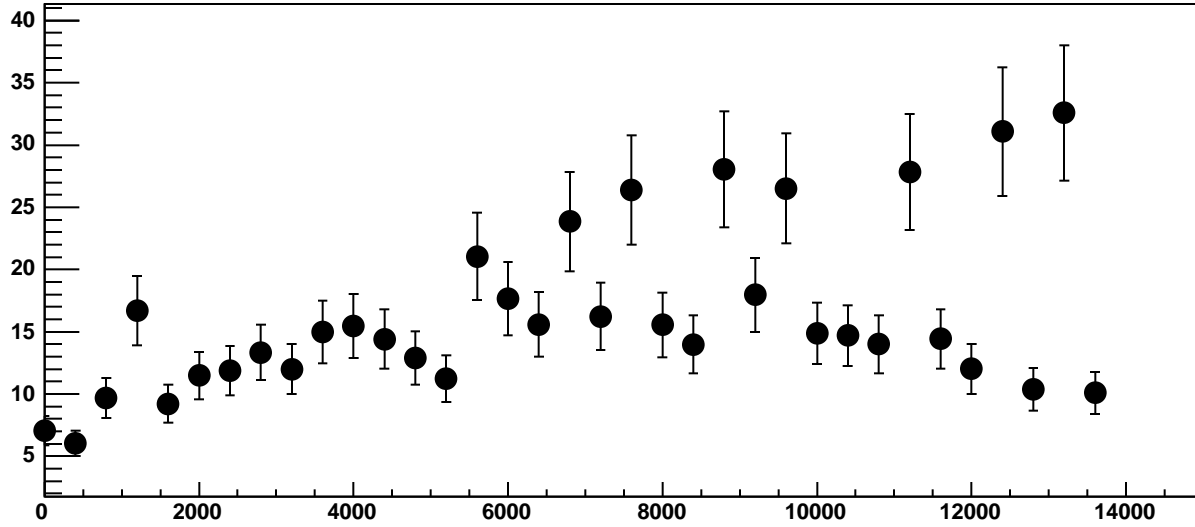
Chip 0, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



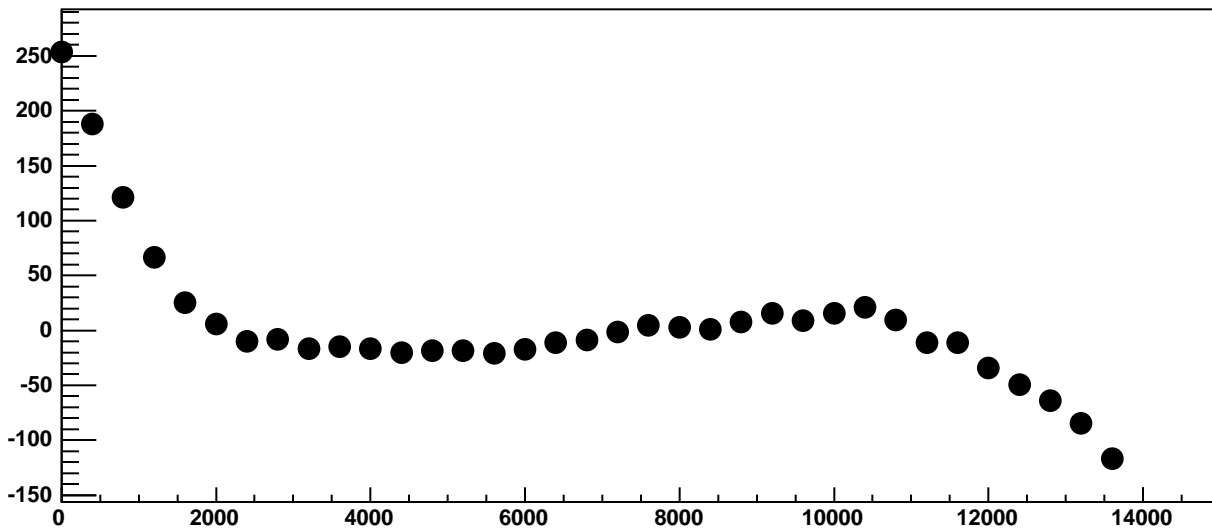
Chip 0, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



Chip 0, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

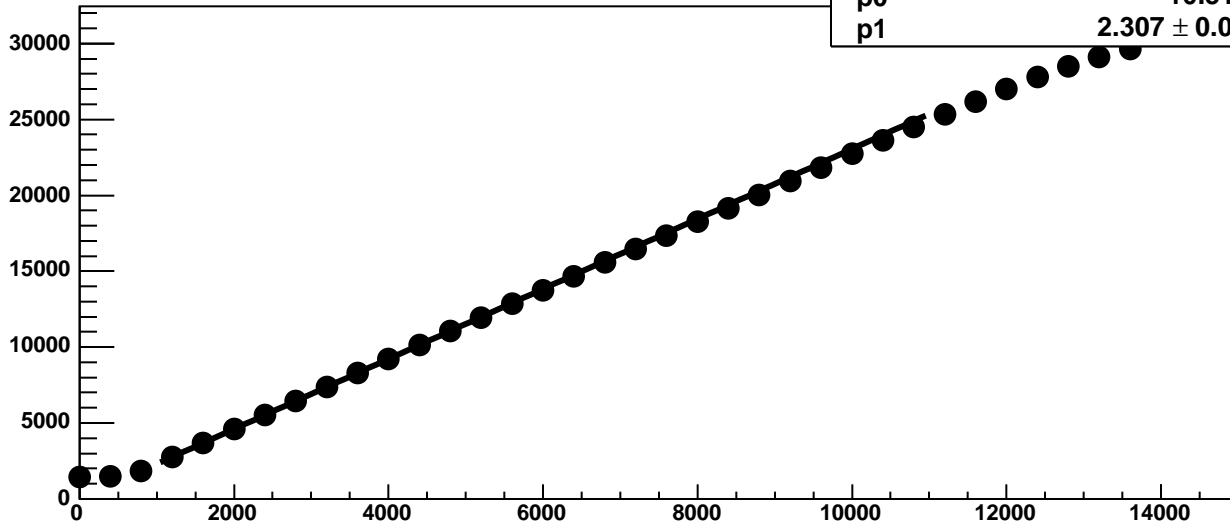


Chip 0, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

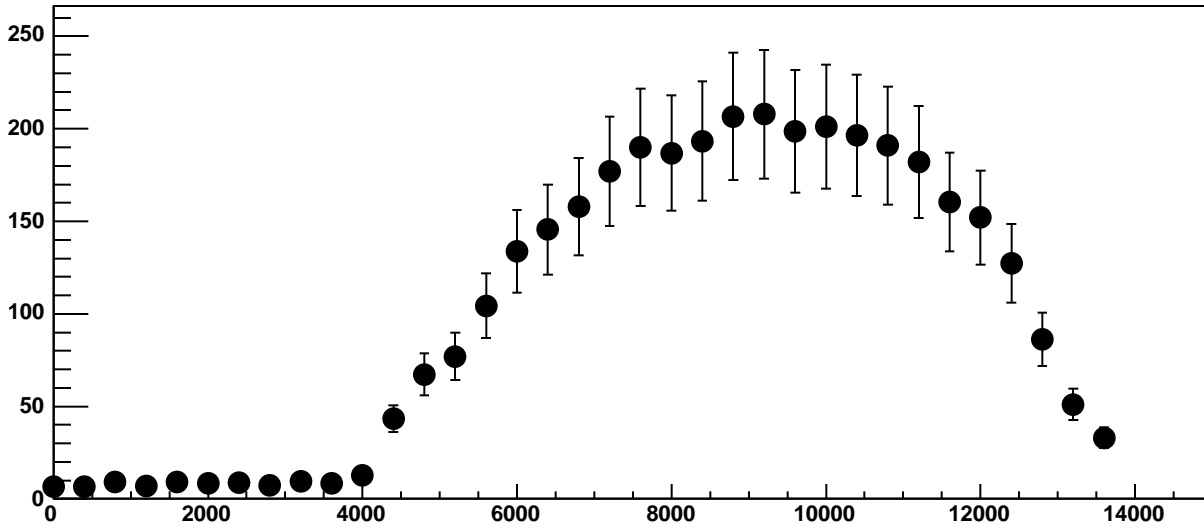


Chip 0, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC

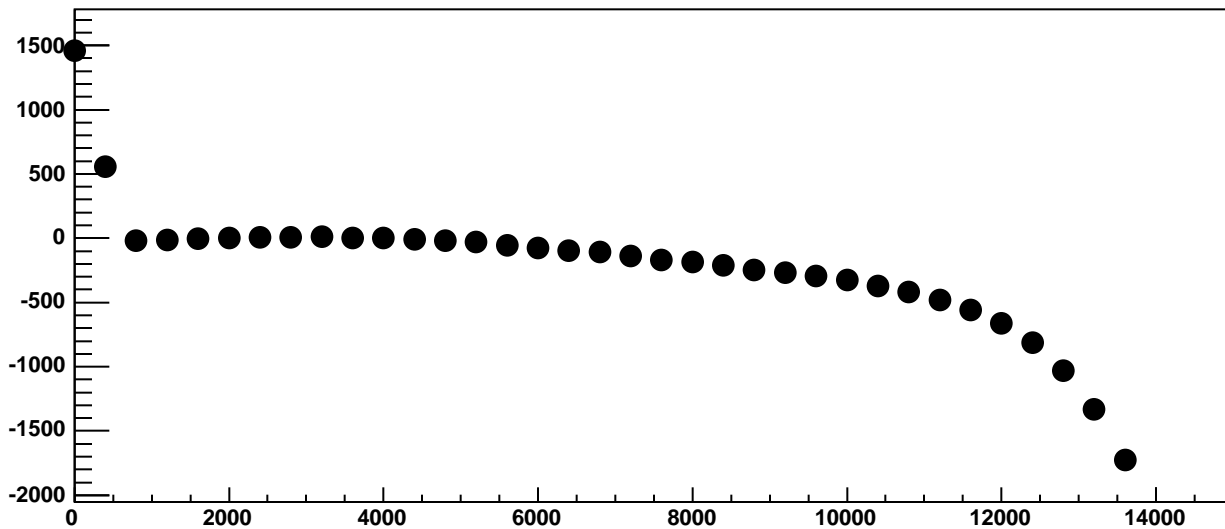
χ^2 / ndf 550.4 / 23
p0 -10.51 ± 1.88
p1 2.307 ± 0.0007116



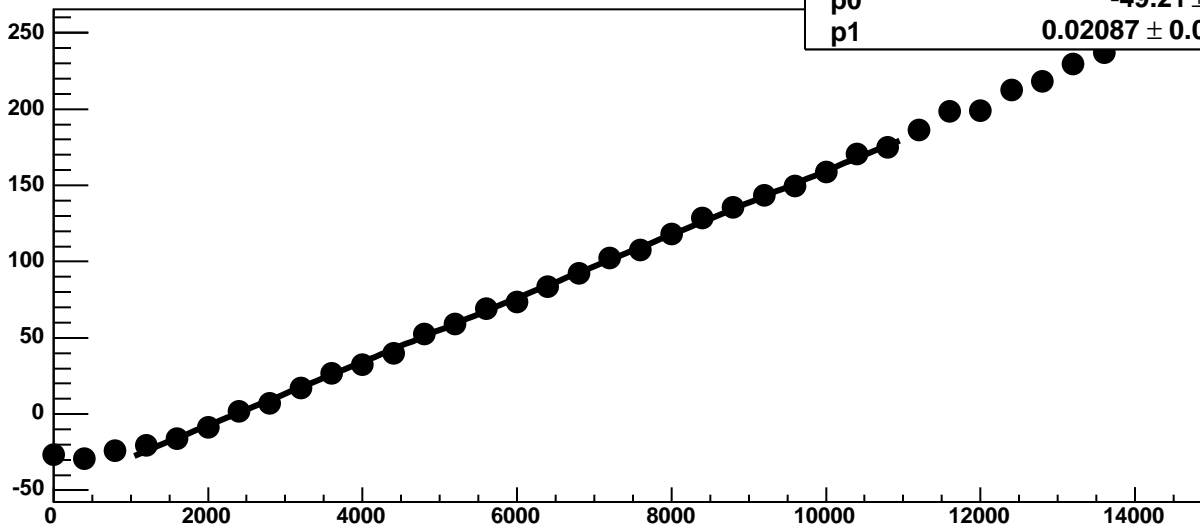
Chip 0, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

19.83 / 23

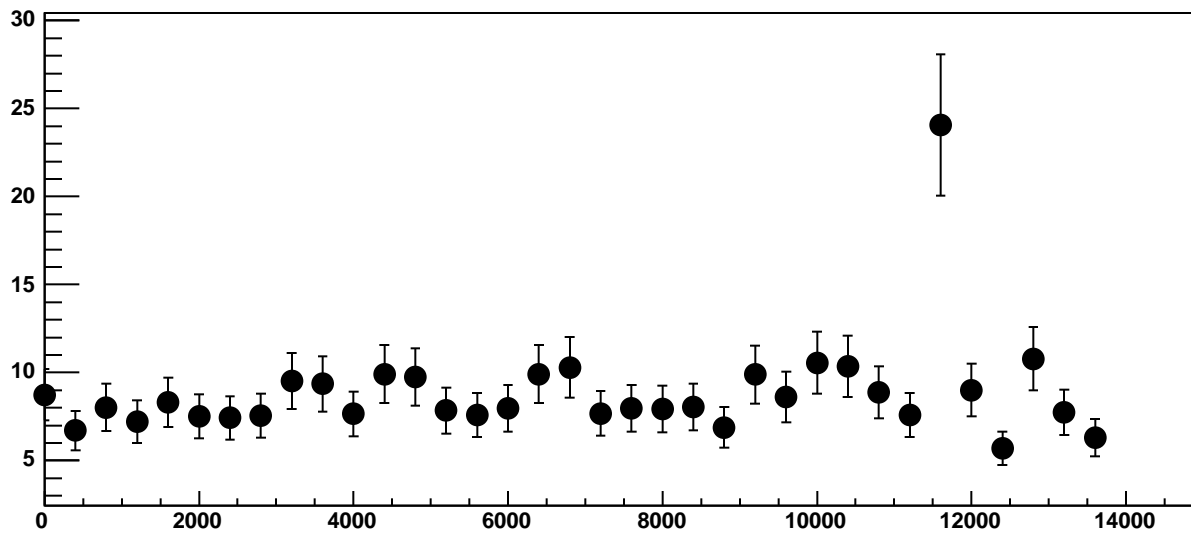
p0

-49.21 ± 0.8589

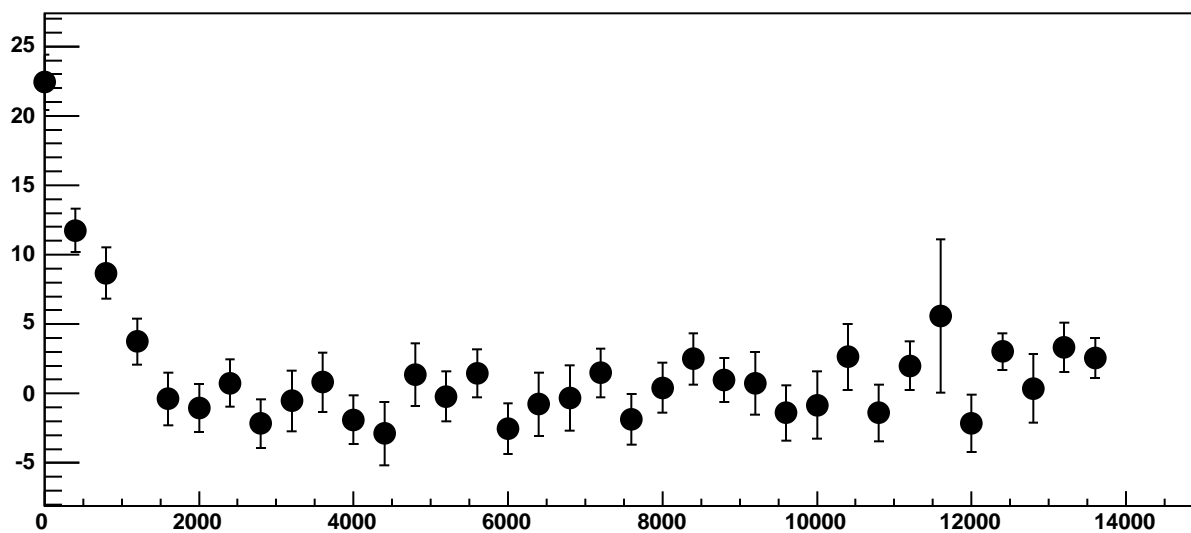
p1

0.02087 ± 0.0001334

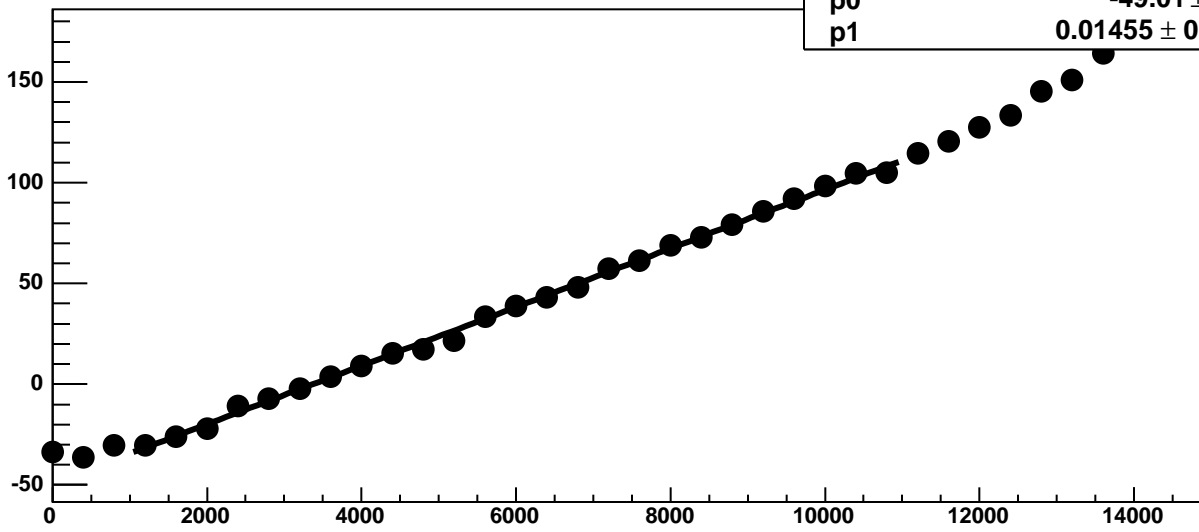
Chip 0, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



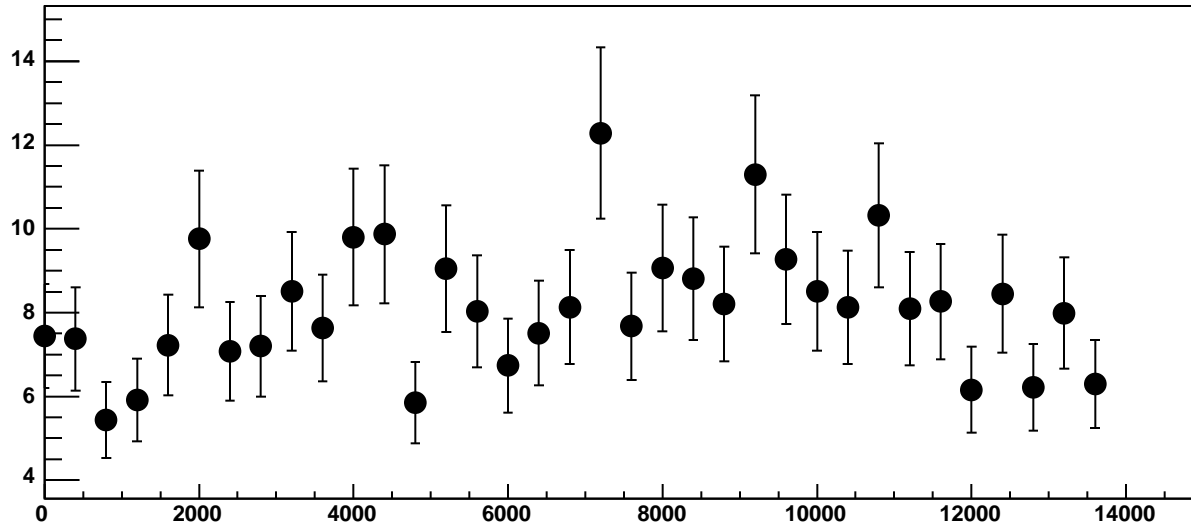
Chip 0, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



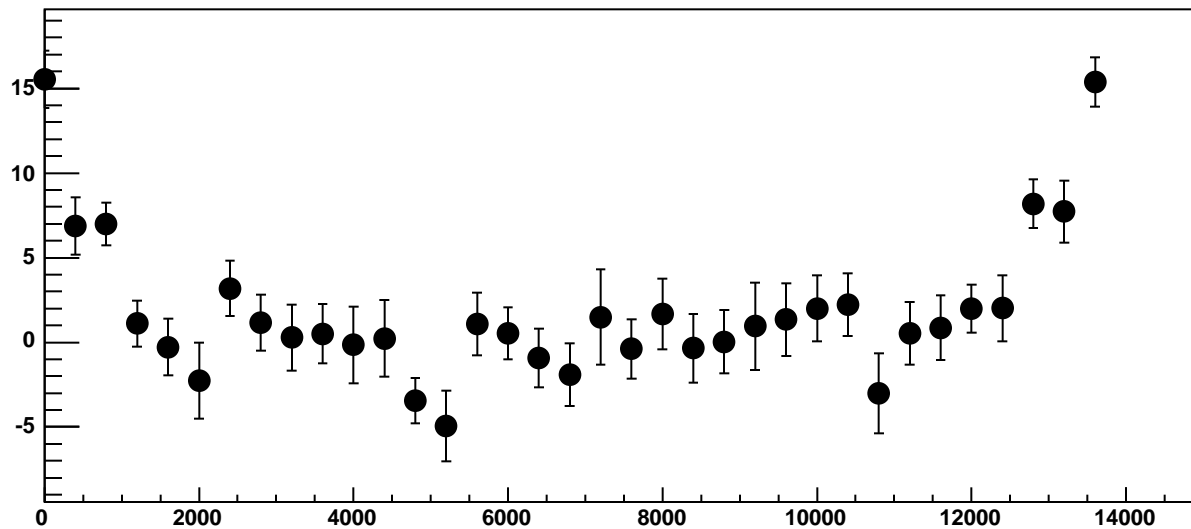
Chip 0, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



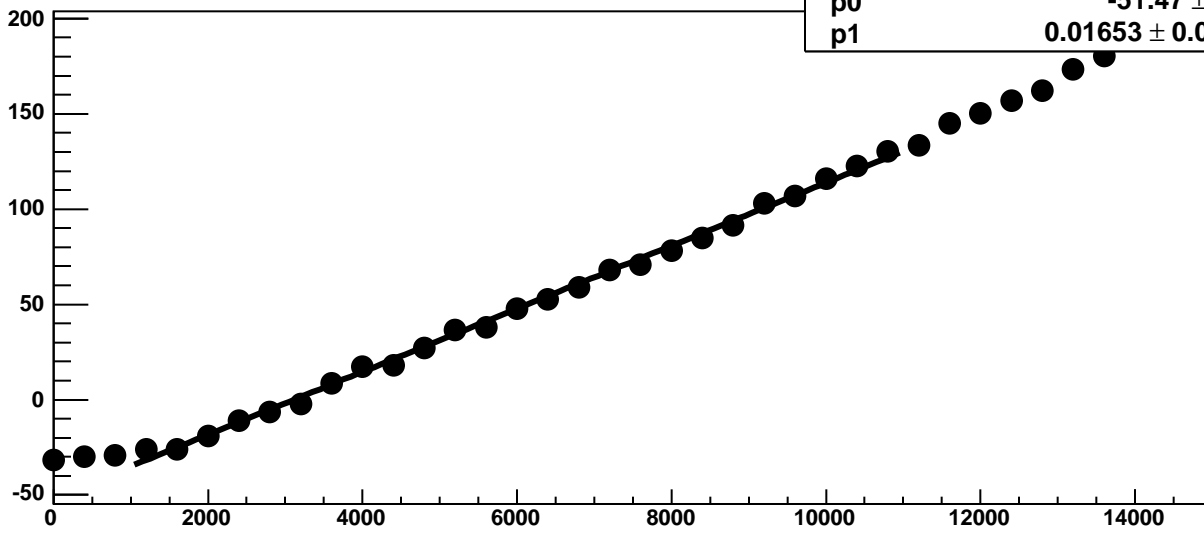
Chip 0, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

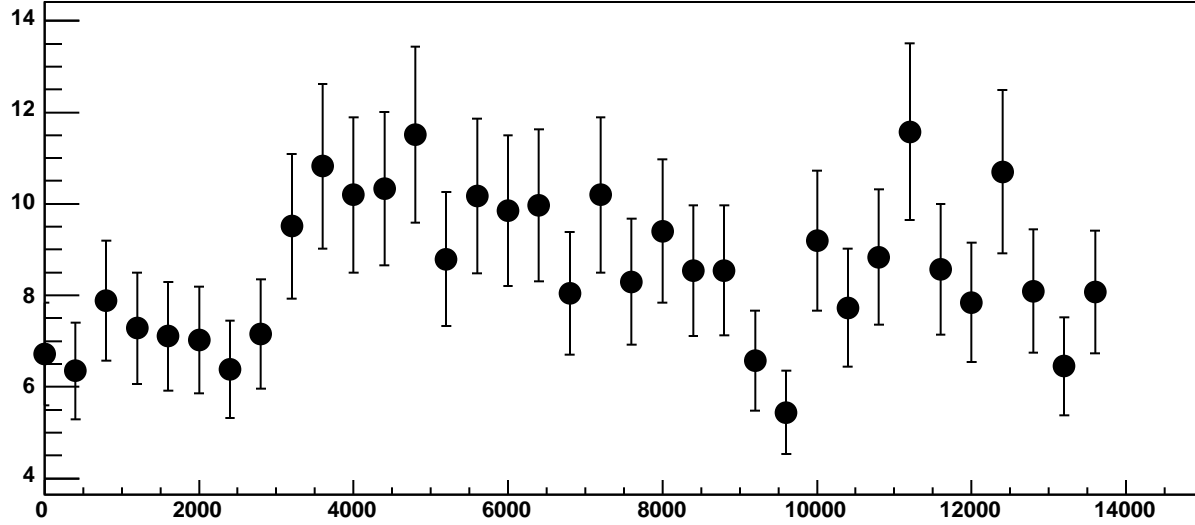


Chip 0, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

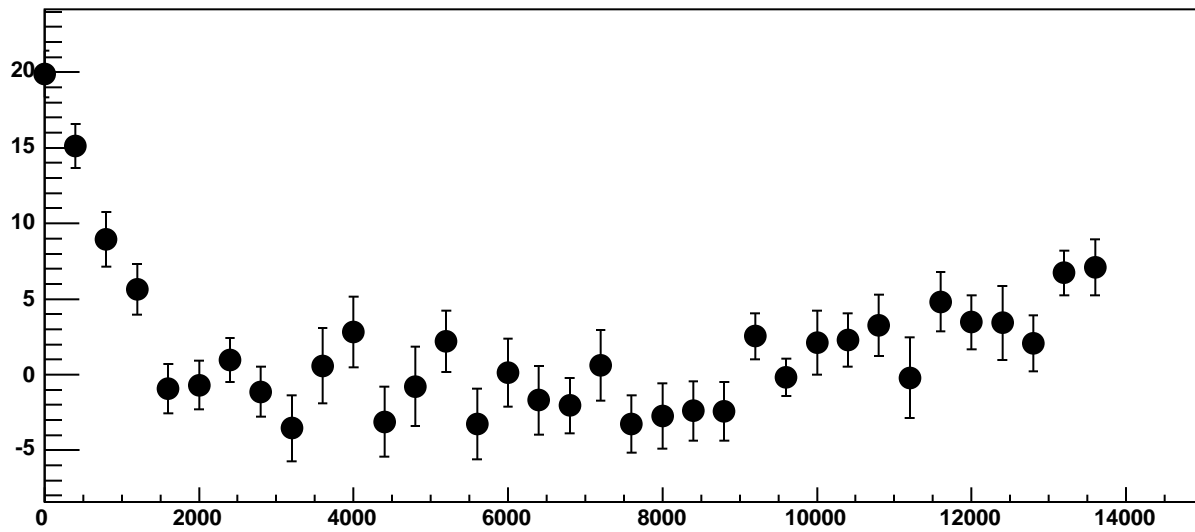


χ^2 / ndf 39.47 / 23
p0 -51.47 ± 0.8137
p1 0.01653 ± 0.0001199

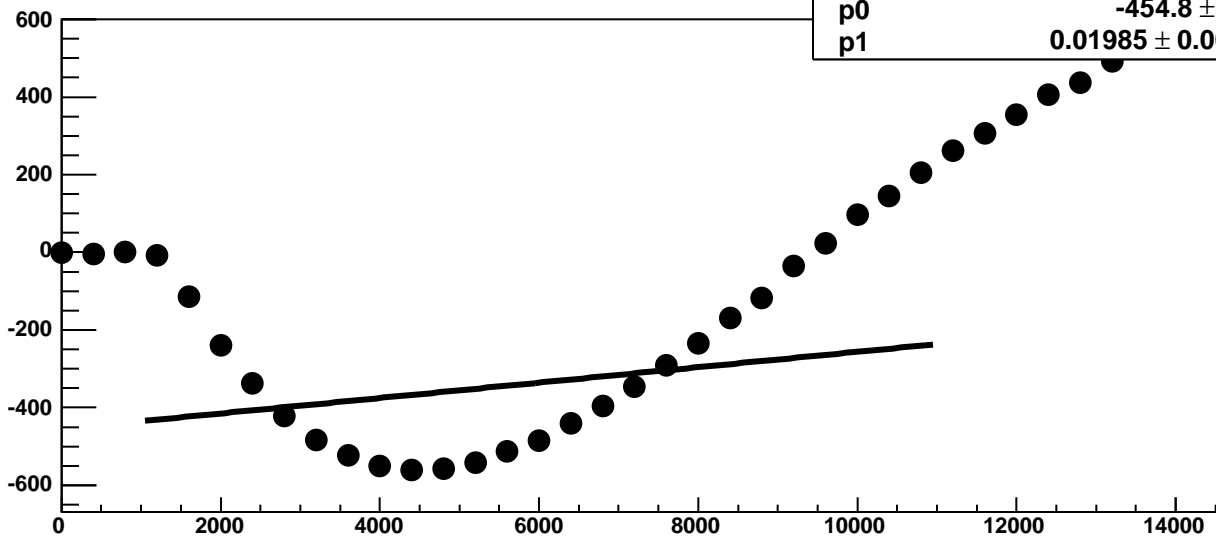
Chip 0, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC

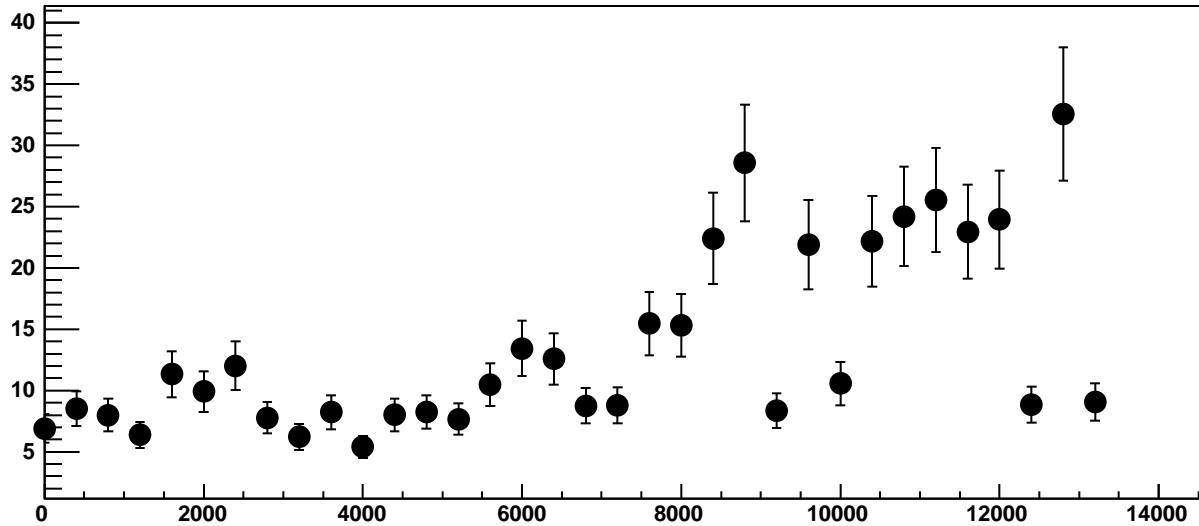


Chip 0, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

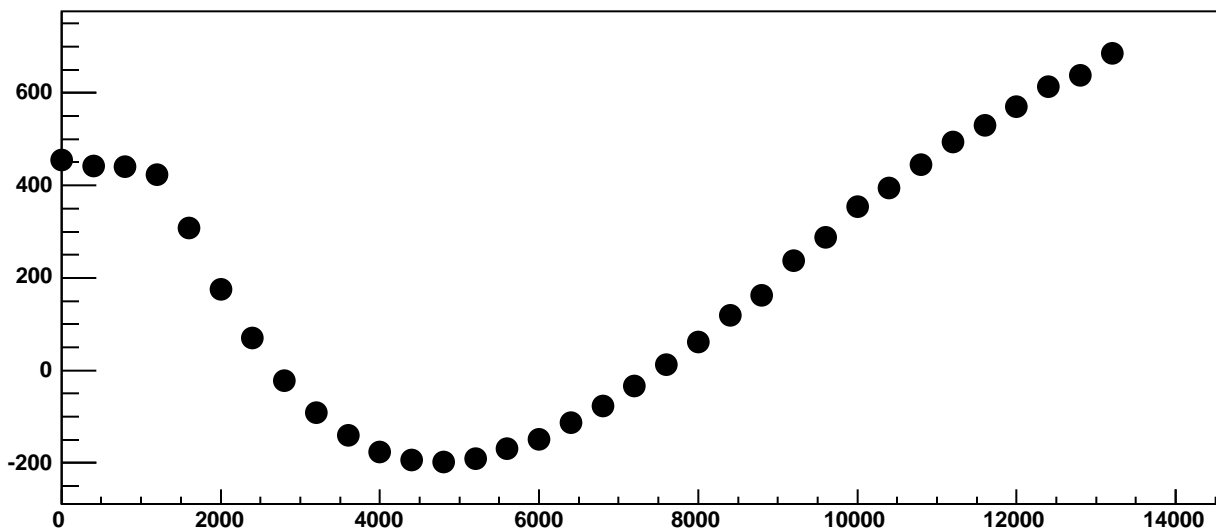


χ^2 / ndf 2.323e+05 / 23
p0 -454.8 ± 0.9476
p1 0.01985 ± 0.0001776

Chip 0, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

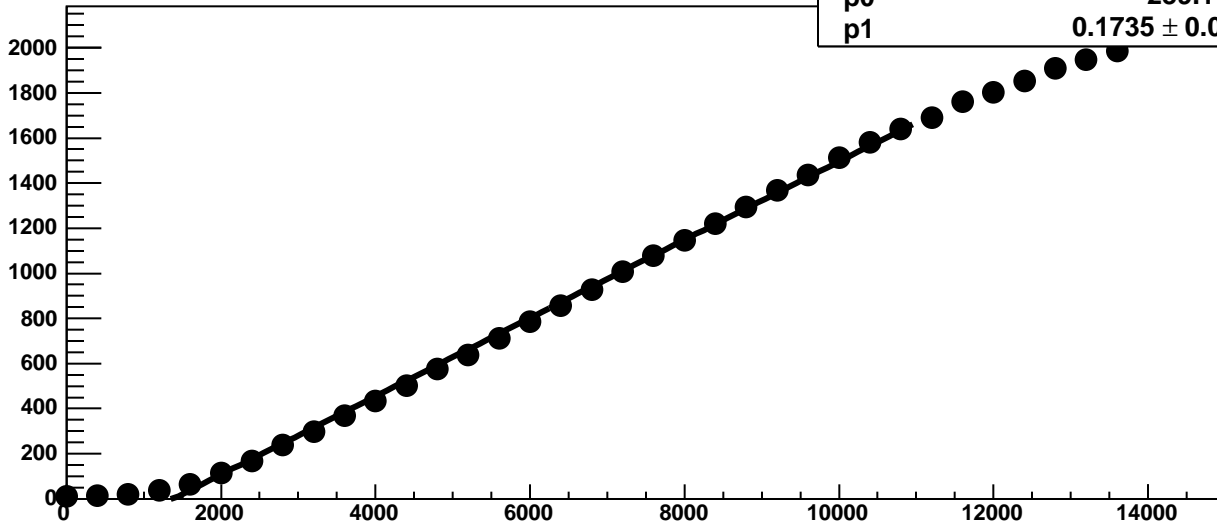


Chip 0, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

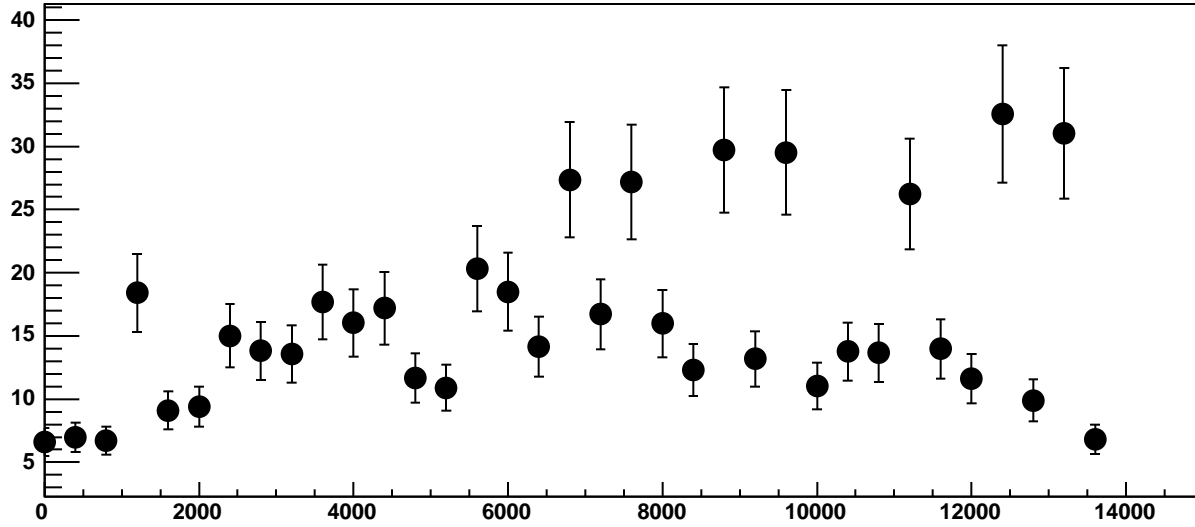


Chip 0, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

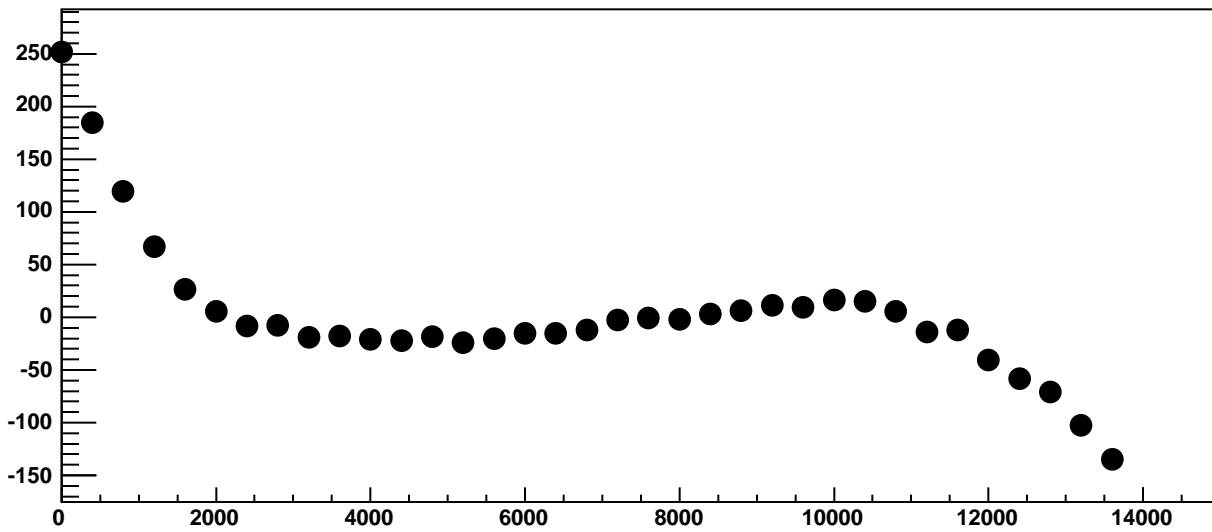
χ^2 / ndf 844.4 / 23
p0 -239.1 ± 1.344
p1 0.1735 ± 0.0002119



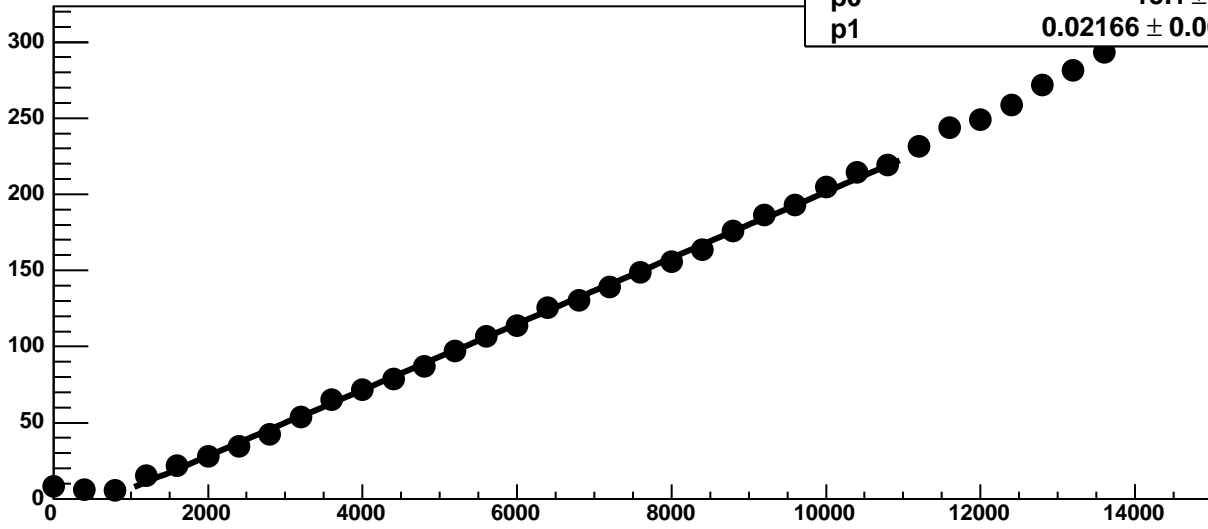
Chip 0, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

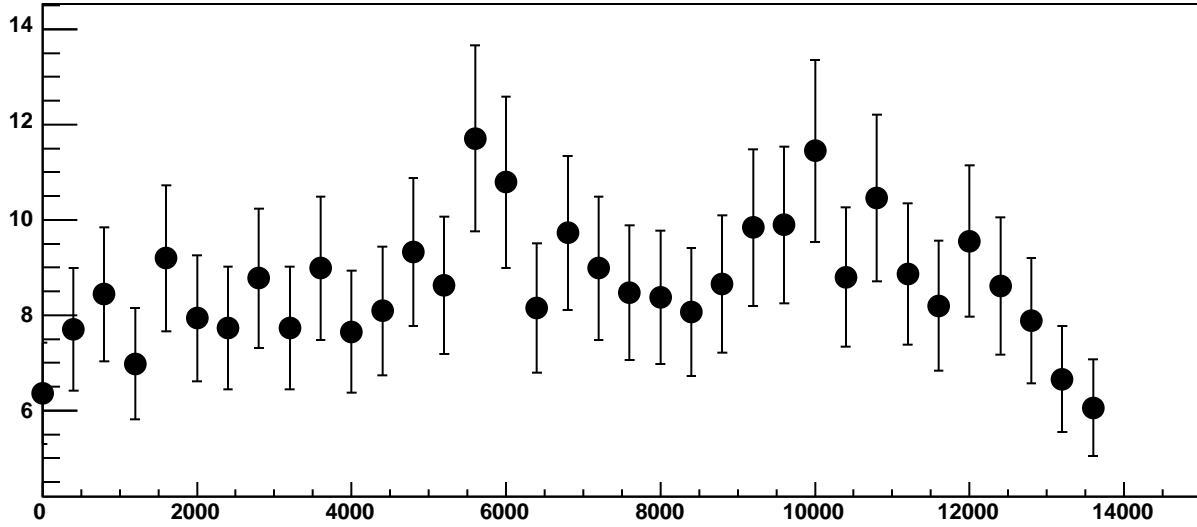


Chip 0, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

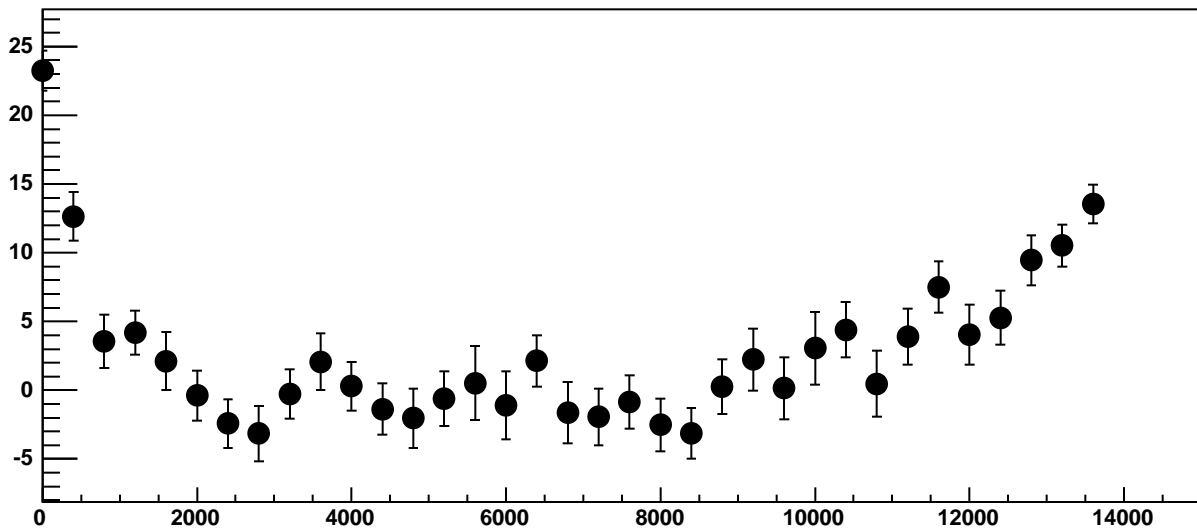


χ^2 / ndf 29.65 / 23
p0 -15.1 ± 0.8785
p1 0.02166 ± 0.0001389

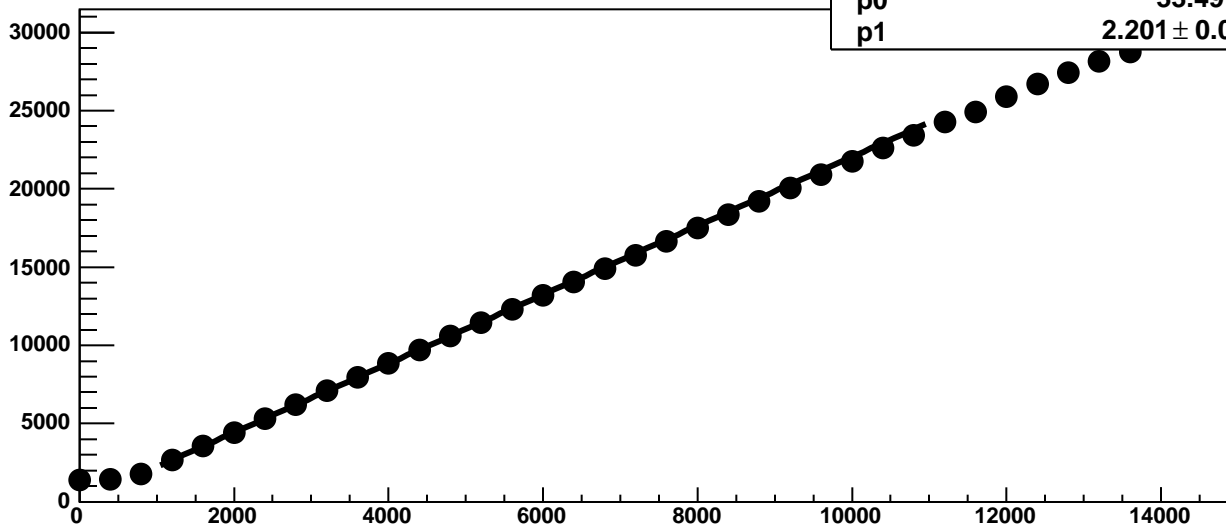
Chip 0, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

476 / 23

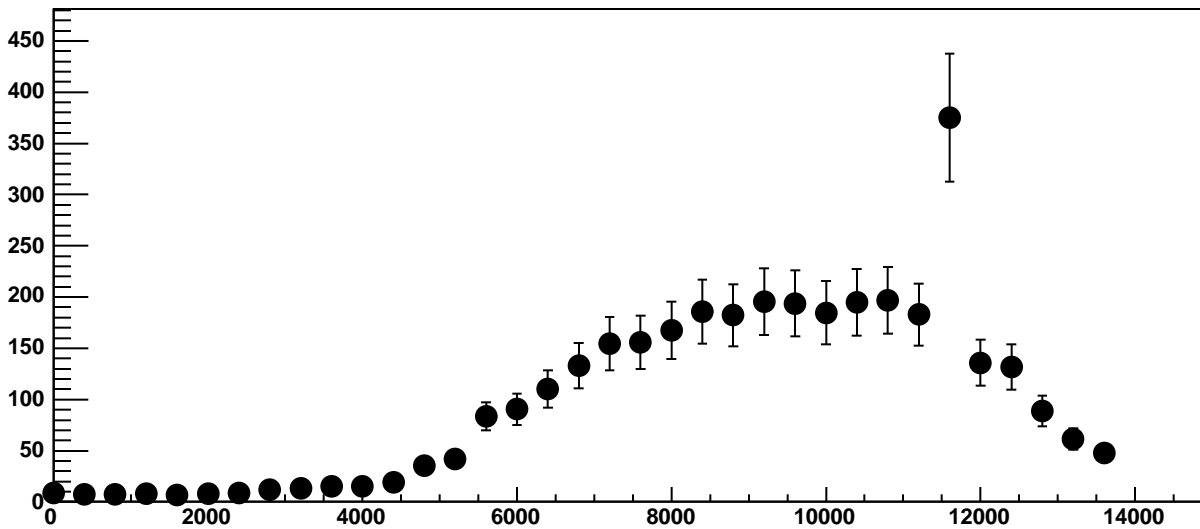
p0

33.49 ± 1.848

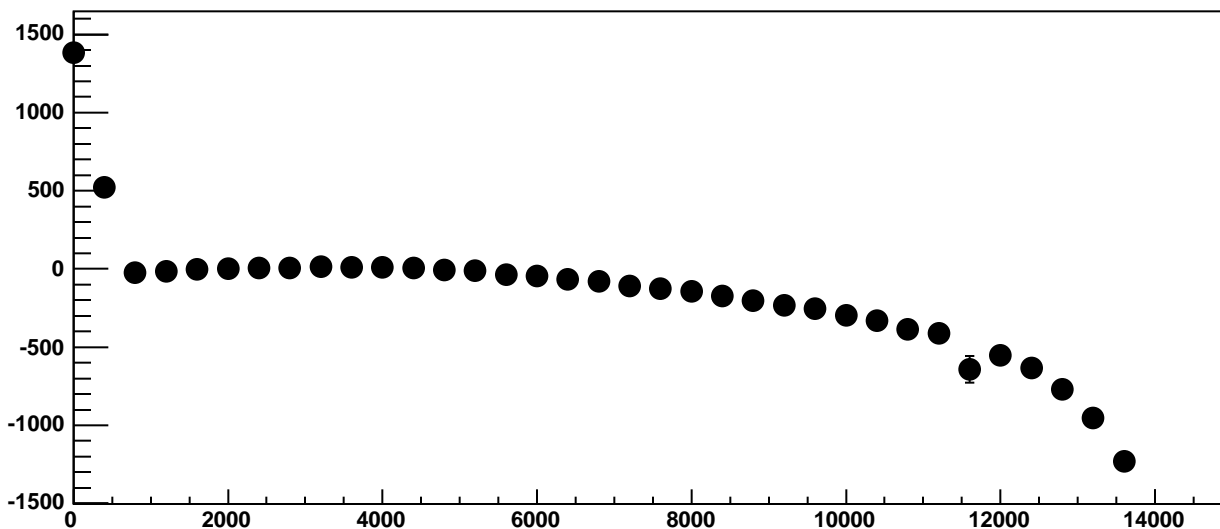
p1

2.201 ± 0.0007355

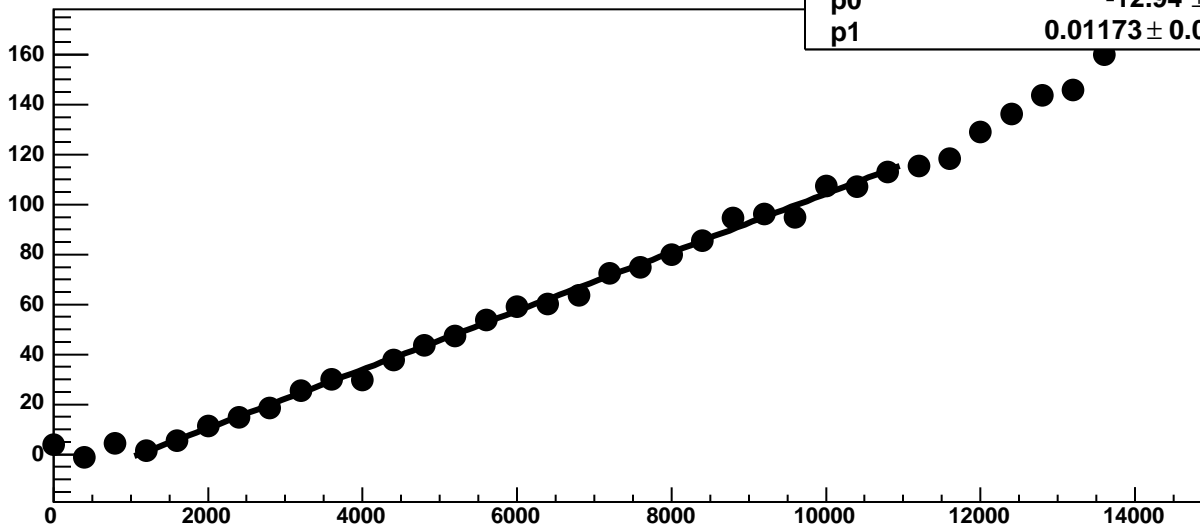
Chip 0, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC

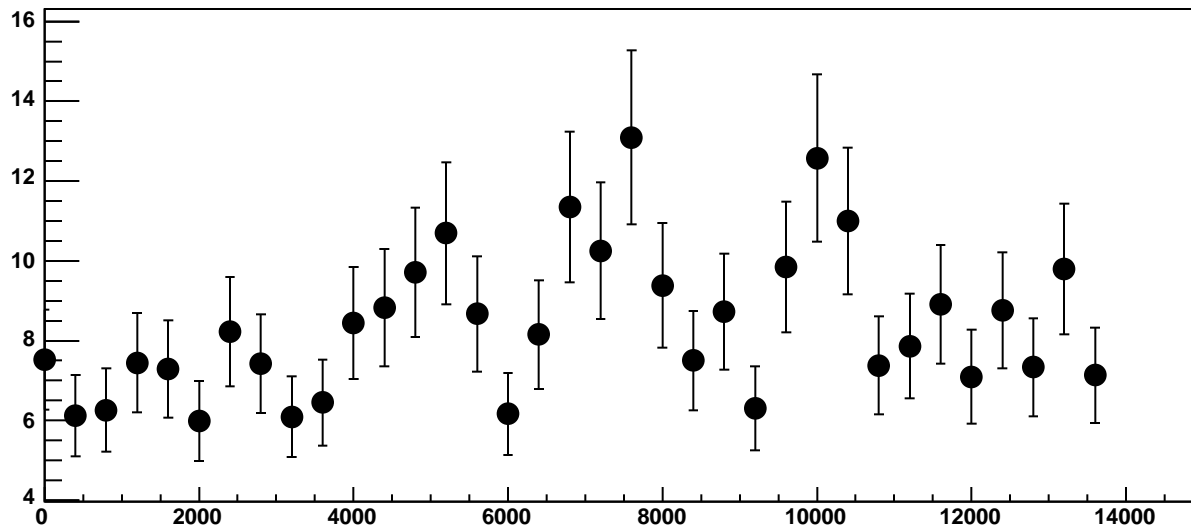


Chip 0, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

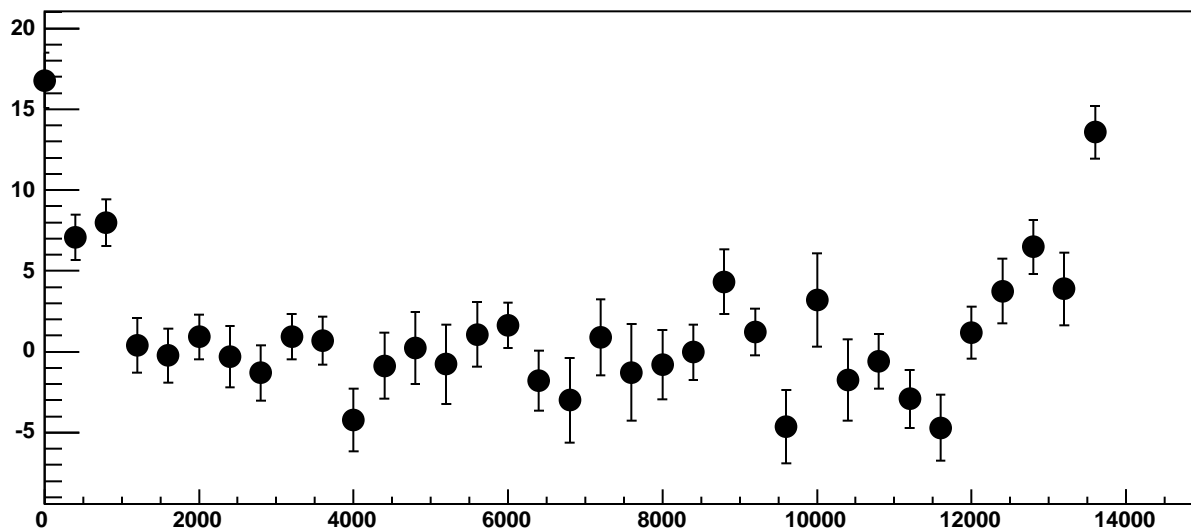


χ^2 / ndf 22.63 / 23
p0 -12.94 ± 0.7834
p1 0.01173 ± 0.0001258

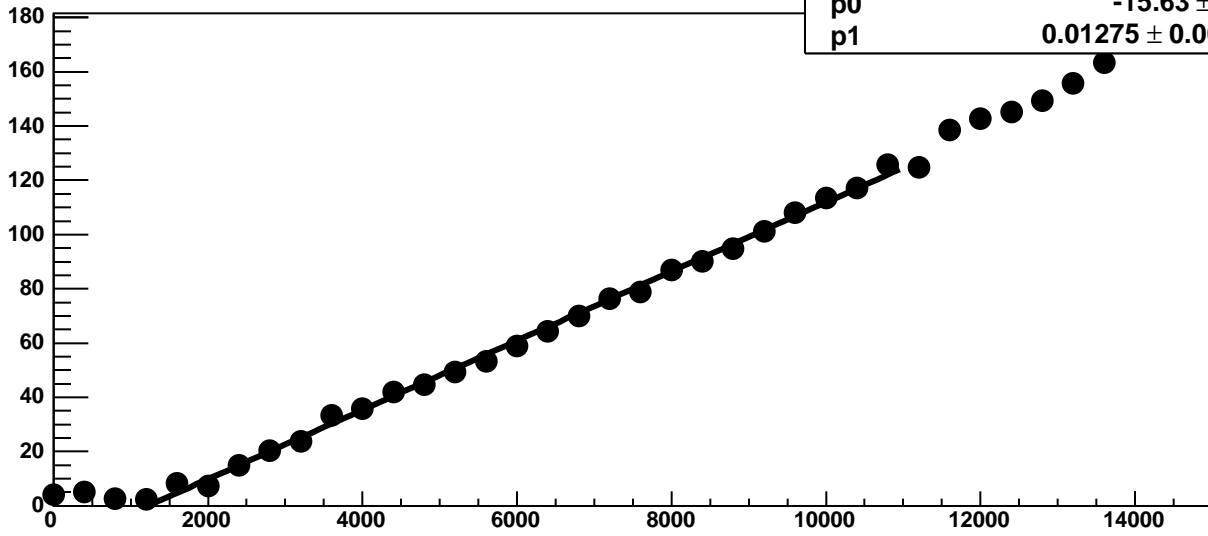
Chip 0, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

26.15 / 23

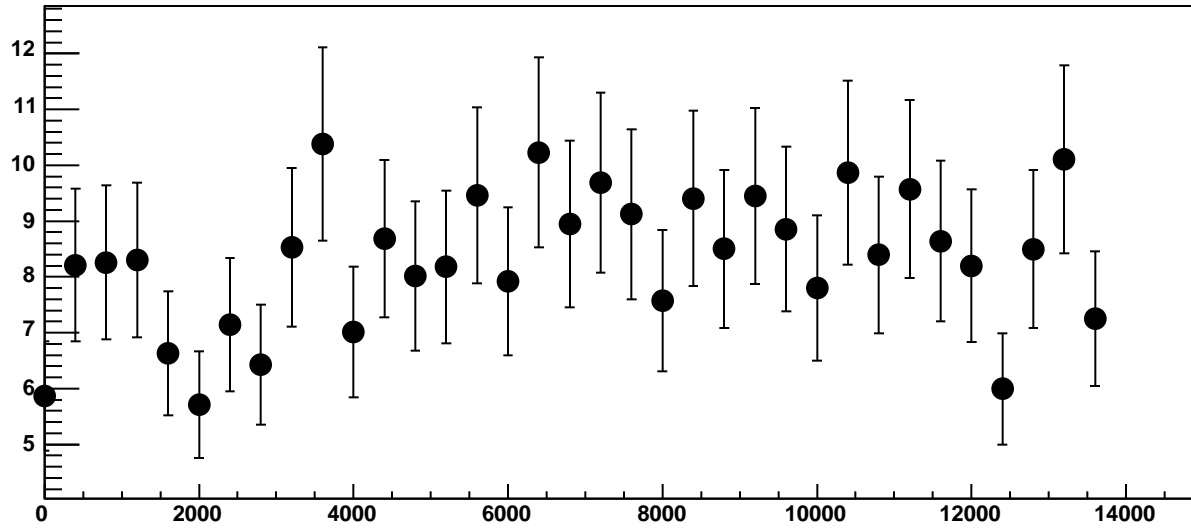
p0

-15.63 ± 0.7831

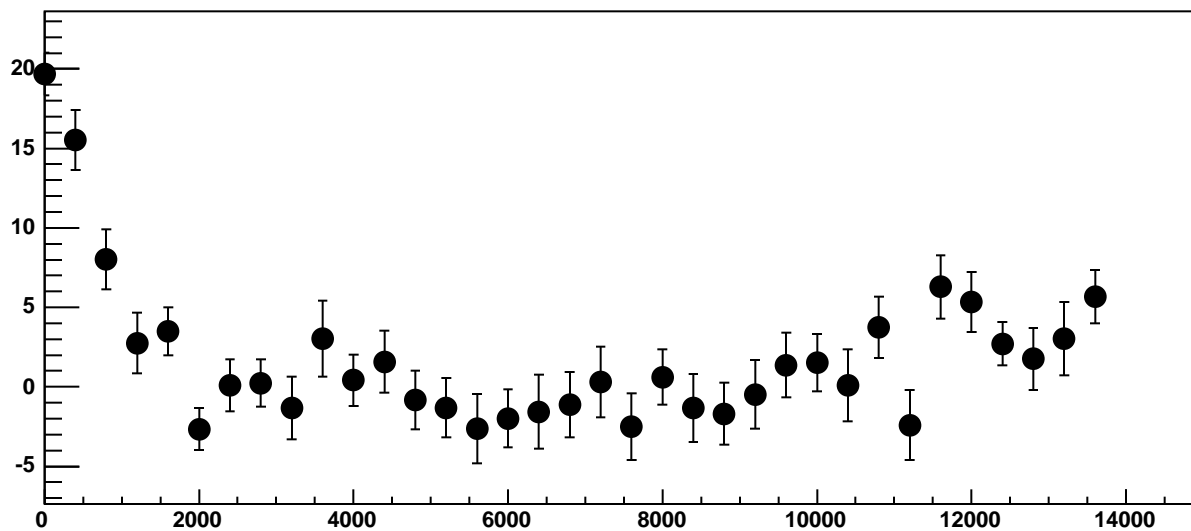
p1

0.01275 ± 0.0001254

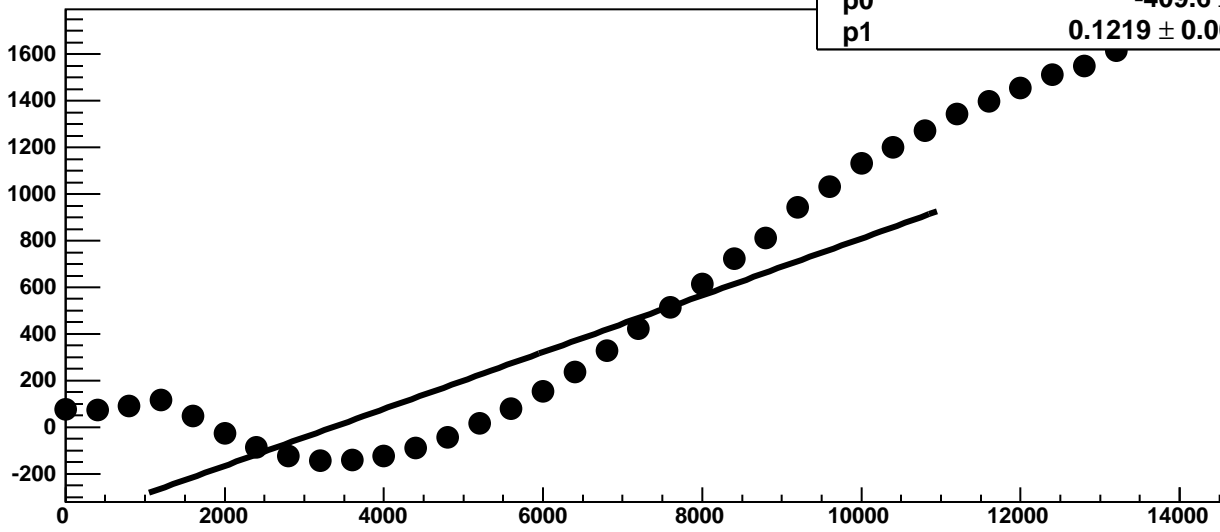
Chip 0, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



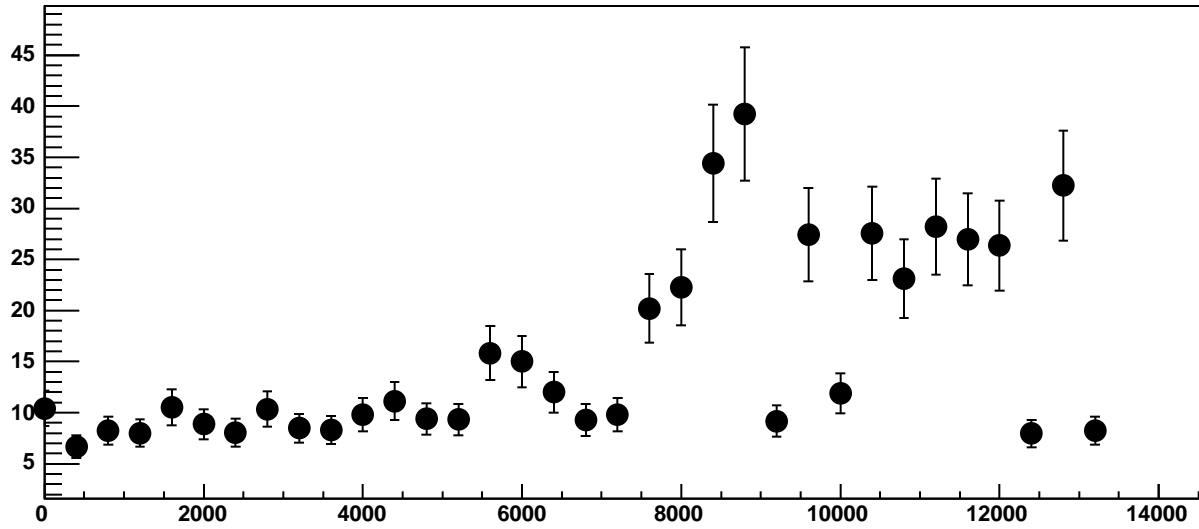
Chip 0, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



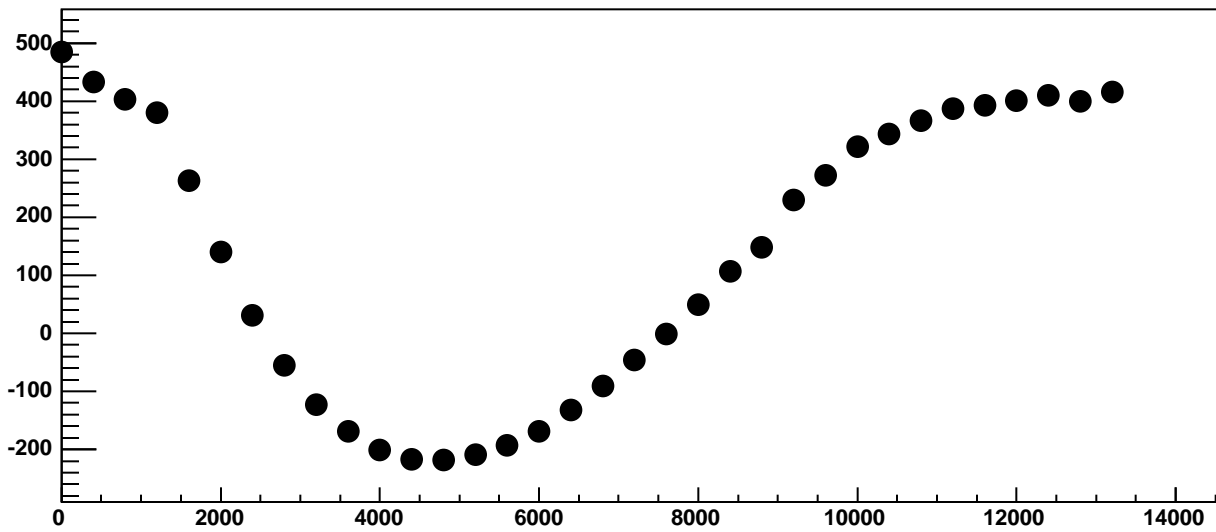
Chip 0, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC



Chip 0, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC

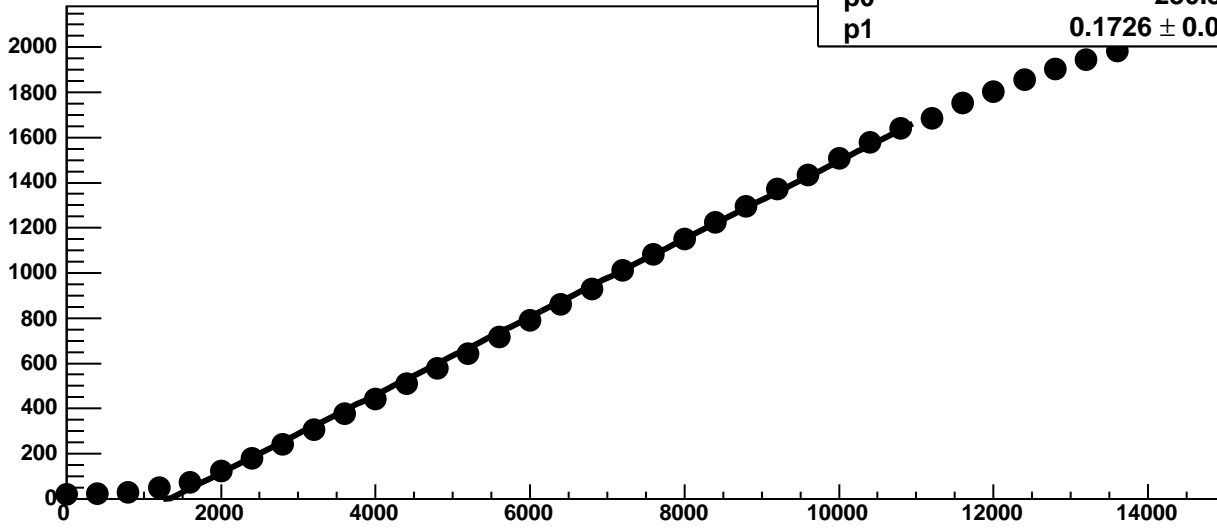


Chip 0, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC

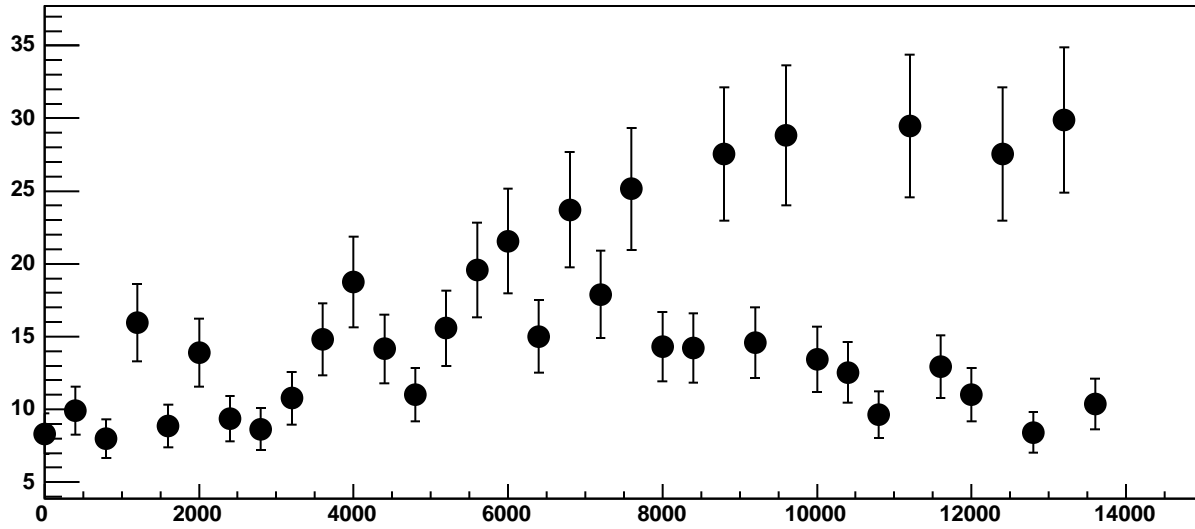


Chip 0, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

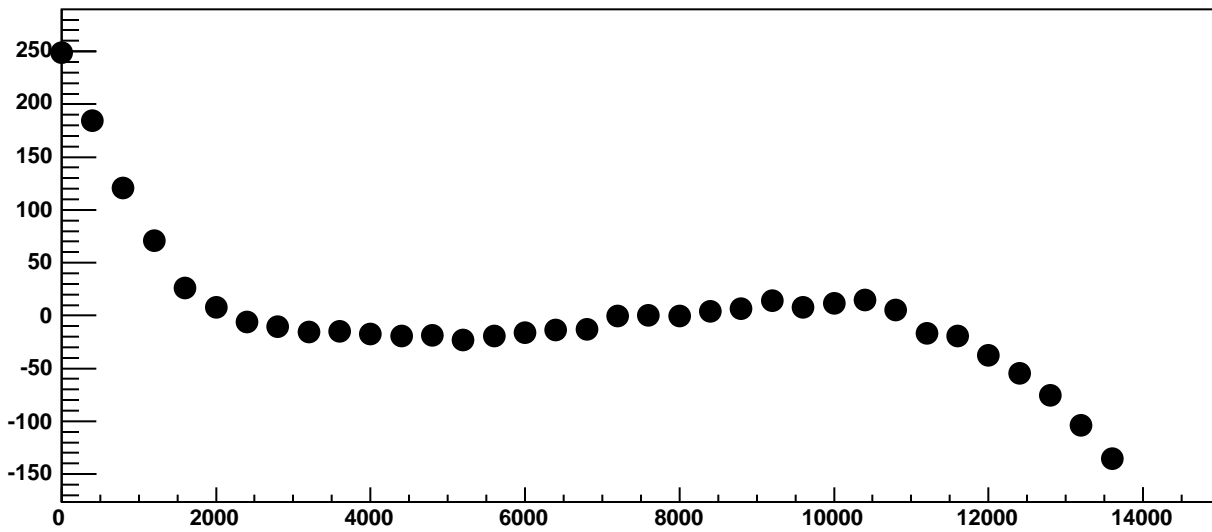
χ^2 / ndf 915.9 / 23
p0 -230.3 ± 1.22
p1 0.1726 ± 0.0001957



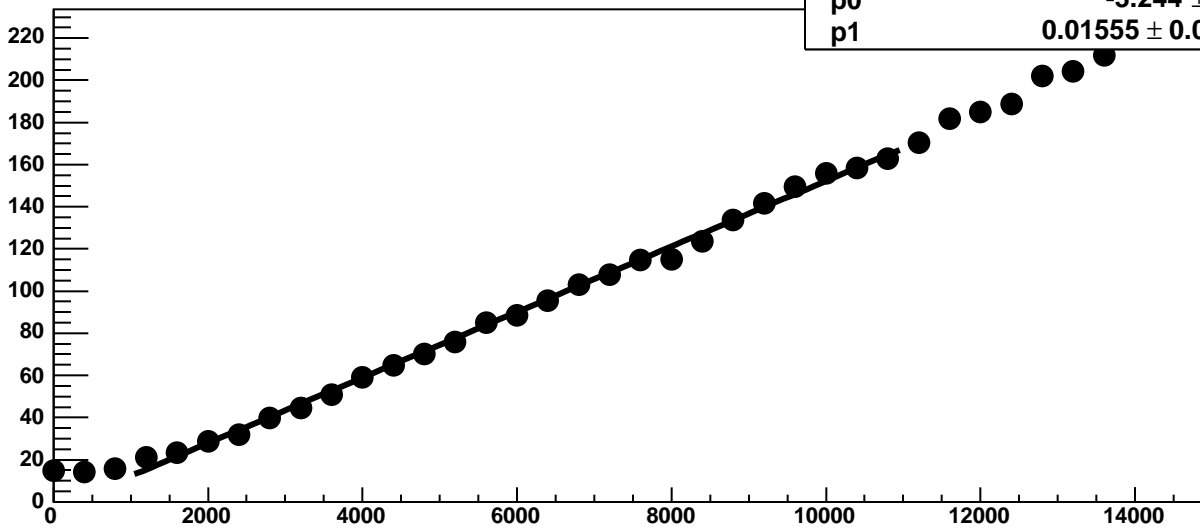
Chip 0, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



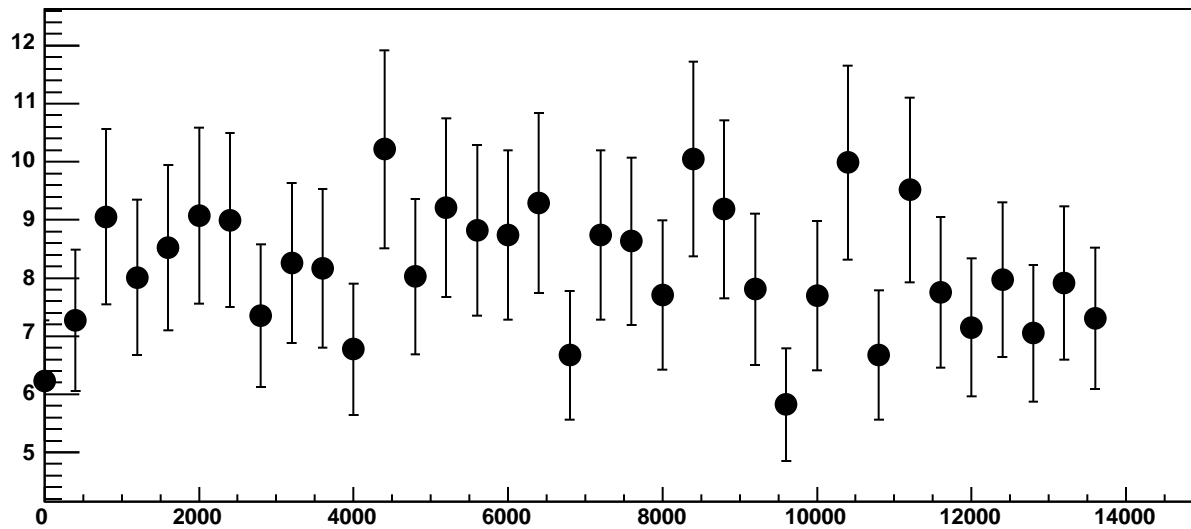
Chip 0, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



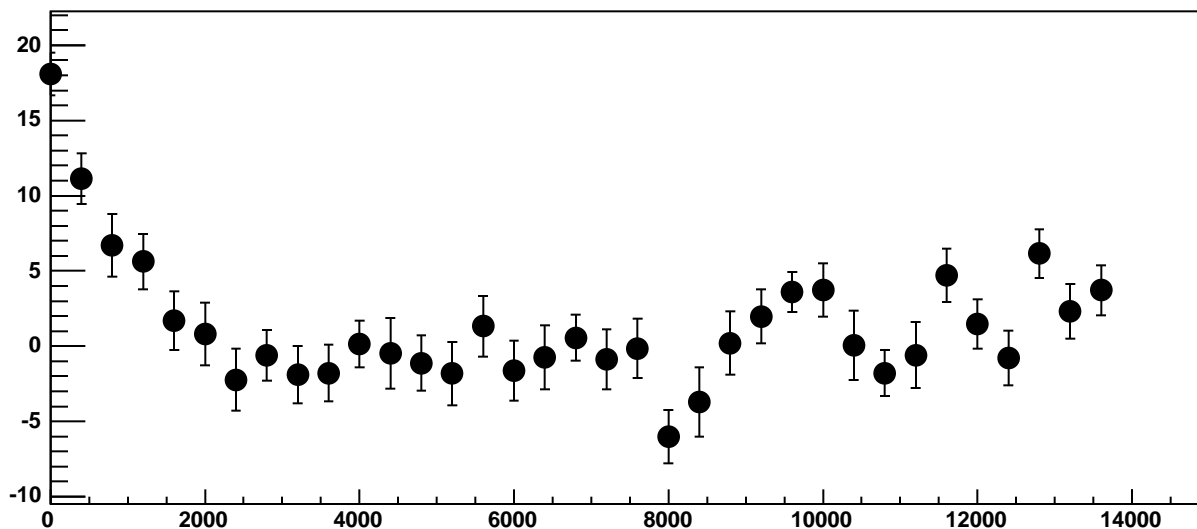
Chip 0, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



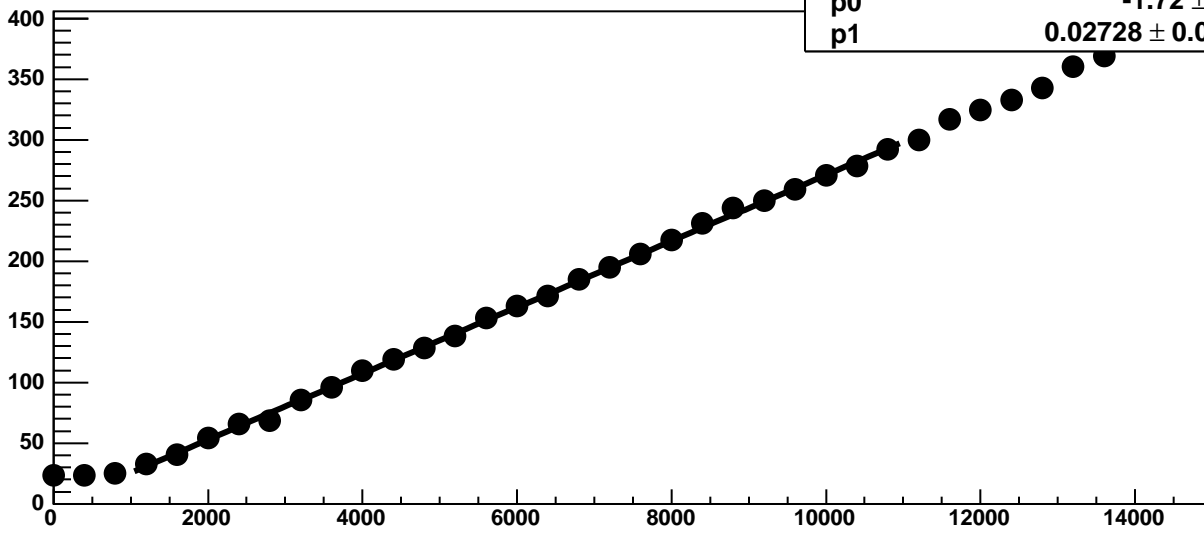
Chip 0, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

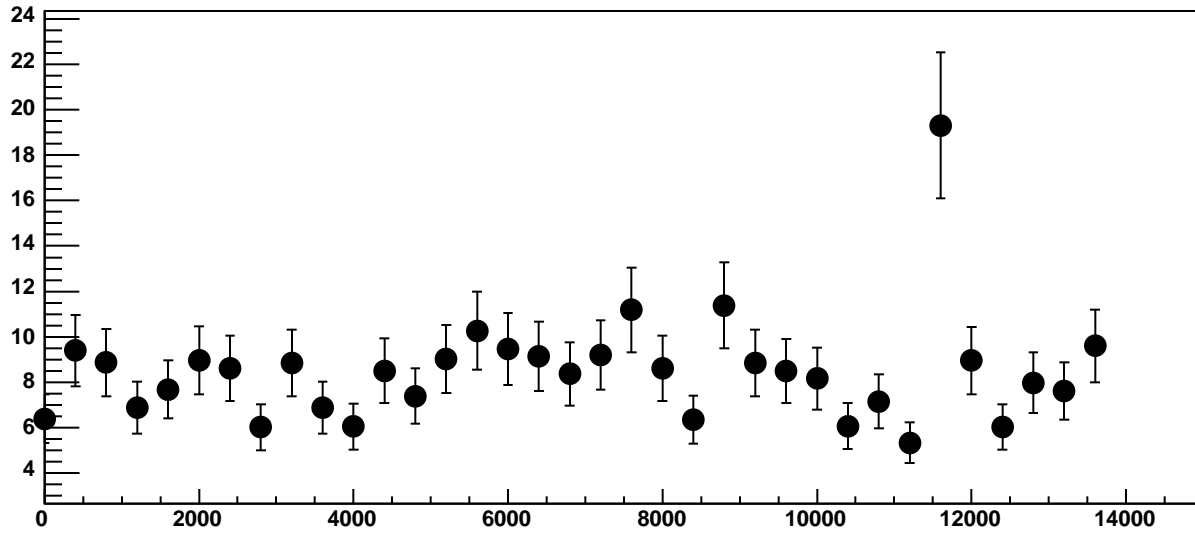


Chip 0, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

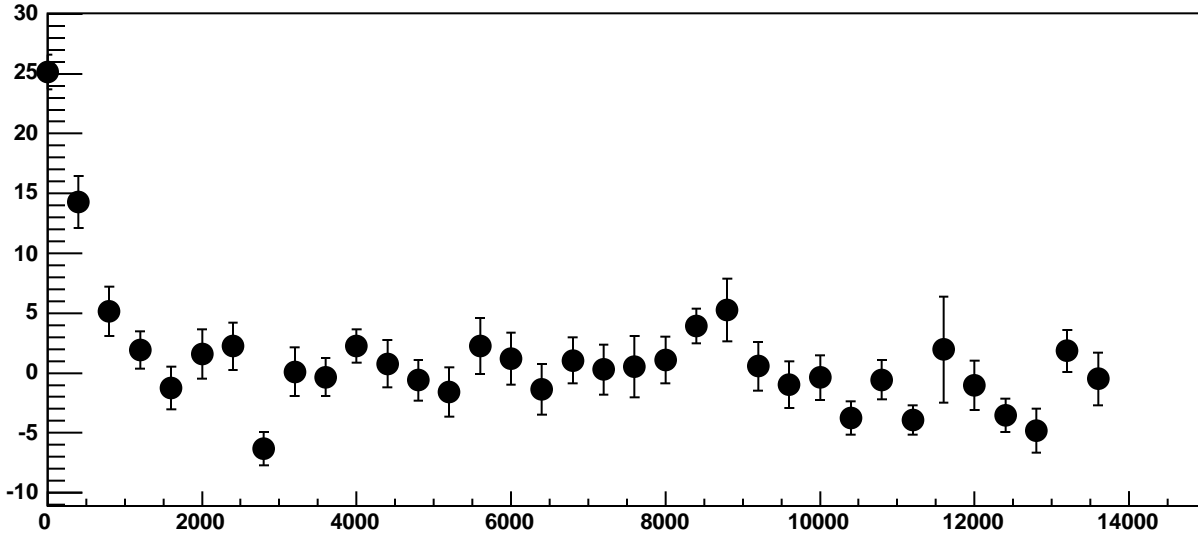


χ^2 / ndf 49.64 / 23
p0 -1.72 ± 0.7902
p1 0.02728 ± 0.0001192

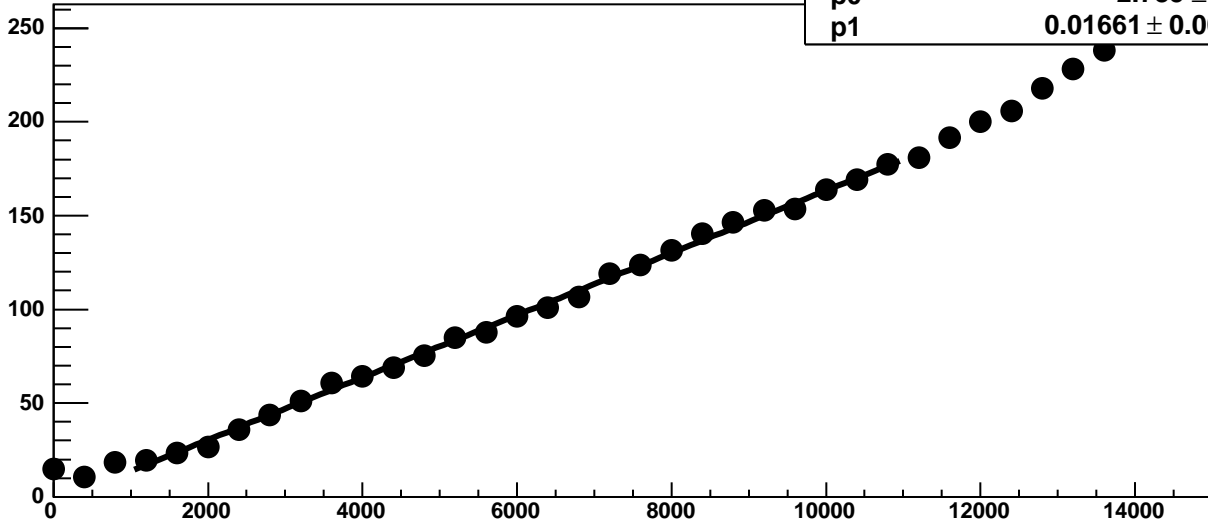
Chip 0, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

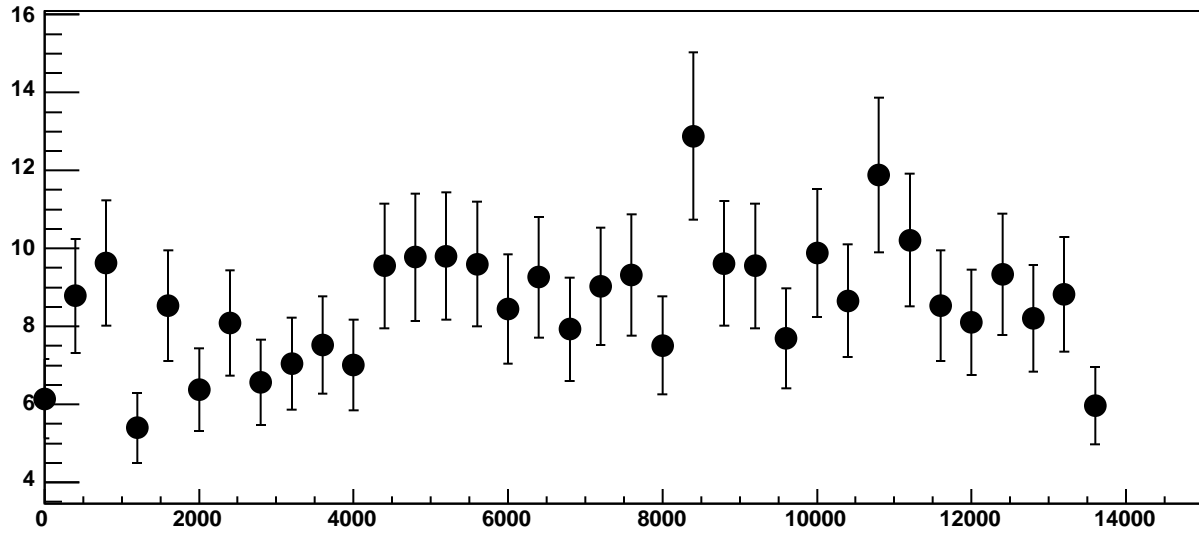


Chip 0, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

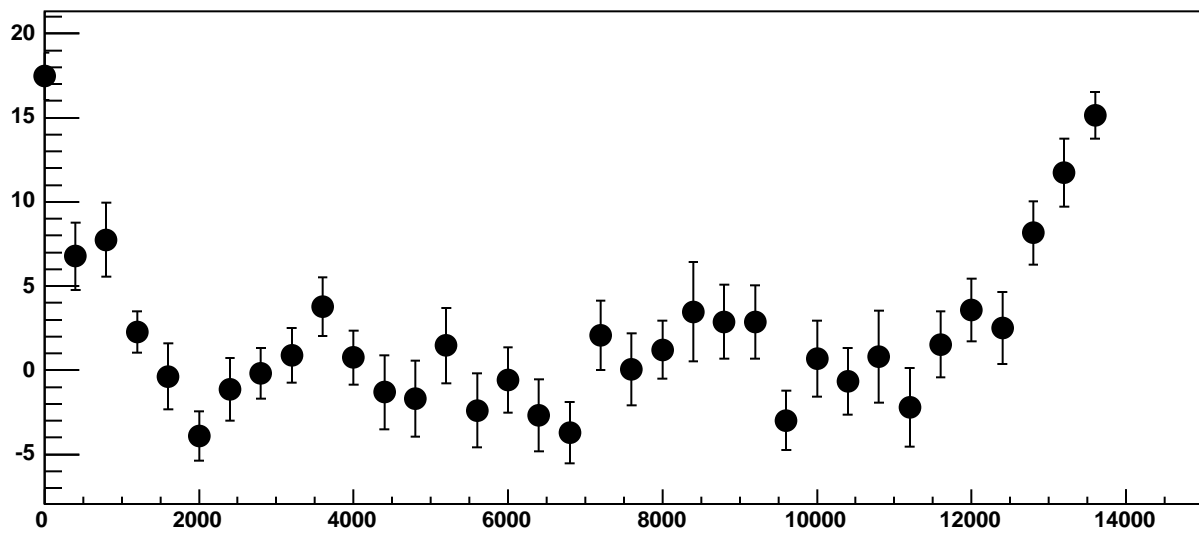


χ^2 / ndf 34.04 / 23
p0 -2.785 ± 0.7688
p1 0.01661 ± 0.0001274

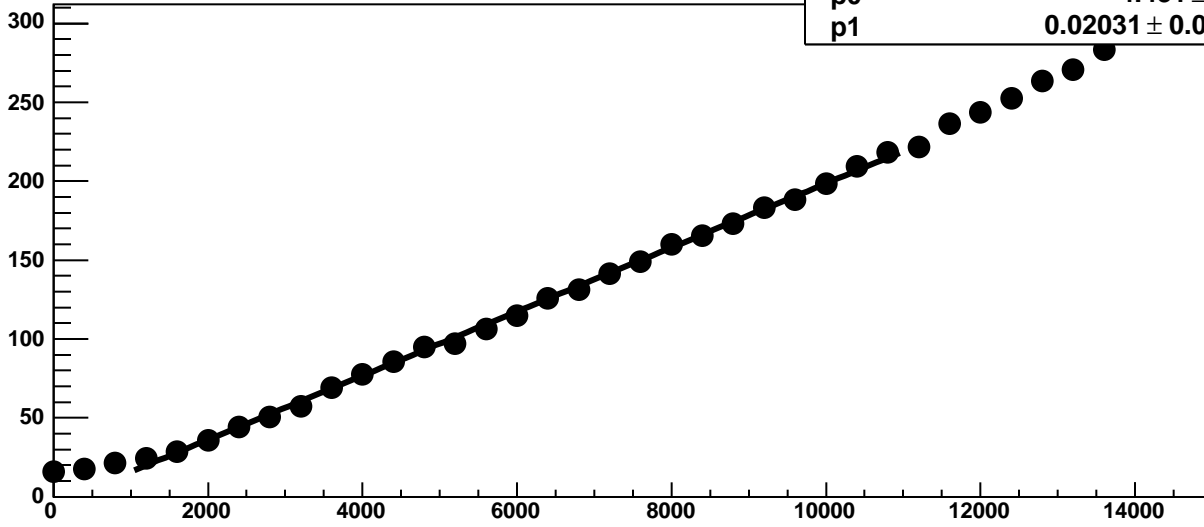
Chip 0, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

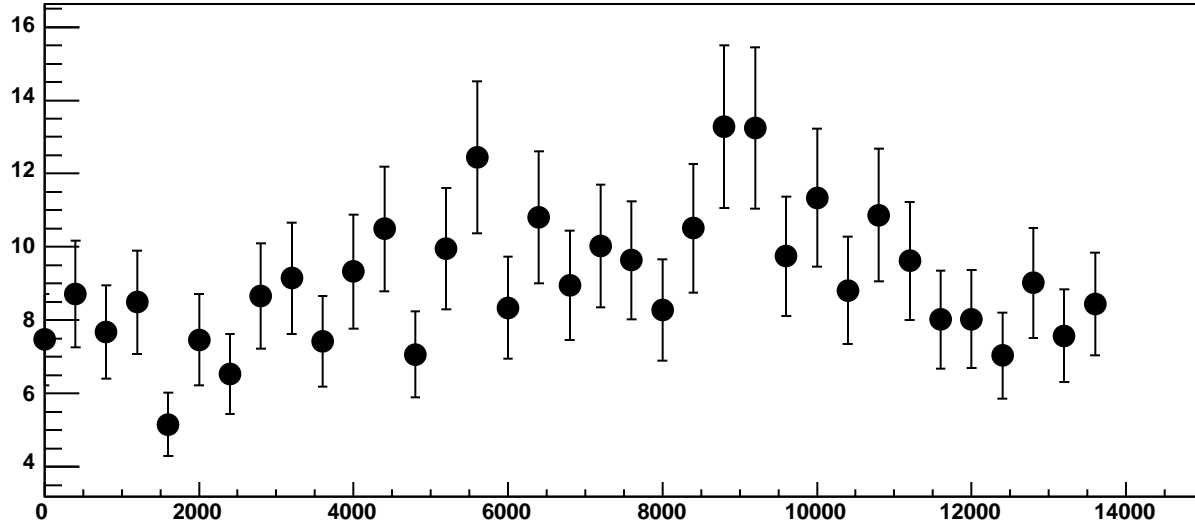


Chip 0, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

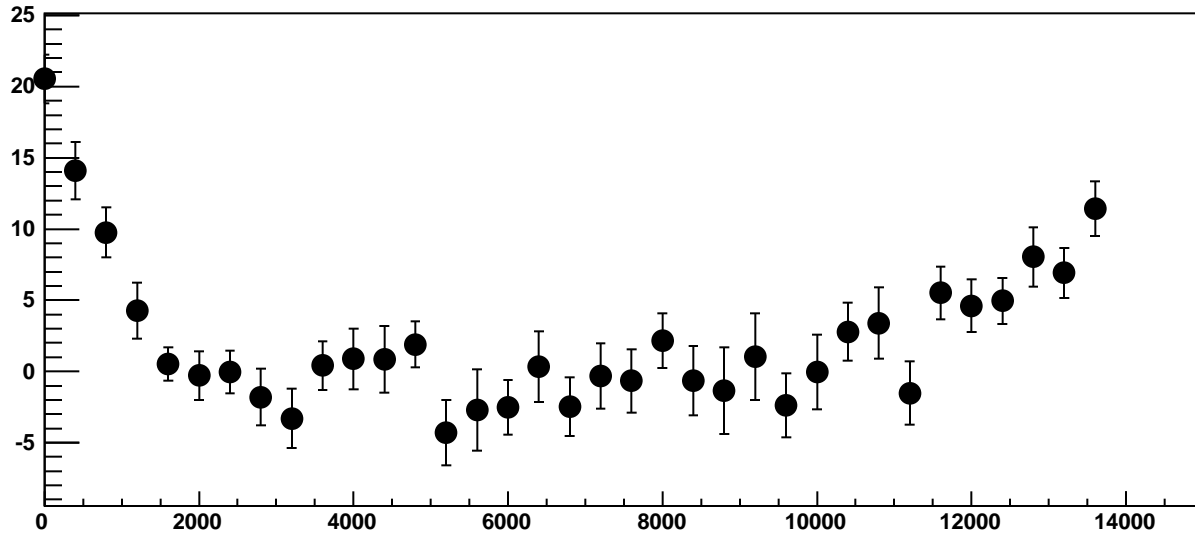


χ^2 / ndf 24.38 / 23
p0 -4.431 ± 0.8093
p1 0.02031 ± 0.0001372

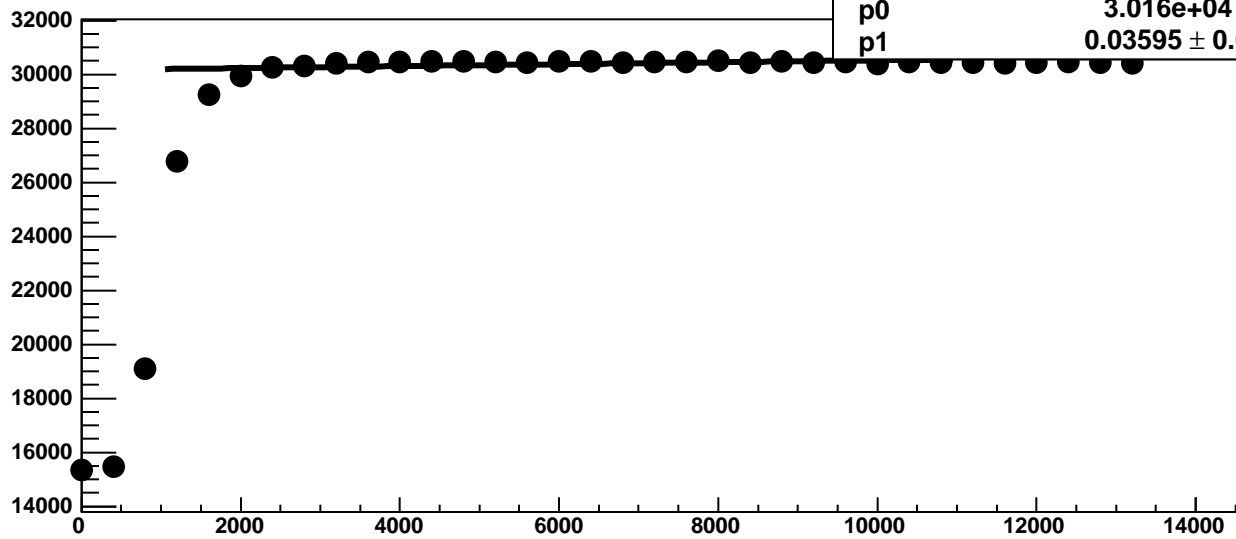
Chip 0, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



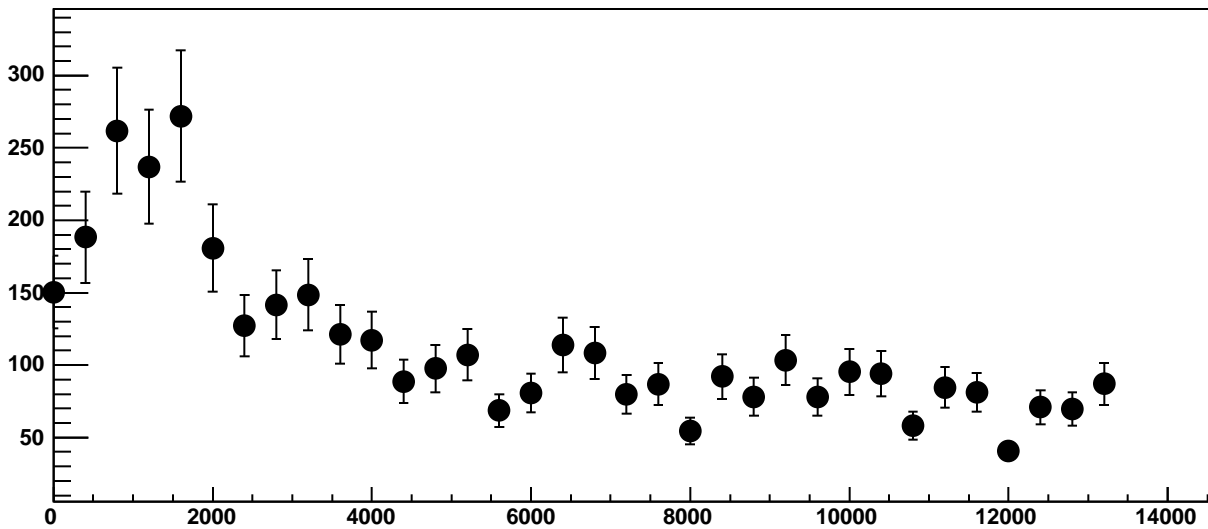
Chip 0, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



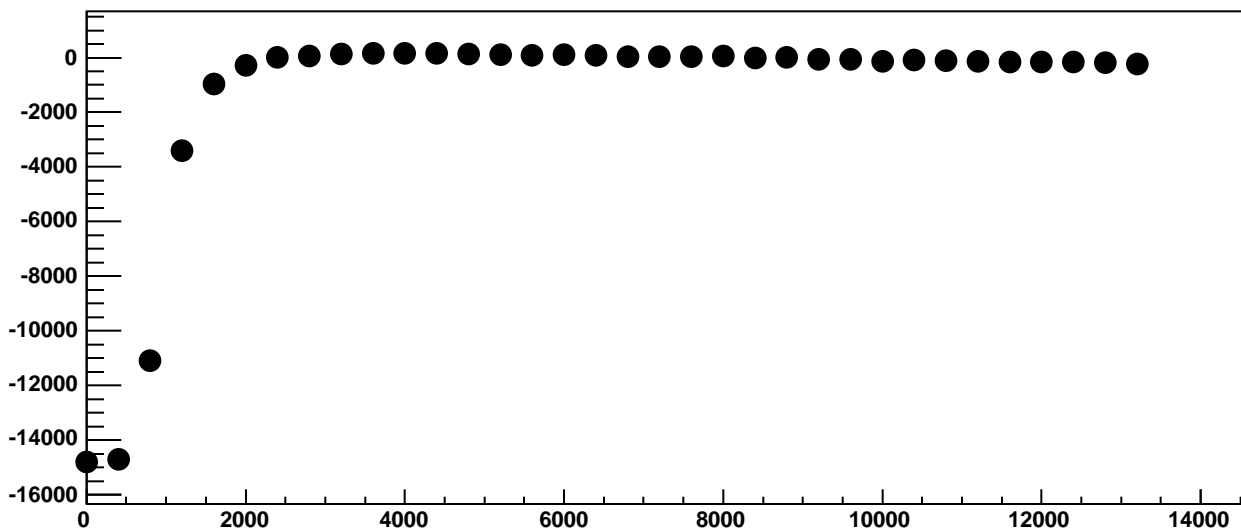
Chip 0, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC



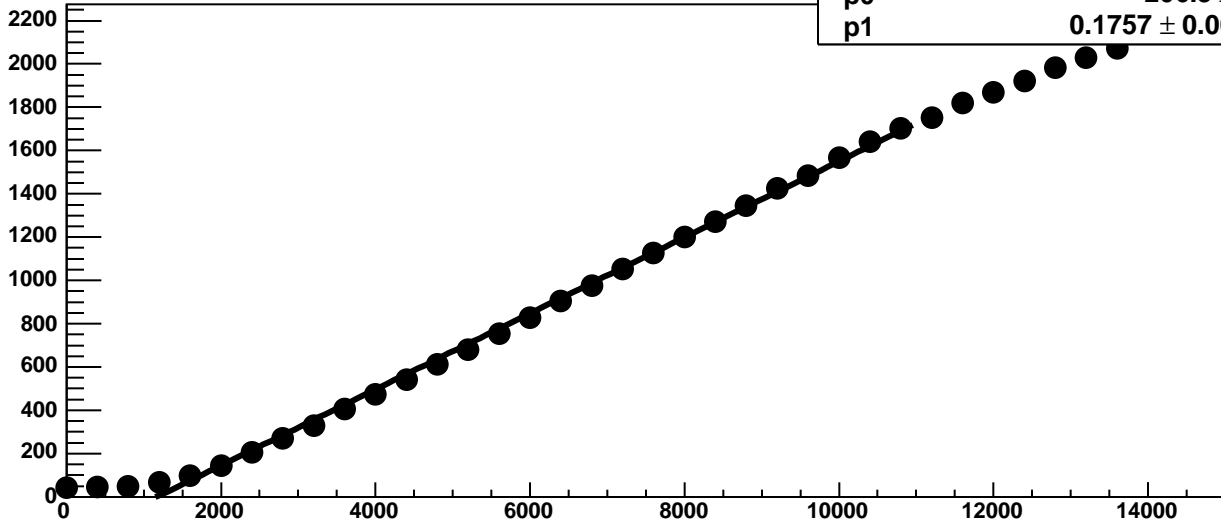
Chip 0, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC



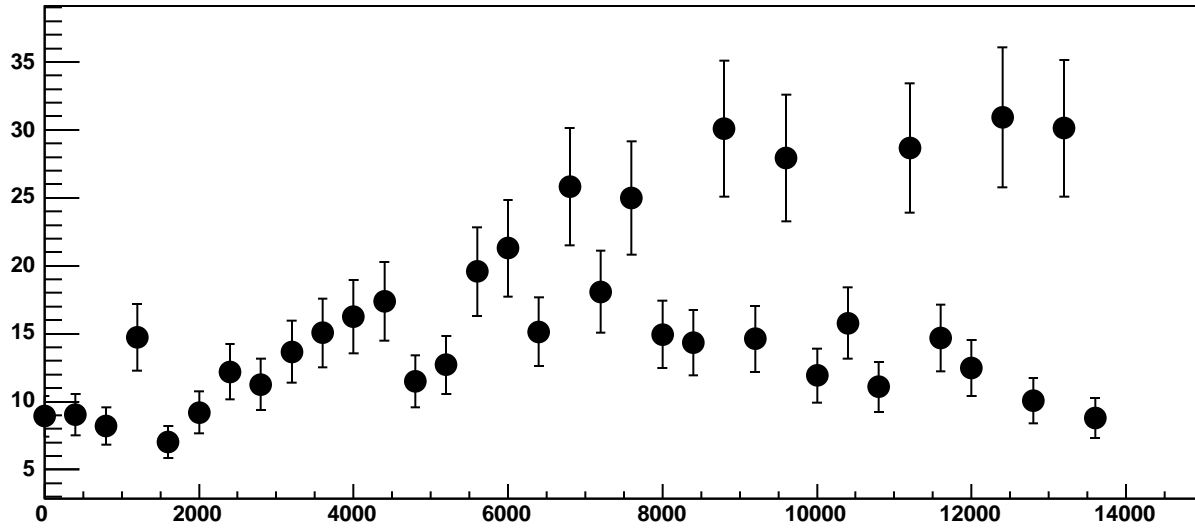
Chip 0, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC



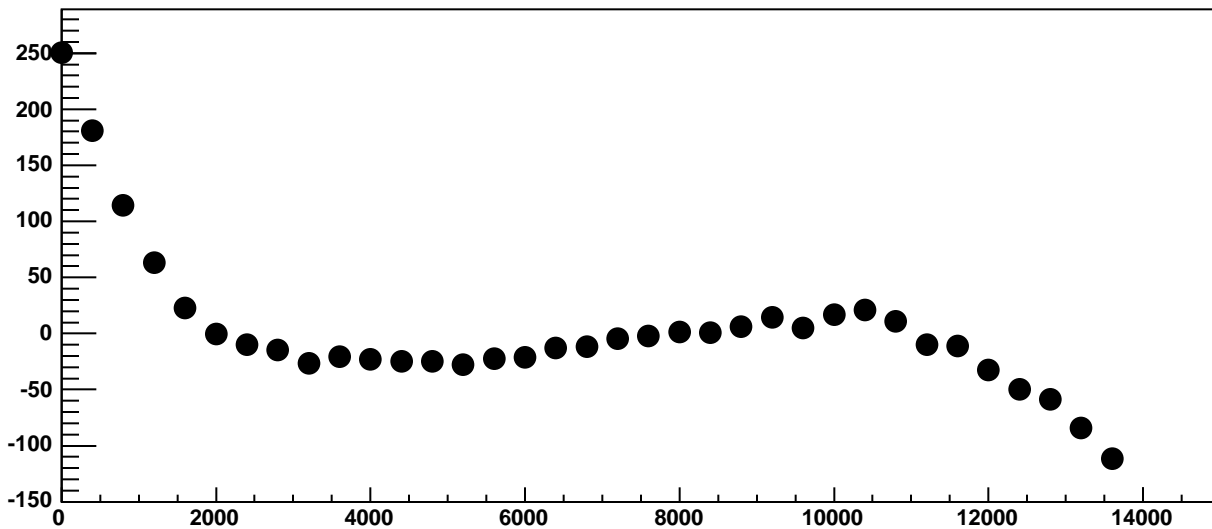
Chip 0, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



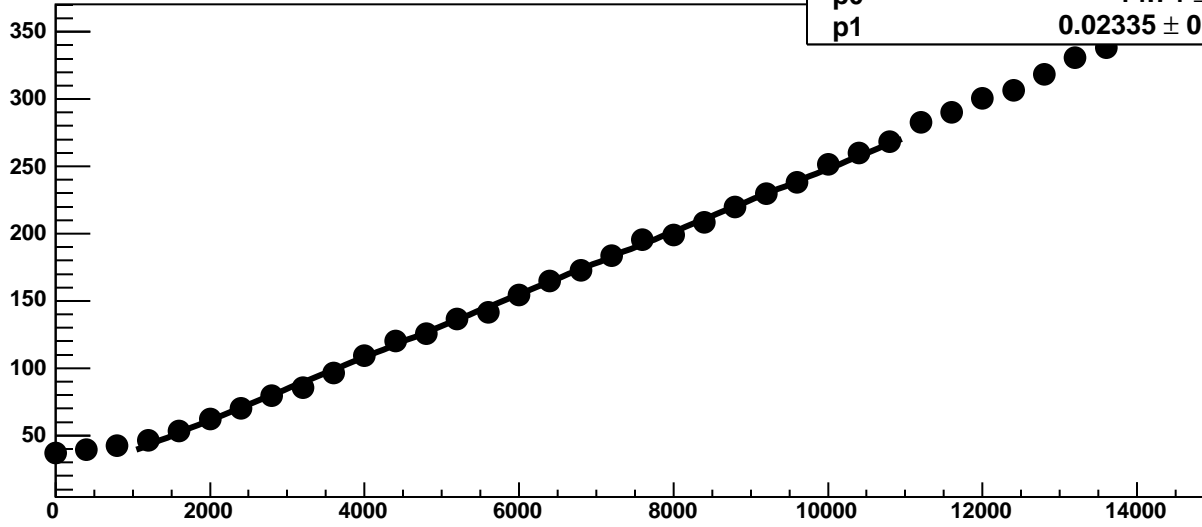
Chip 0, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

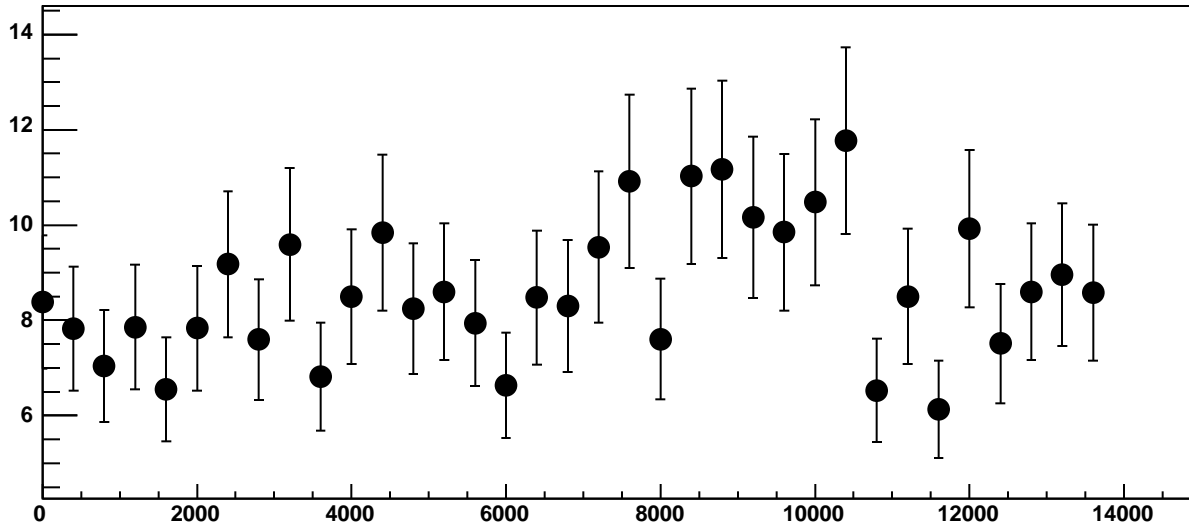


Chip 0, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

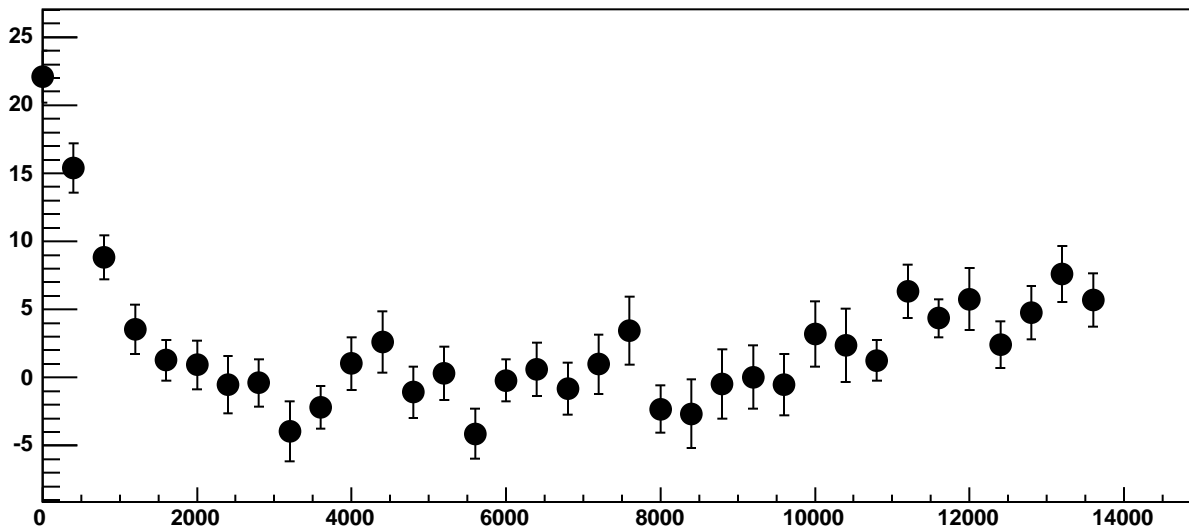


χ^2 / ndf 25.81 / 23
p0 14.74 ± 0.8414
p1 0.02335 ± 0.000133

Chip 0, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

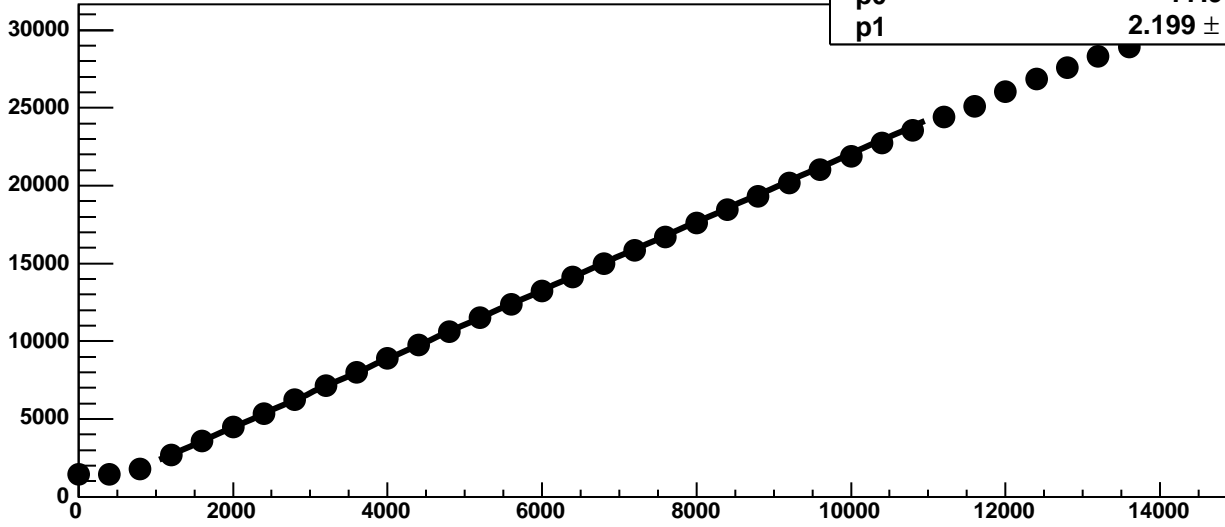


Chip 0, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

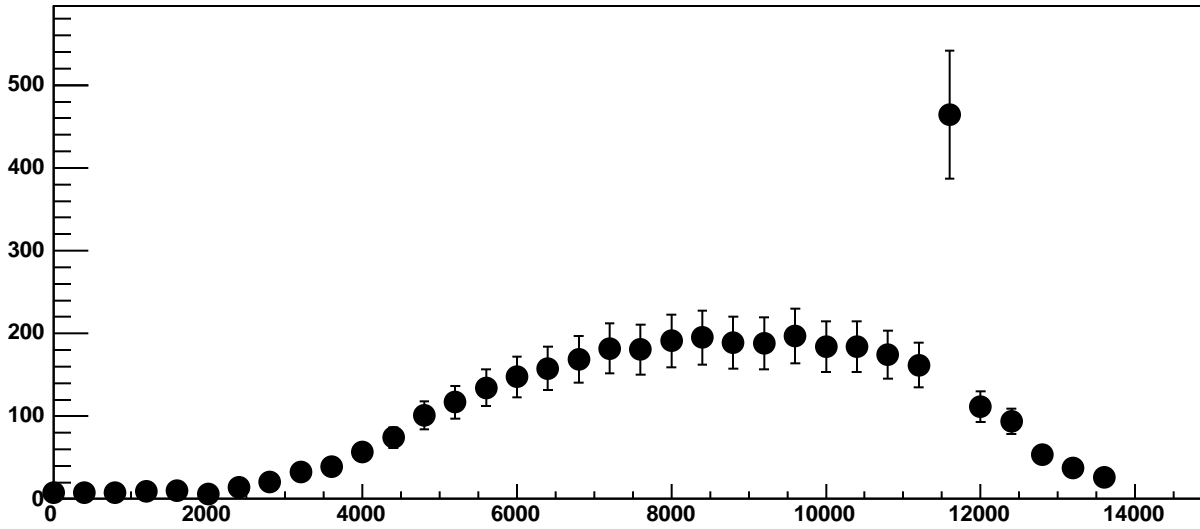


Chip 0, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC

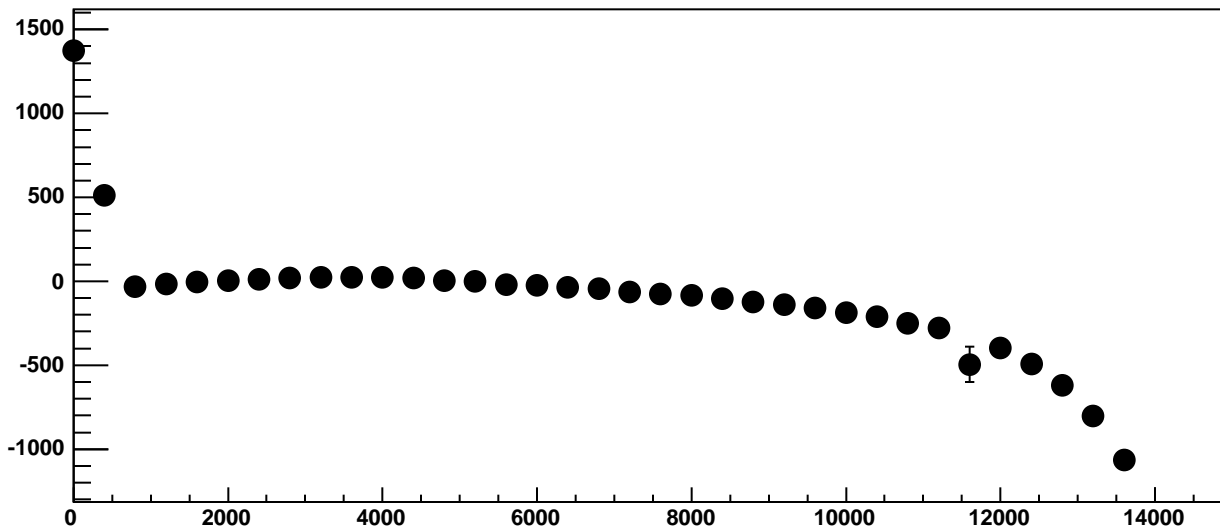
χ^2 / ndf 278.1 / 23
p0 77.9 ± 2.533
p1 2.199 ± 0.00121



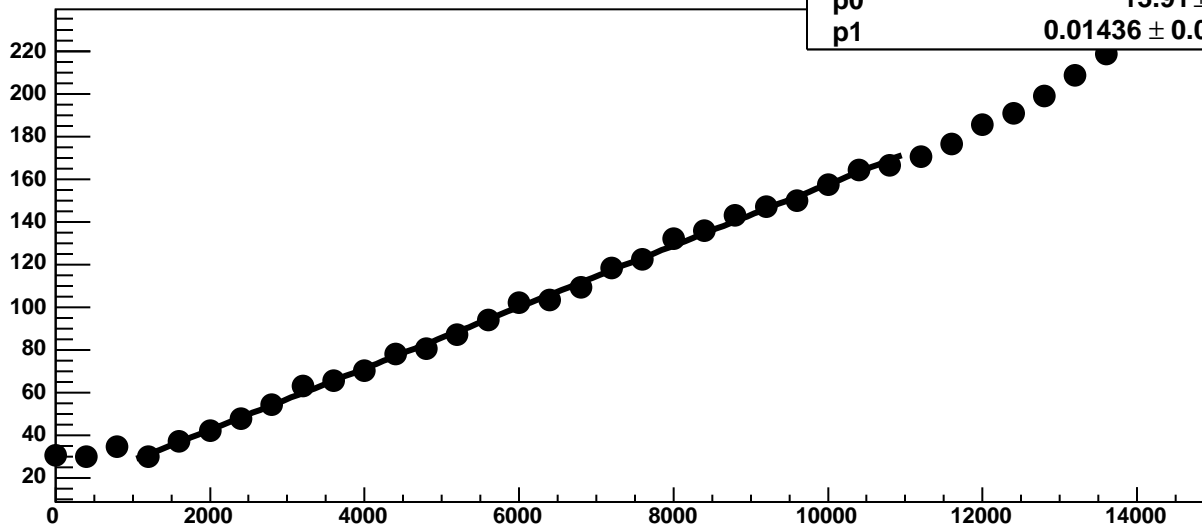
Chip 0, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

24.32 / 23

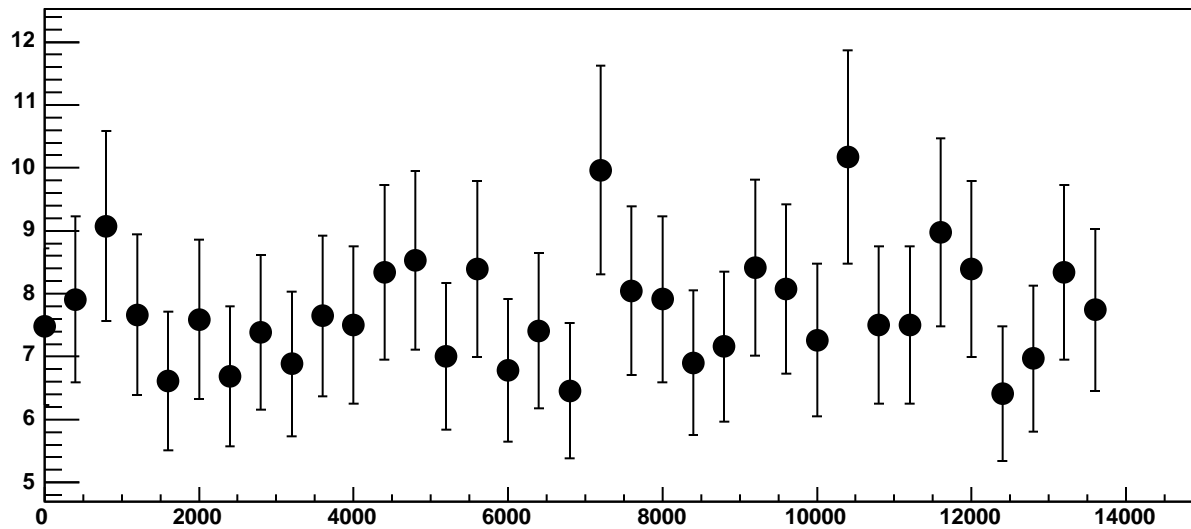
p0

13.91 ± 0.7803

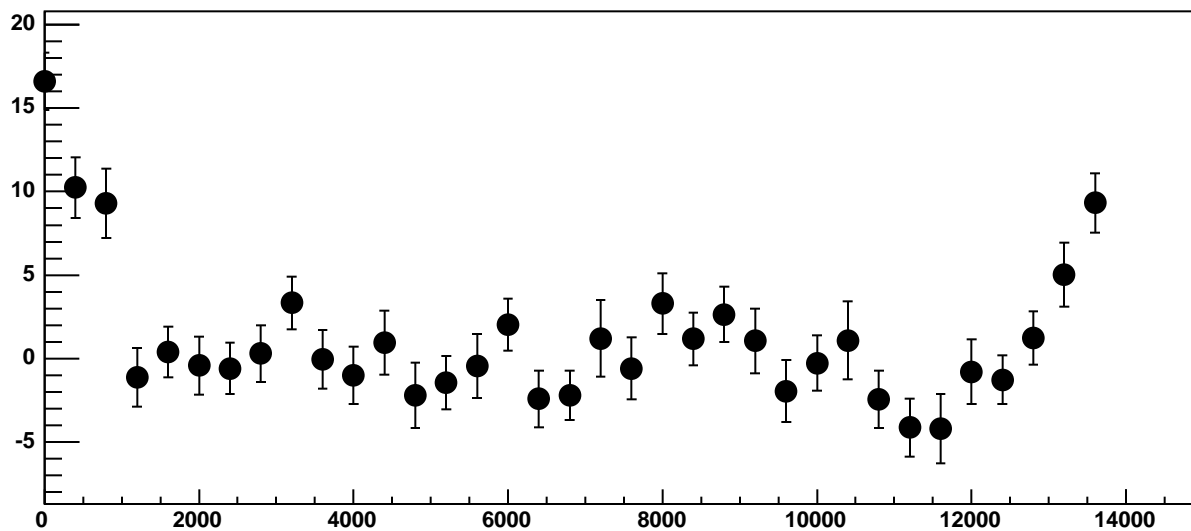
p1

0.01436 ± 0.0001204

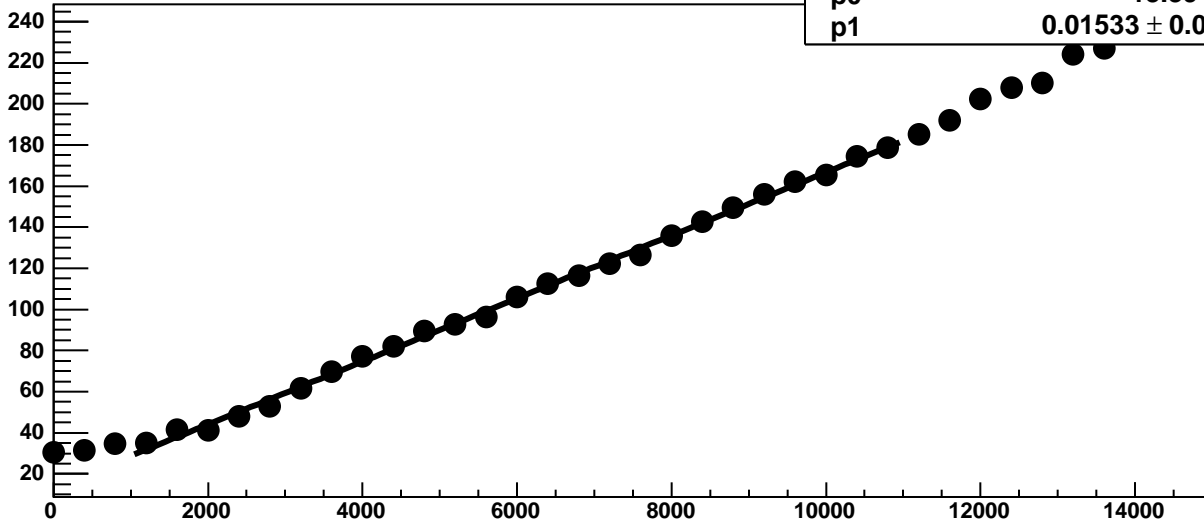
Chip 0, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

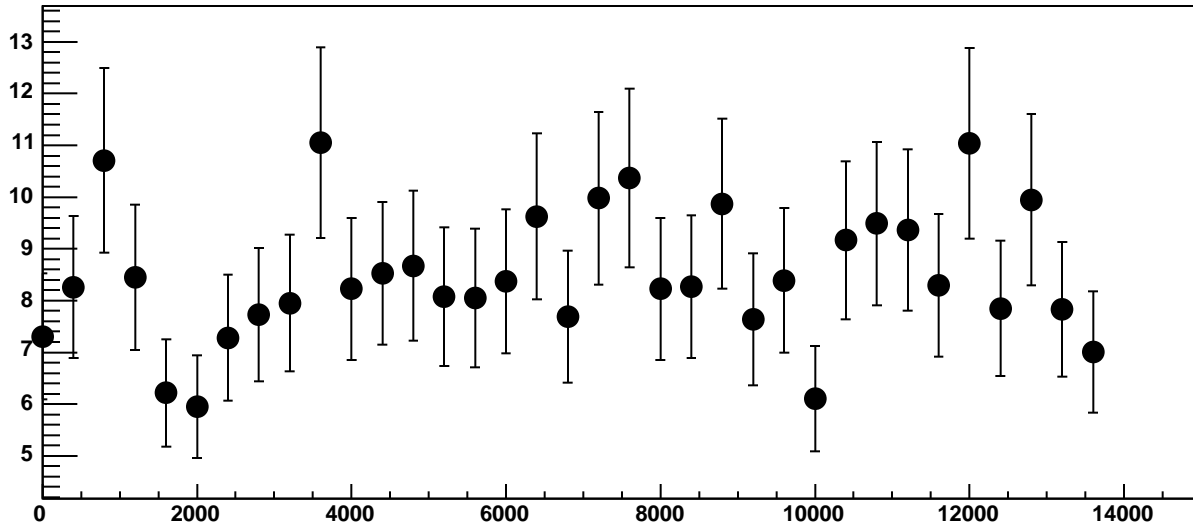


Chip 0, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

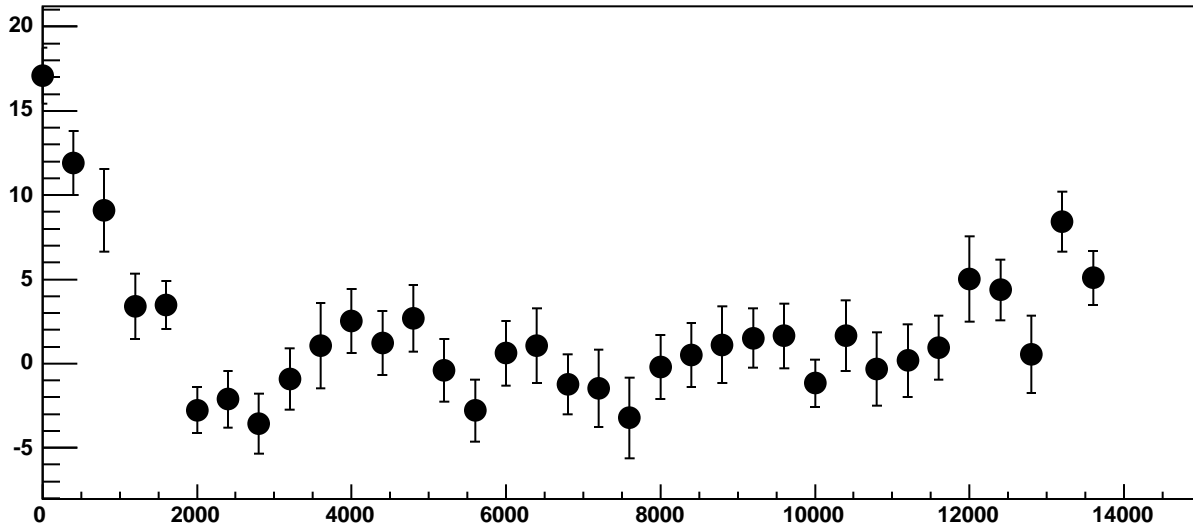


χ^2 / ndf 31.67 / 23
p0 13.39 ± 0.792
p1 0.01533 ± 0.0001225

Chip 0, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

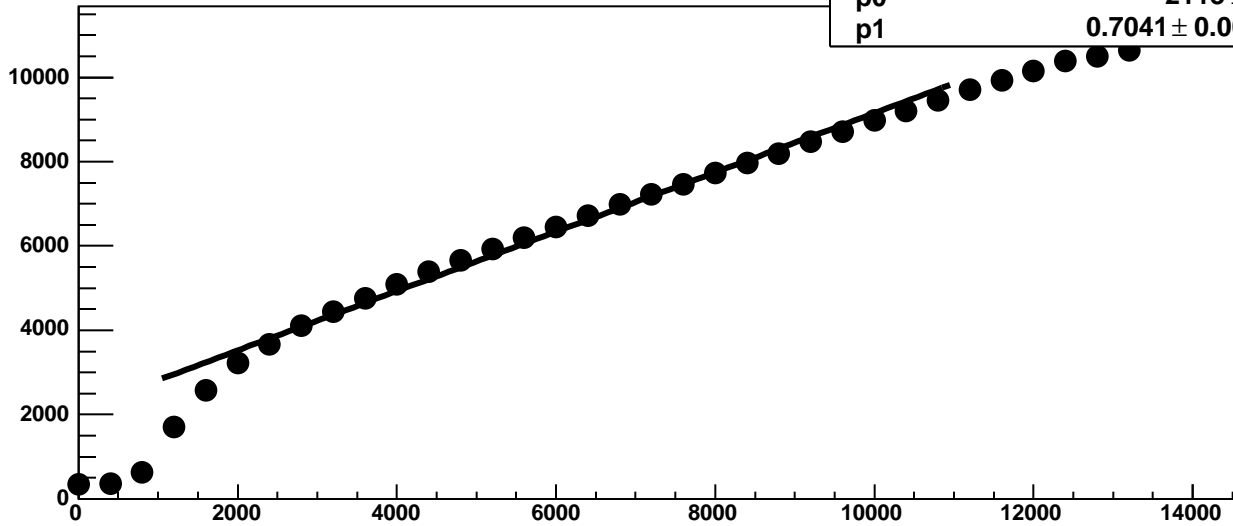


Chip 0, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

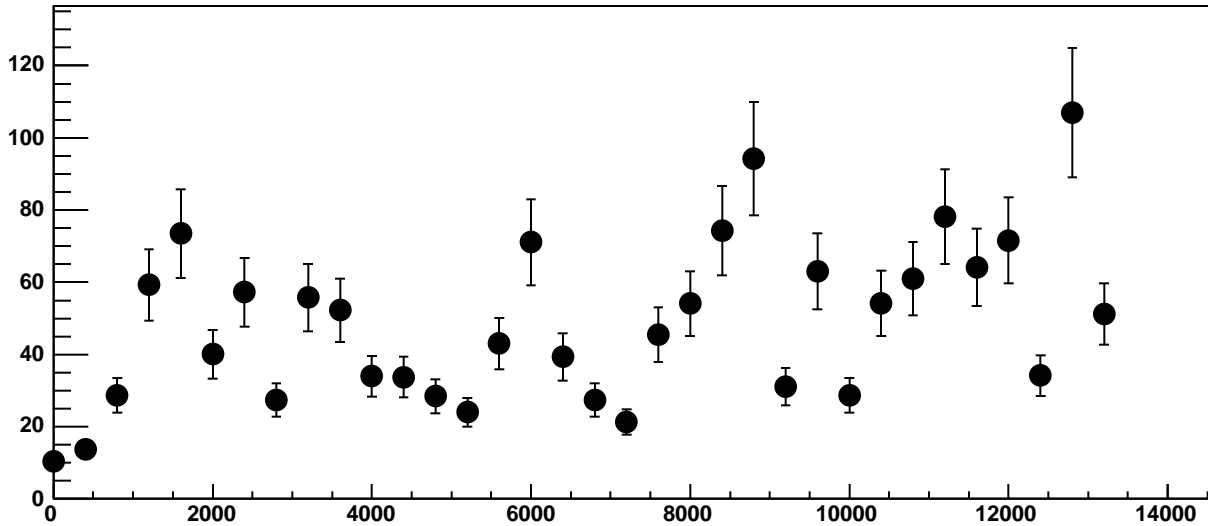


Chip 0, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC

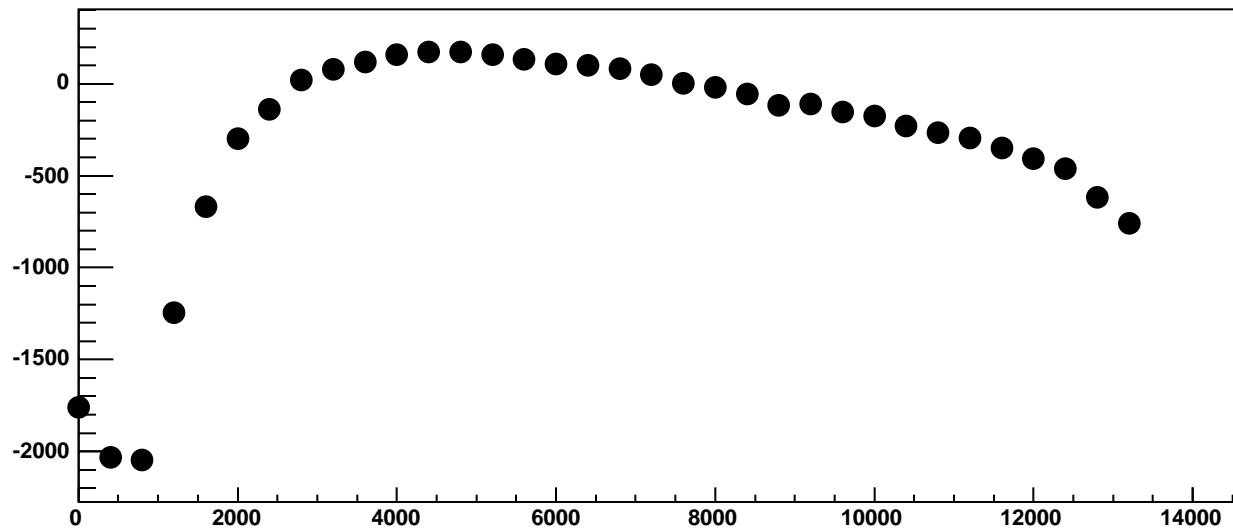
χ^2 / ndf 1.612e+04 / 23
p0 2113 ± 4.578
p1 0.7041 ± 0.0007054



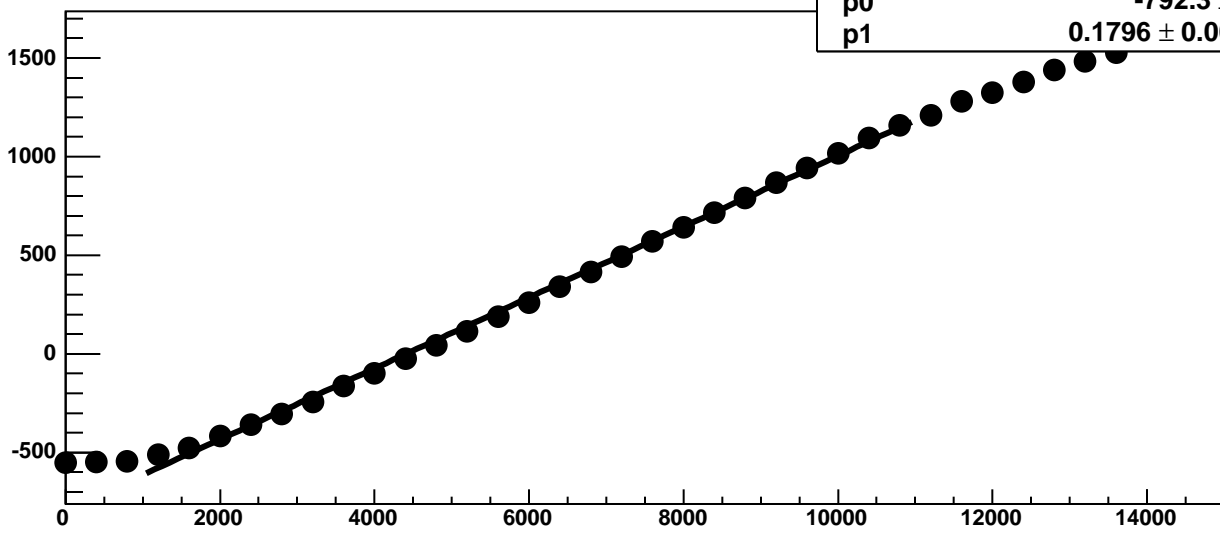
Chip 0, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC



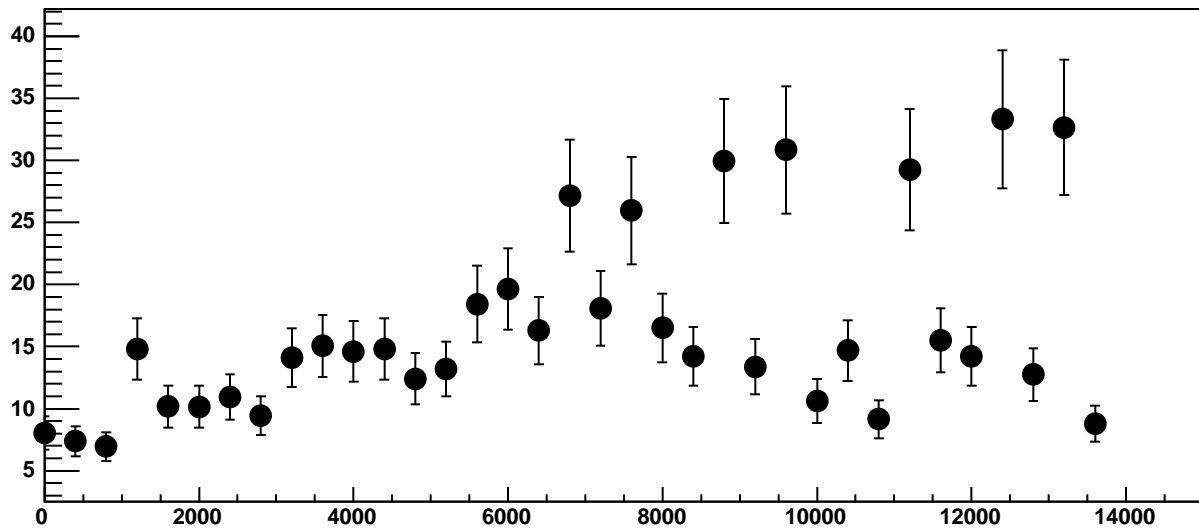
Chip 0, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC



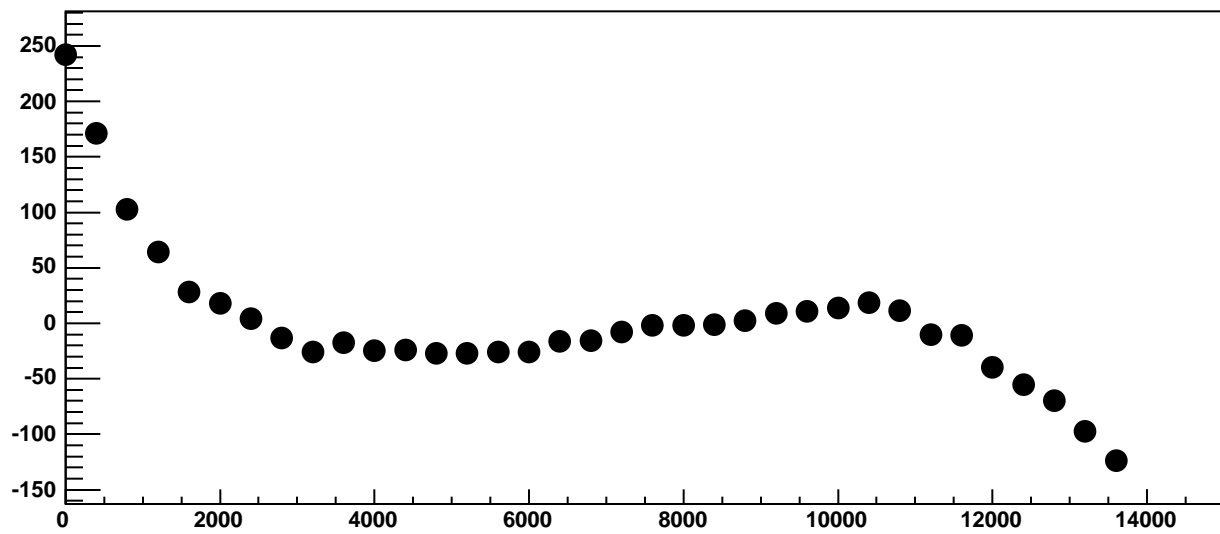
Chip 1, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC



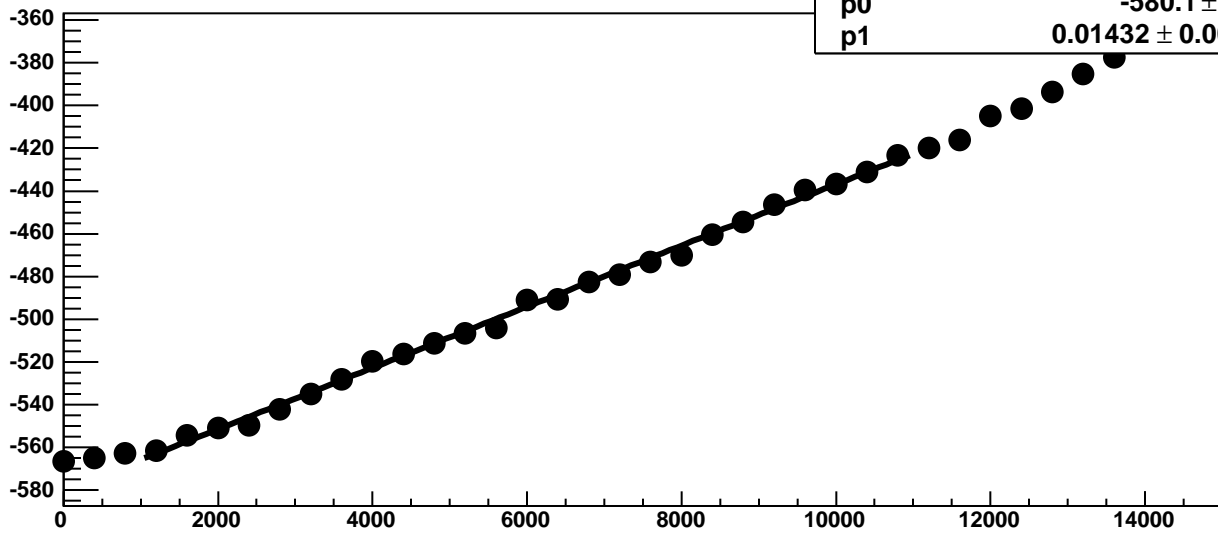
Chip 1, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



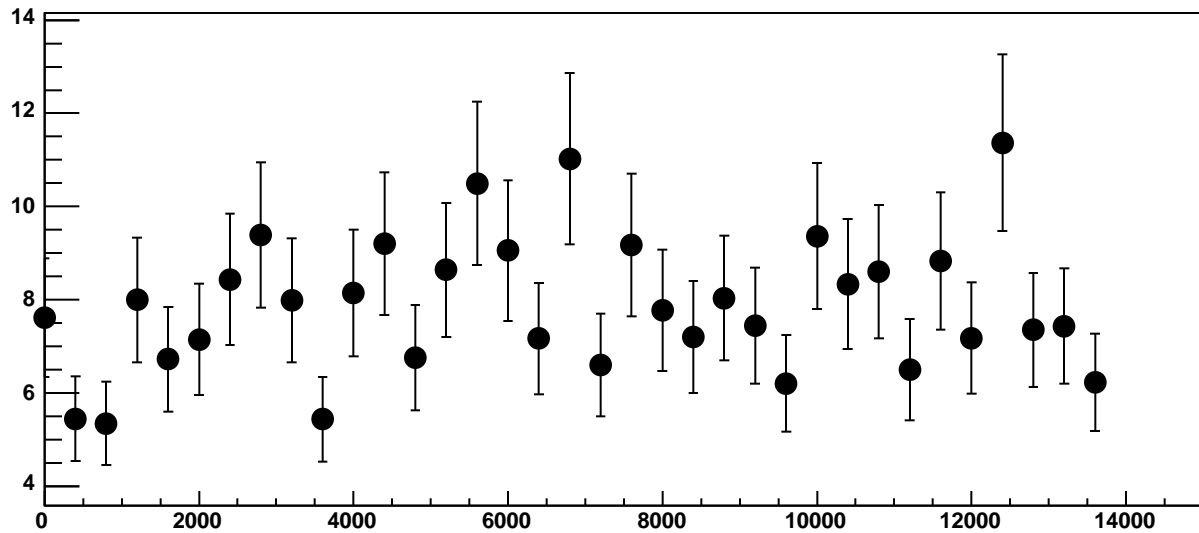
Chip 1, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC



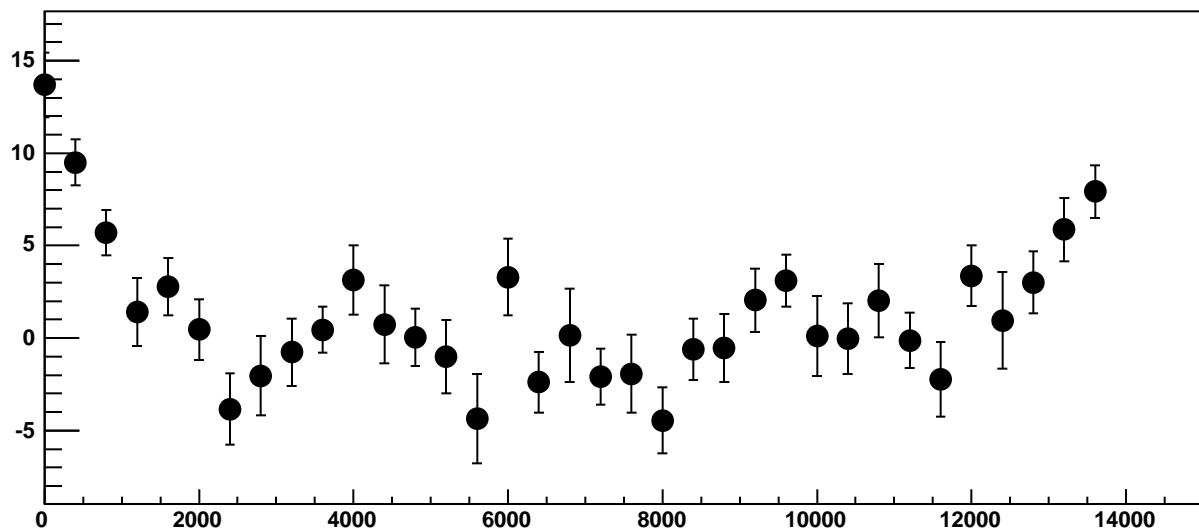
Chip 1, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC



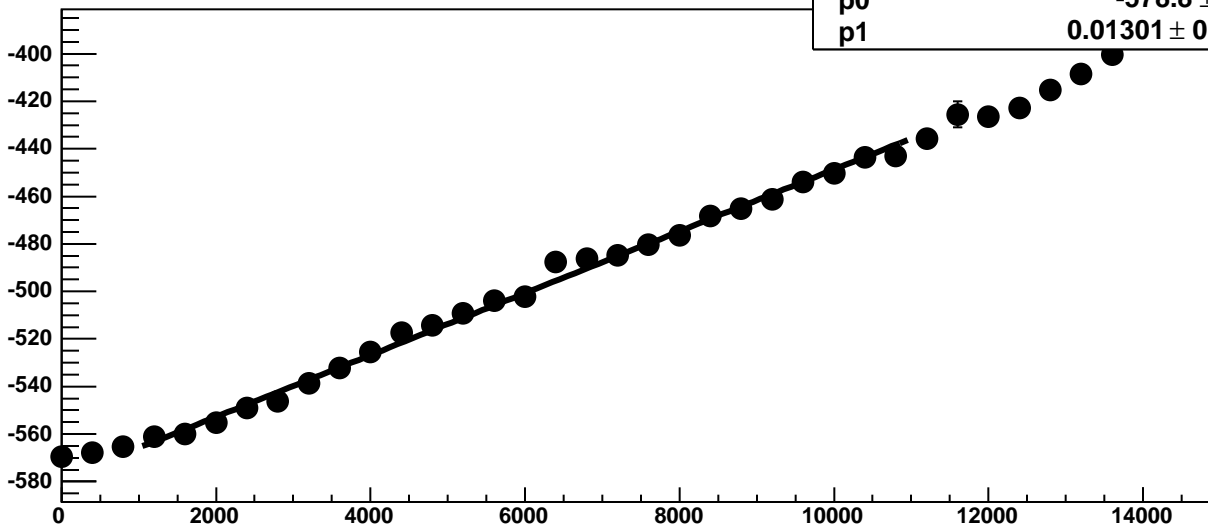
Chip 1, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



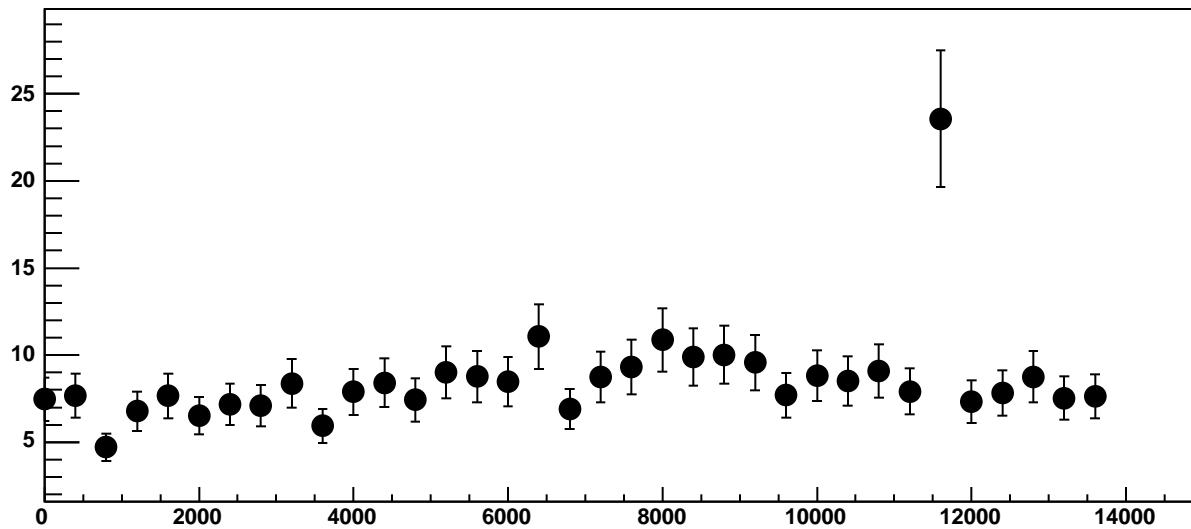
Chip 1, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



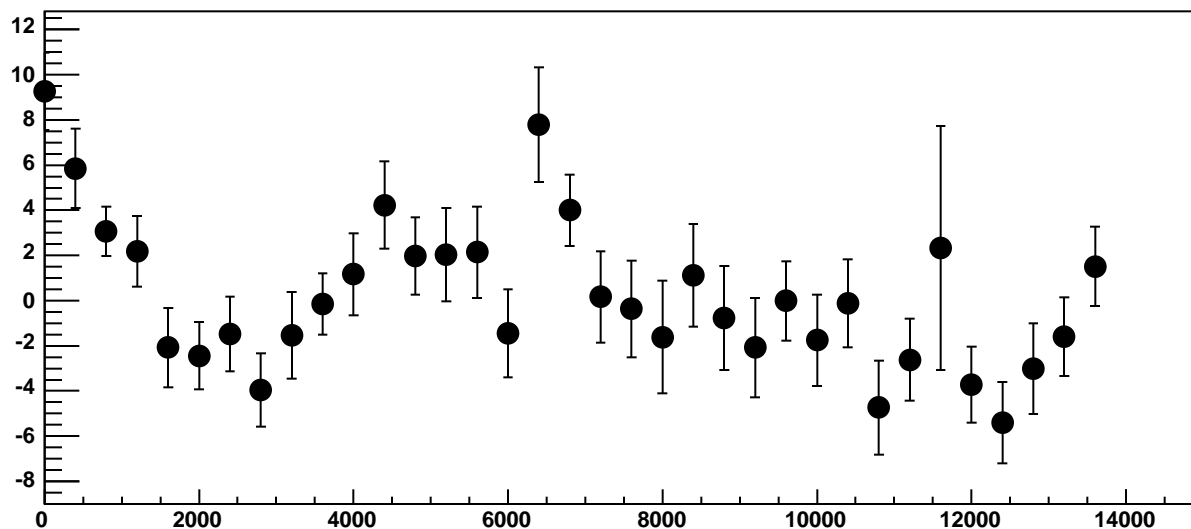
Chip 1, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



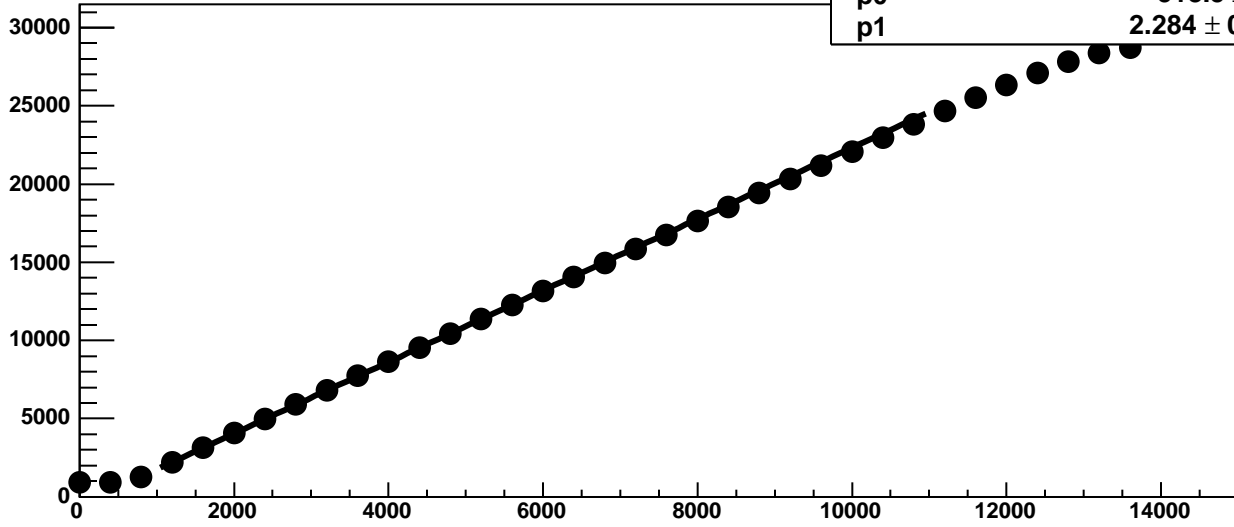
Chip 1, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



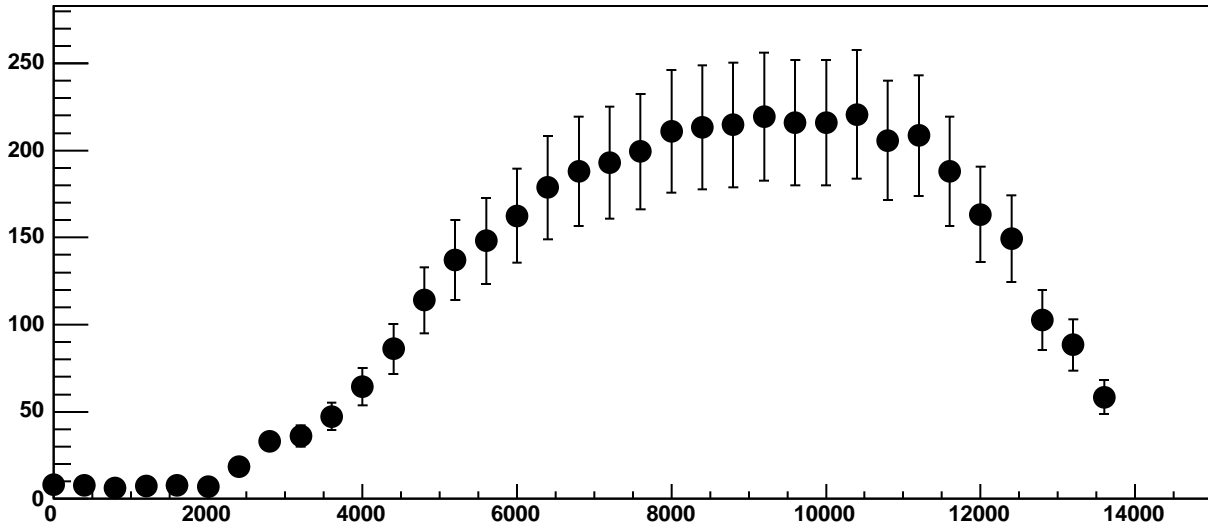
Chip 1, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



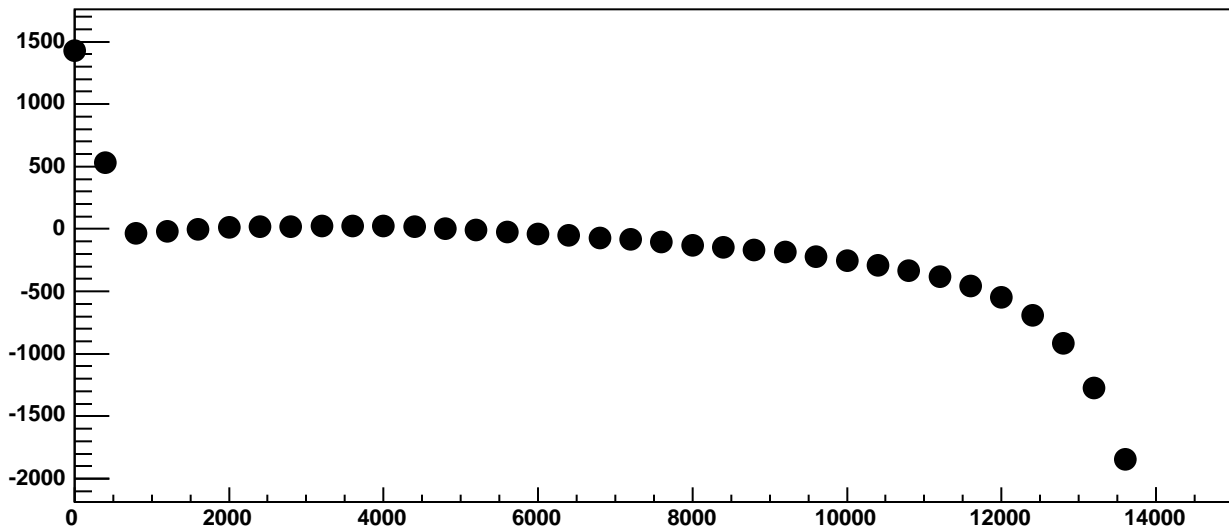
Chip 1, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC



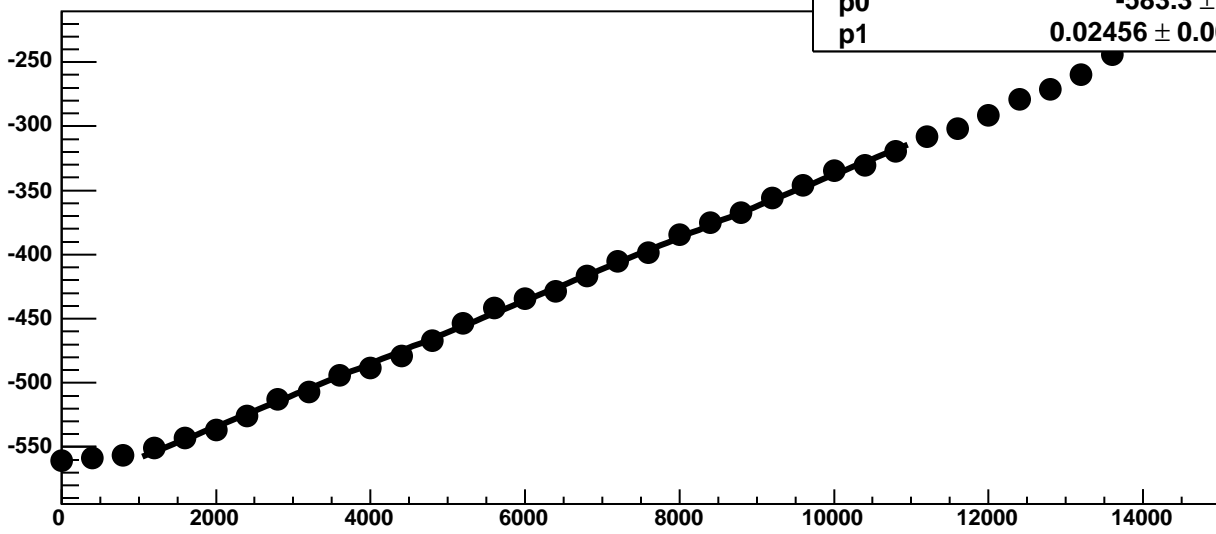
Chip 1, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC

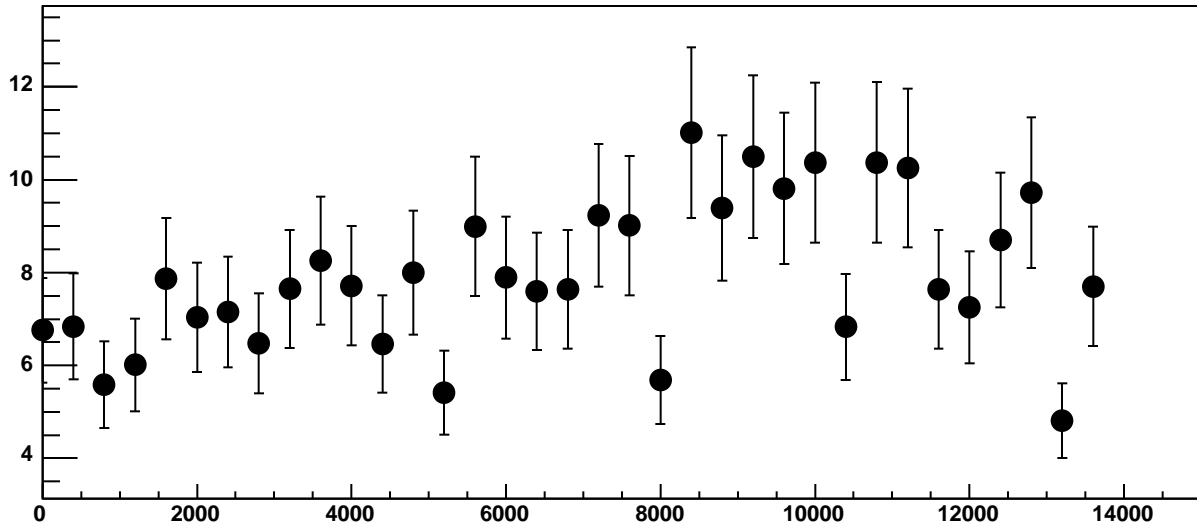


Chip 1, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

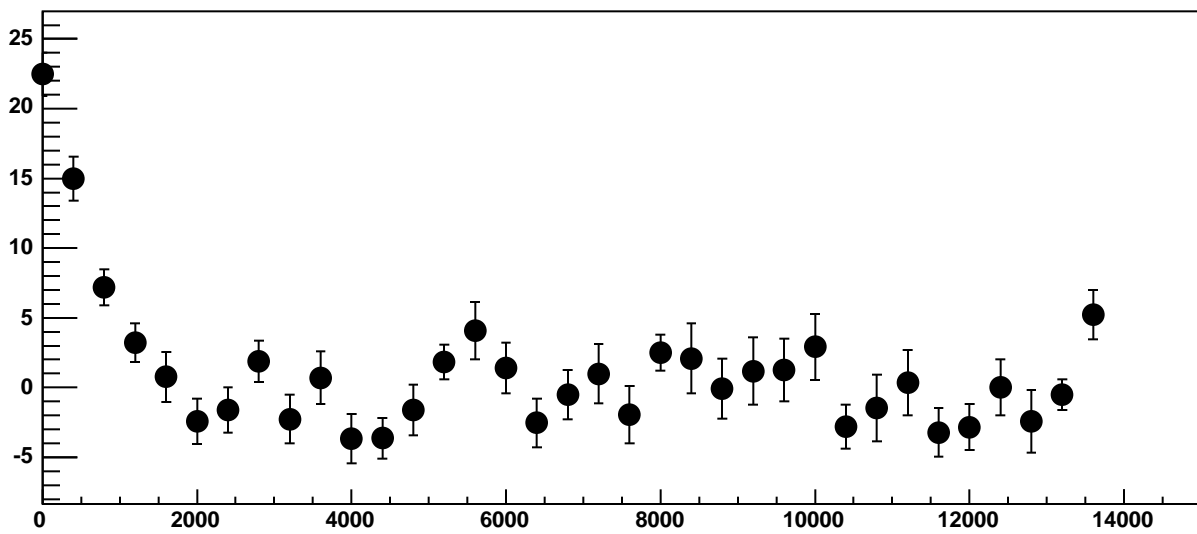


χ^2 / ndf 43.52 / 23
p0 -583.3 ± 0.7675
p1 0.02456 ± 0.0001249

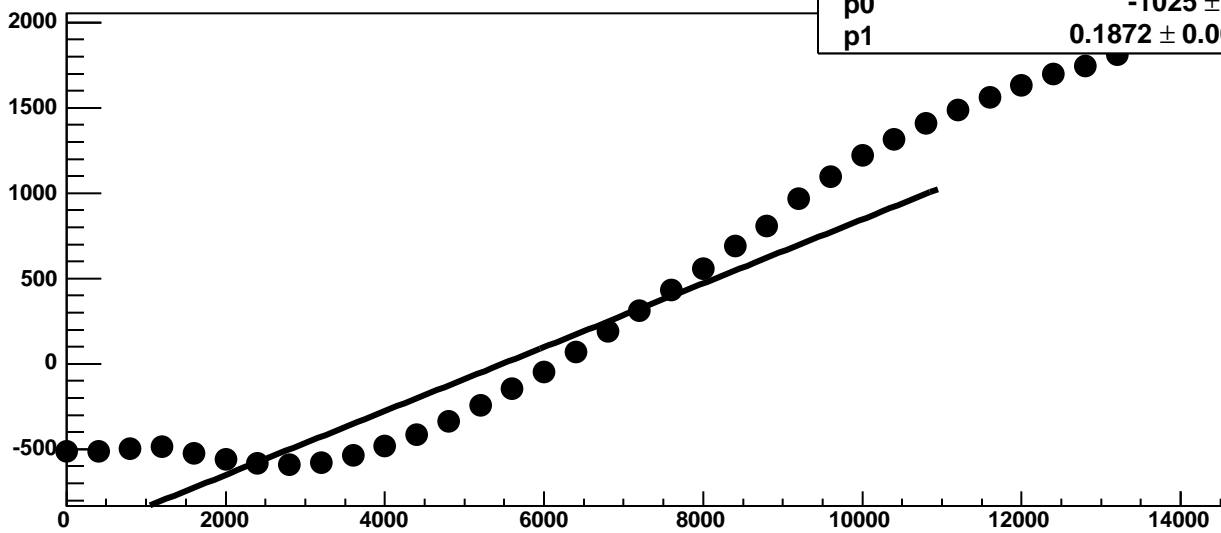
Chip 1, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

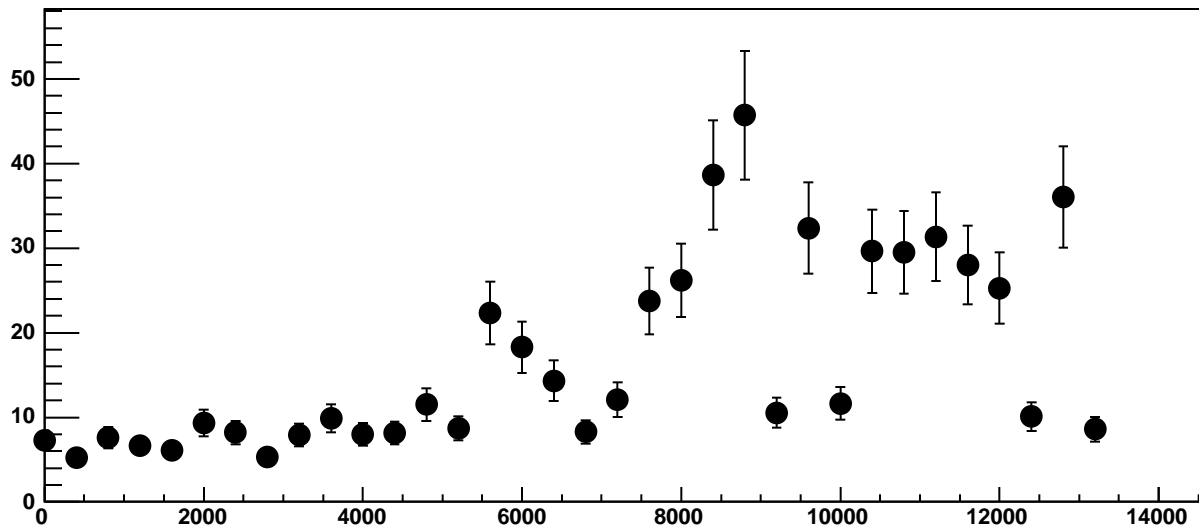


Chip 1, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

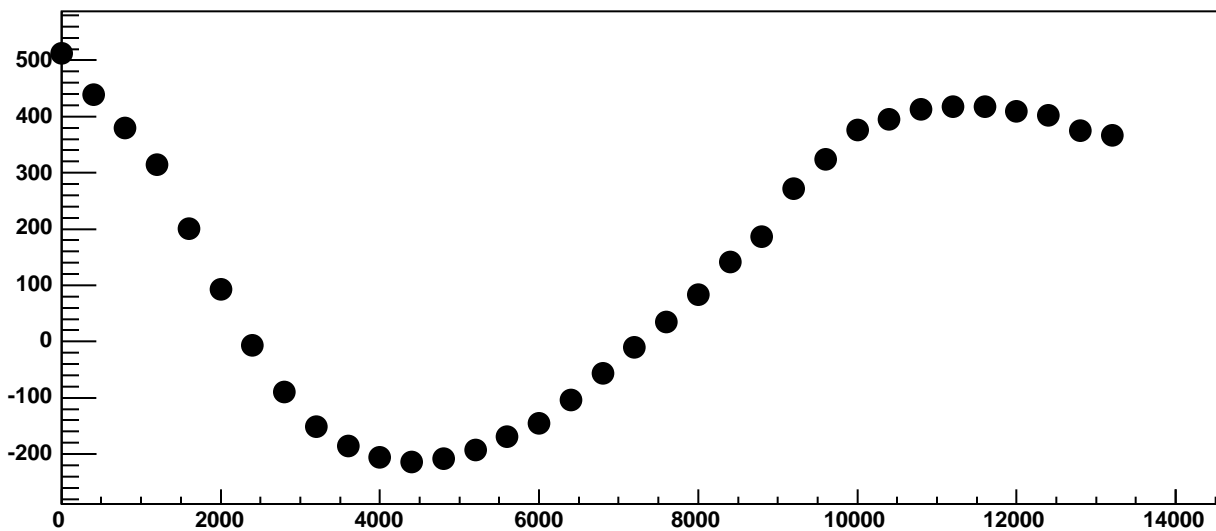


χ^2 / ndf 1.705e+05 / 23
p0 -1025 ± 0.9003
p1 0.1872 ± 0.0001912

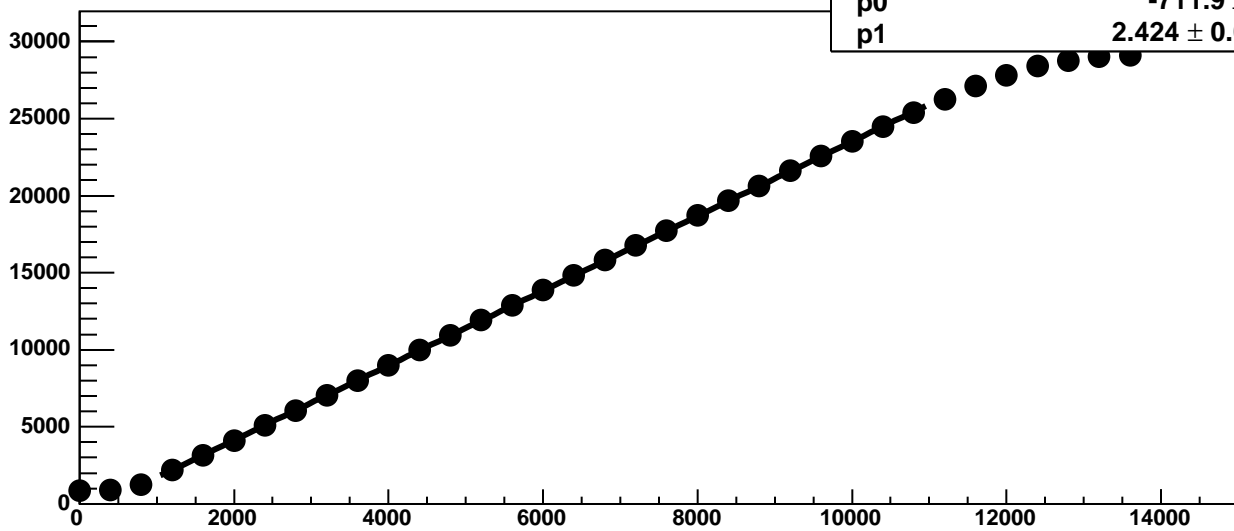
Chip 1, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



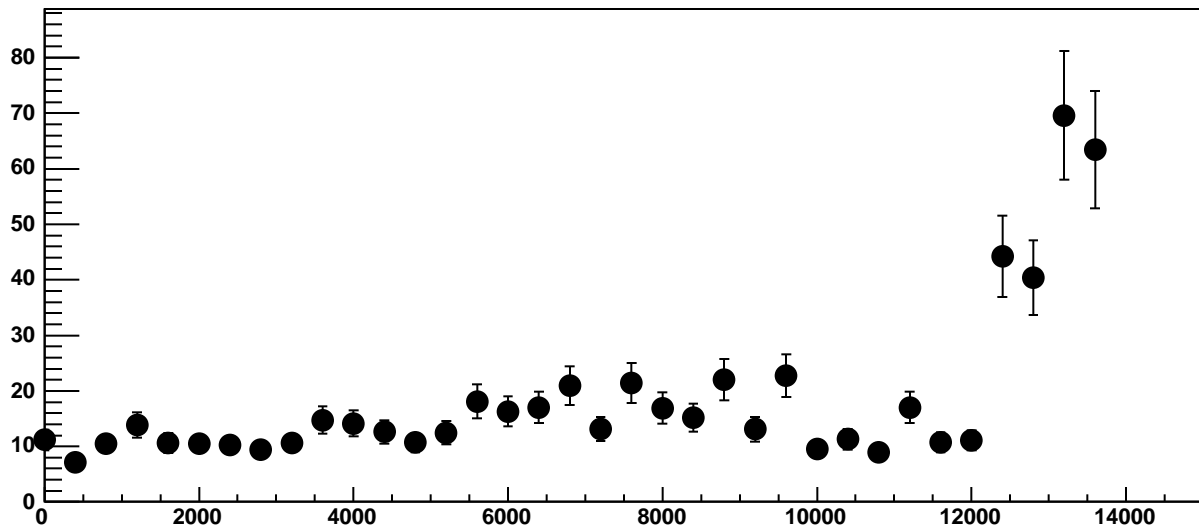
Chip 1, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



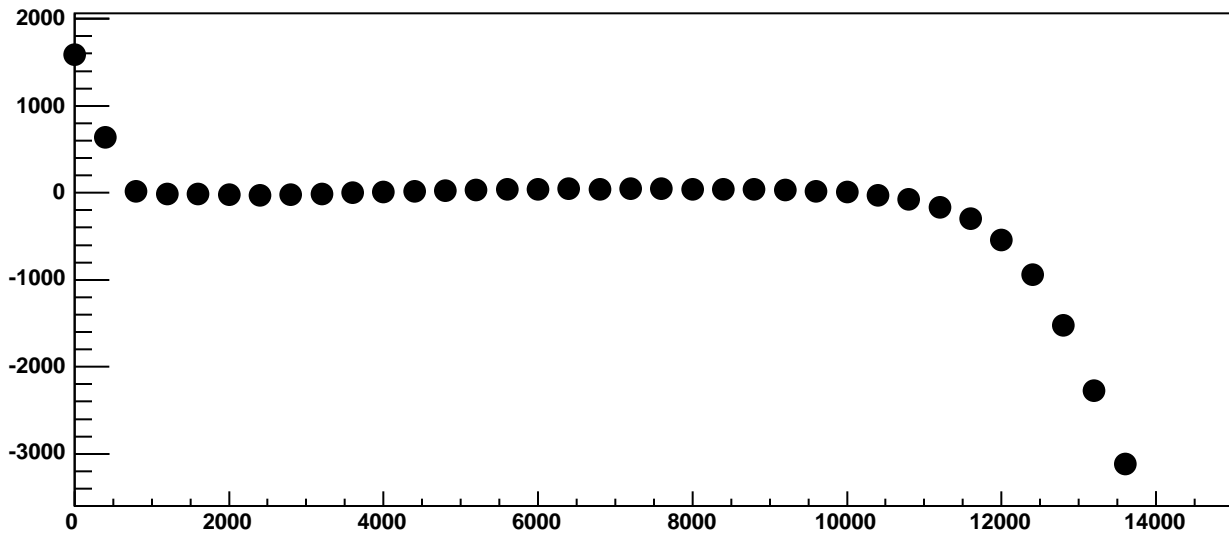
Chip 1, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



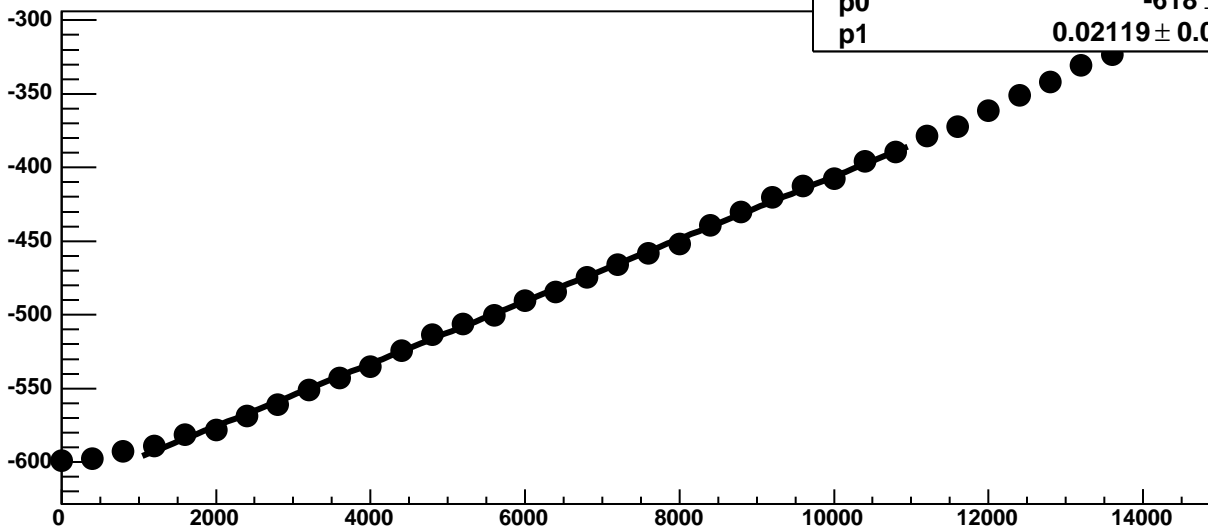
Chip 1, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC

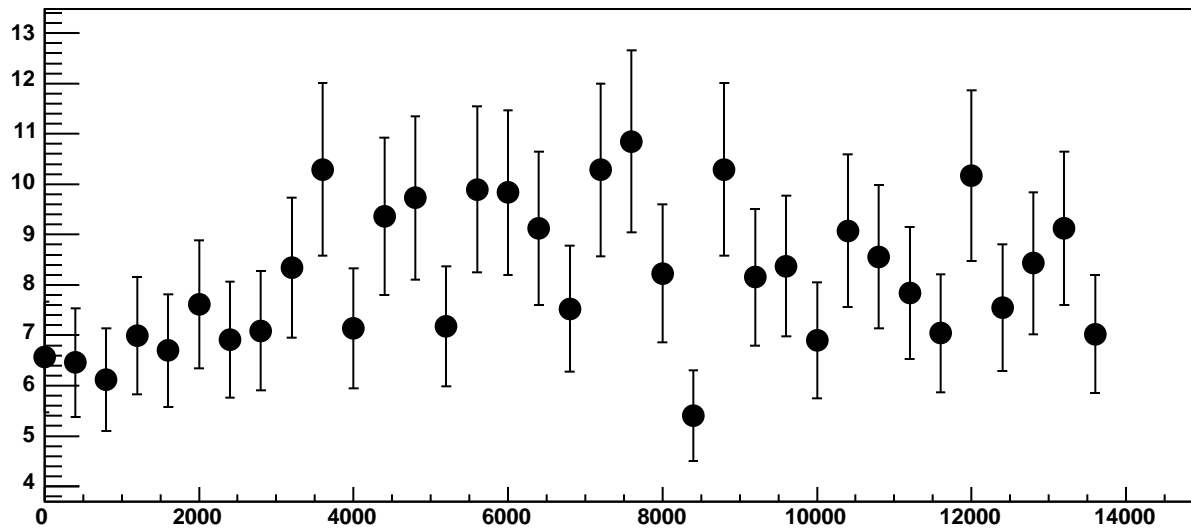


Chip 1, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

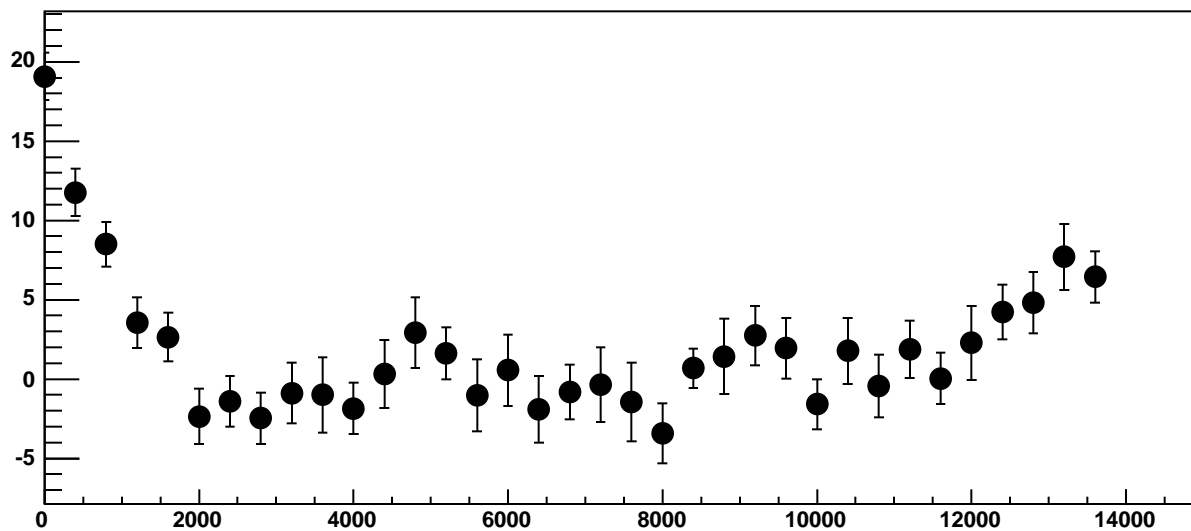


χ^2 / ndf 27.74 / 23
p0 -618 ± 0.7981
p1 0.02119 ± 0.0001218

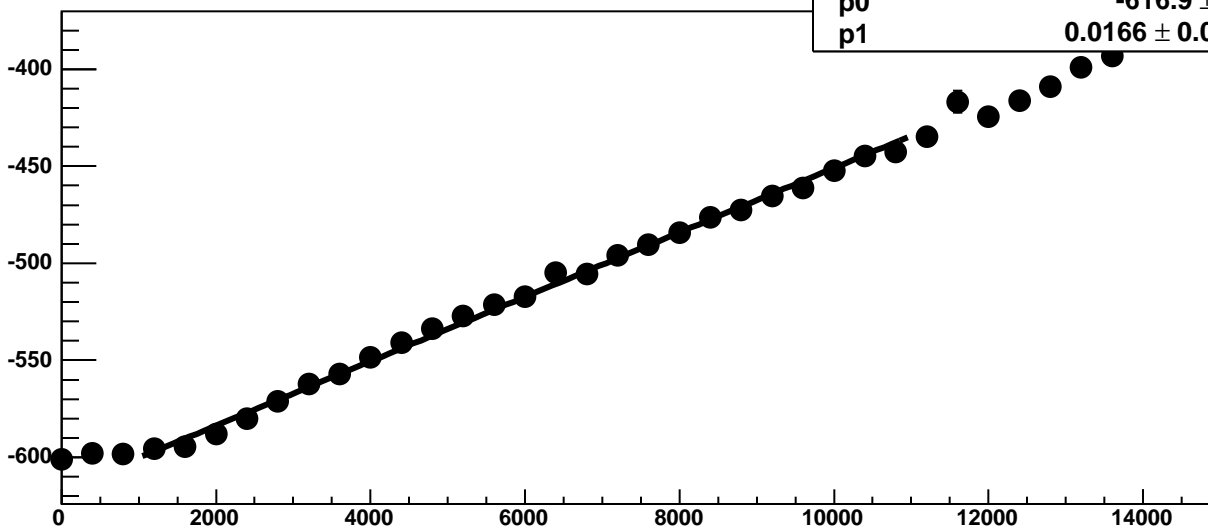
Chip 1, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



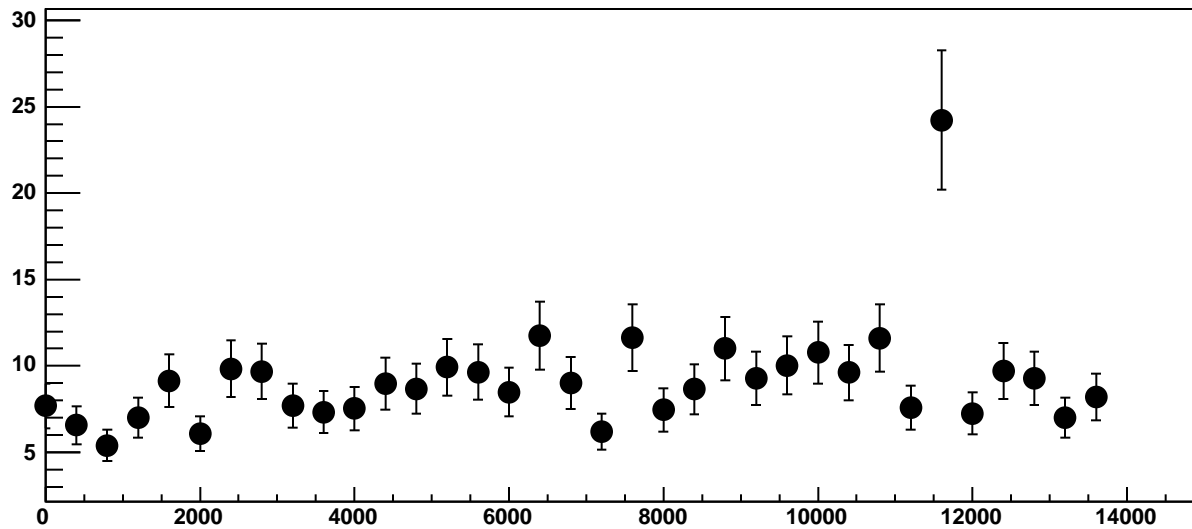
Chip 1, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



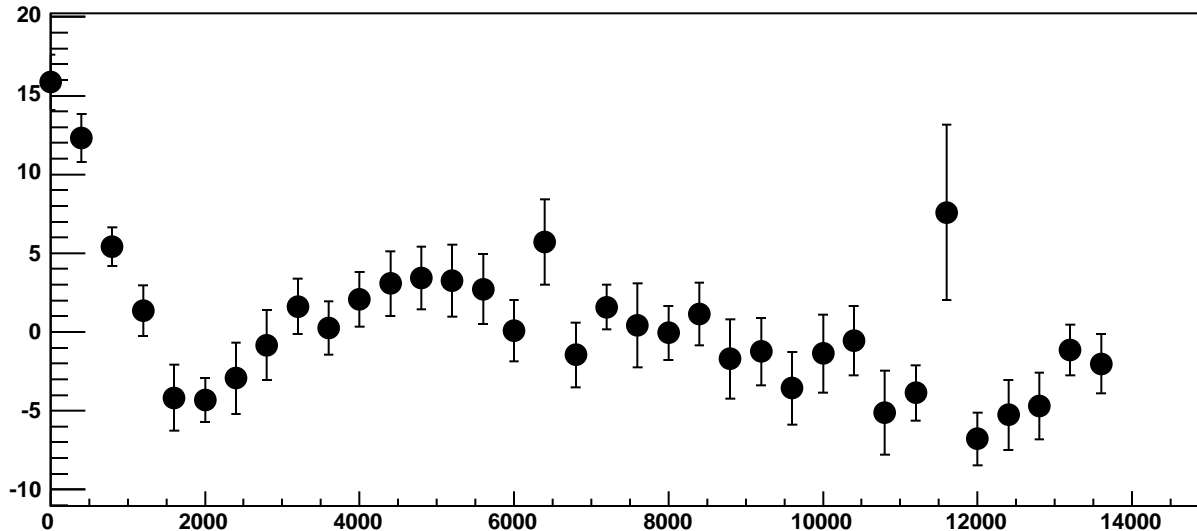
Chip 1, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



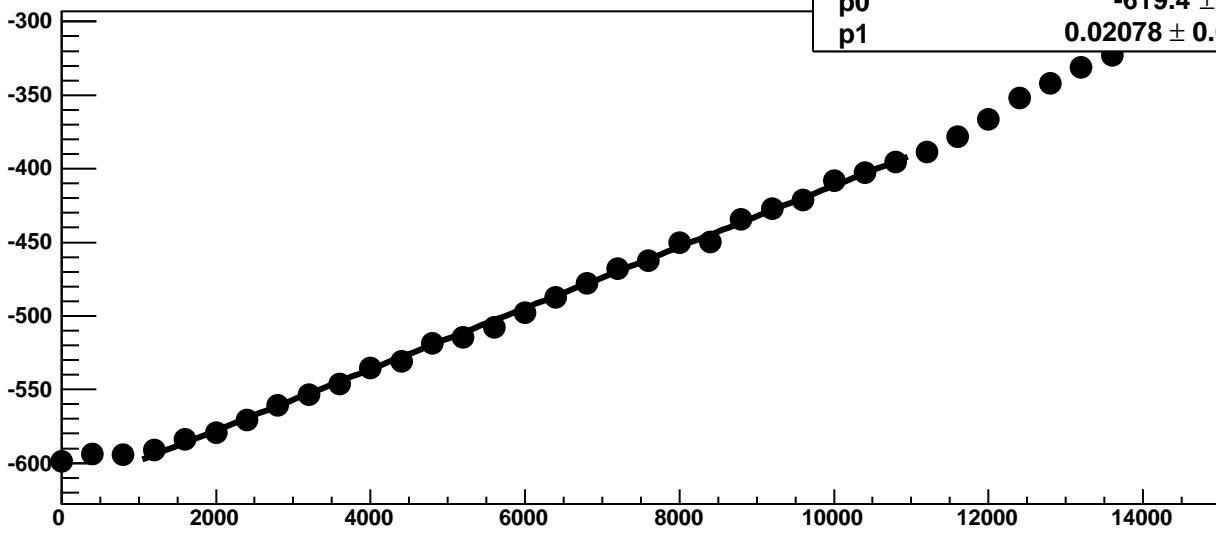
Chip 1, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

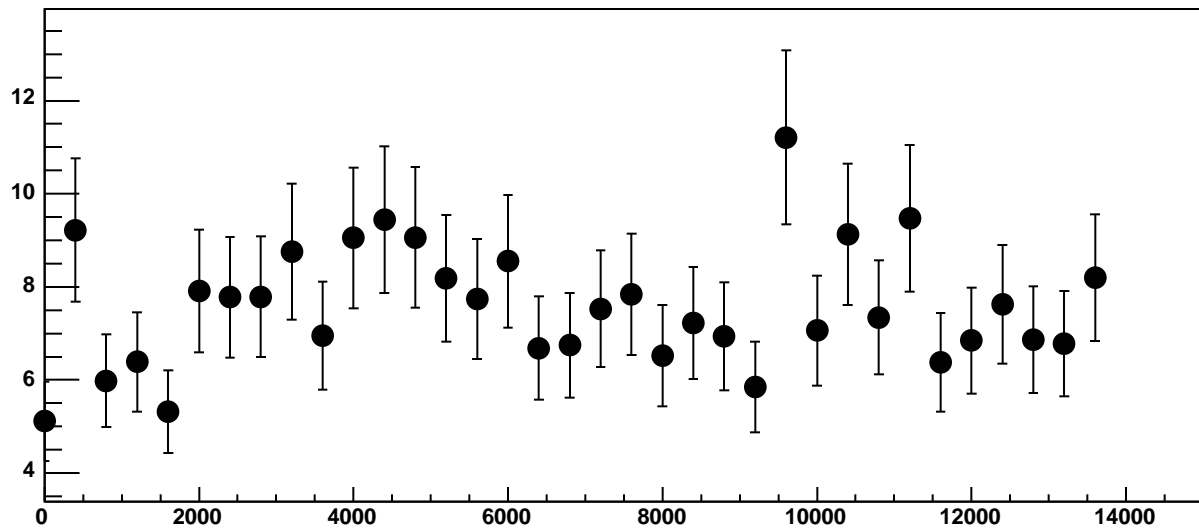


Chip 1, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC

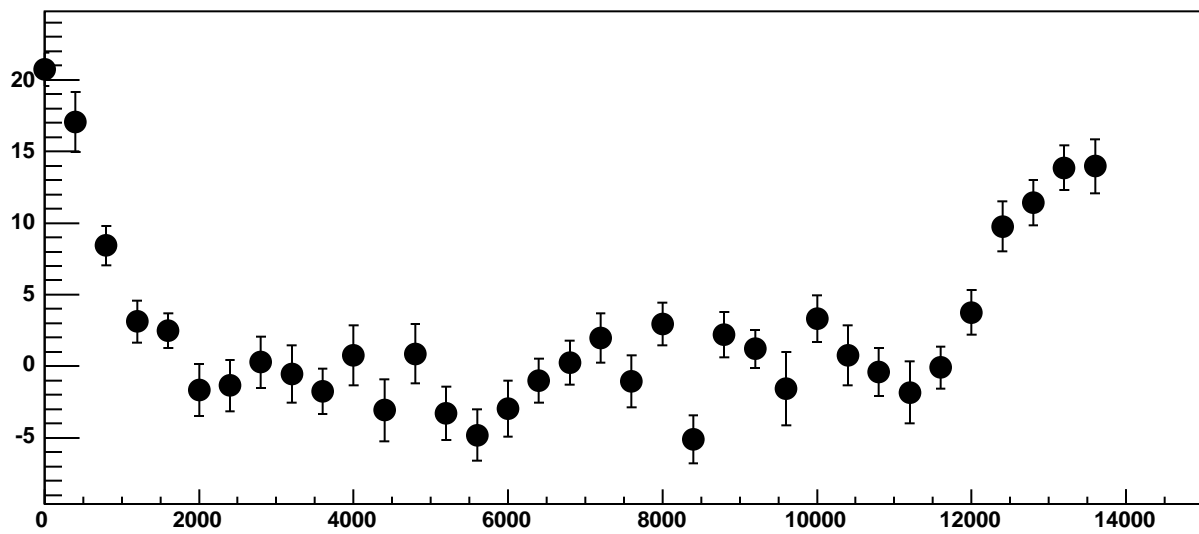


χ^2 / ndf 49.46 / 23
p0 -619.4 ± 0.7547
p1 0.02078 ± 0.000114

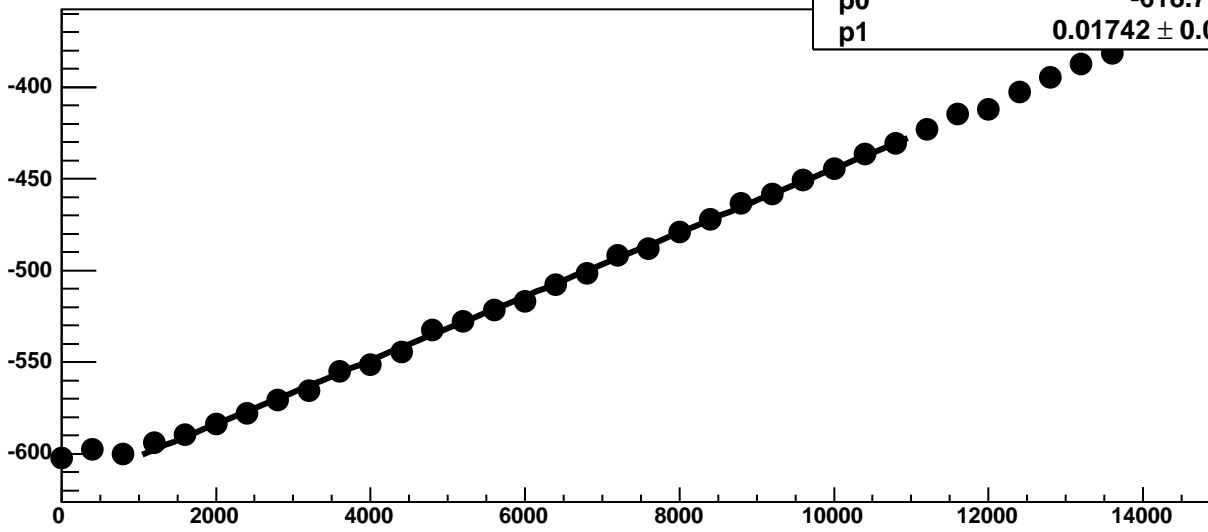
Chip 1, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC

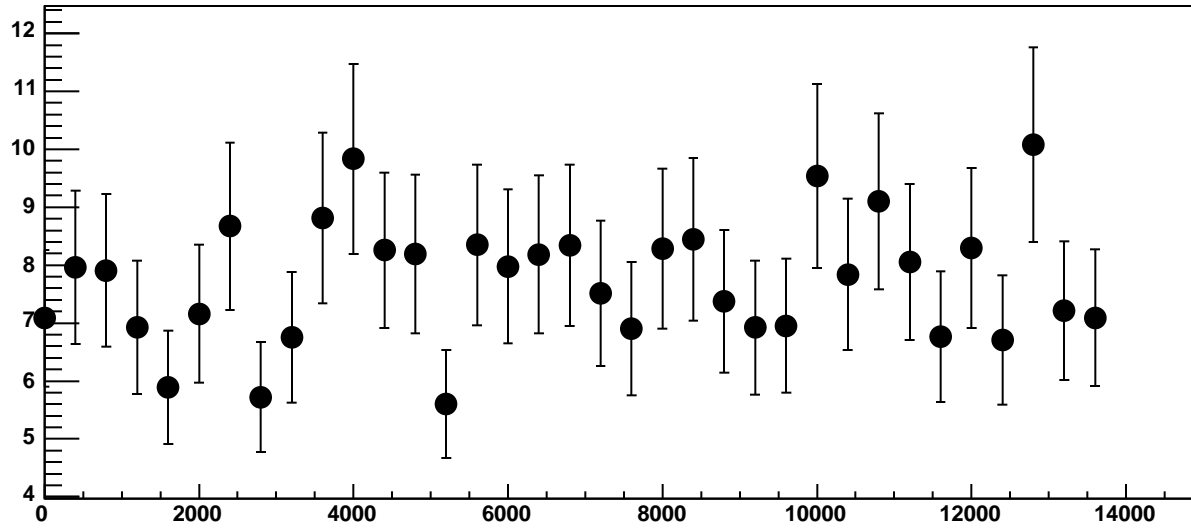


Chip 1, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

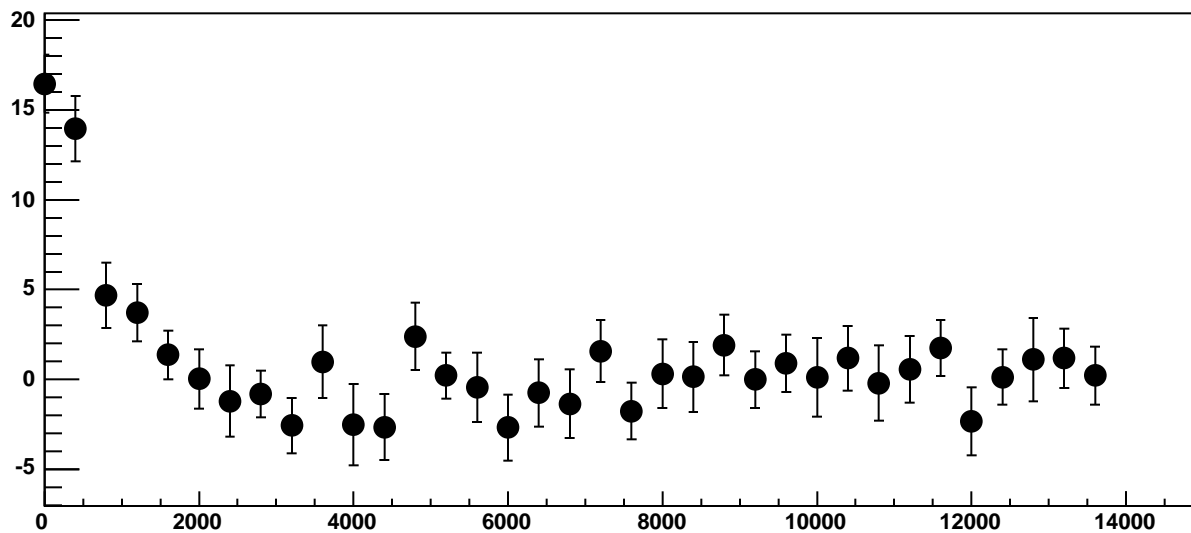


χ^2 / ndf 22.12 / 23
p0 -618.7 ± 0.754
p1 0.01742 ± 0.0001179

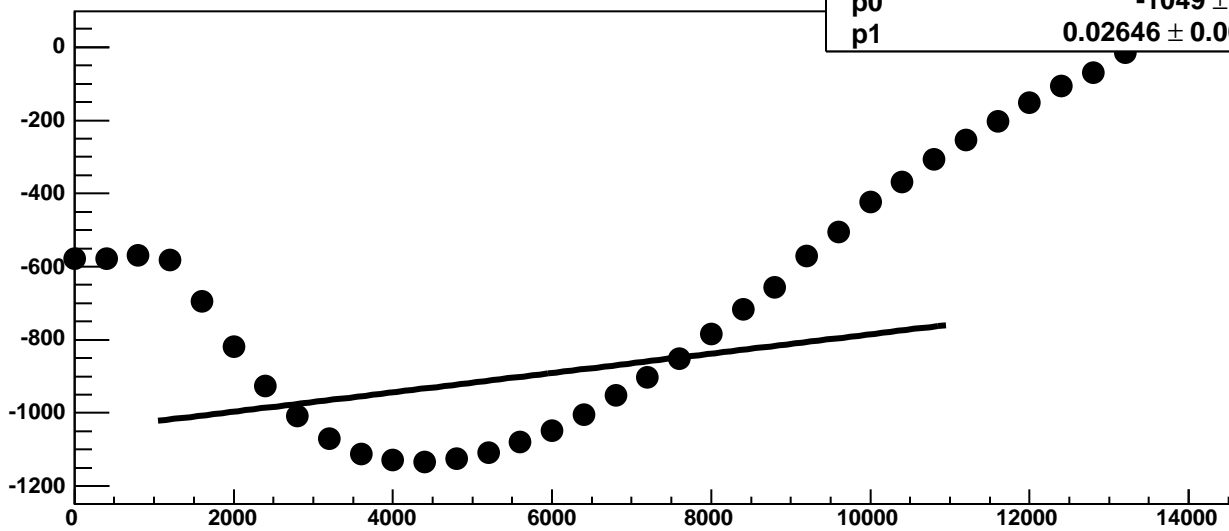
Chip 1, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

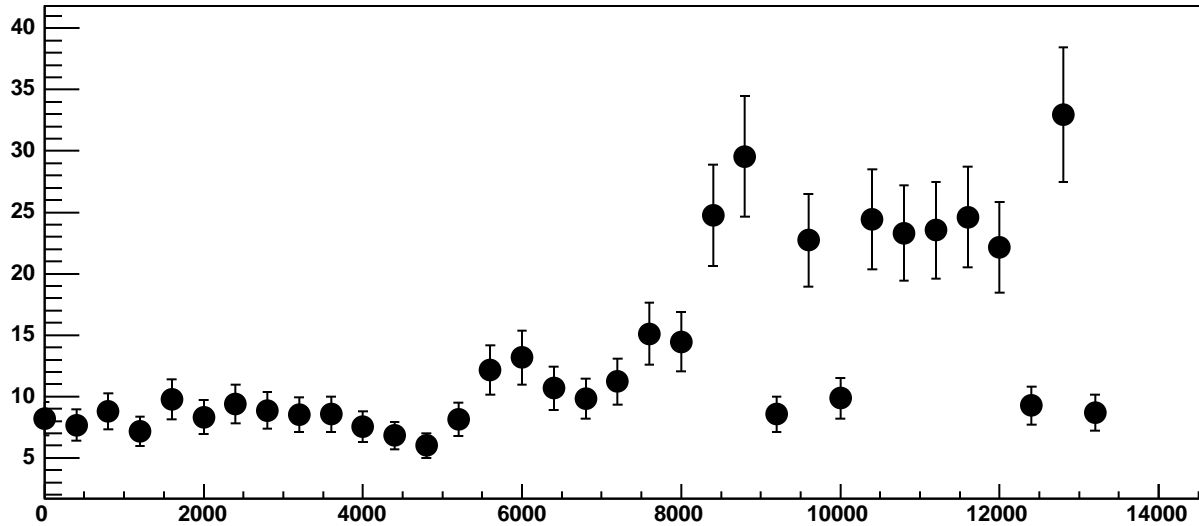


Chip 1, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

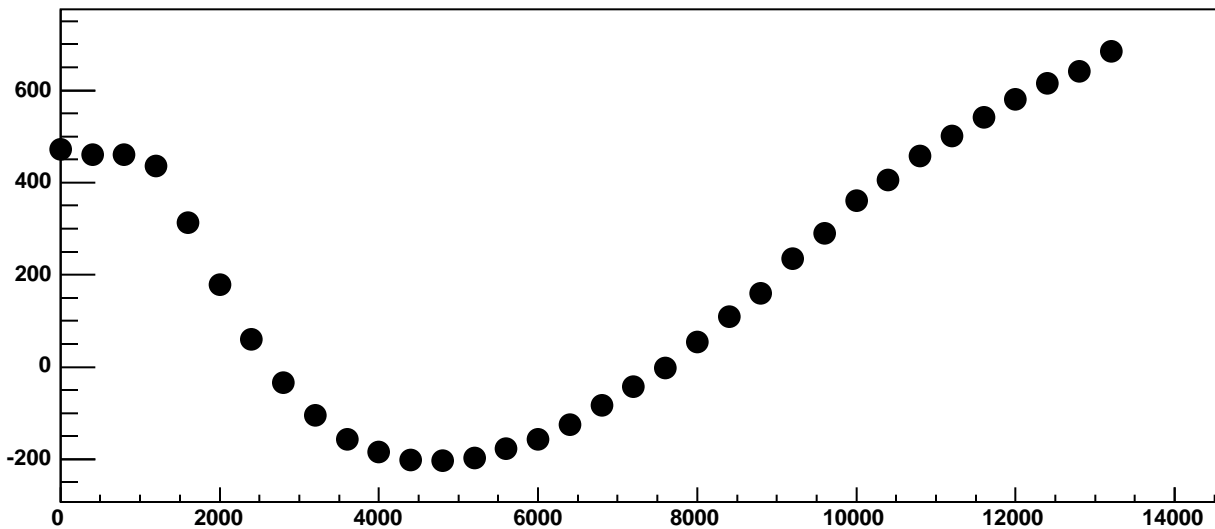


χ^2 / ndf 2.369e+05 / 23
p0 -1049 ± 0.9756
p1 0.02646 ± 0.0001808

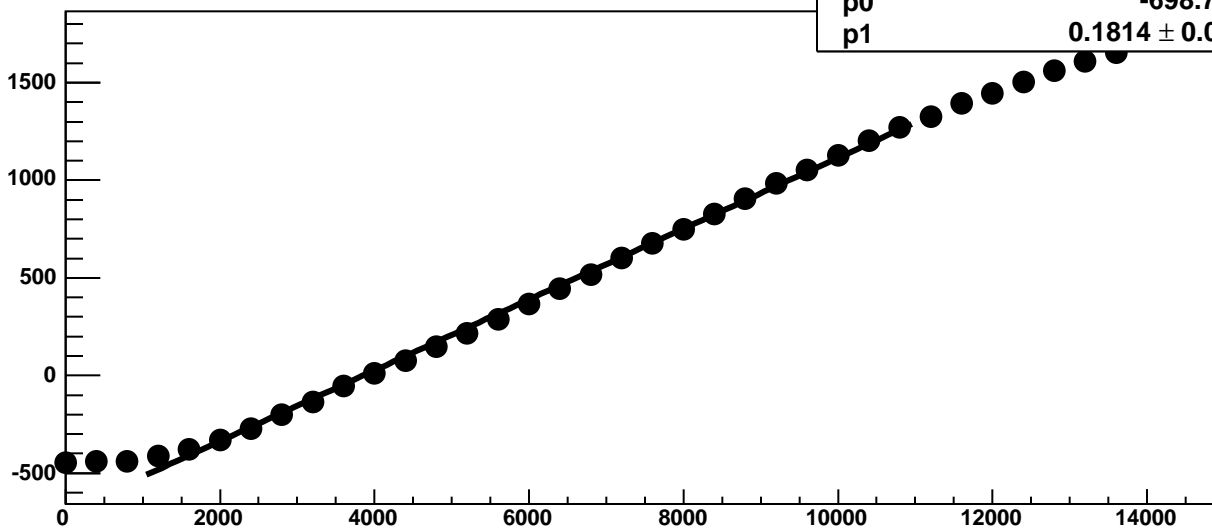
Chip 1, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

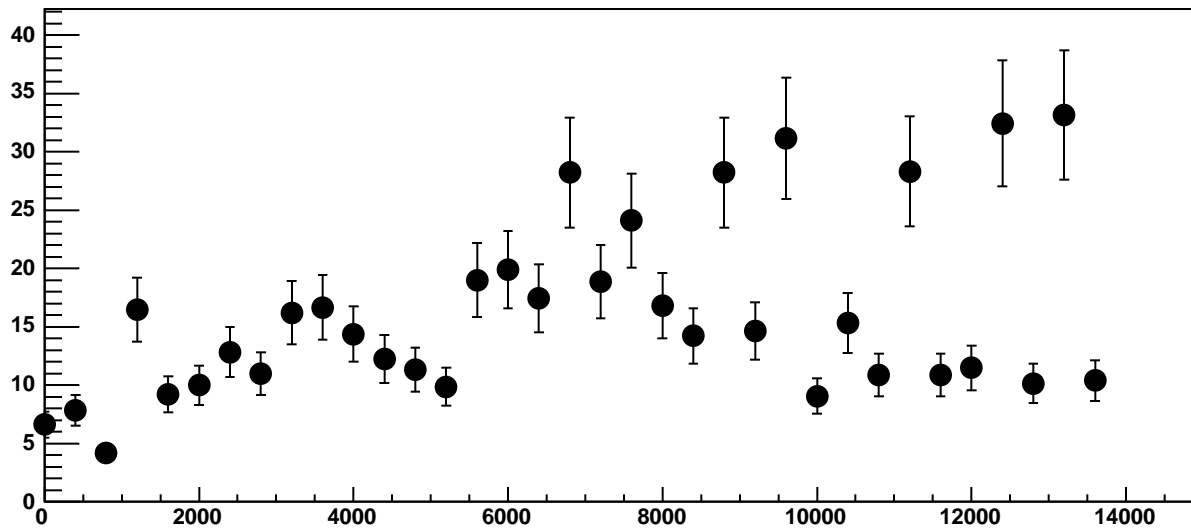


Chip 1, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC

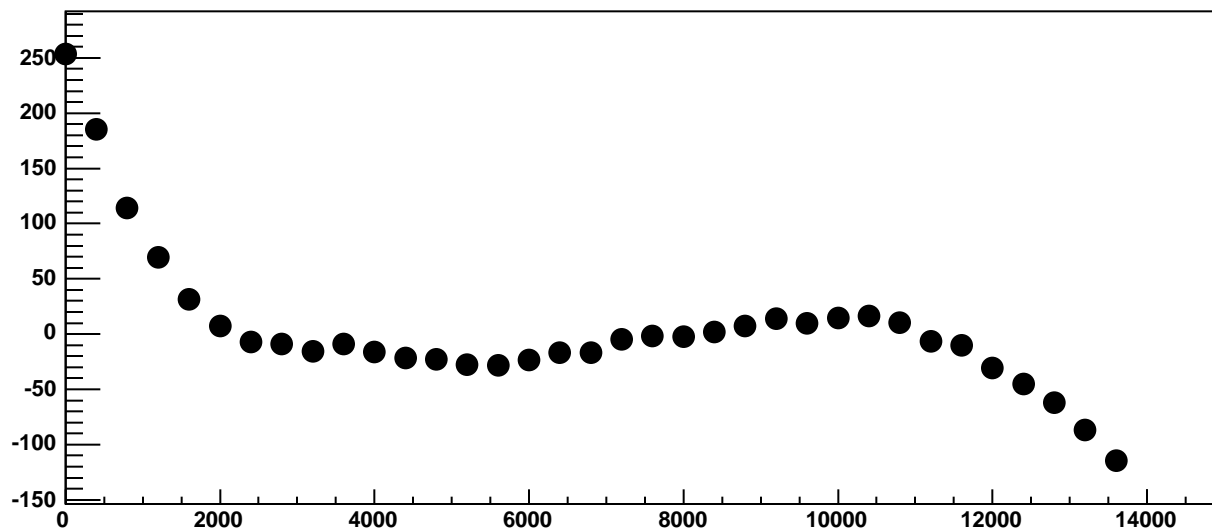


χ^2 / ndf 1124 / 23
p0 -698.7 ± 1.281
p1 0.1814 ± 0.0002005

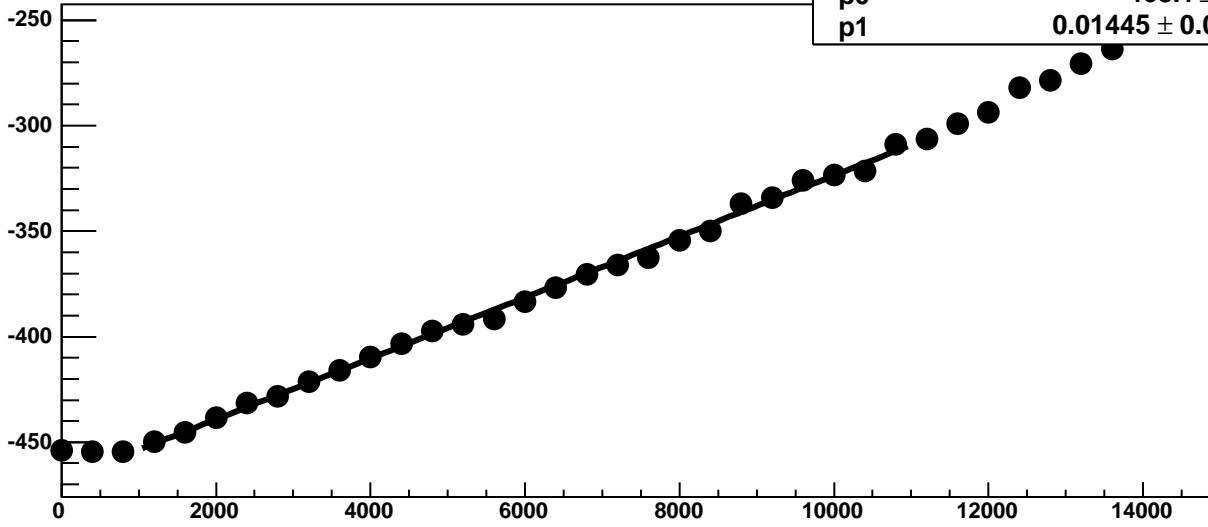
Chip 1, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

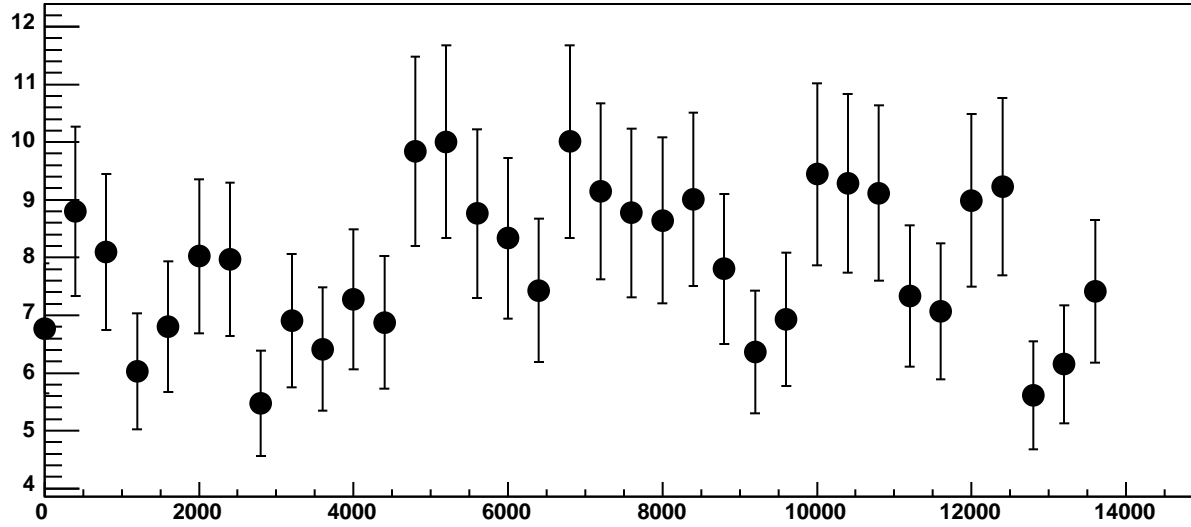


Chip 1, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

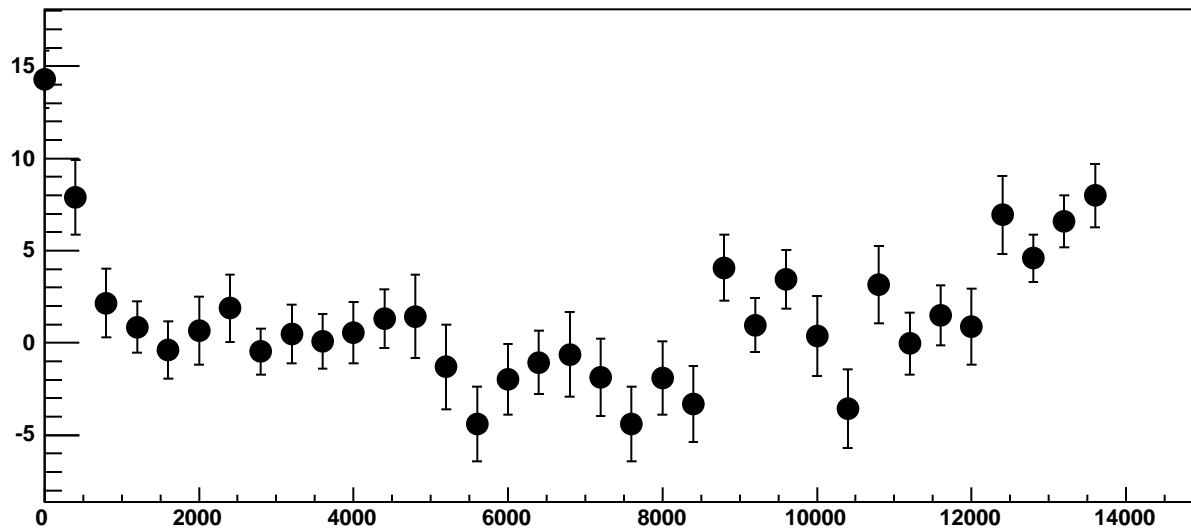


χ^2 / ndf 34.2 / 23
p0 -468.1 ± 0.7444
p1 0.01445 ± 0.0001189

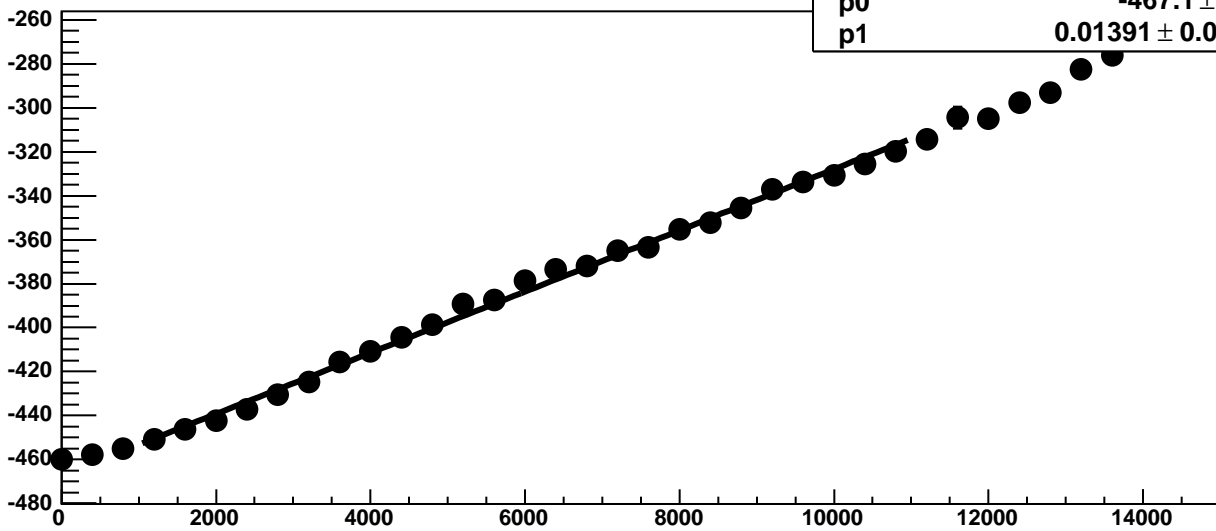
Chip 1, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

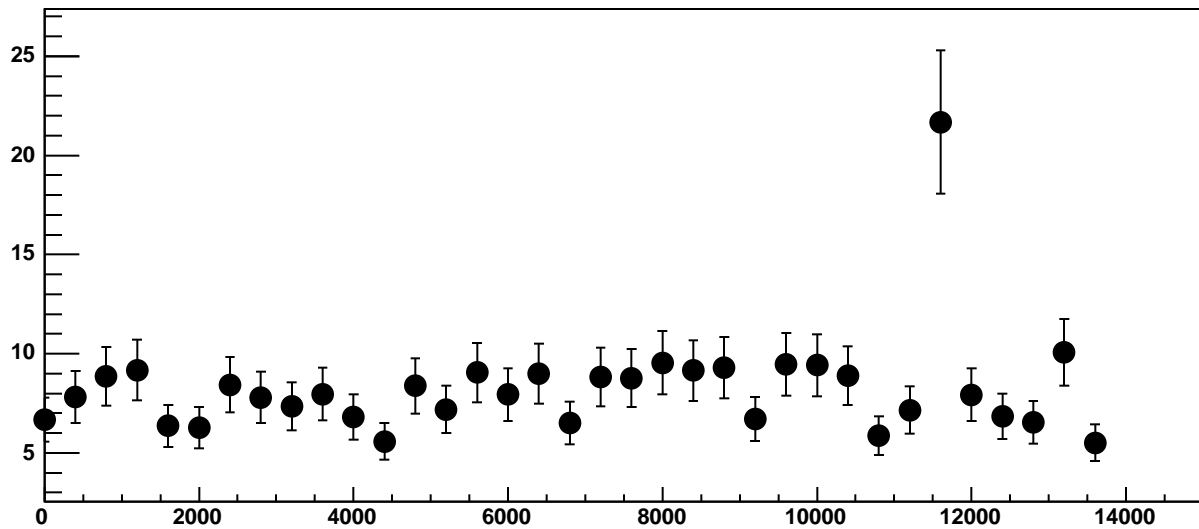


Chip 1, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

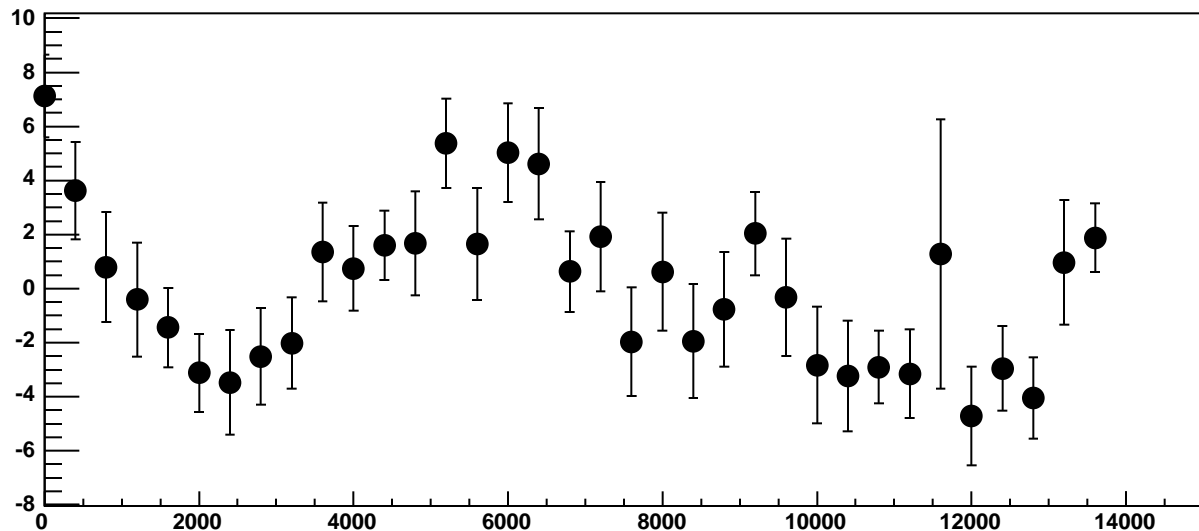


χ^2 / ndf 52.95 / 23
p0 -467.1 ± 0.7763
p1 0.01391 ± 0.0001199

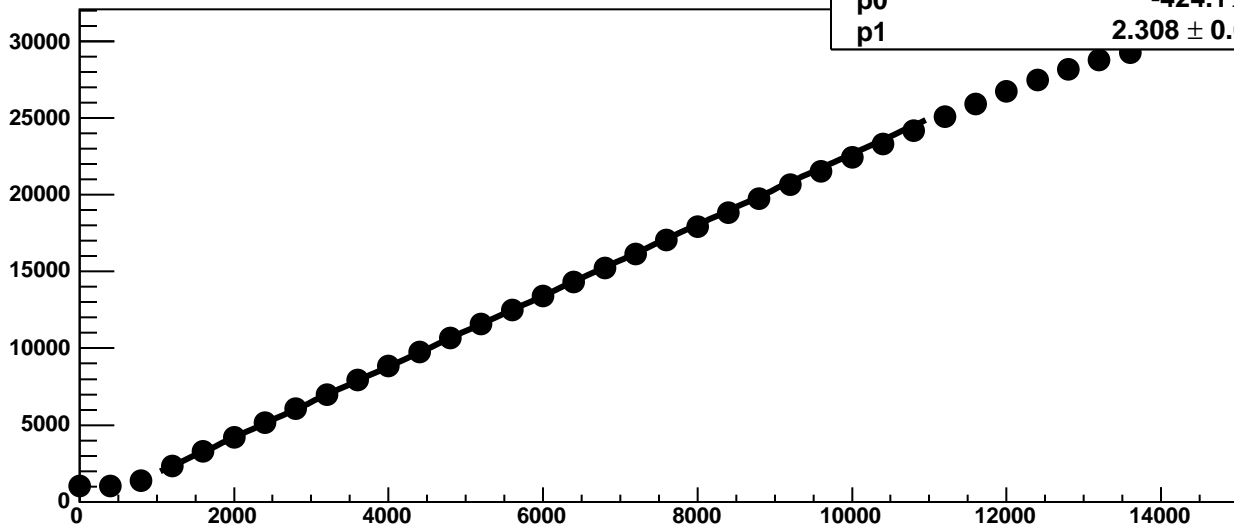
Chip 1, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



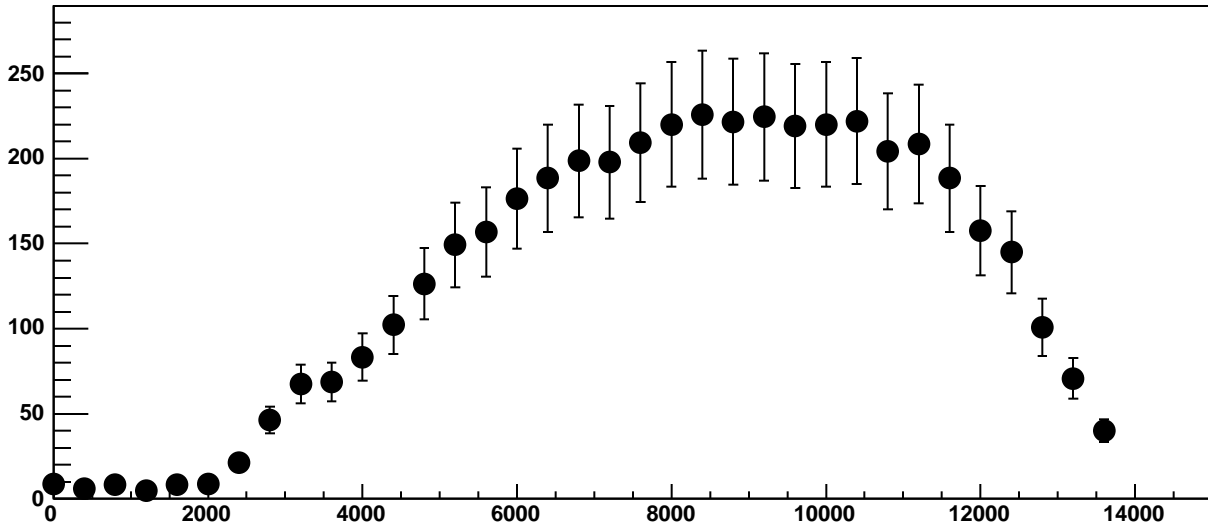
Chip 1, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



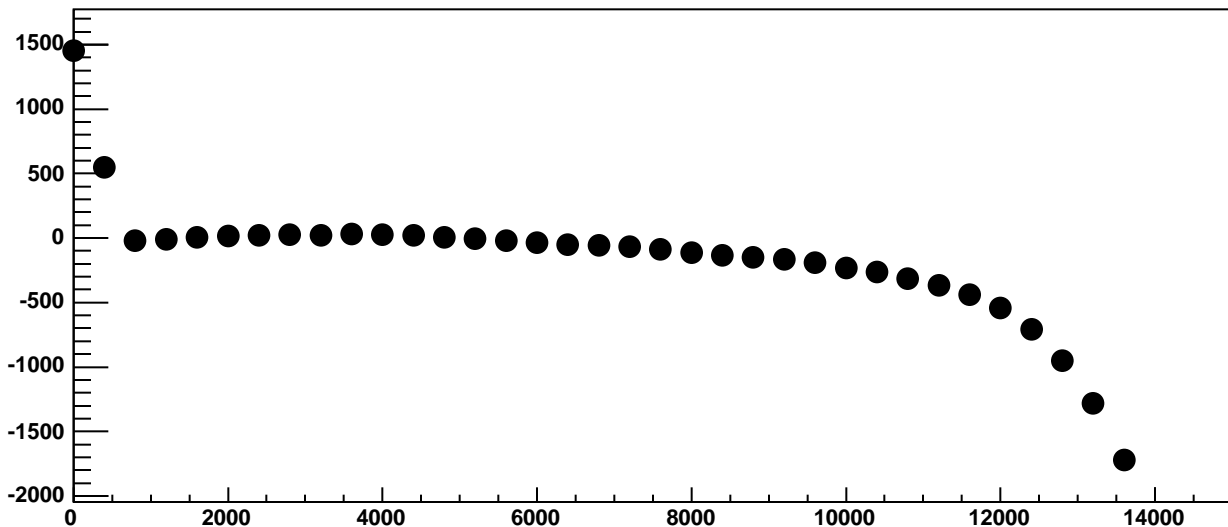
Chip 1, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC



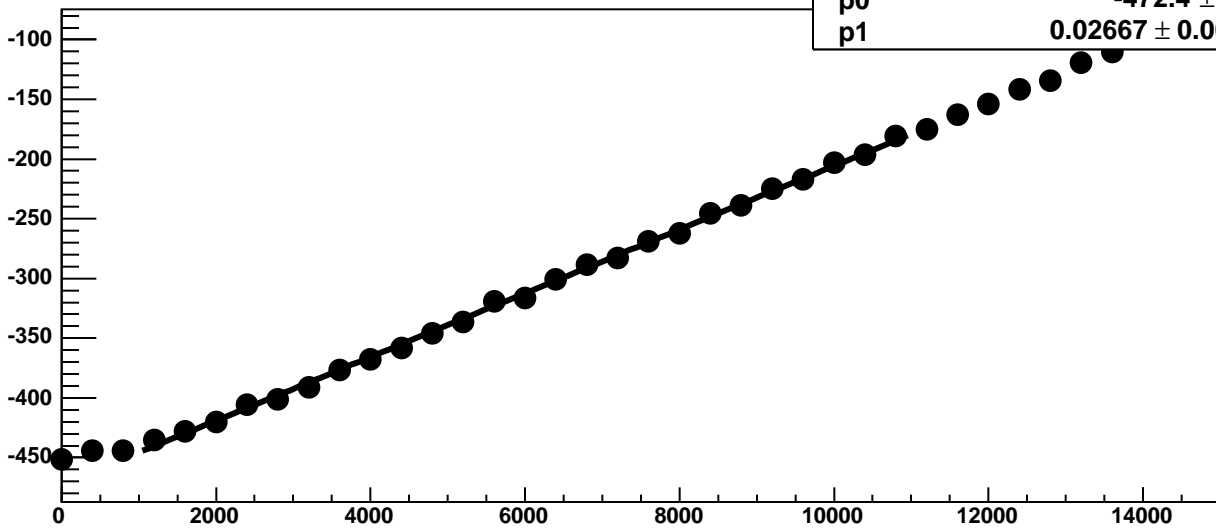
Chip 1, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC

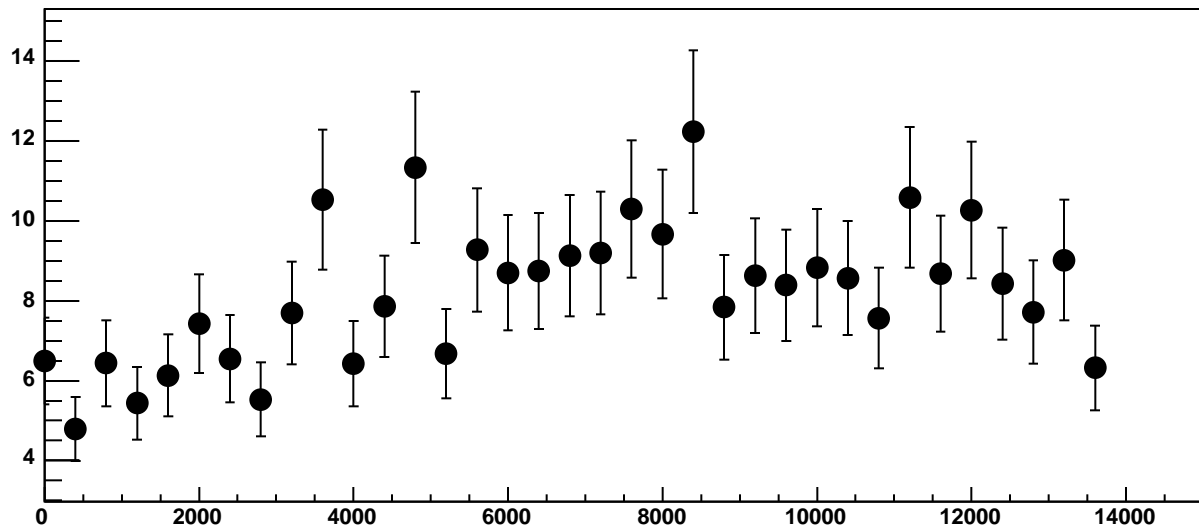


Chip 1, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

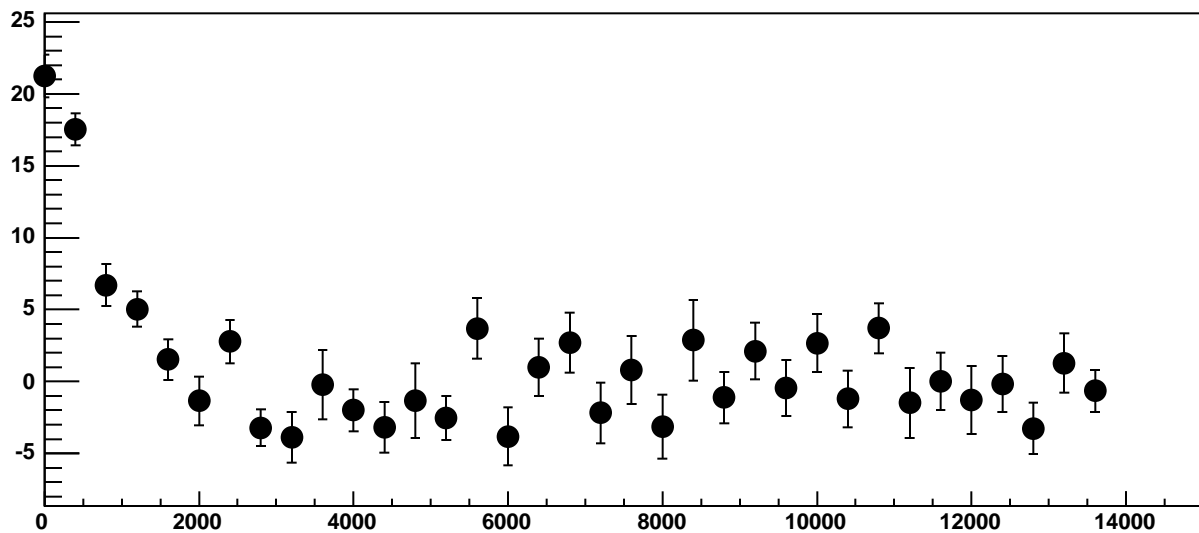


χ^2 / ndf 61.92 / 23
p0 -472.4 \pm 0.7157
p1 0.02667 \pm 0.0001176

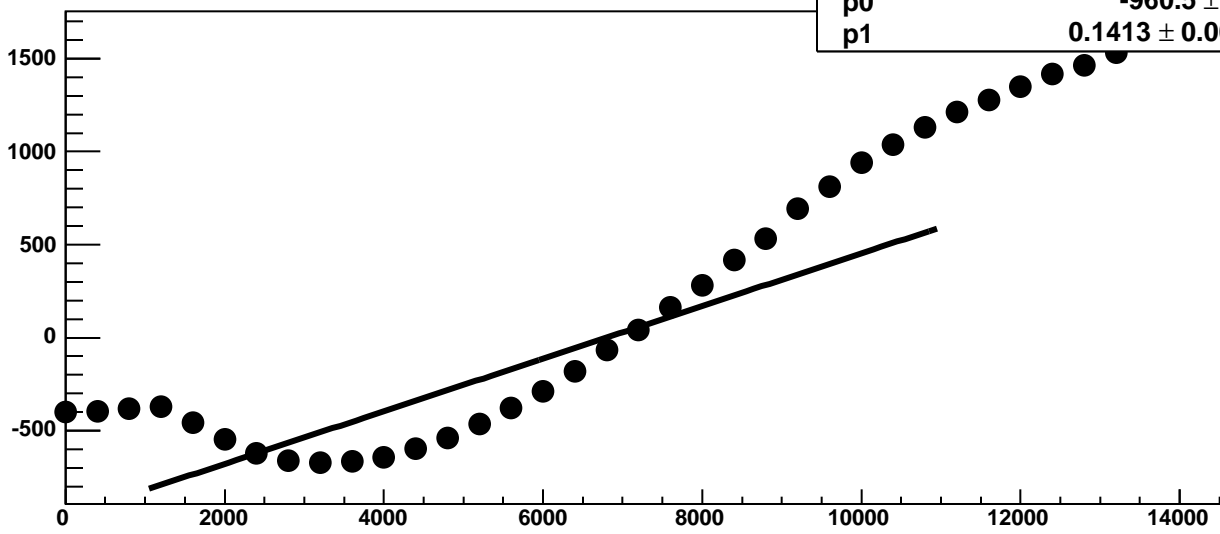
Chip 1, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

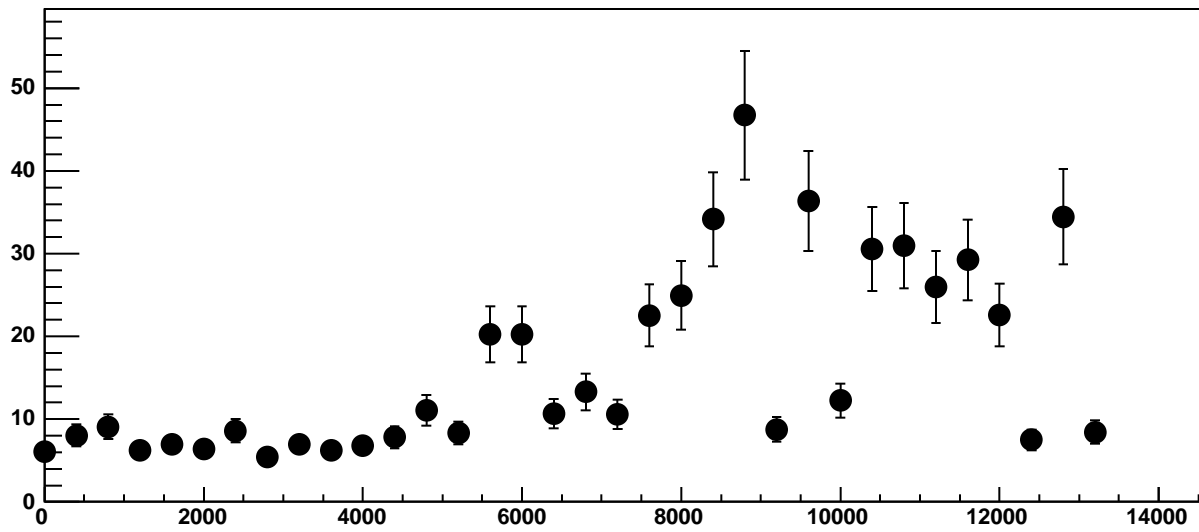


Chip 1, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

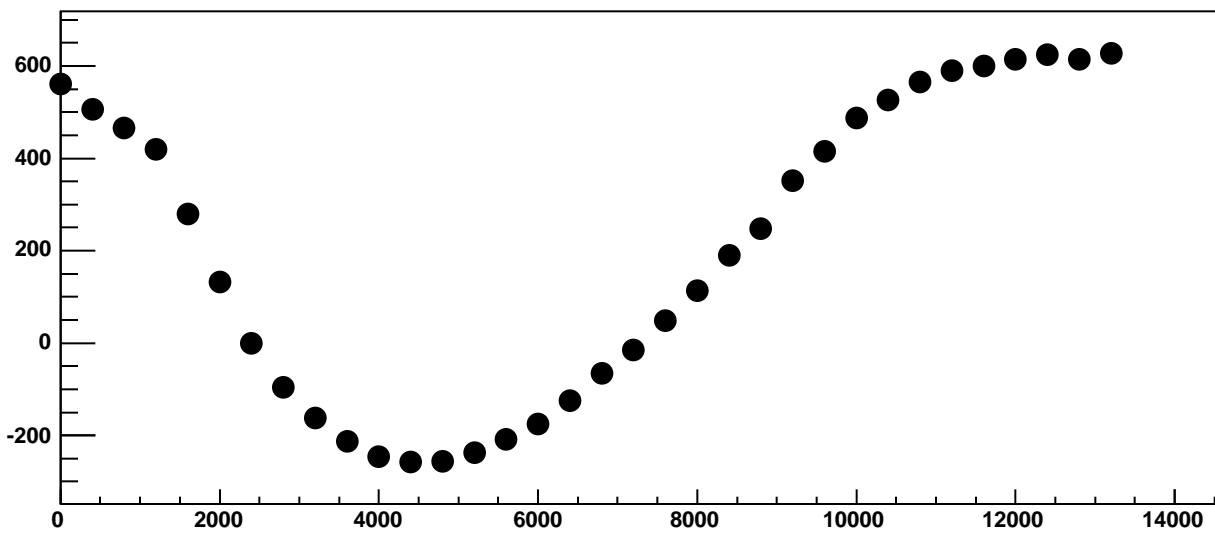


χ^2 / ndf 3.174e+05 / 23
p0 -960.5 ± 0.8524
p1 0.1413 ± 0.0001852

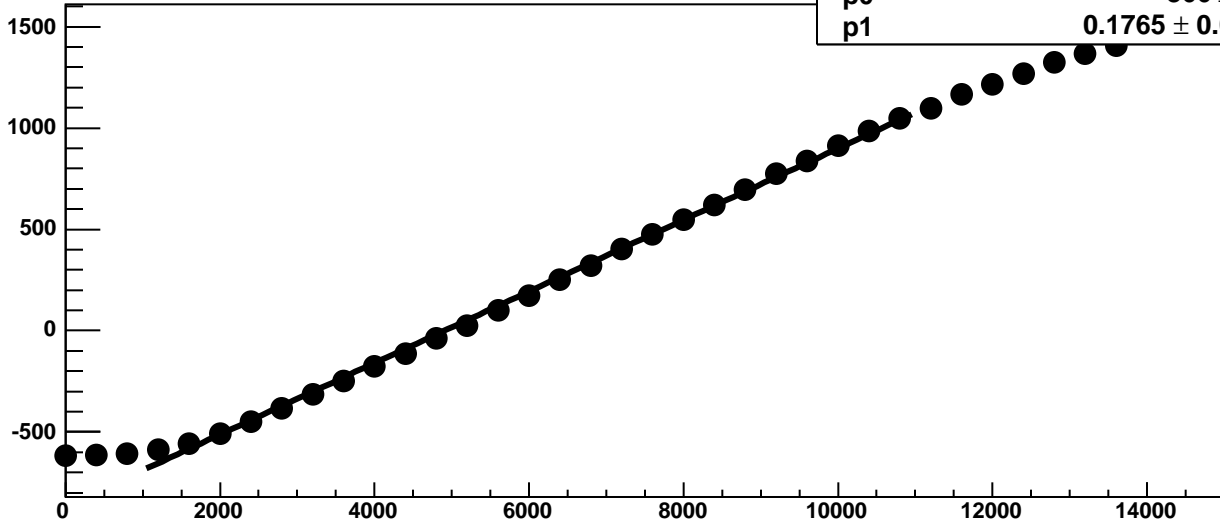
Chip 1, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

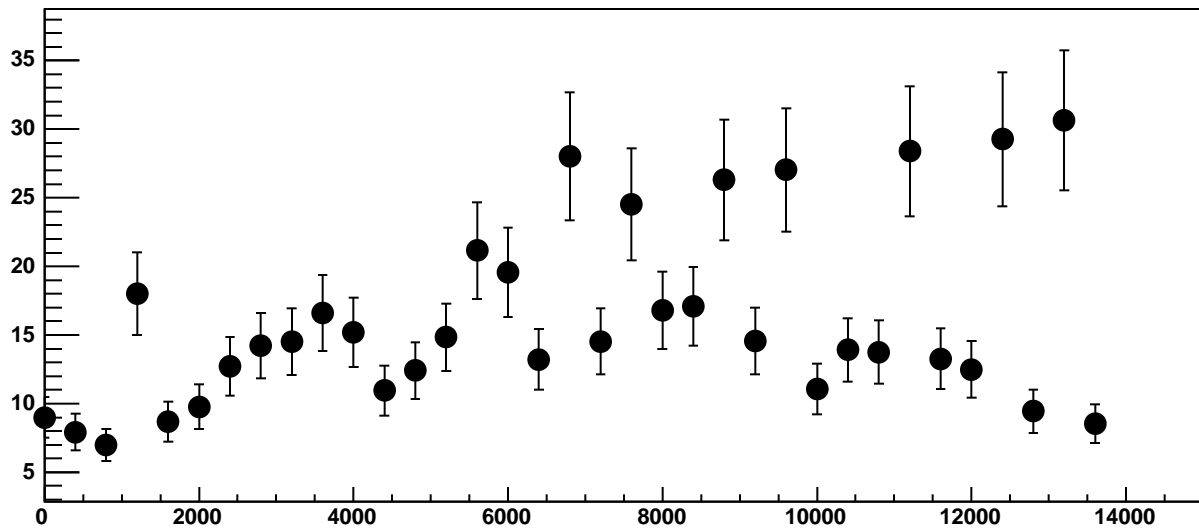


Chip 1, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

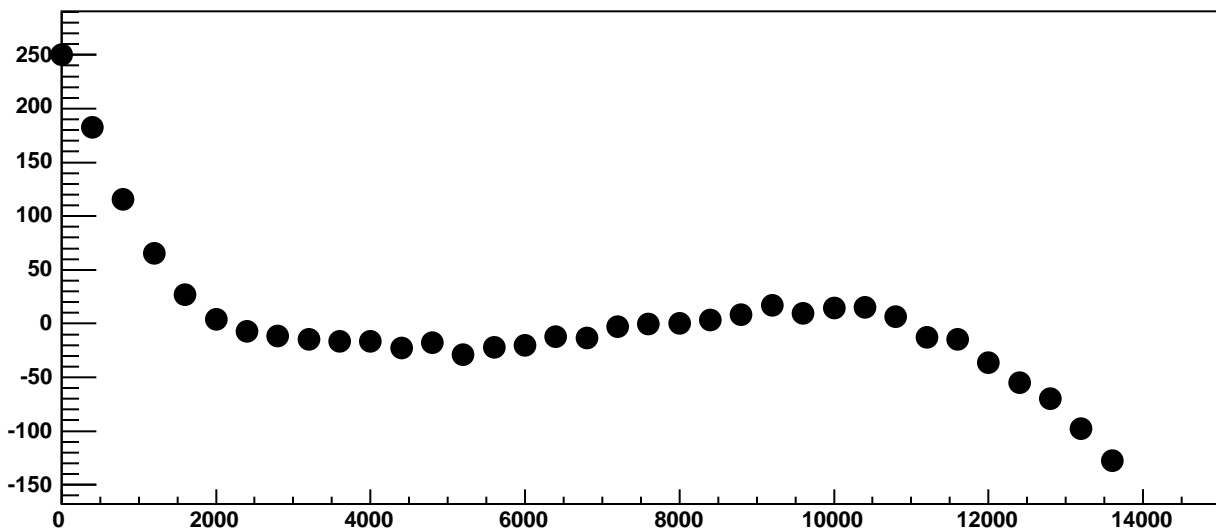


χ^2 / ndf 857.4 / 23
p0 -866 ± 1.318
p1 0.1765 ± 0.000213

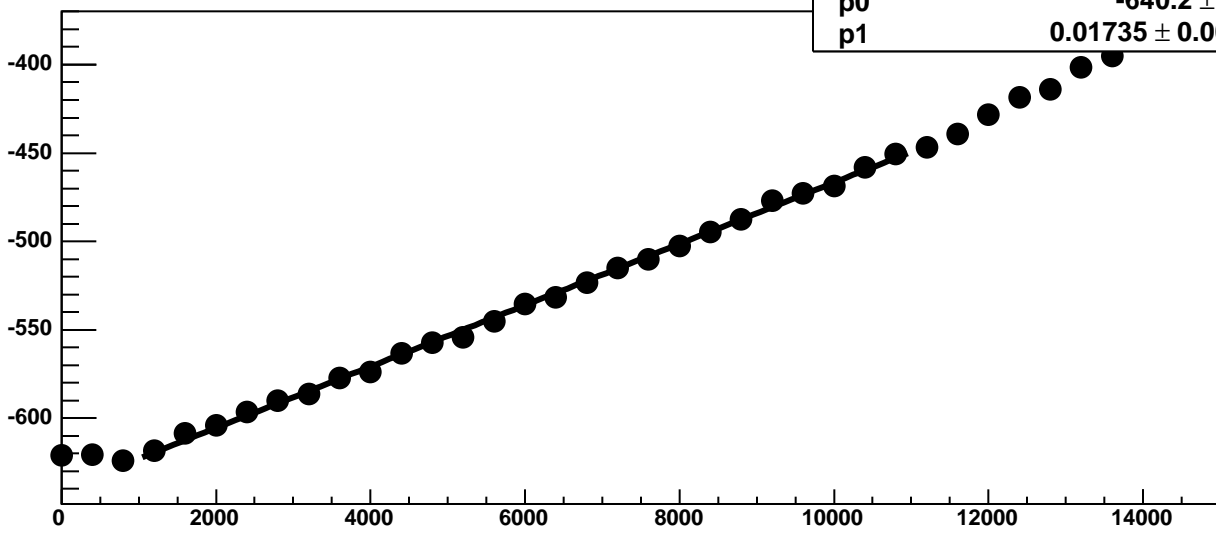
Chip 1, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

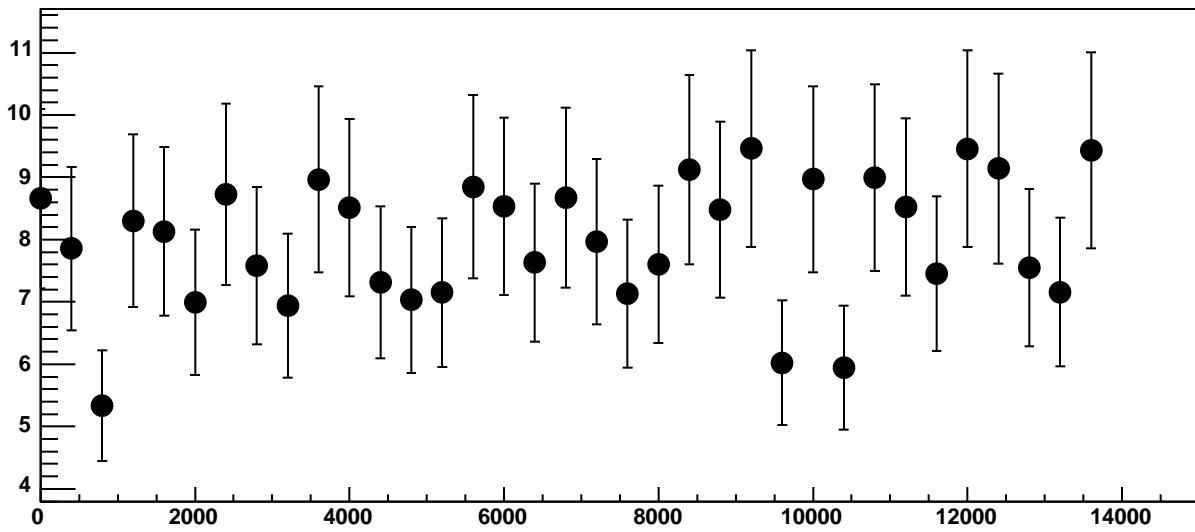


Chip 1, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC

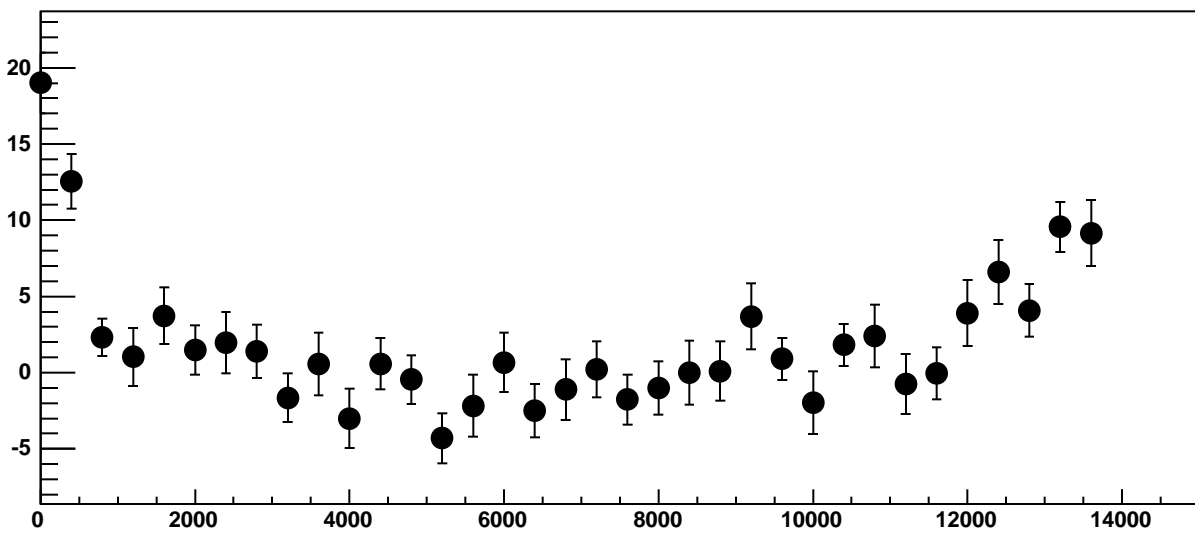


χ^2 / ndf 29.82 / 23
p0 -640.2 ± 0.8234
p1 0.01735 ± 0.0001223

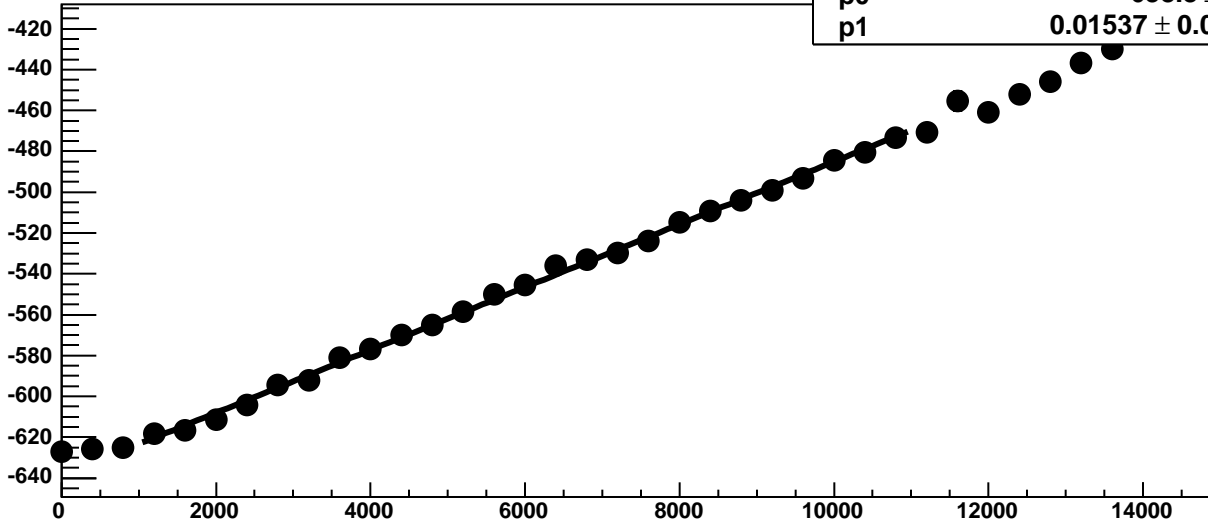
Chip 1, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



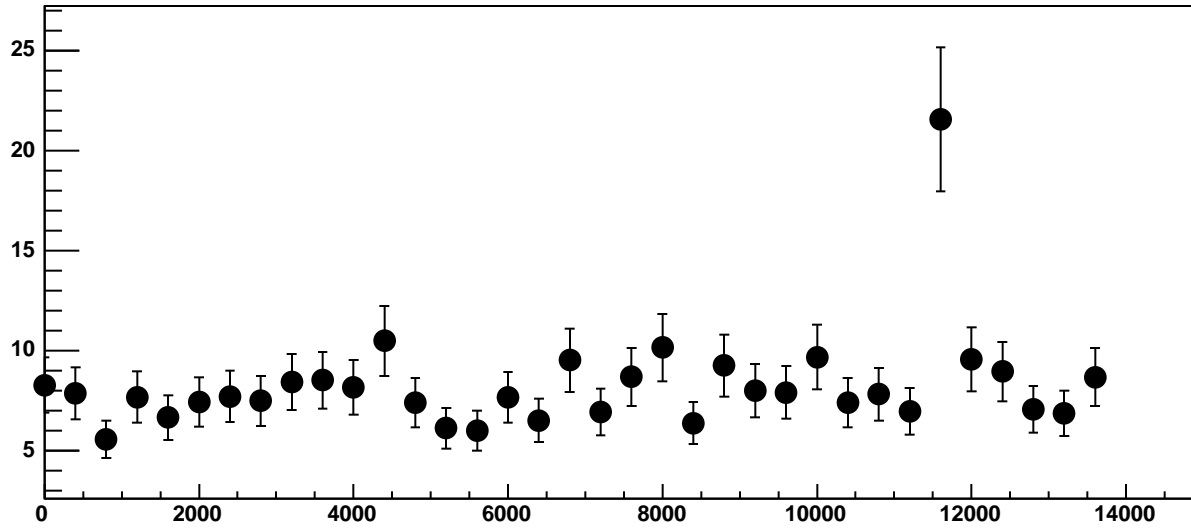
Chip 1, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC



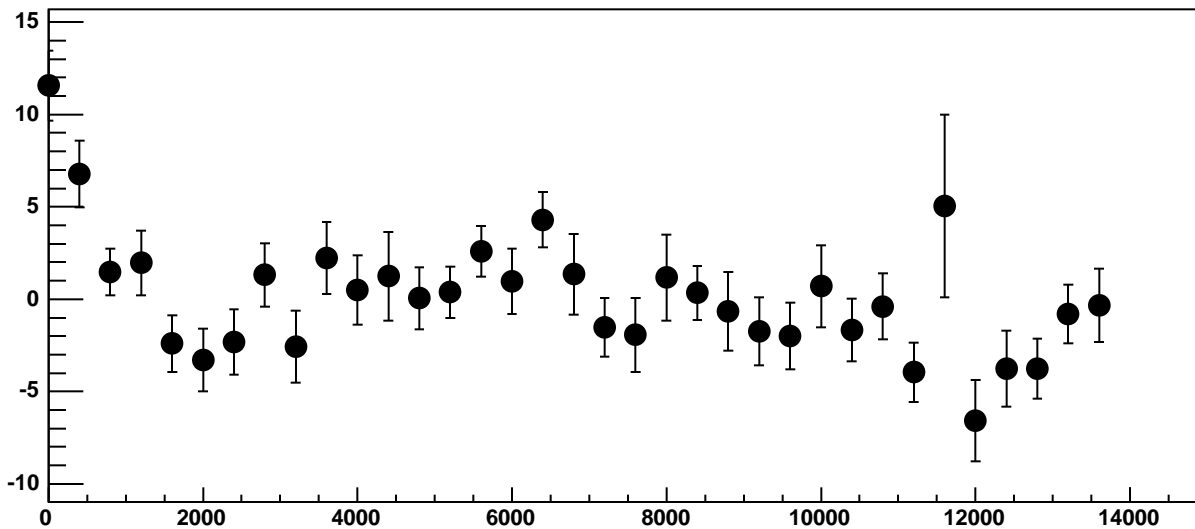
Chip 1, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



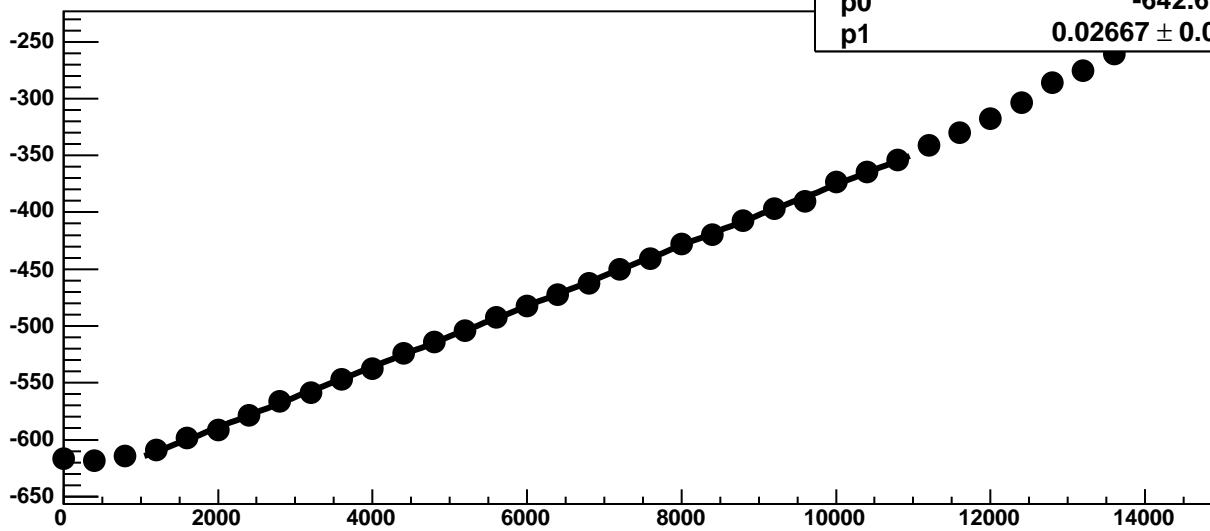
Chip 1, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

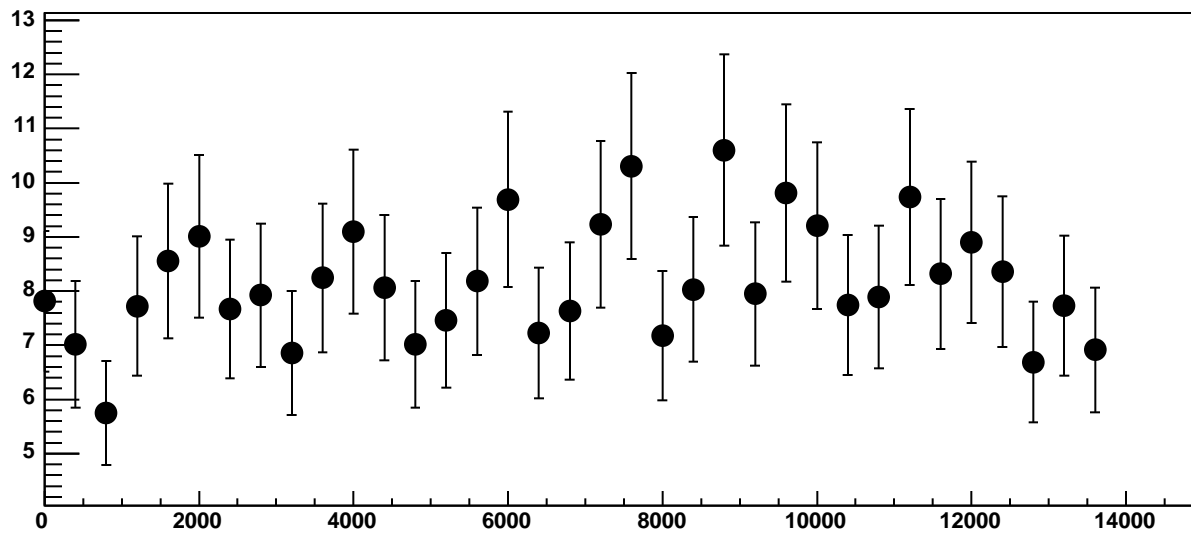


Chip 1, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

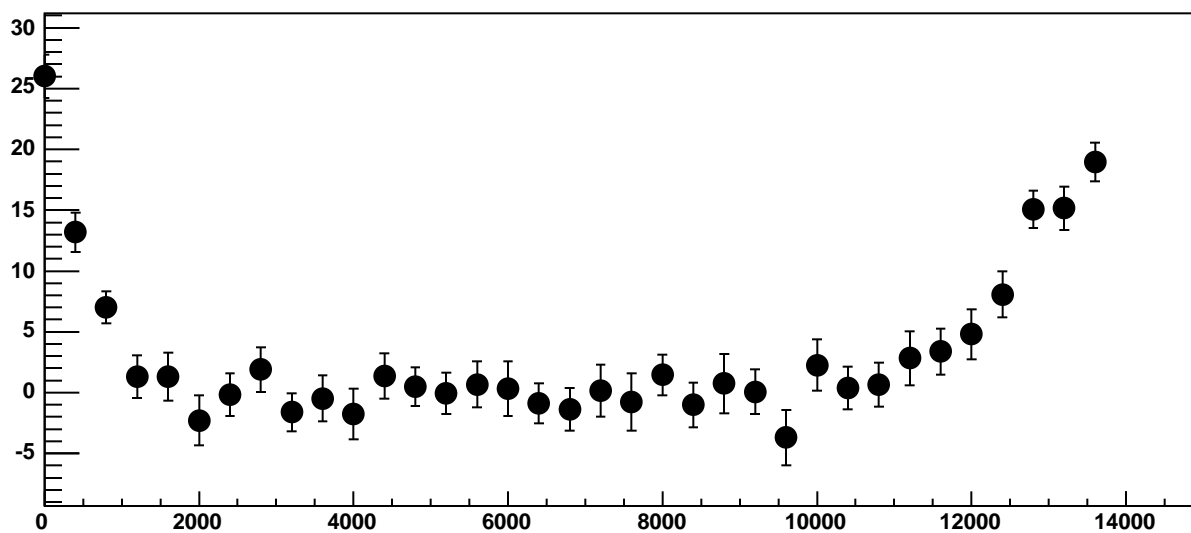


χ^2 / ndf 12.16 / 23
p0 -642.6 ± 0.853
p1 0.02667 ± 0.0001308

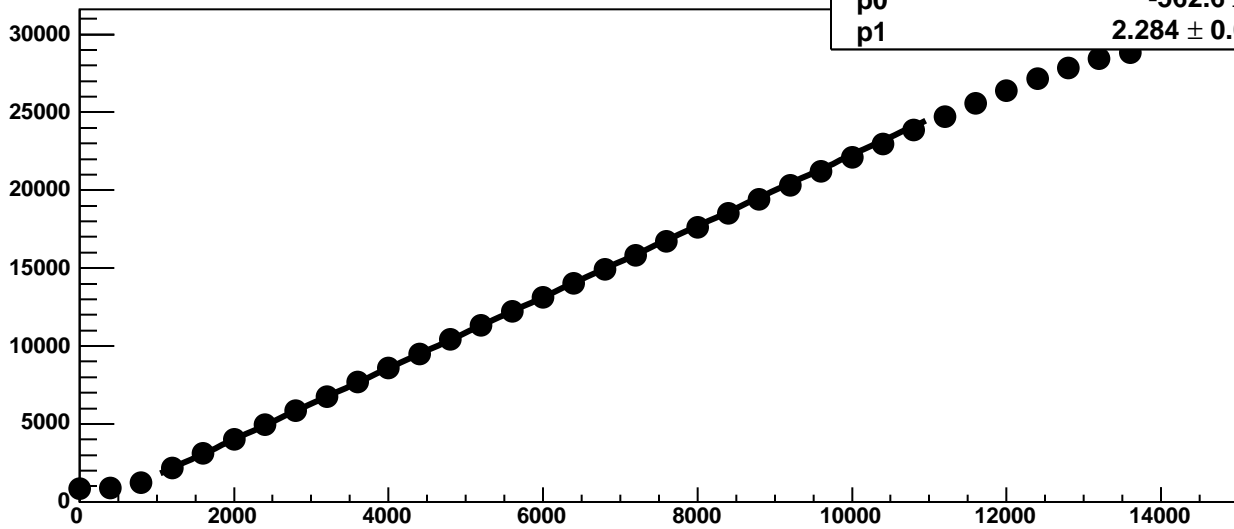
Chip 1, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

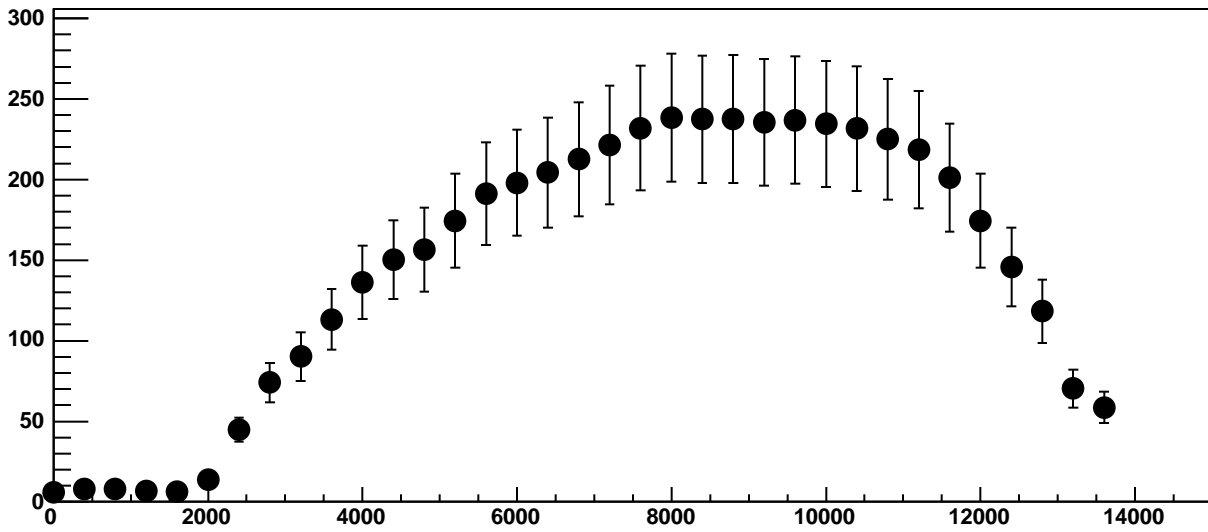


Chip 1, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC

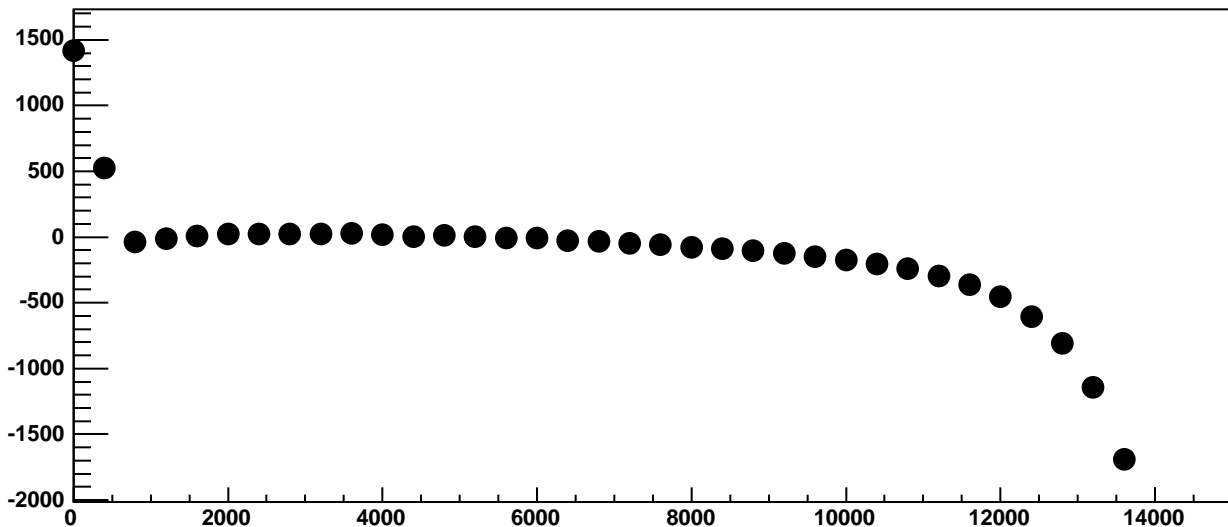


χ^2 / ndf 233.3 / 23
p0 -562.6 ± 2.725
p1 2.284 ± 0.001652

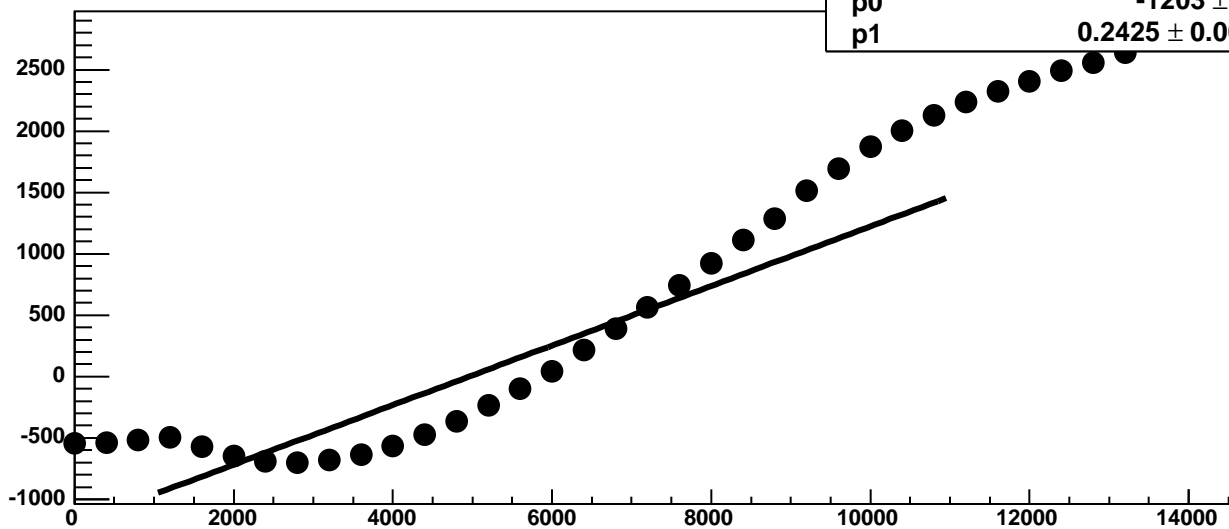
Chip 1, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC

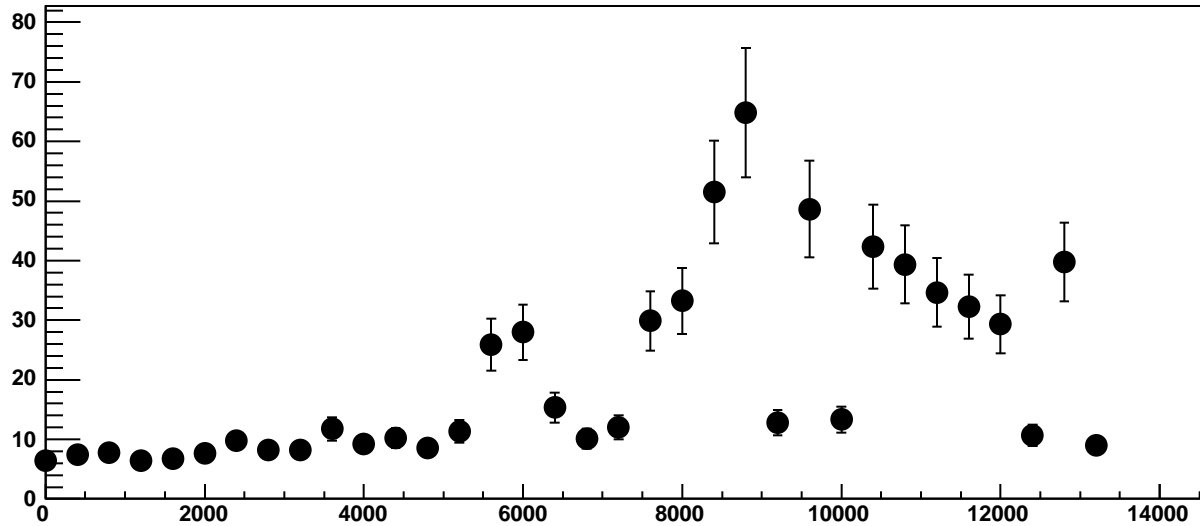


Chip 1, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

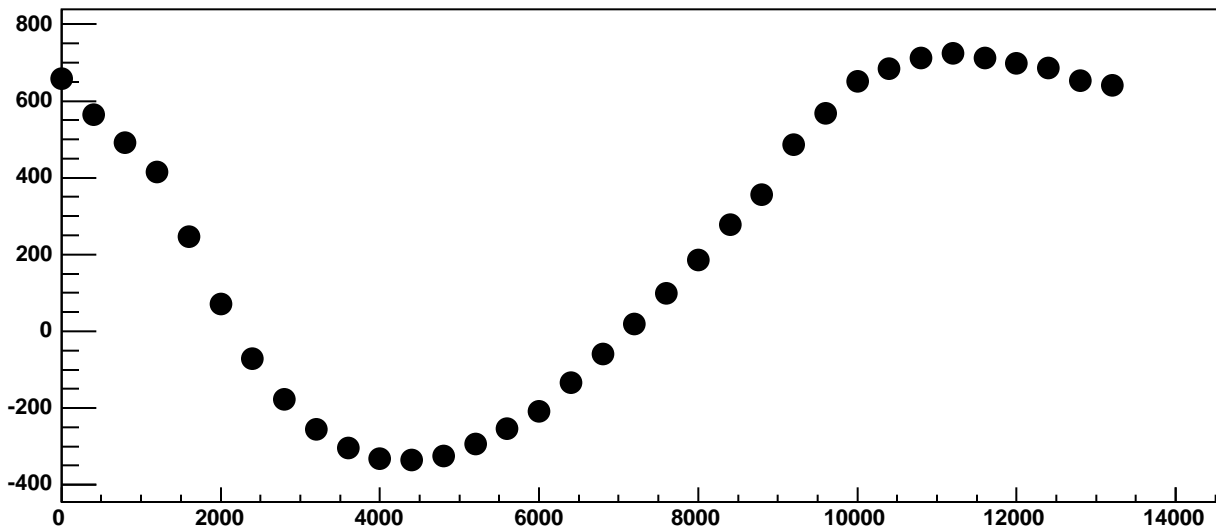


χ^2 / ndf 3.254e+05 / 23
p0 -1203 ± 0.9874
p1 0.2425 ± 0.0002173

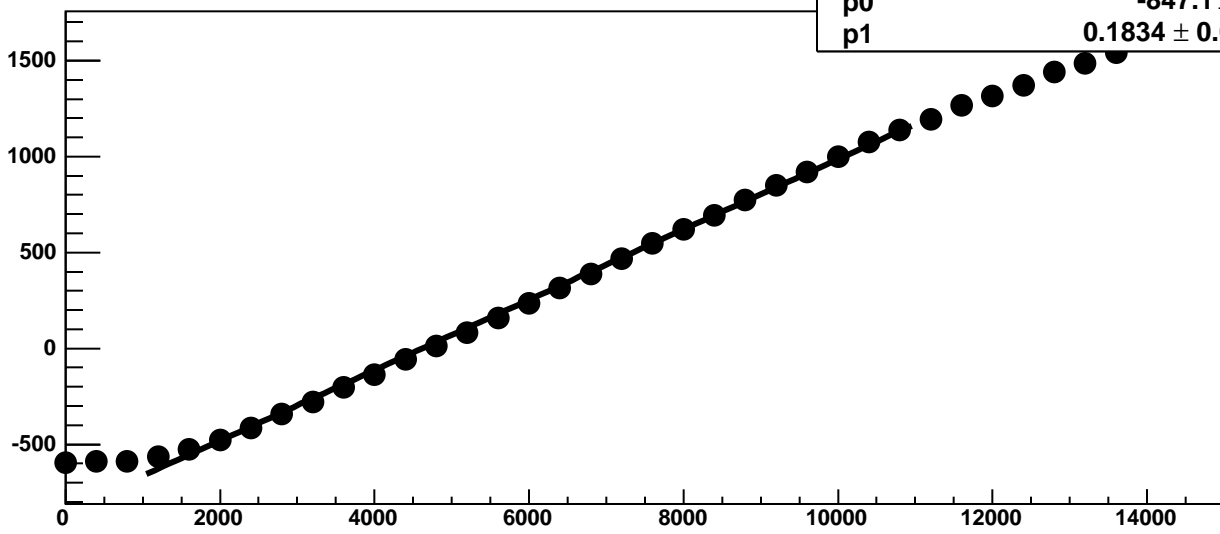
Chip 1, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



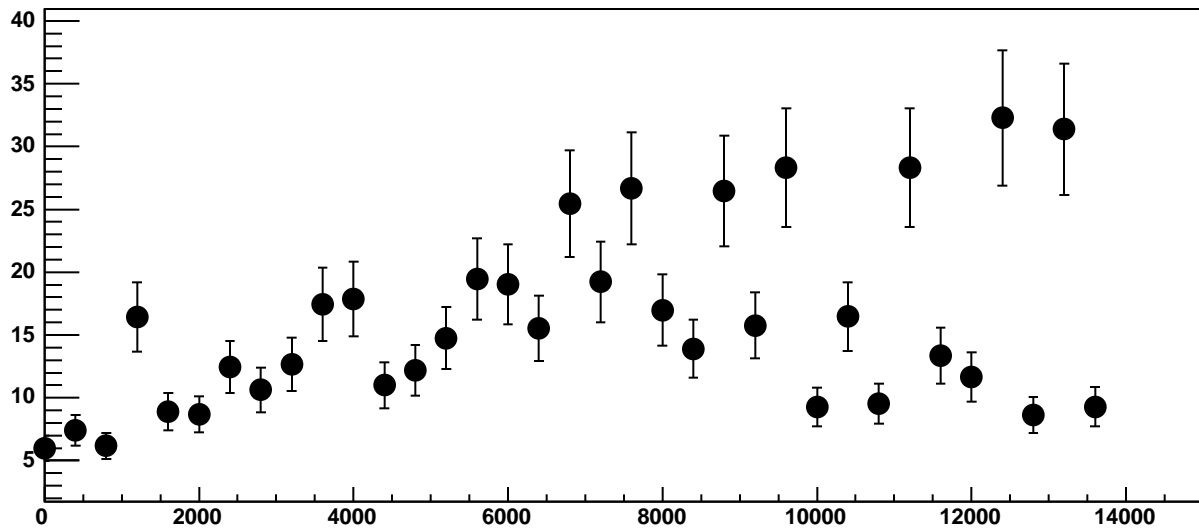
Chip 1, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



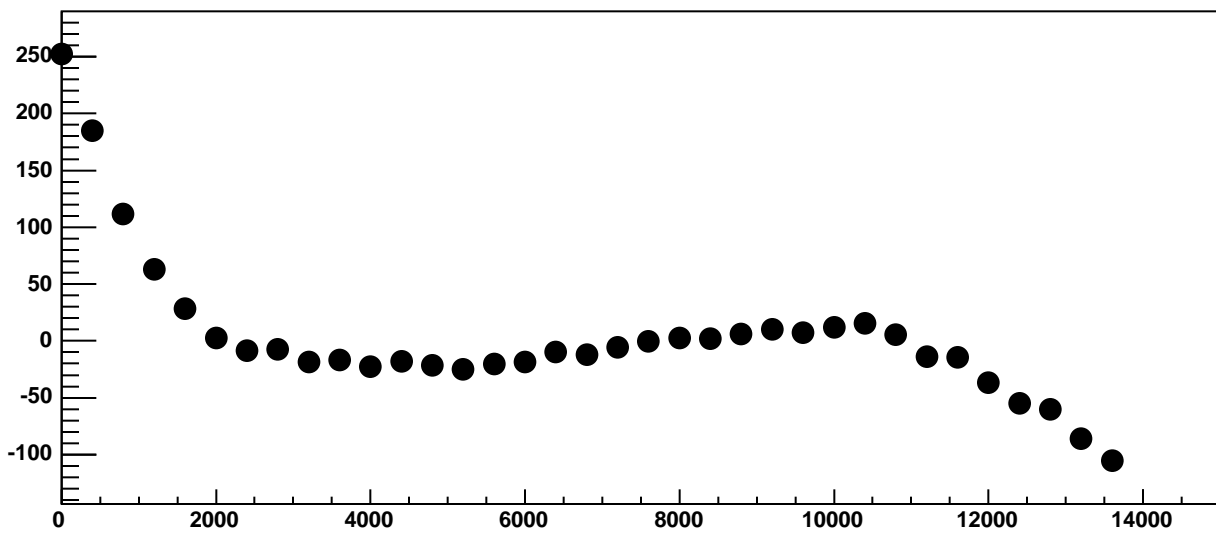
Chip 1, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



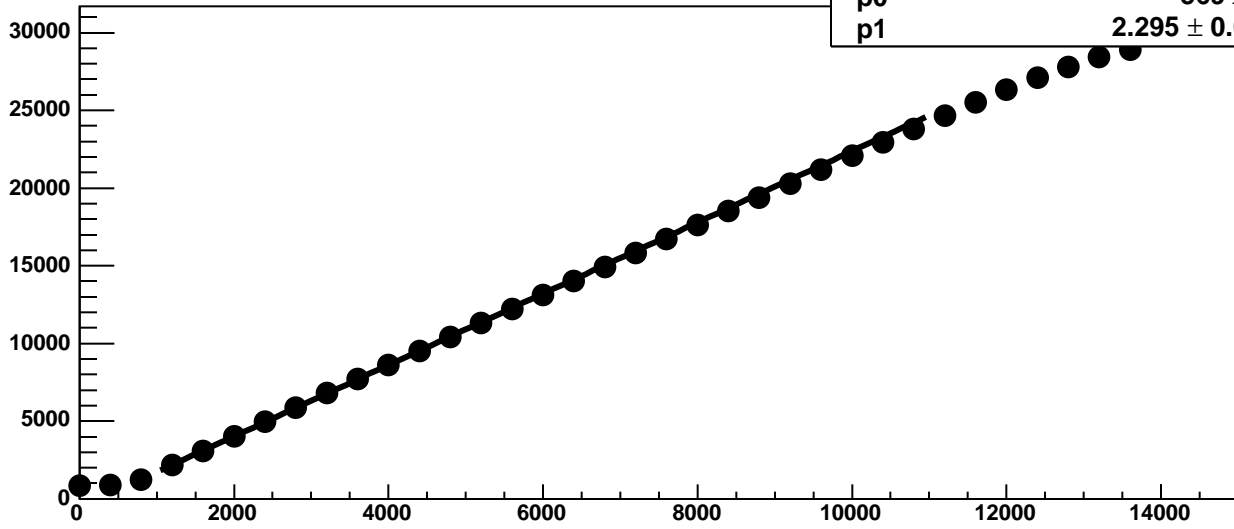
Chip 1, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



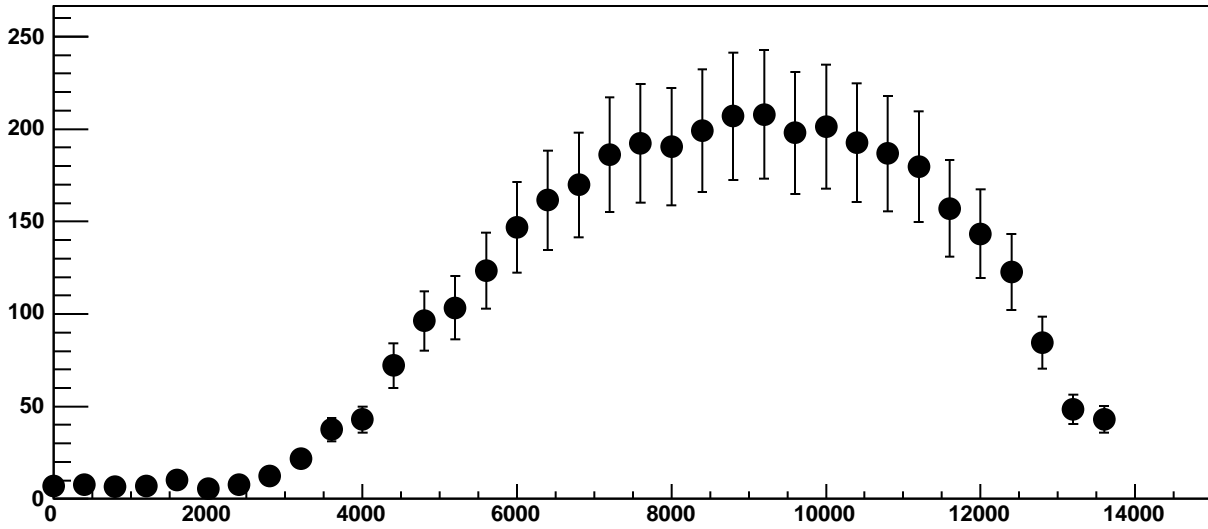
Chip 1, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



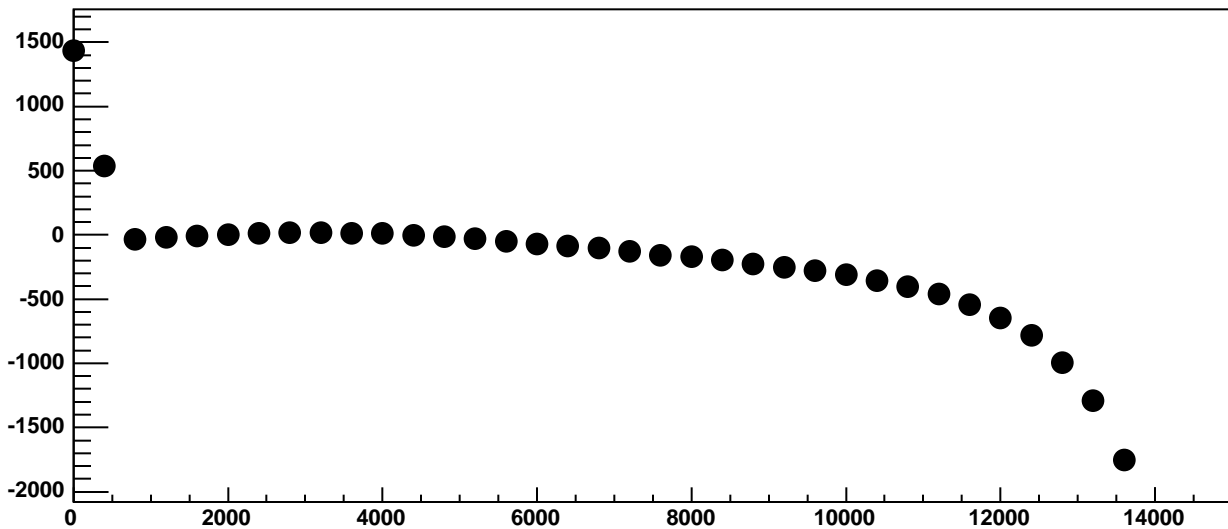
Chip 1, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC



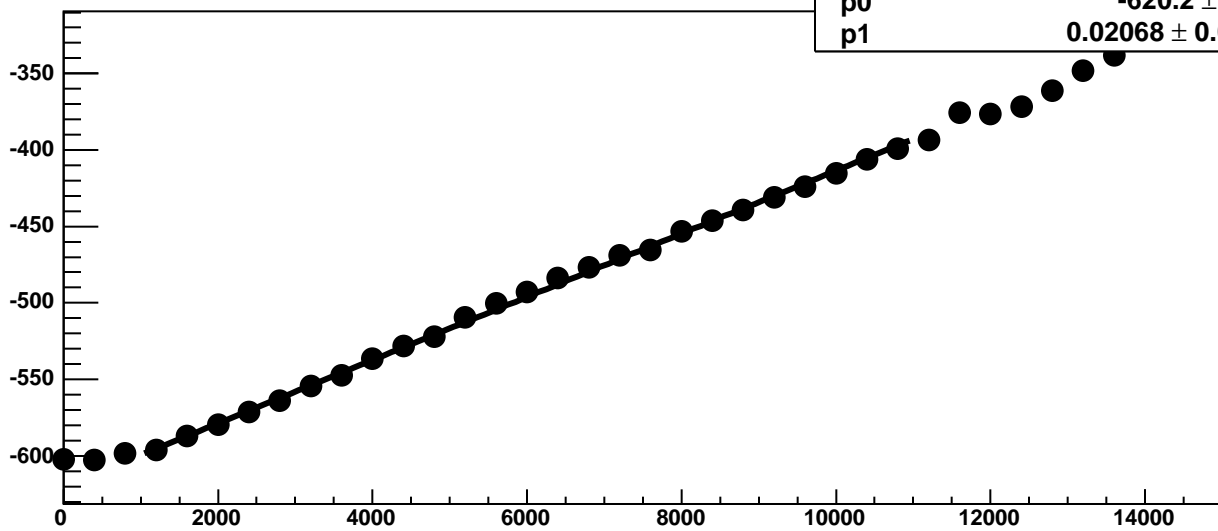
Chip 1, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC

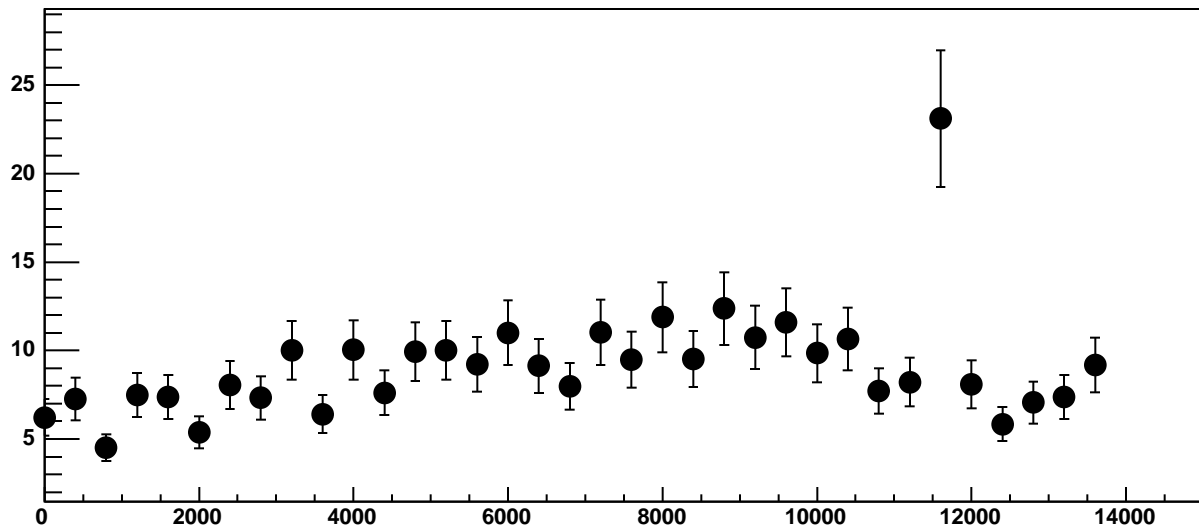


Chip 1, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

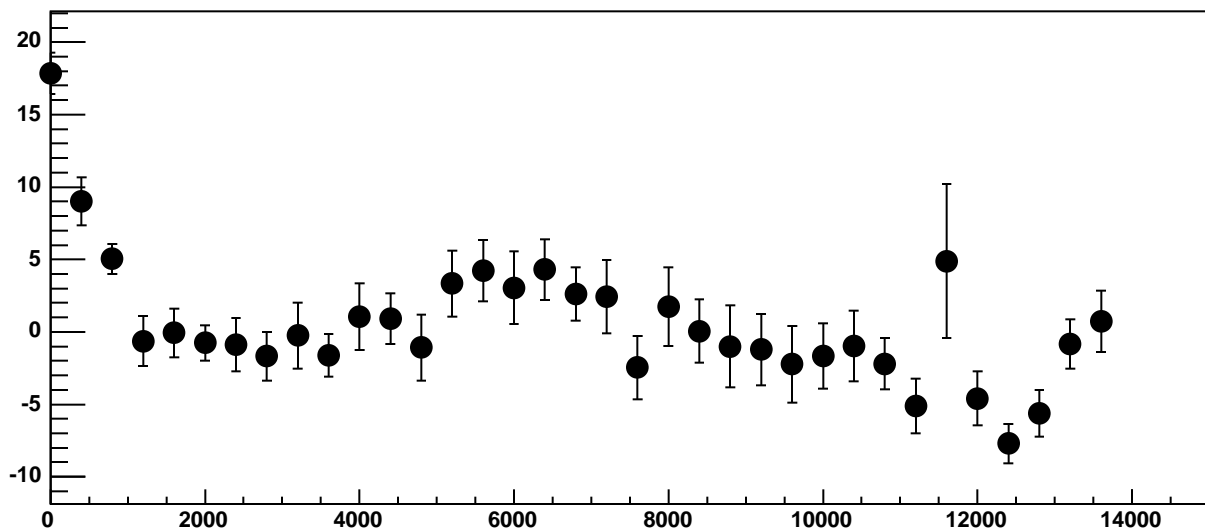


χ^2 / ndf 23.43 / 23
p0 -620.2 ± 0.8048
p1 0.02068 ± 0.000134

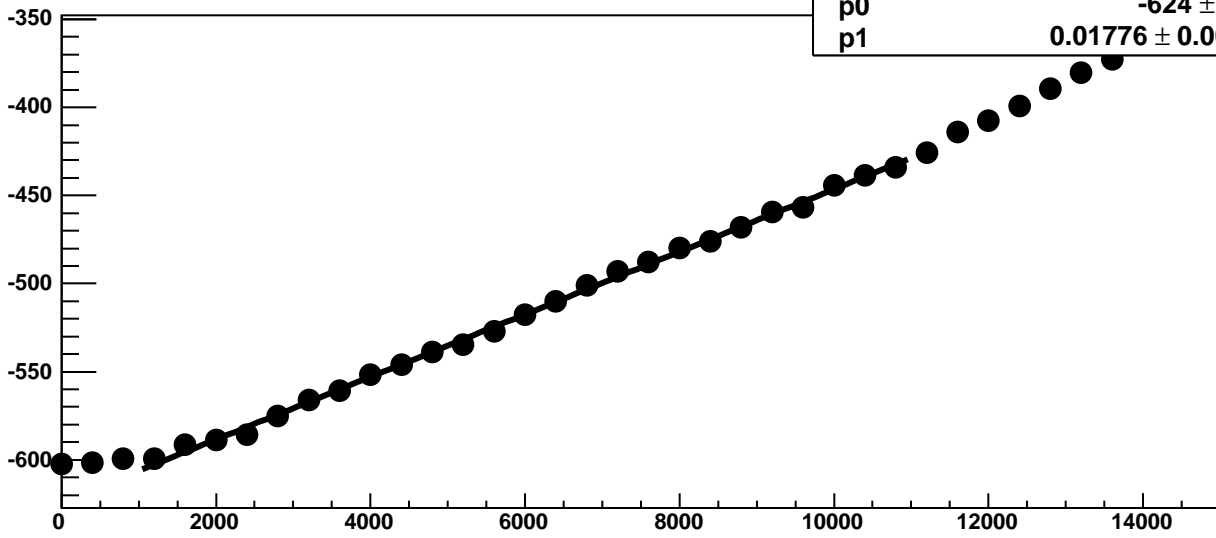
Chip 1, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

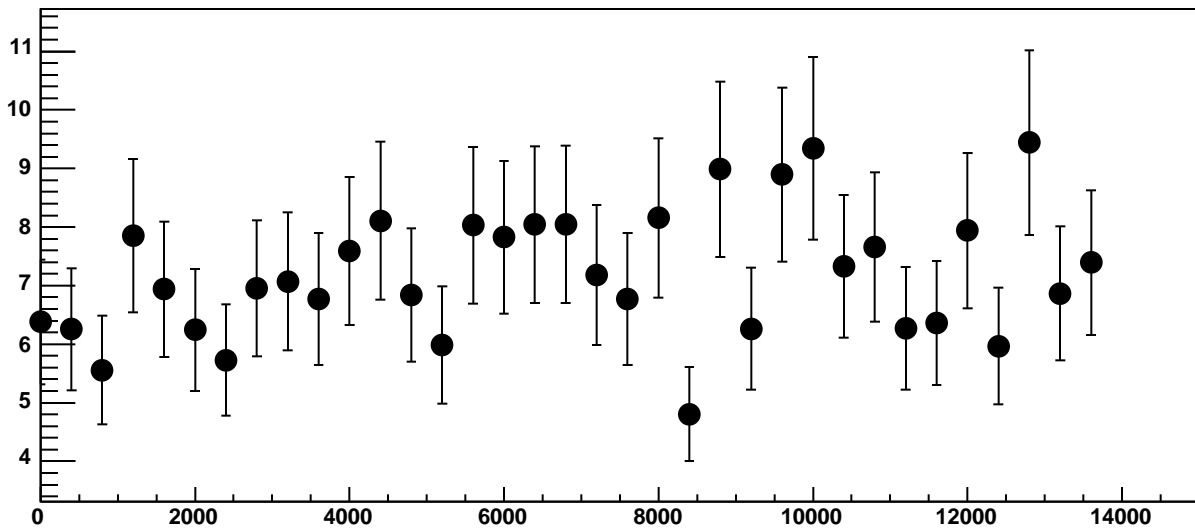


Chip 1, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

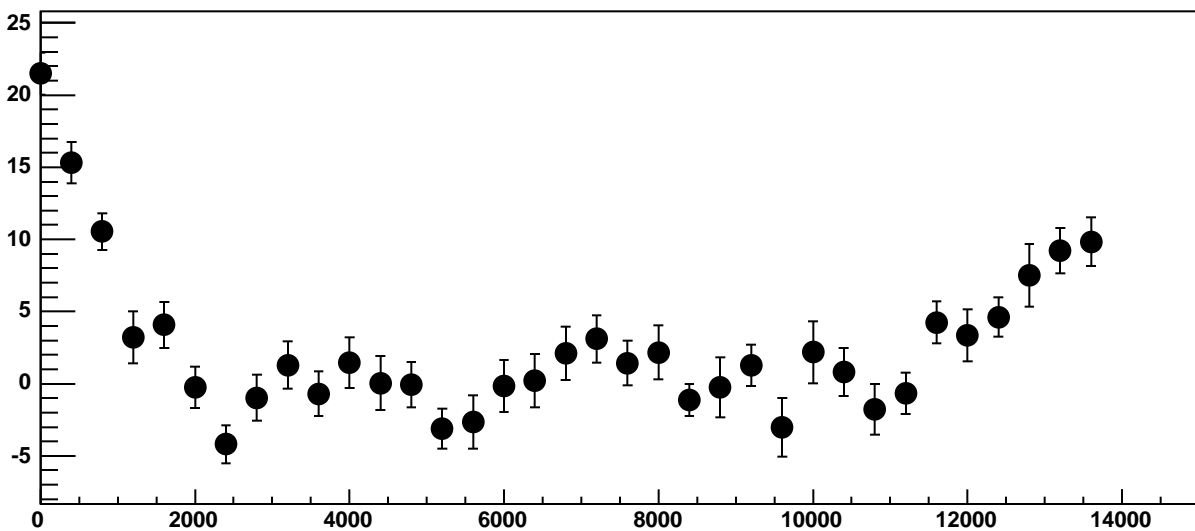


χ^2 / ndf 42.47 / 23
p0 -624 ± 0.7369
p1 0.01776 ± 0.0001135

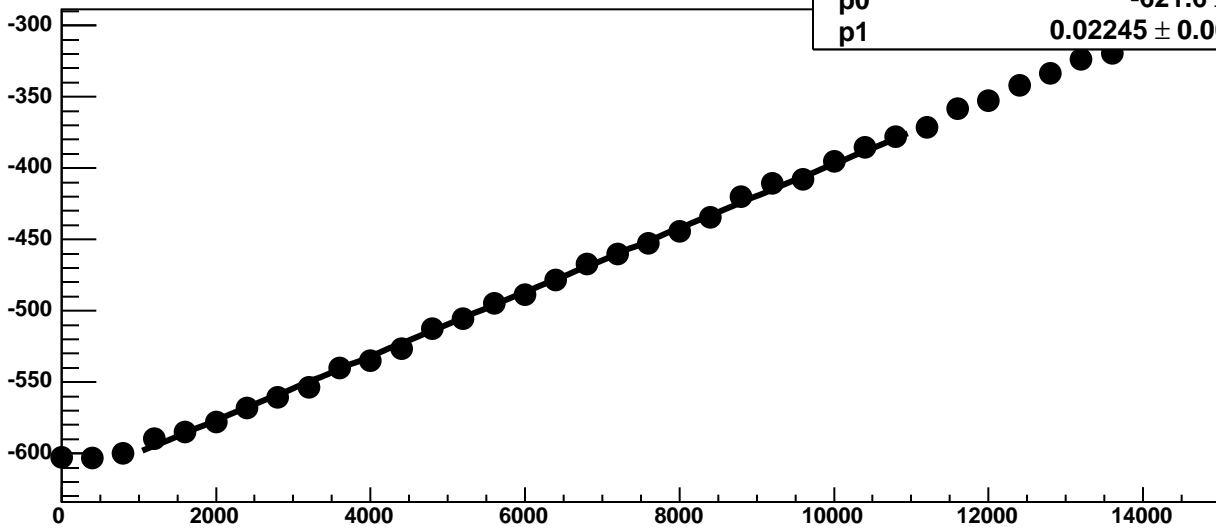
Chip 1, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

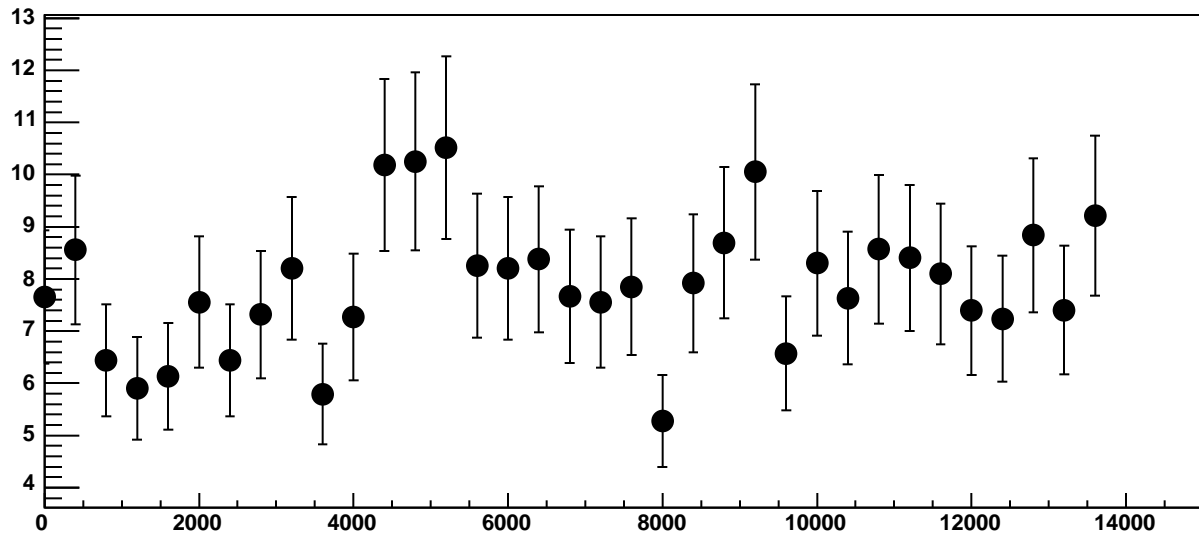


Chip 1, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

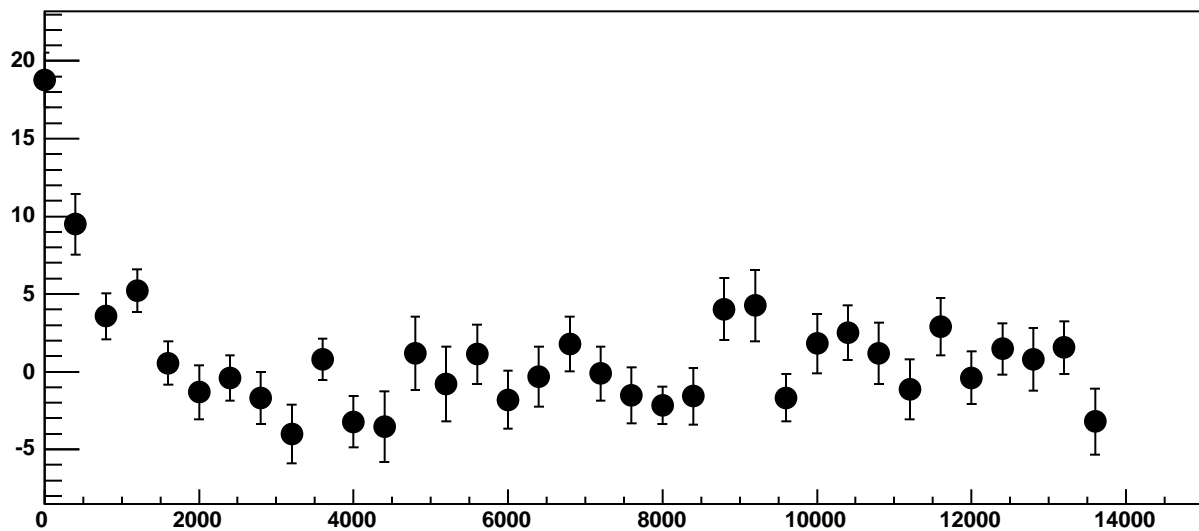


χ^2 / ndf 47.14 / 23
p0 -621.6 ± 0.736
p1 0.02245 ± 0.0001143

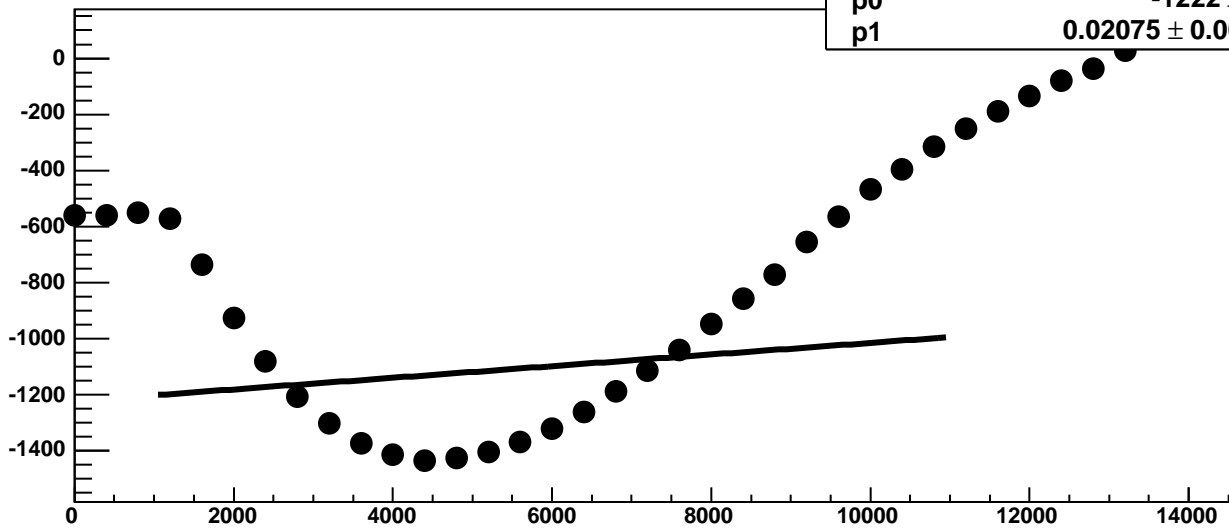
Chip 1, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC

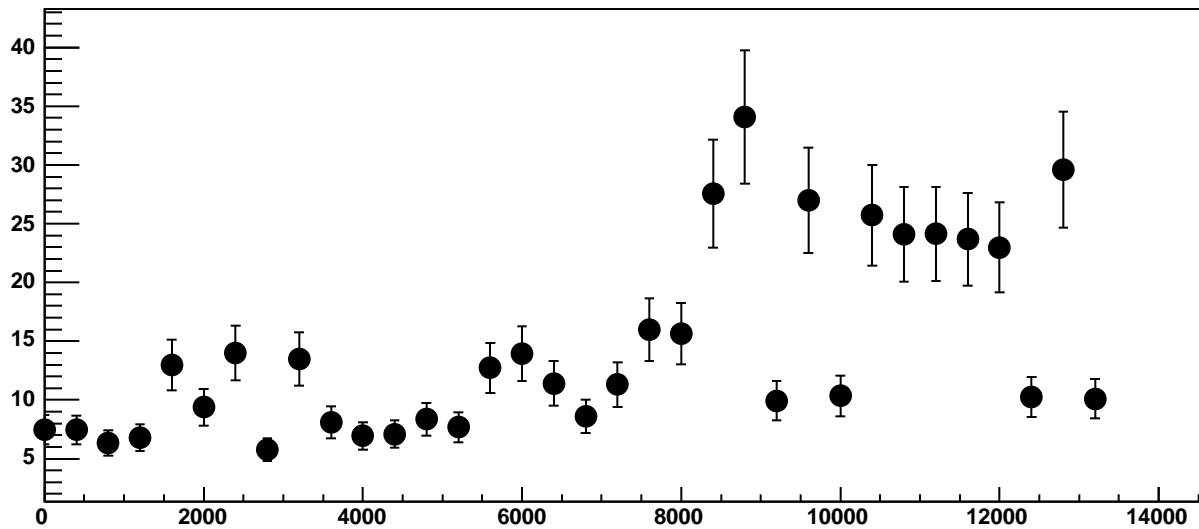


Chip 1, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

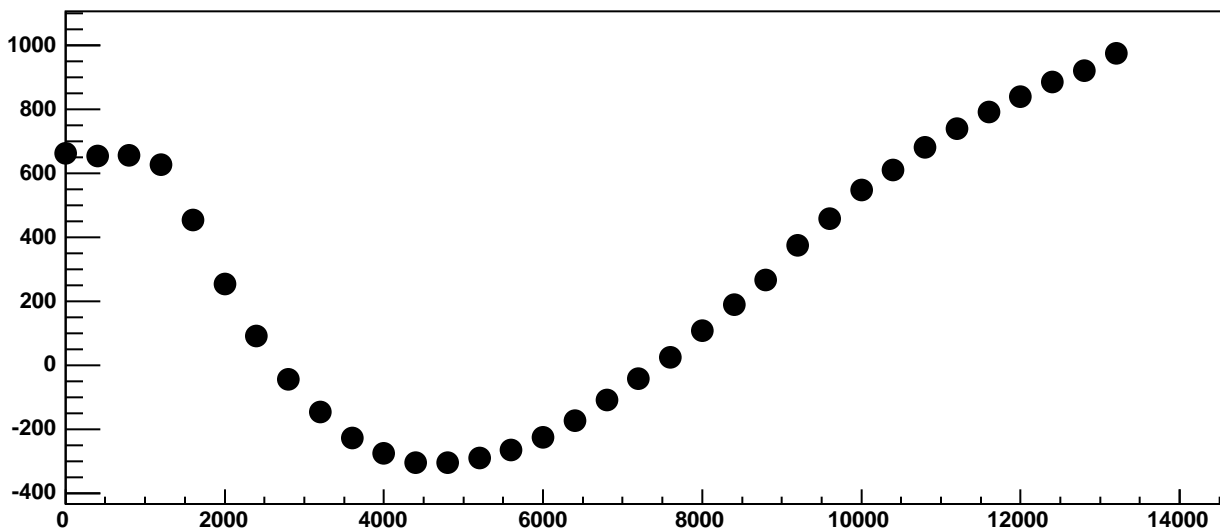


χ^2 / ndf 4.686e+05 / 23
p0 -1222 ± 1.008
p1 0.02075 ± 0.0001903

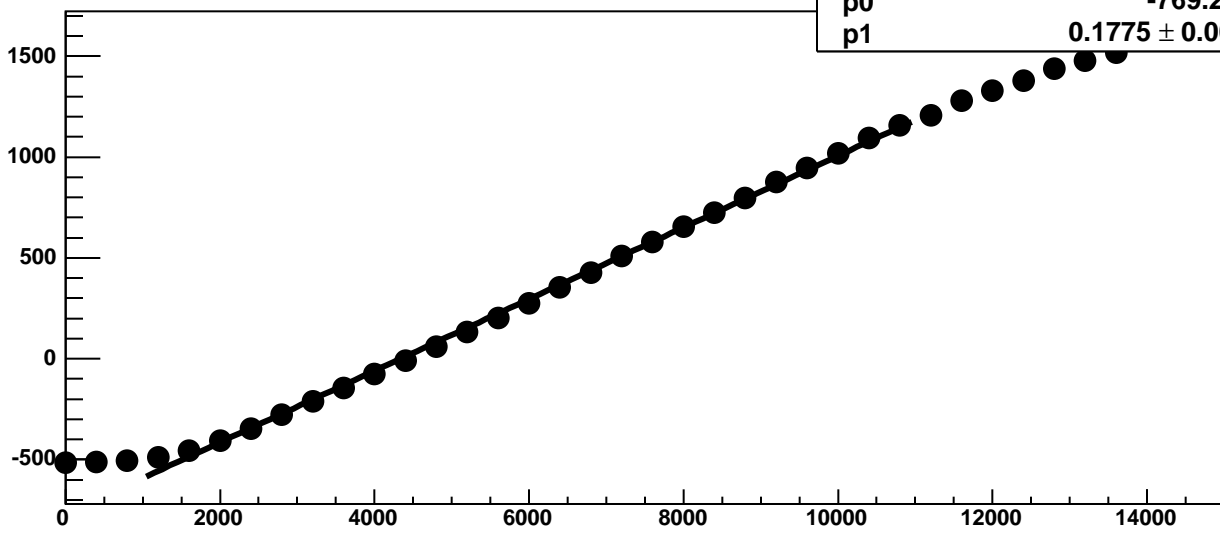
Chip 1, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



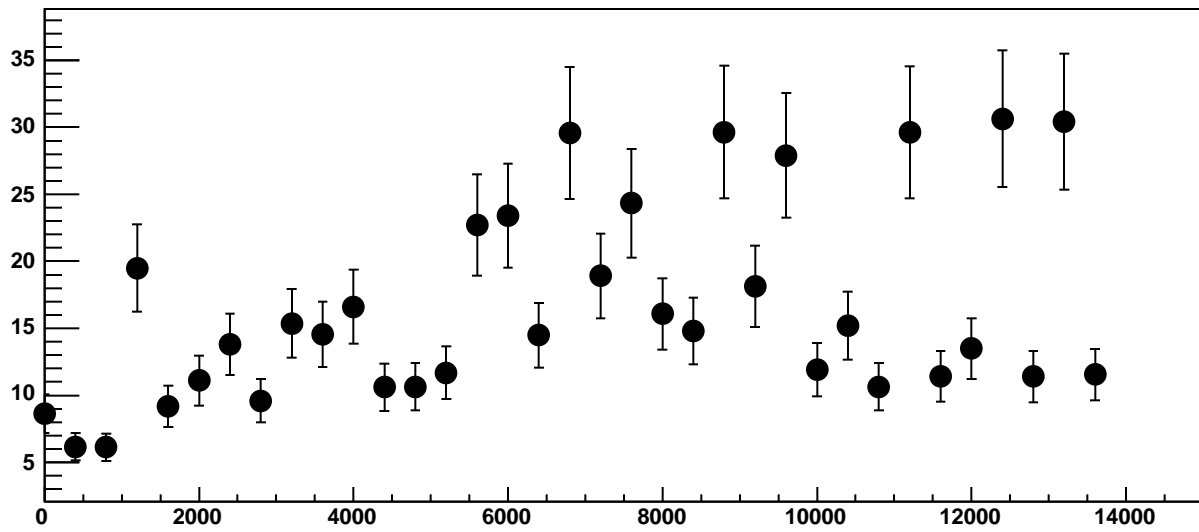
Chip 1, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



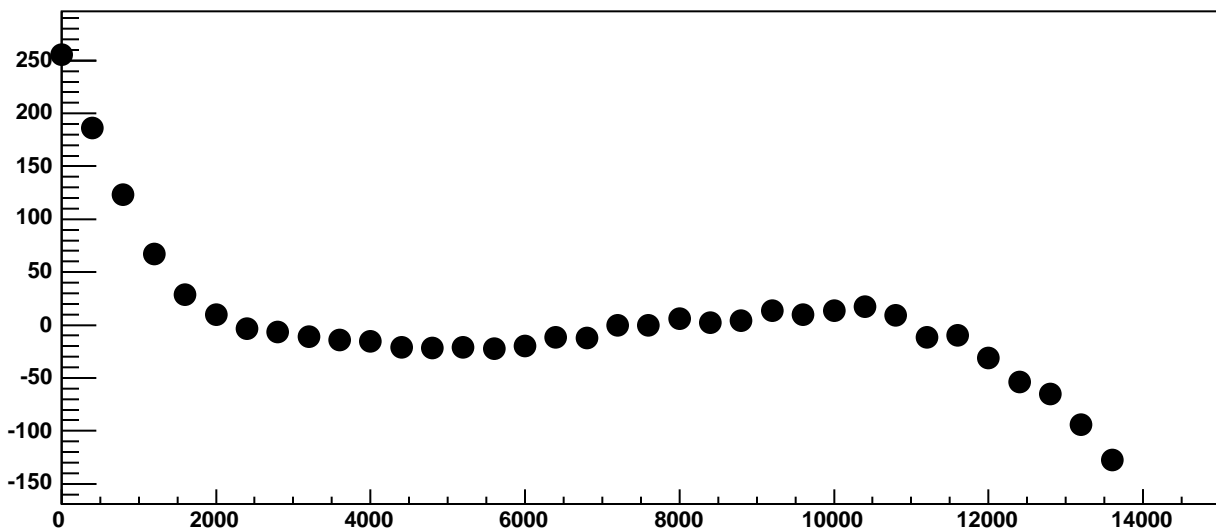
Chip 1, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



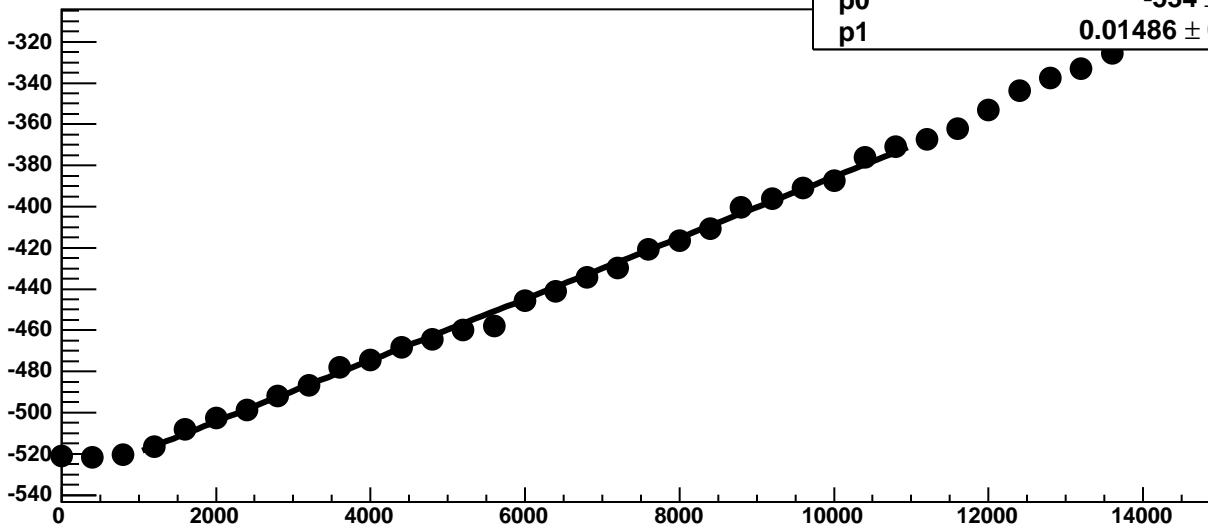
Chip 1, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

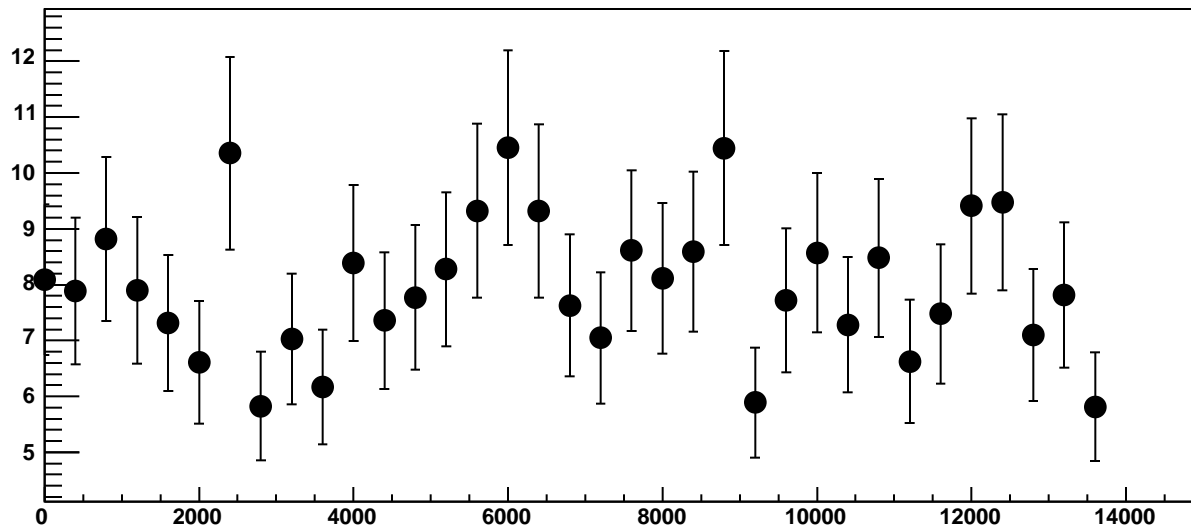


Chip 1, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

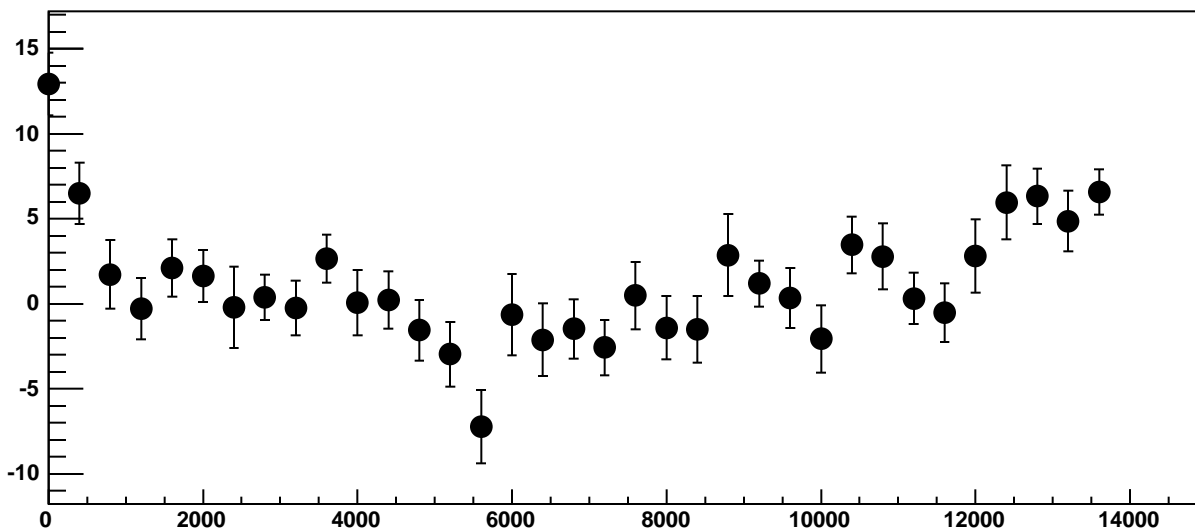


χ^2 / ndf 36.16 / 23
p0 -534 ± 0.7811
p1 0.01486 ± 0.00012

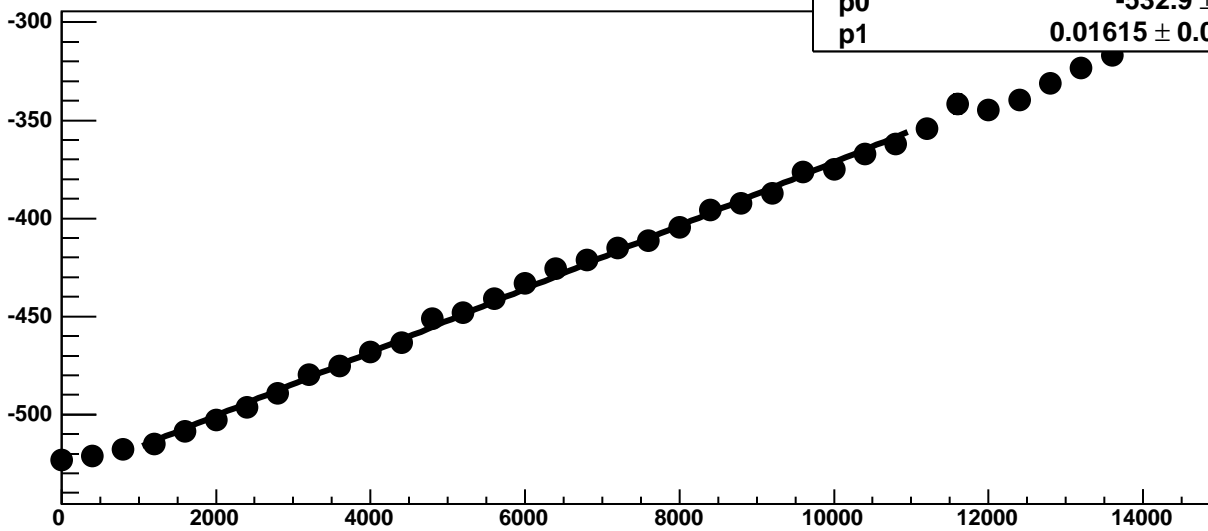
Chip 1, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

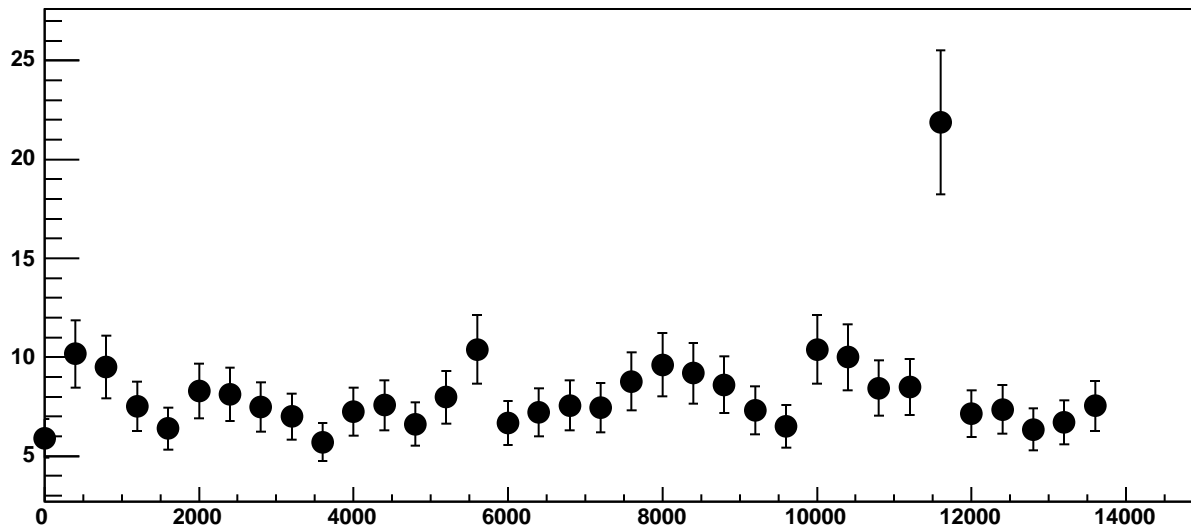


Chip 1, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

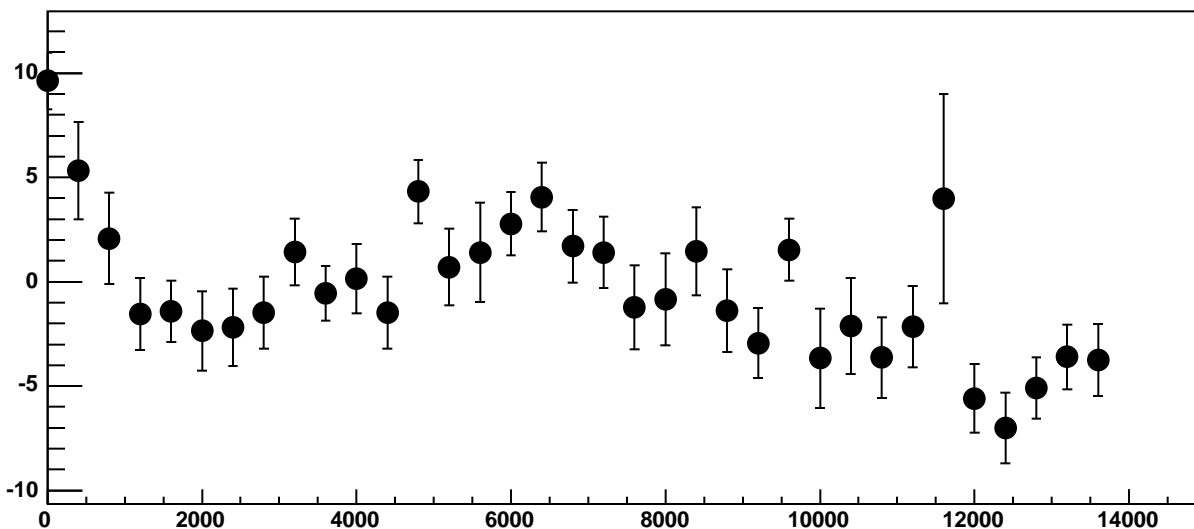


χ^2 / ndf 39 / 23
p0 -532.9 ± 0.7845
p1 0.01615 ± 0.0001246

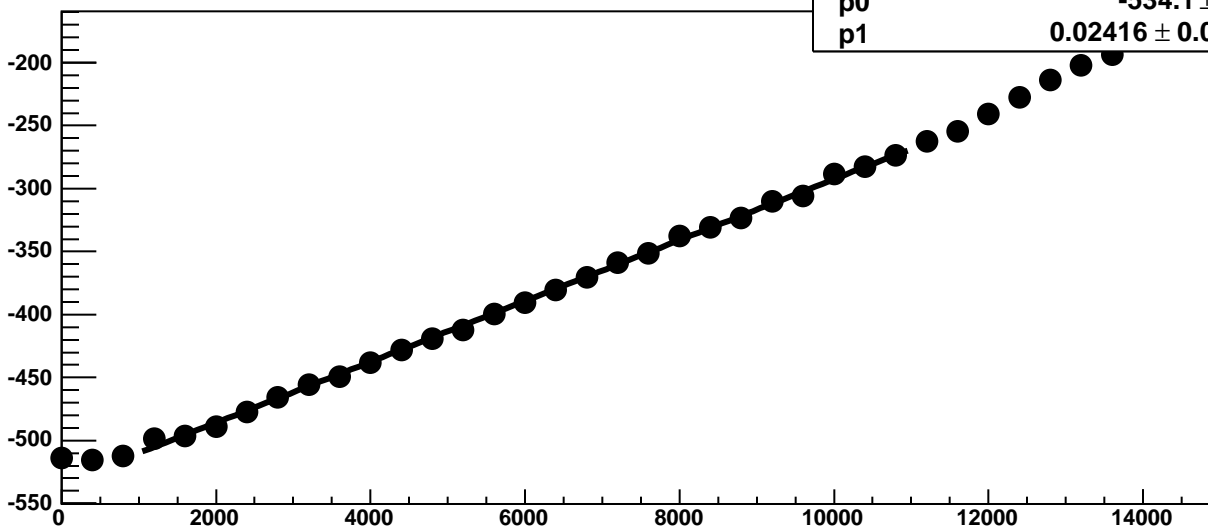
Chip 1, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

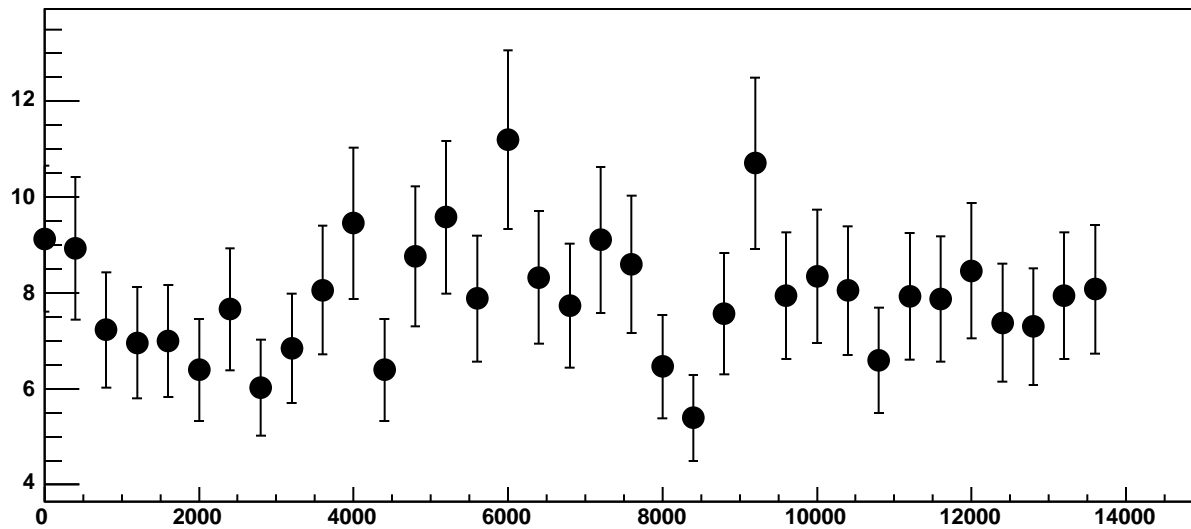


Chip 1, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

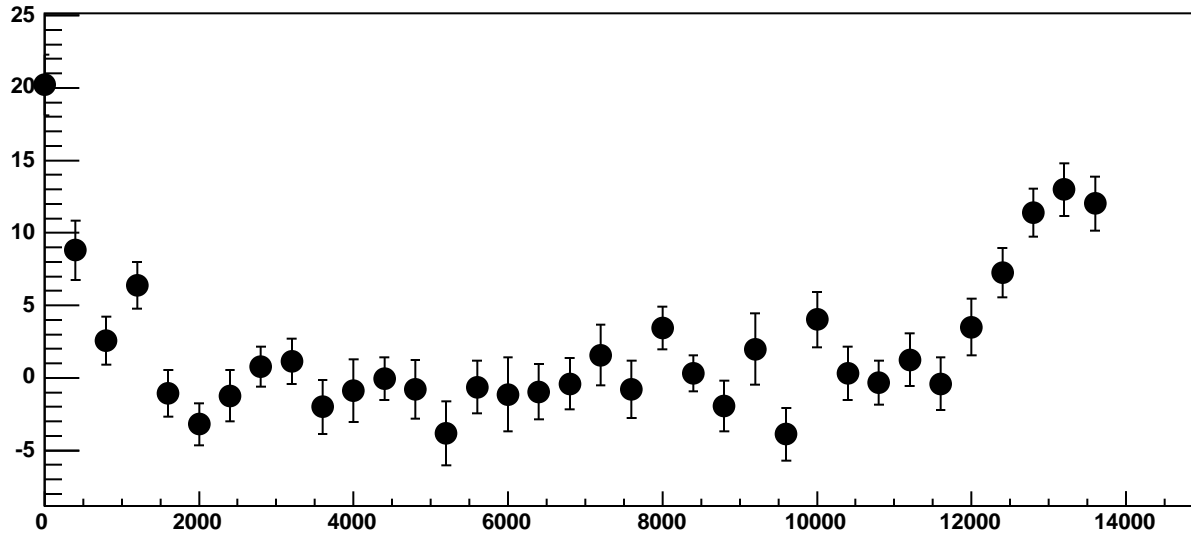


χ^2 / ndf 44.8 / 23
p0 -534.1 ± 0.7542
p1 0.02416 ± 0.0001148

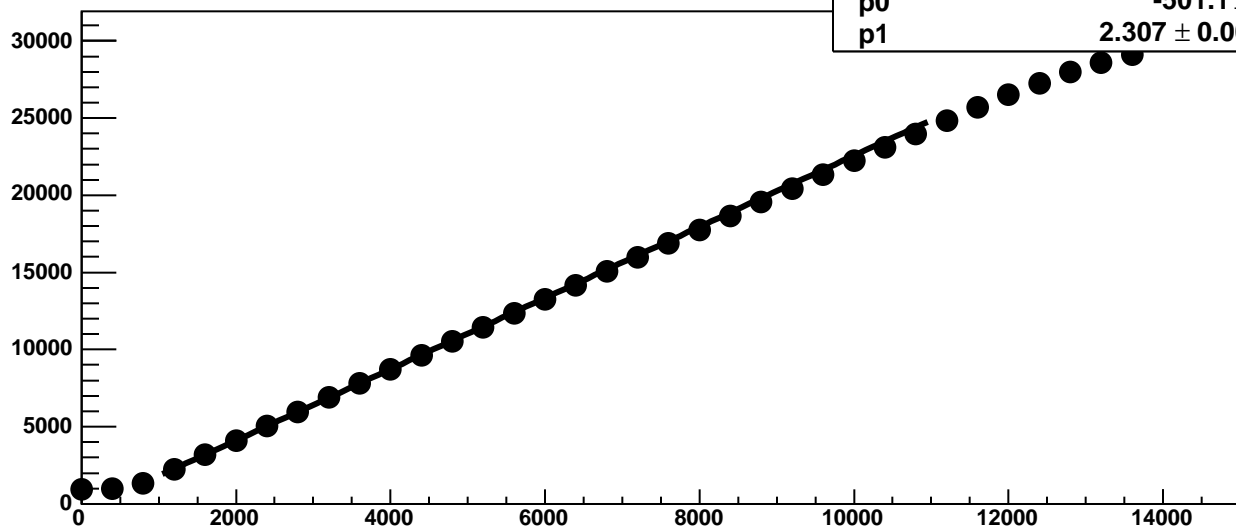
Chip 1, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



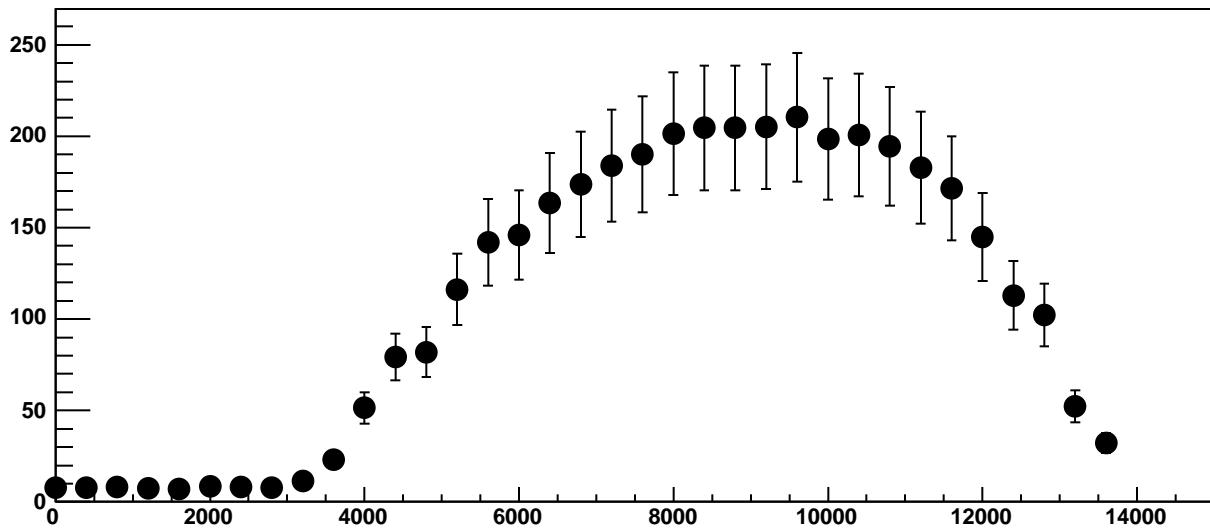
Chip 1, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



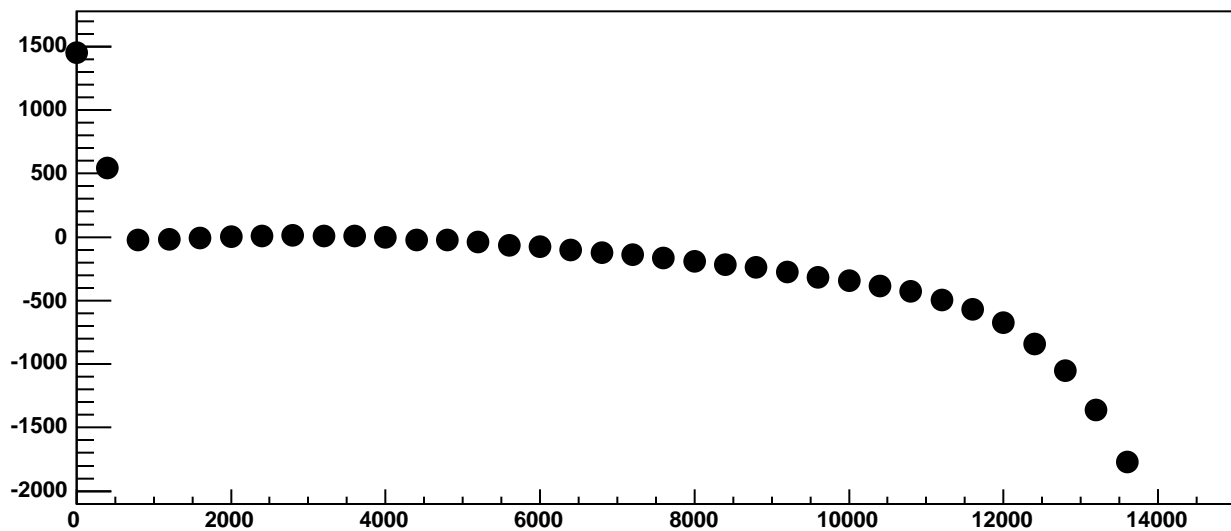
Chip 1, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC



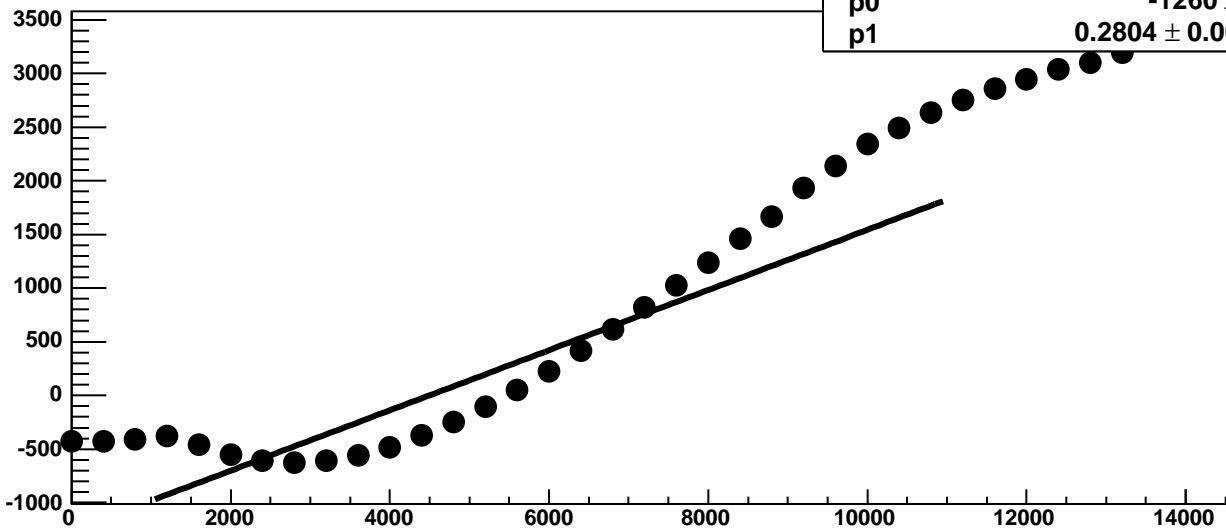
Chip 1, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC

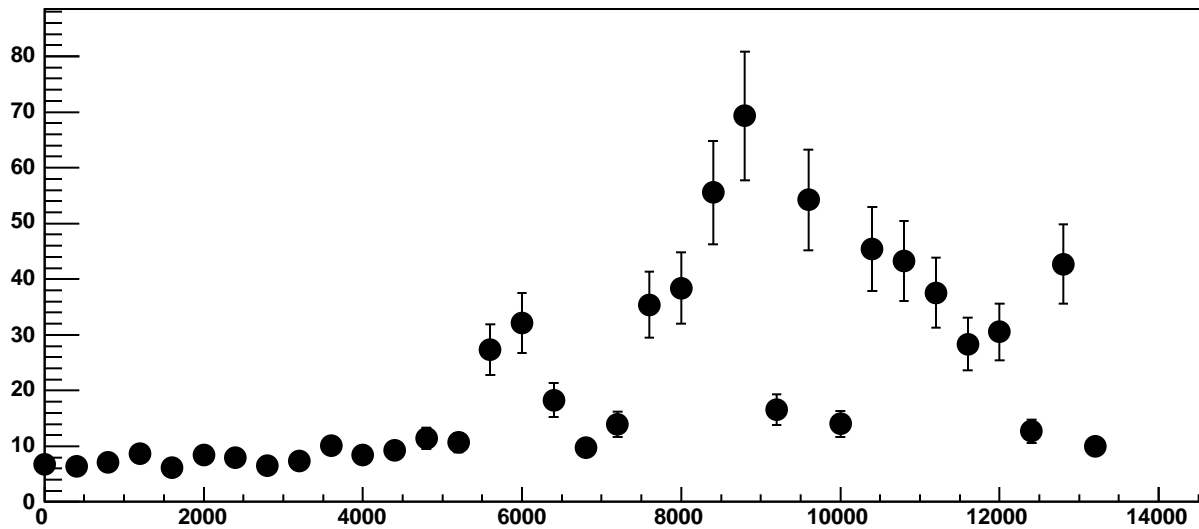


Chip 1, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

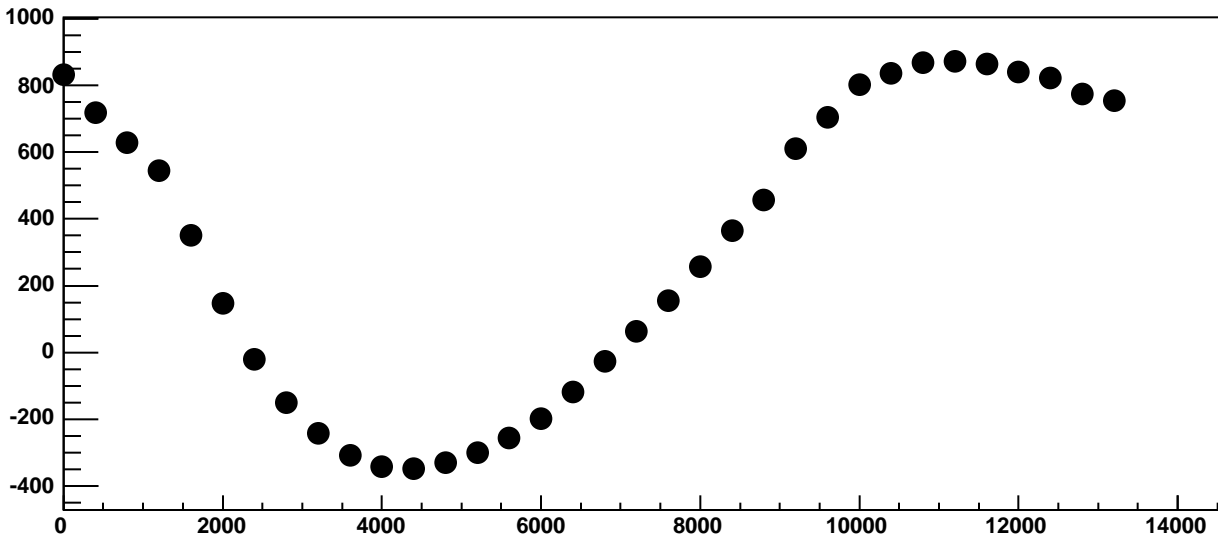


χ^2 / ndf 3.912e+05 / 23
p0 -1260 ± 1.023
p1 0.2804 ± 0.0002378

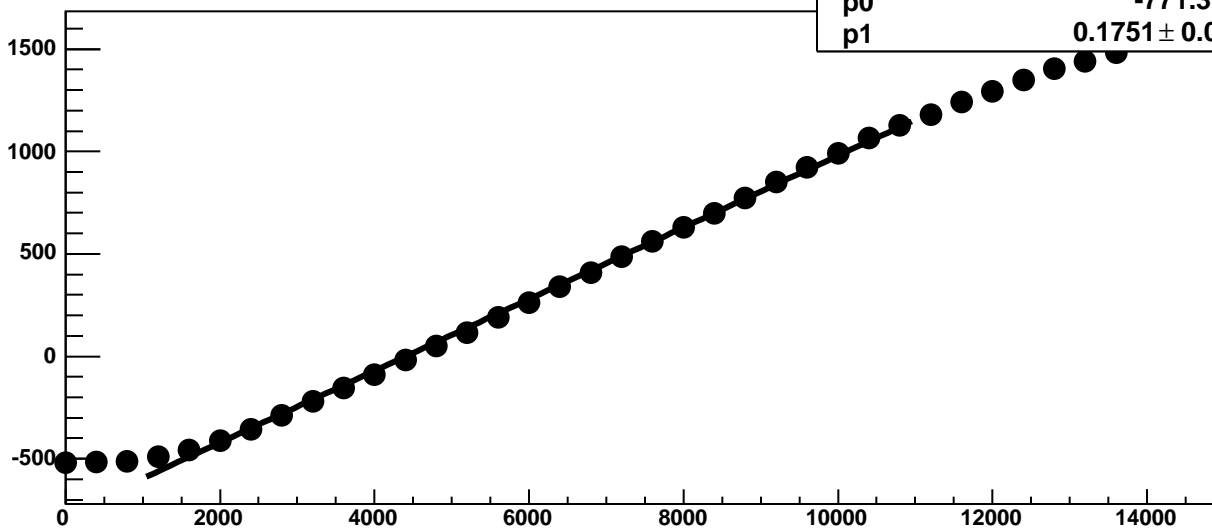
Chip 1, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



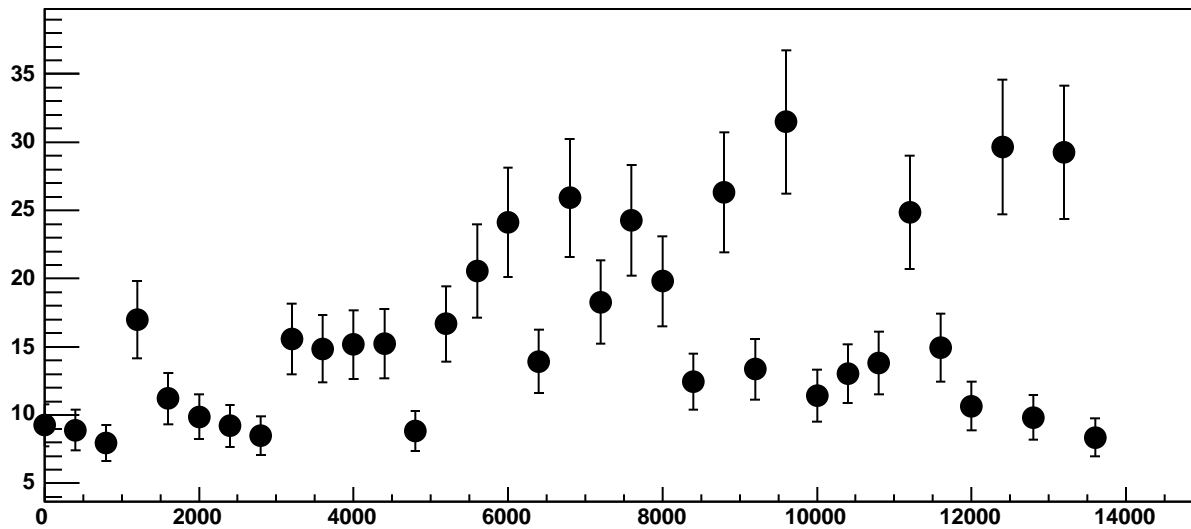
Chip 1, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



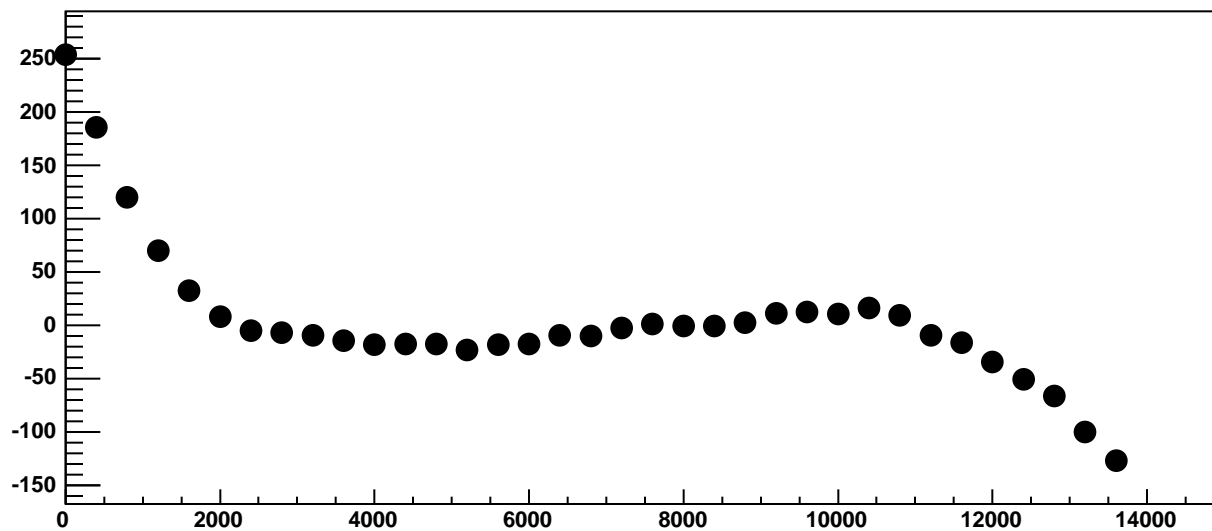
Chip 1, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



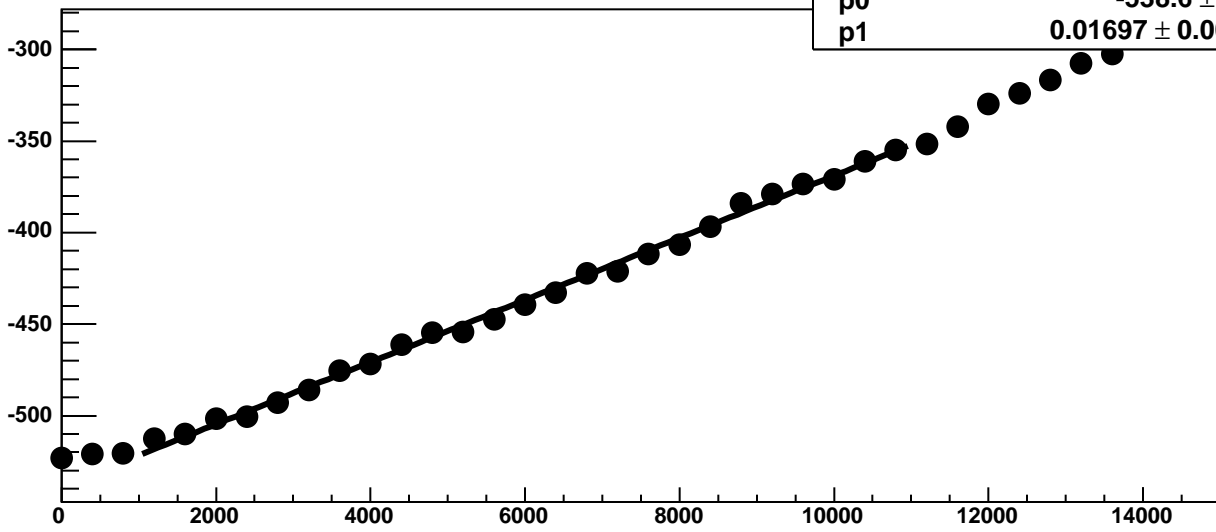
Chip 1, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

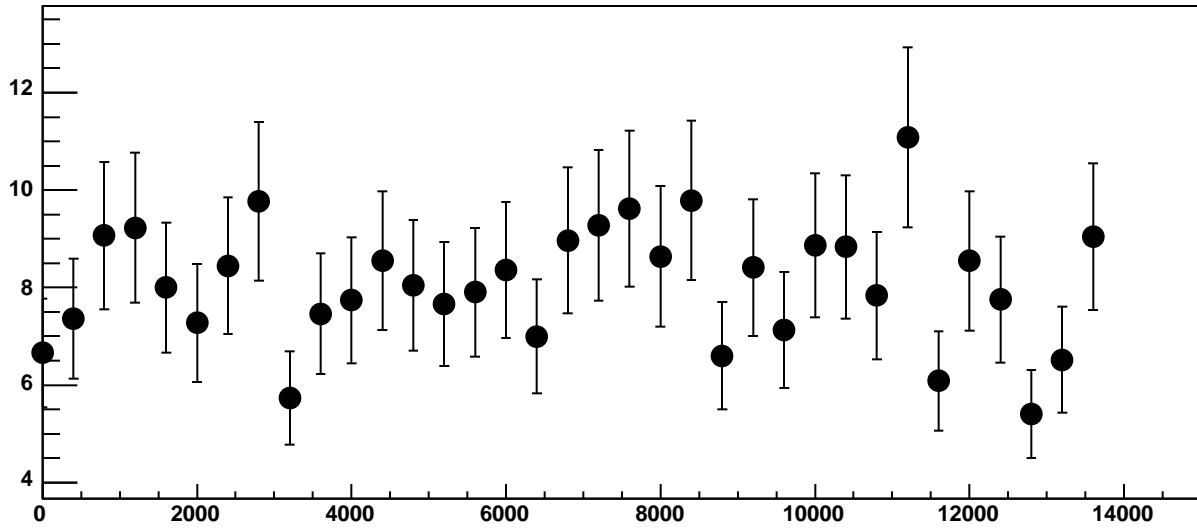


Chip 1, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

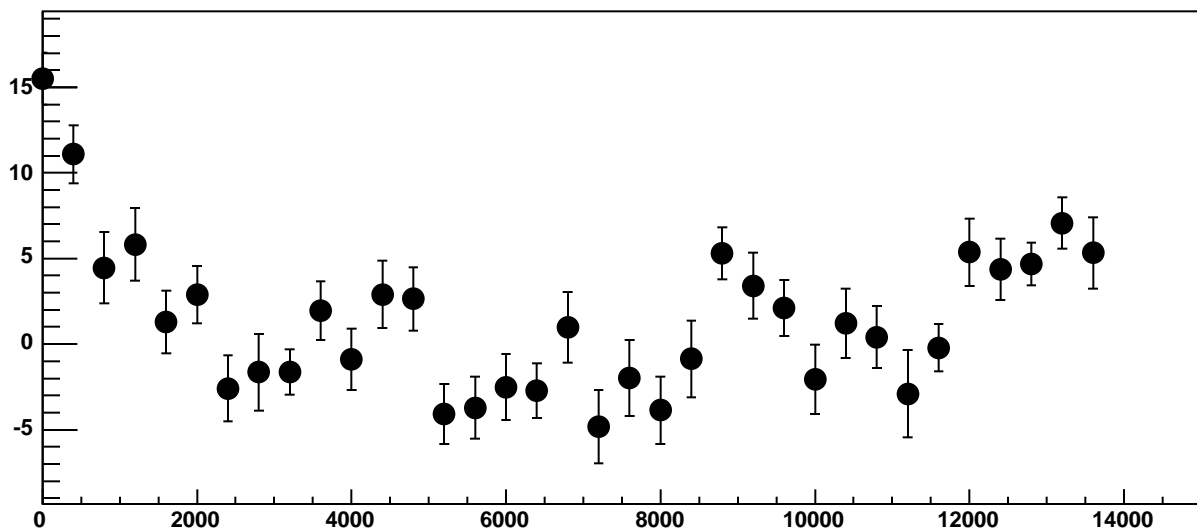


χ^2 / ndf 63.45 / 23
p0 -538.6 ± 0.8392
p1 0.01697 ± 0.0001279

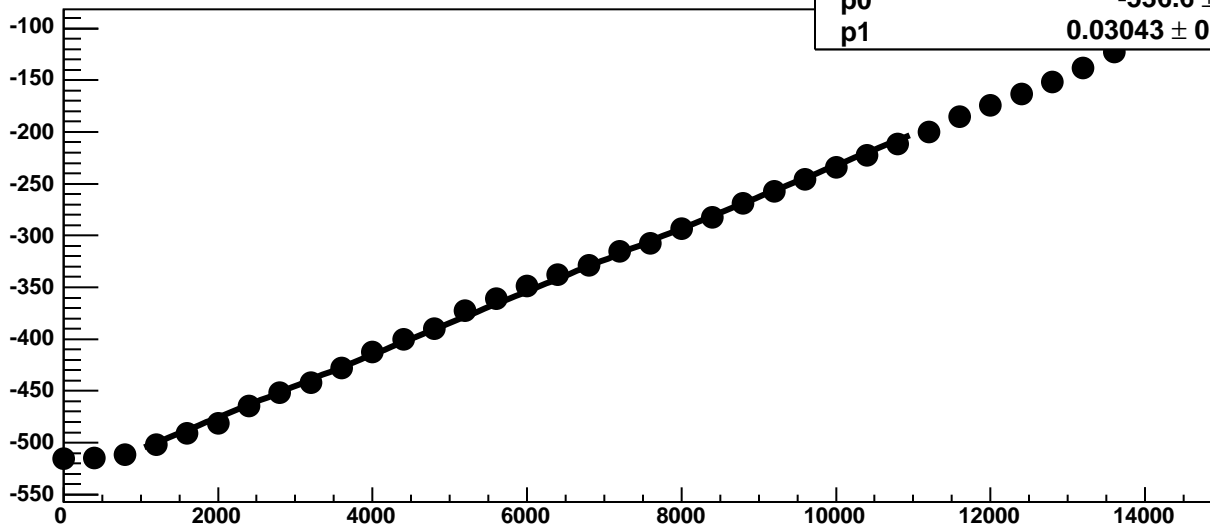
Chip 1, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



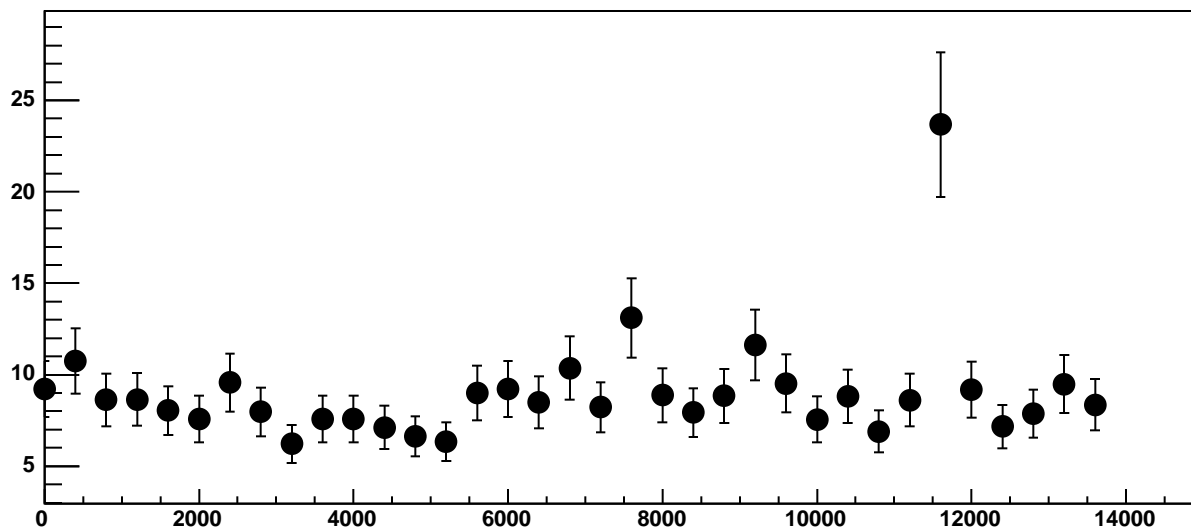
Chip 1, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



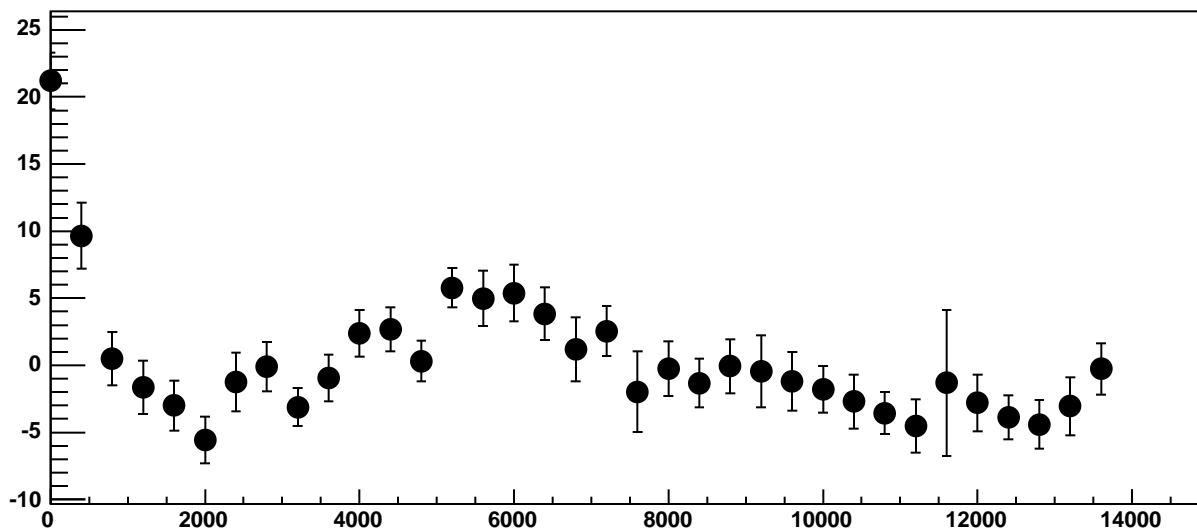
Chip 1, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC



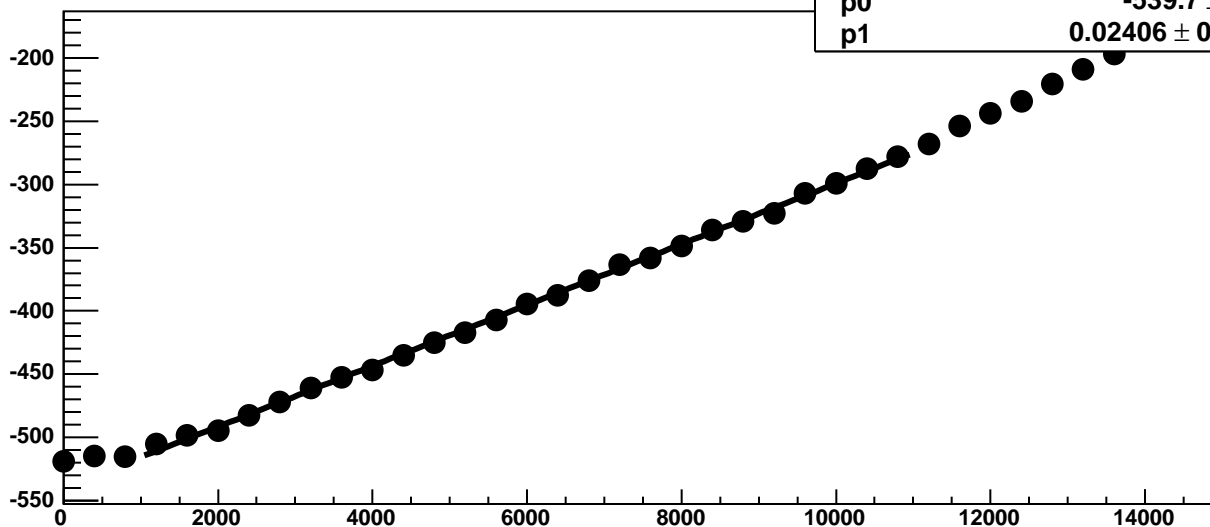
Chip 1, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

42.83 / 23

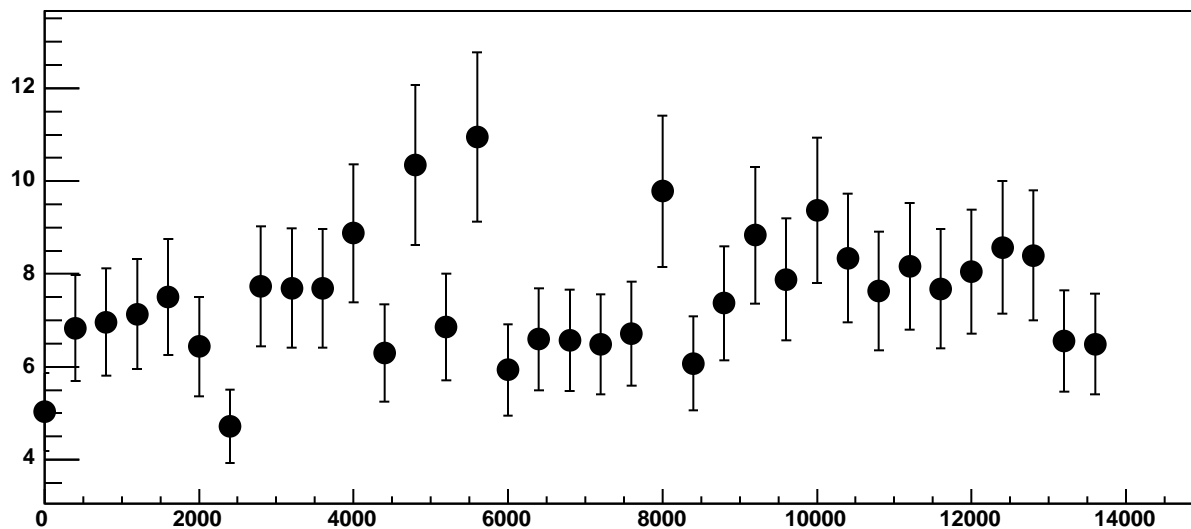
p0

-539.7 ± 0.7381

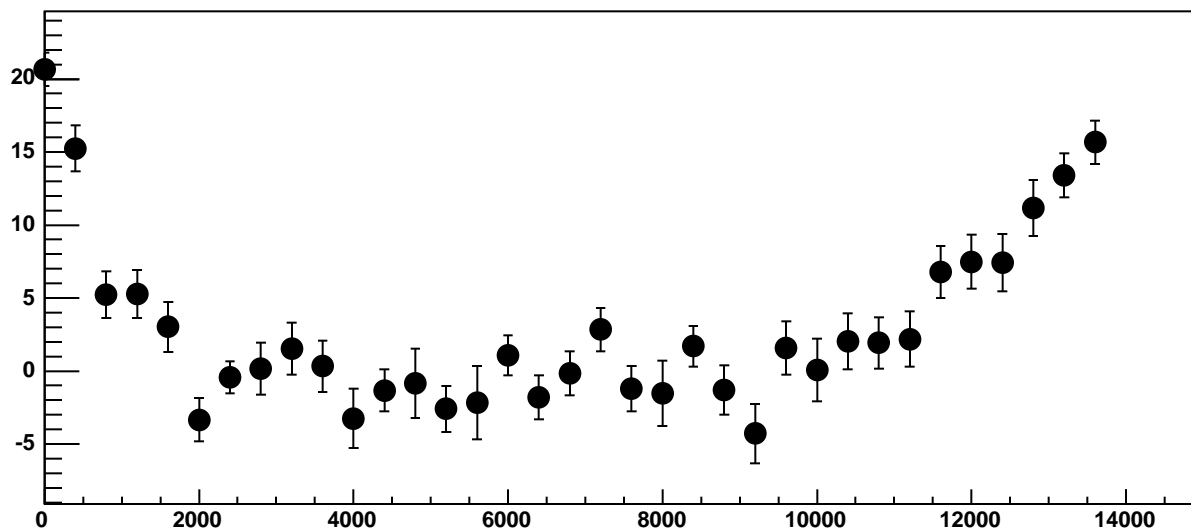
p1

0.02406 ± 0.000116

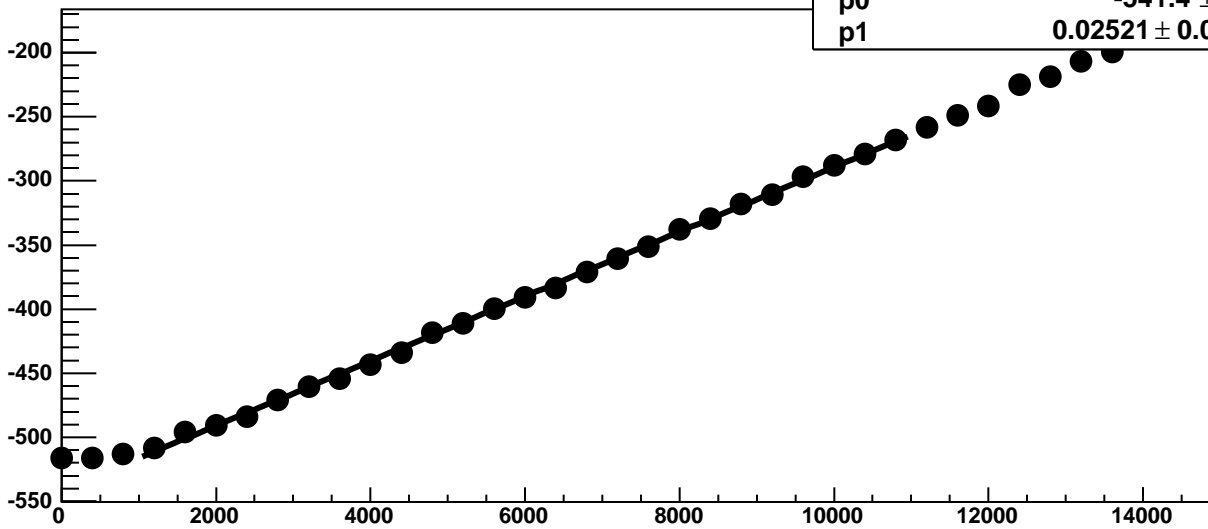
Chip 1, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

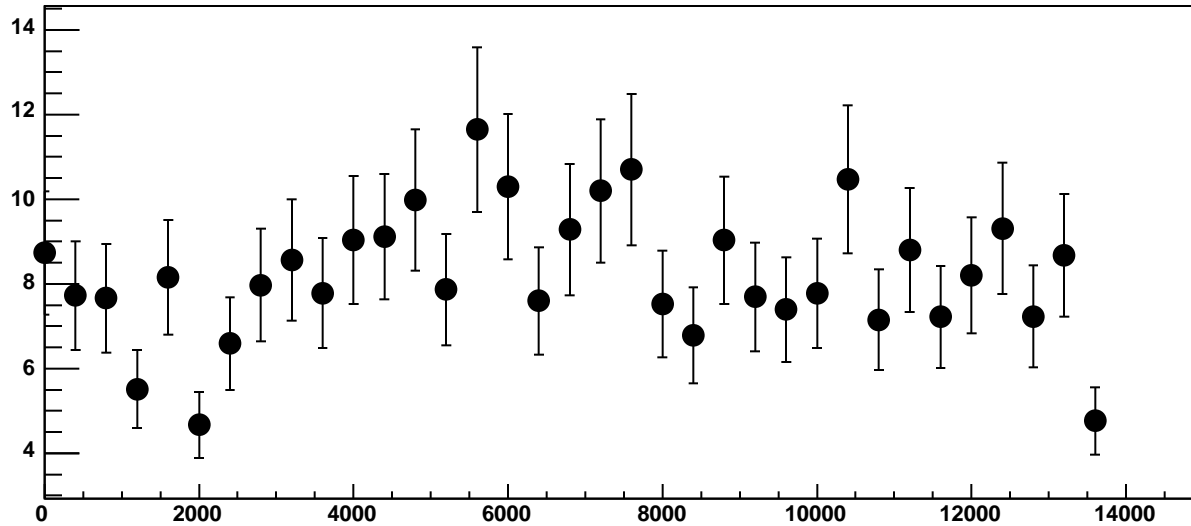


Chip 1, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

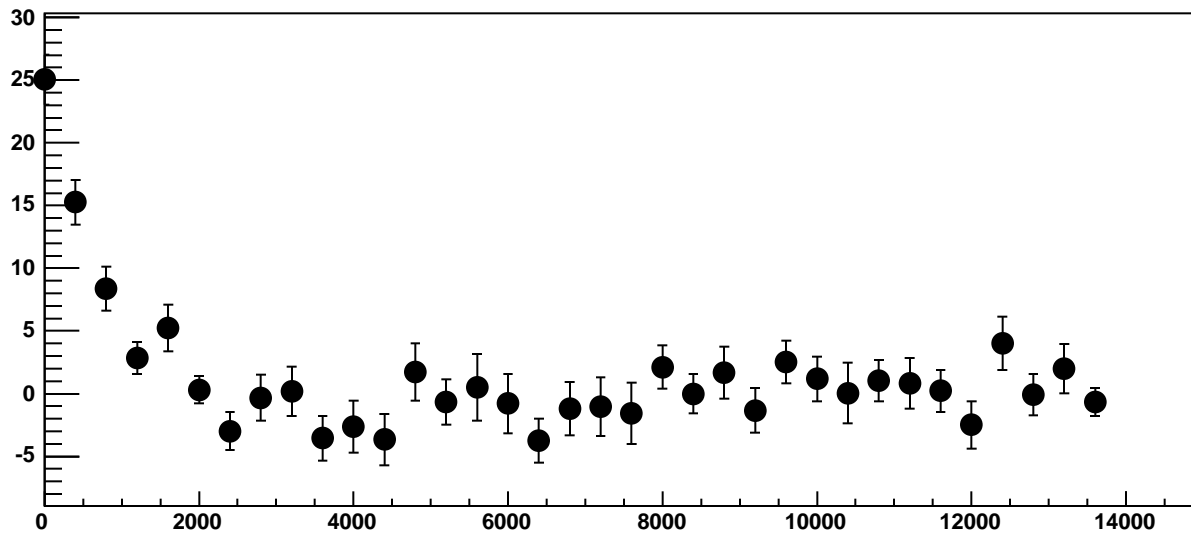


χ^2 / ndf 37.88 / 23
p0 -541.4 ± 0.7192
p1 0.02521 ± 0.0001135

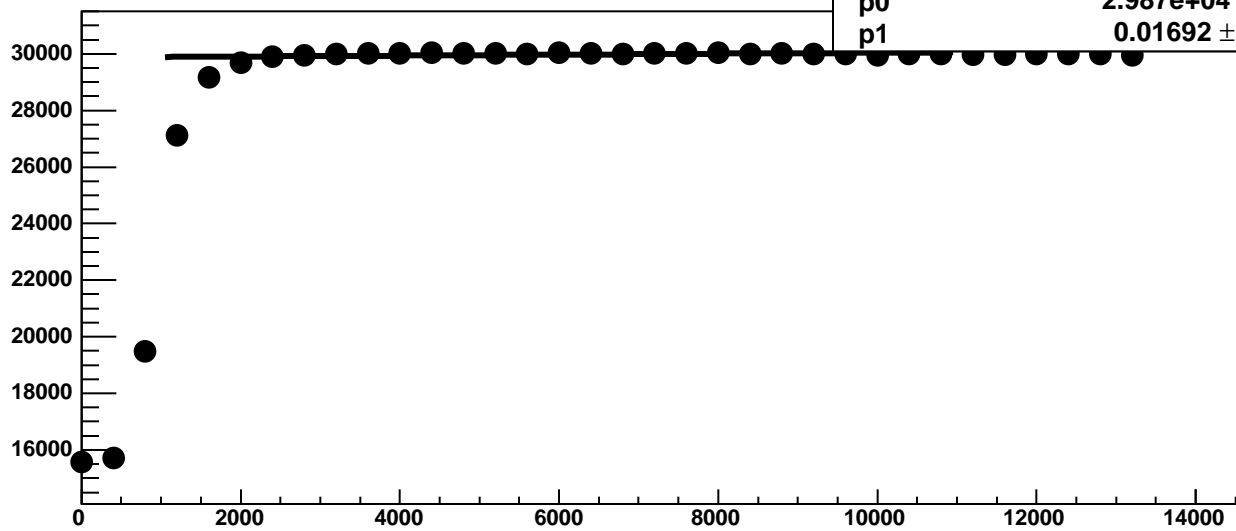
Chip 1, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



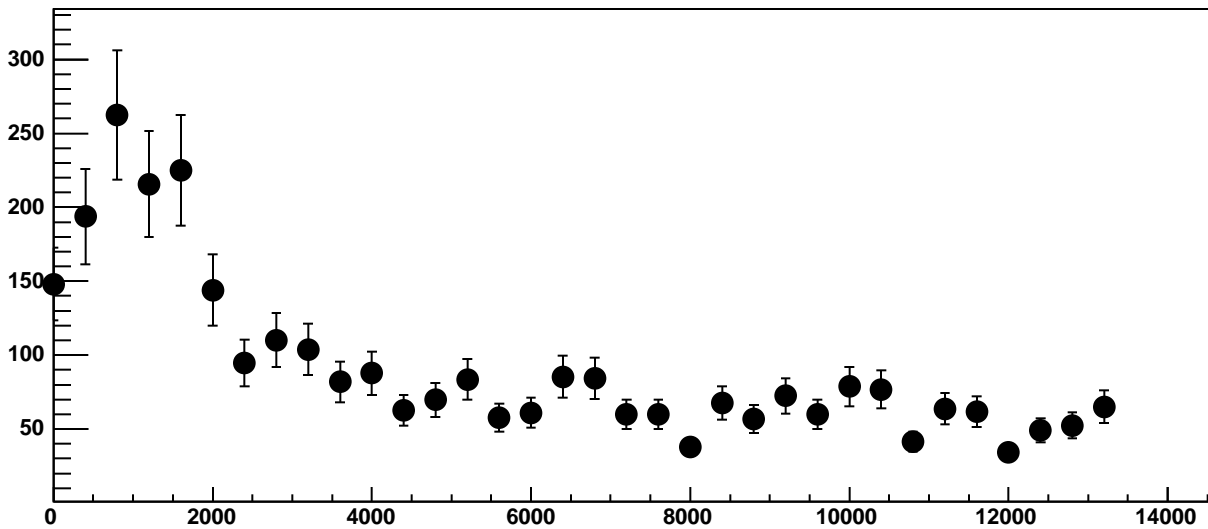
Chip 1, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



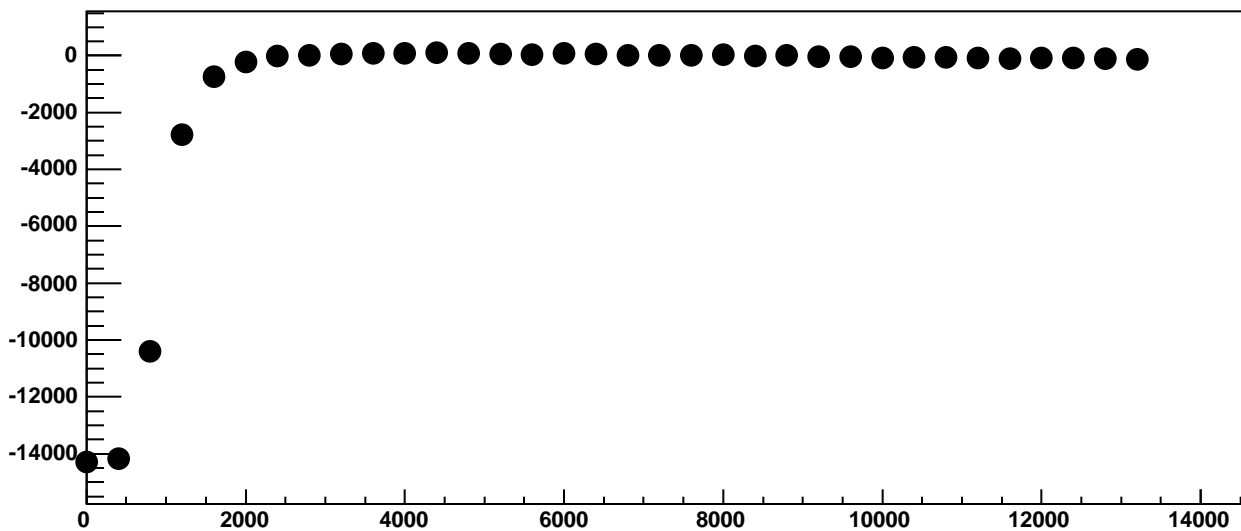
Chip 1, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC



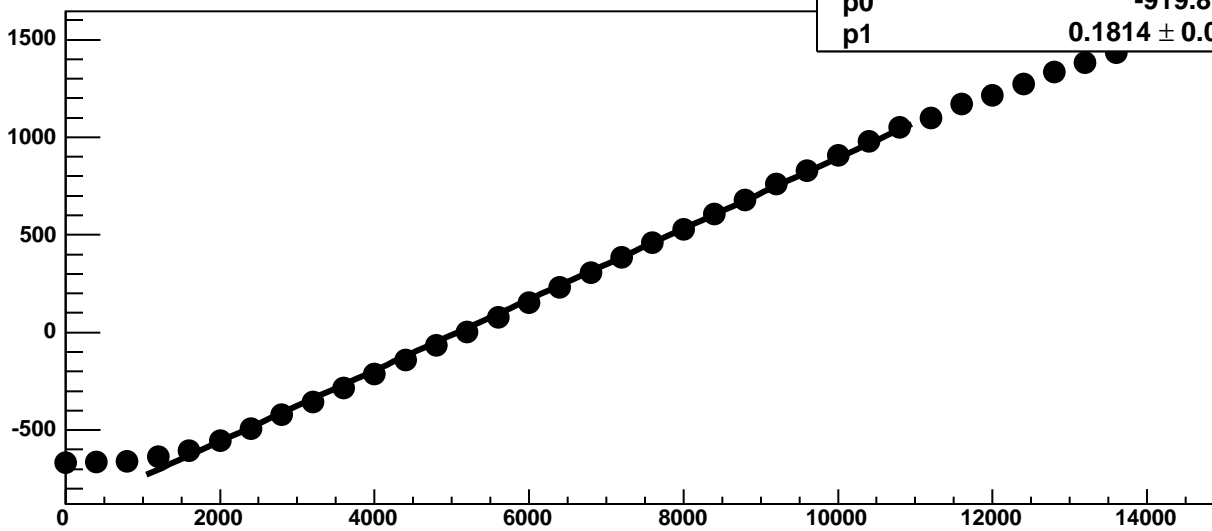
Chip 1, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC



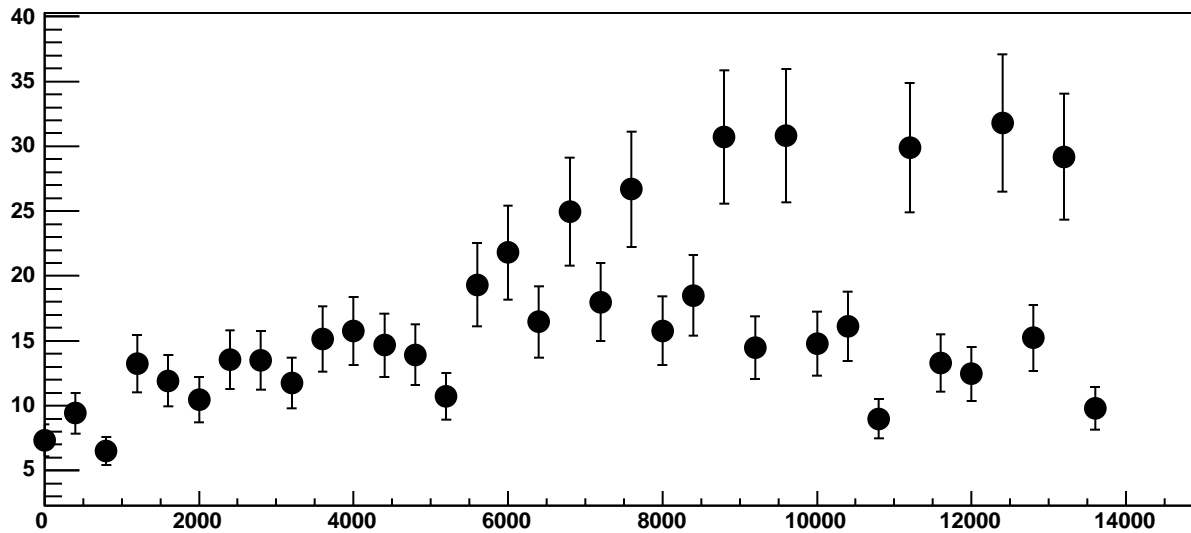
Chip 1, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC



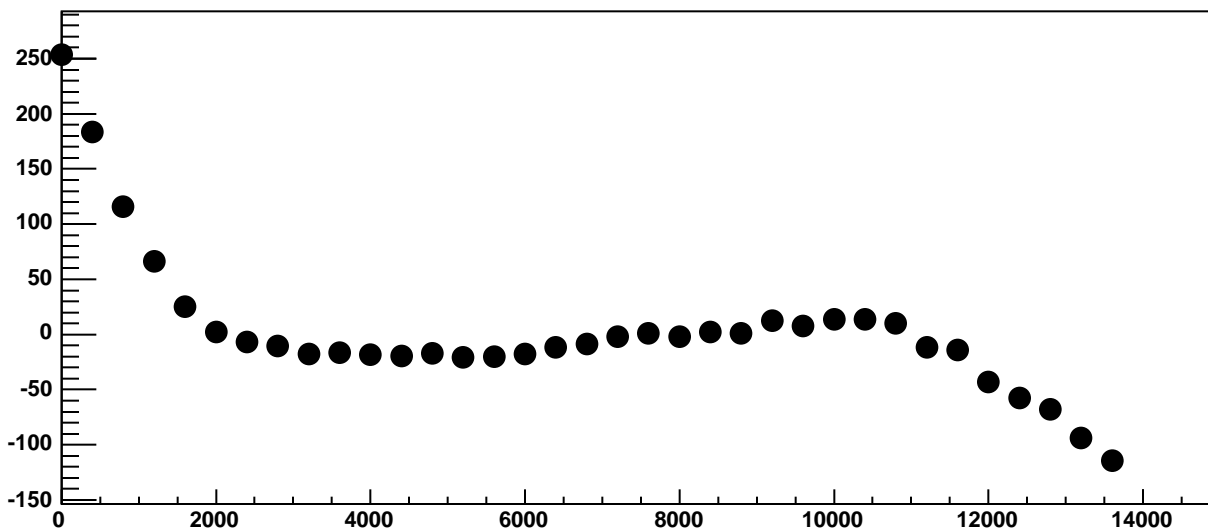
Chip 1, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



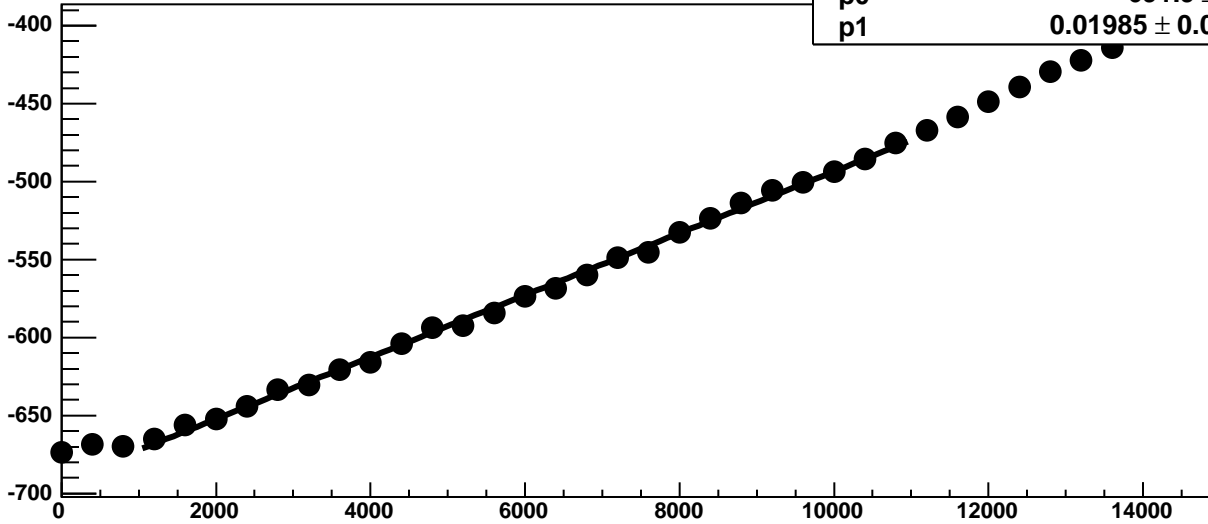
Chip 1, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

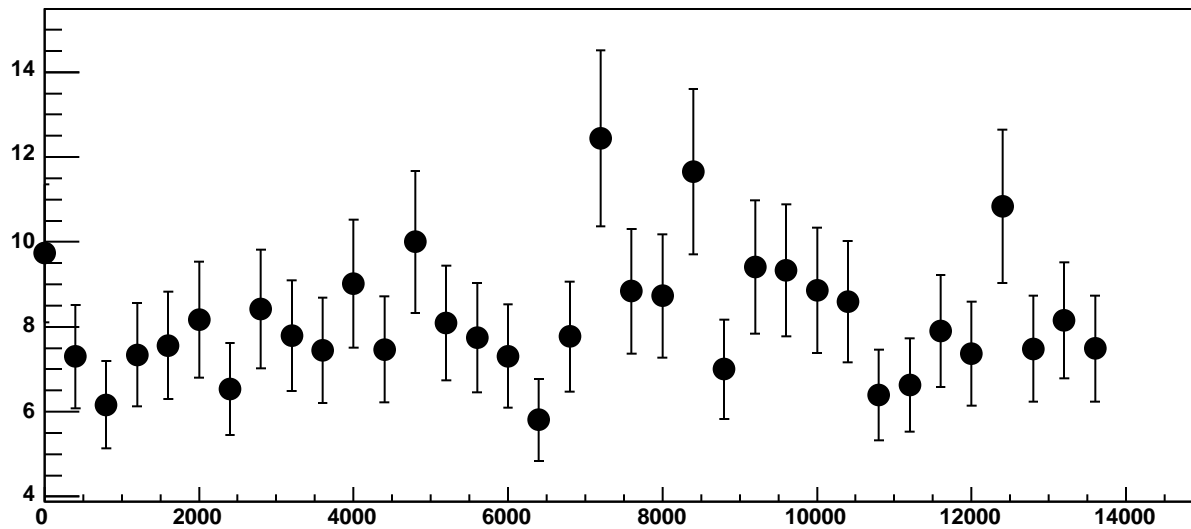


Chip 1, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

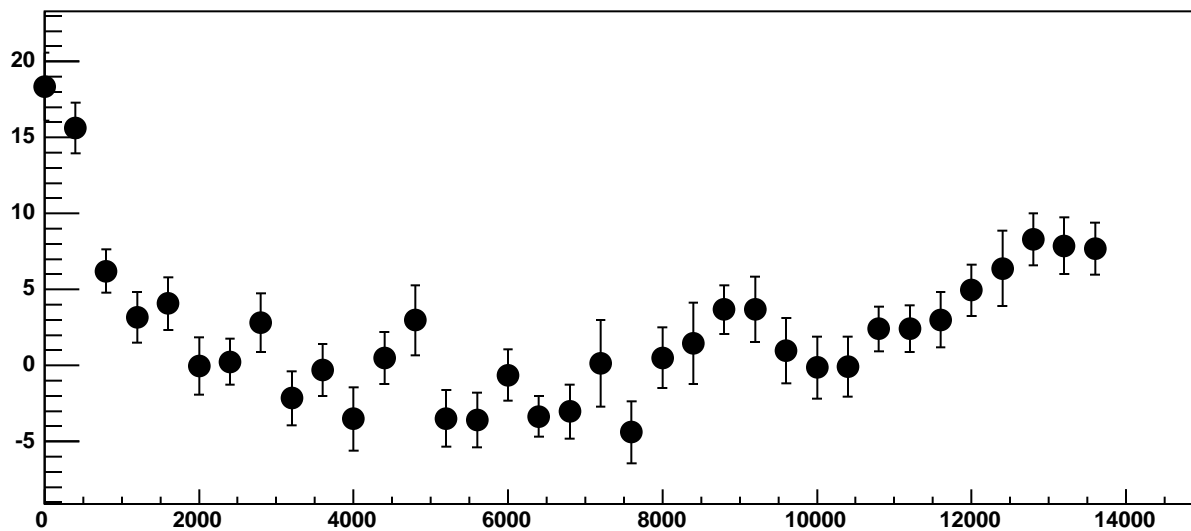


χ^2 / ndf 50.52 / 23
p0 -691.9 ± 0.8116
p1 0.01985 ± 0.0001244

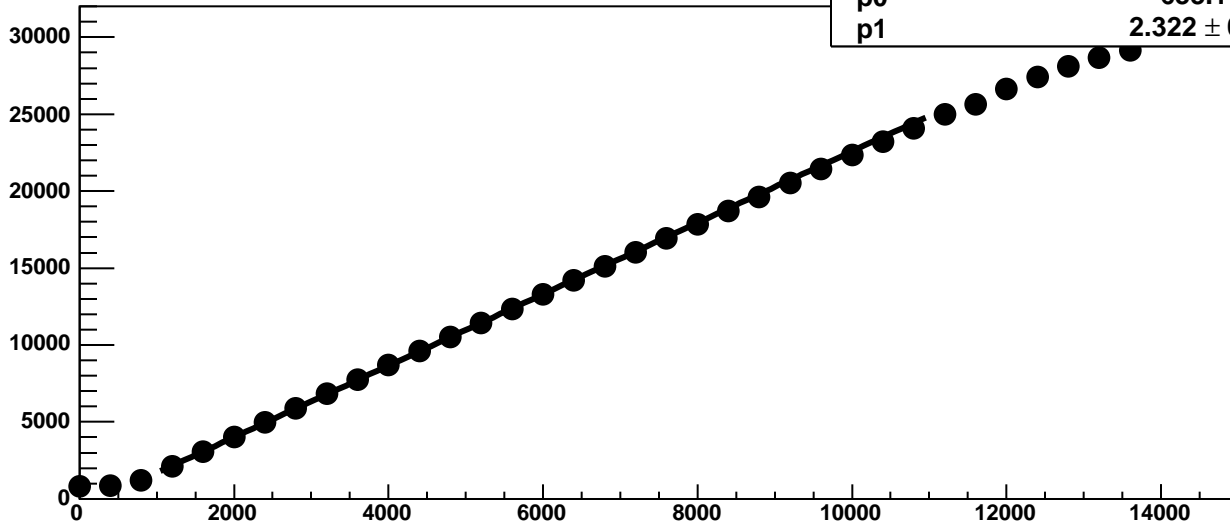
Chip 1, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

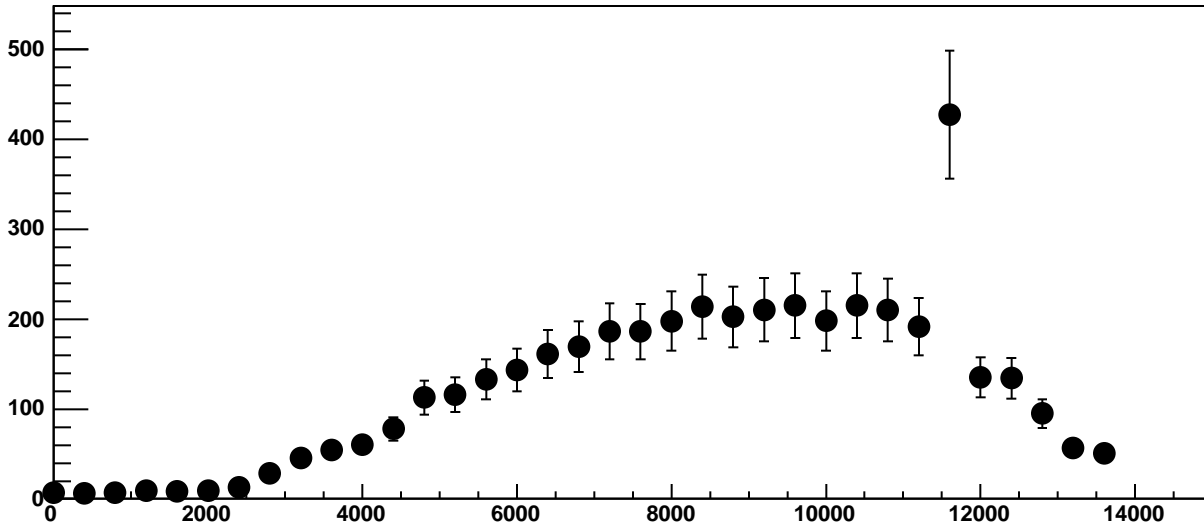


Chip 1, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC

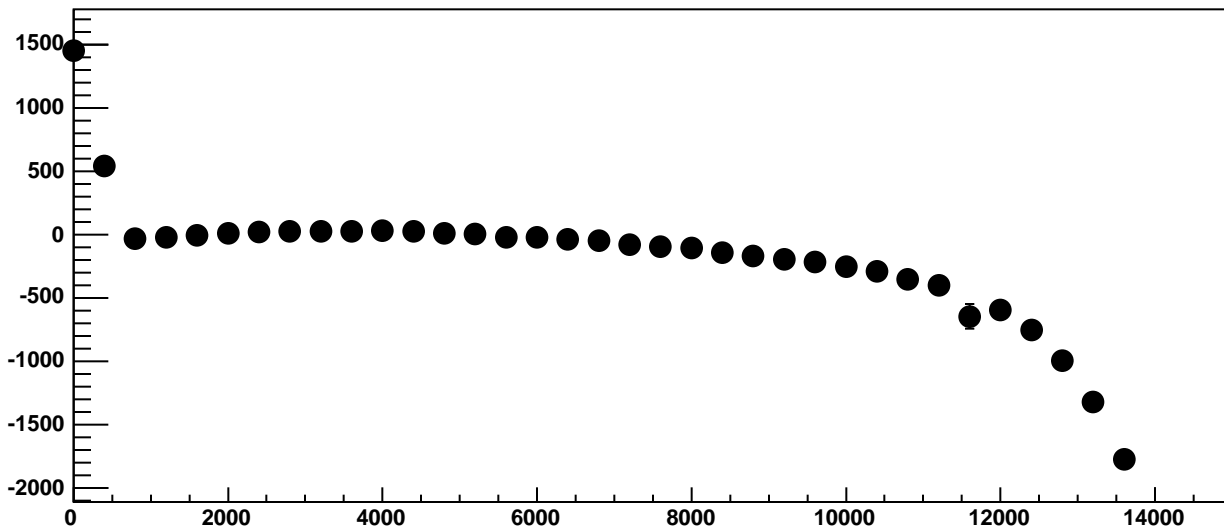


χ^2 / ndf 411.1 / 23
p0 -633.1 ± 2.644
p1 2.322 ± 0.00131

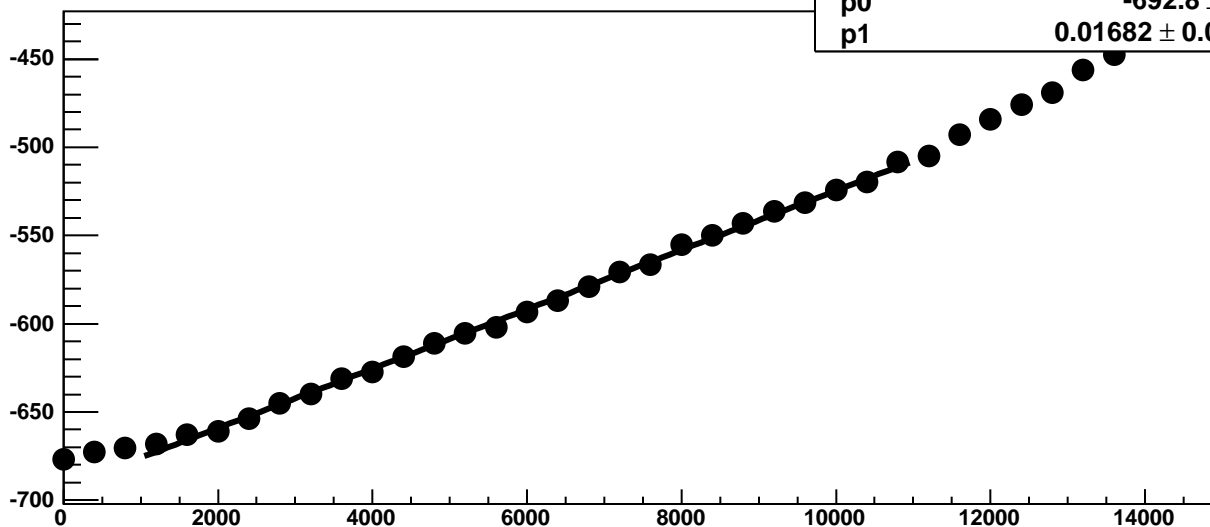
Chip 1, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



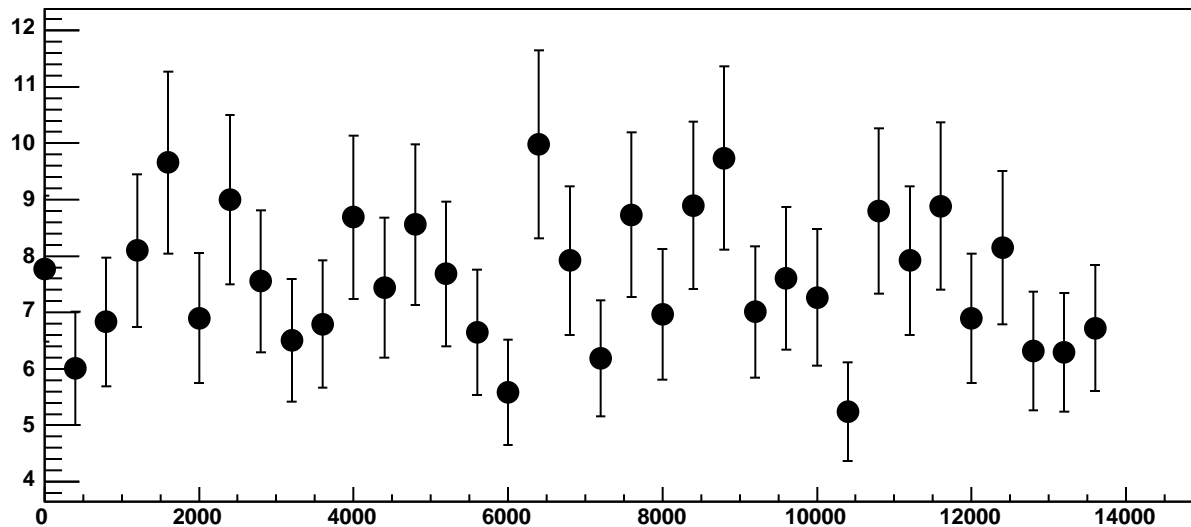
Chip 1, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC



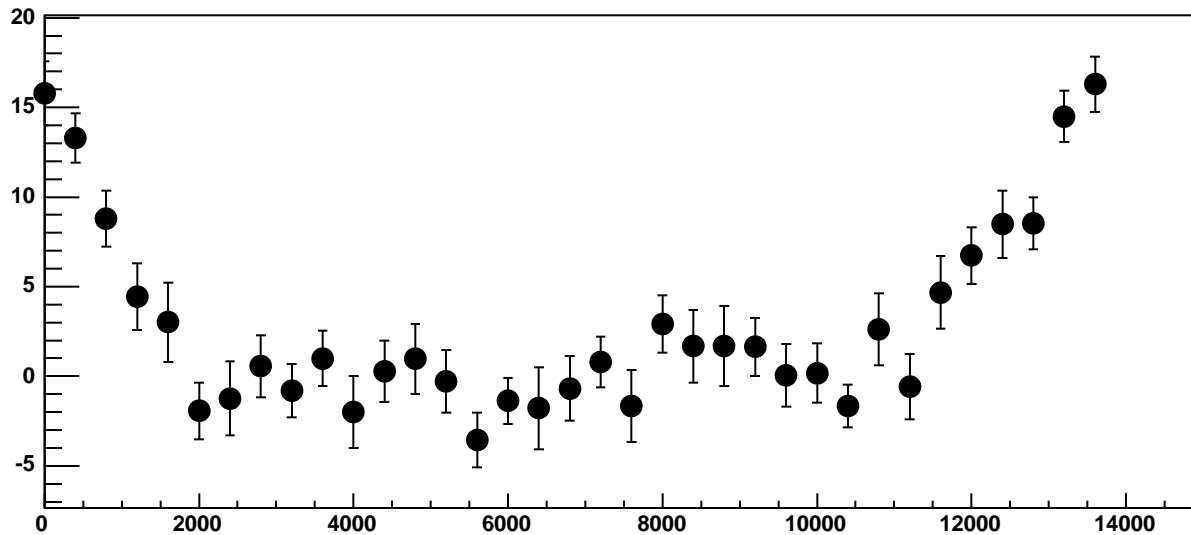
Chip 1, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



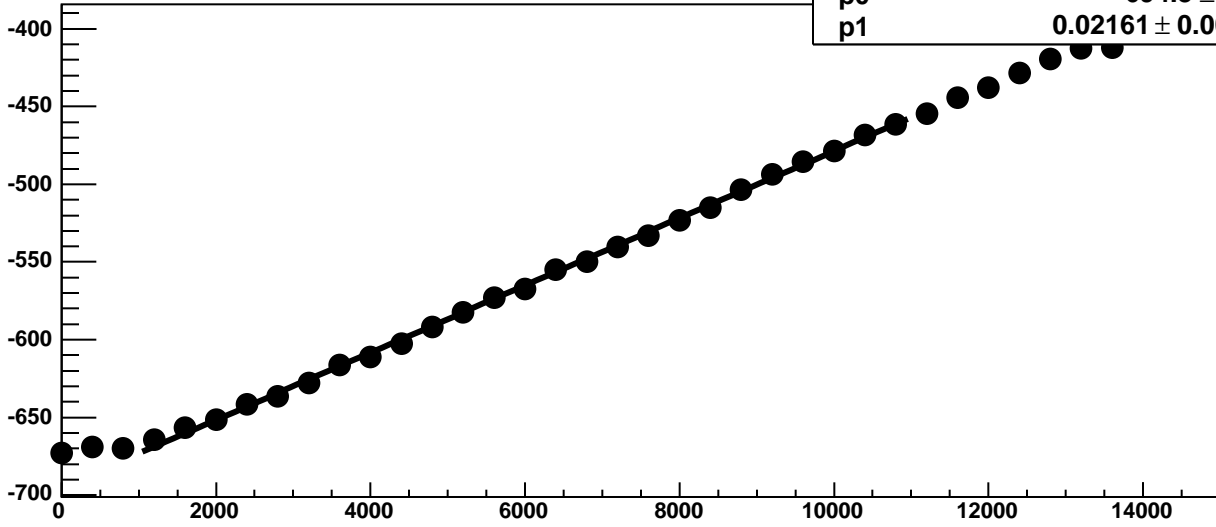
Chip 1, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

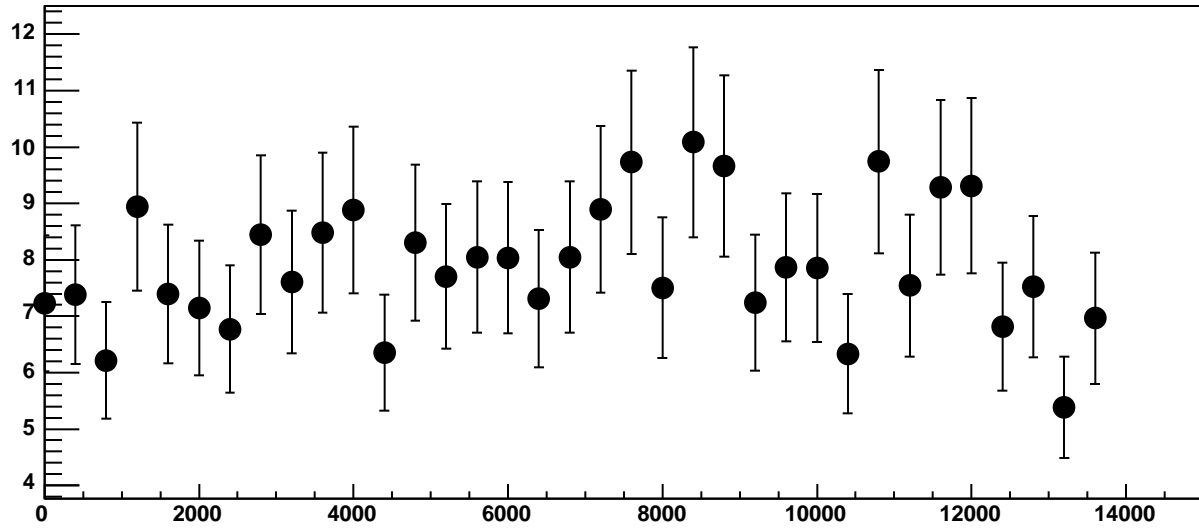


Chip 1, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

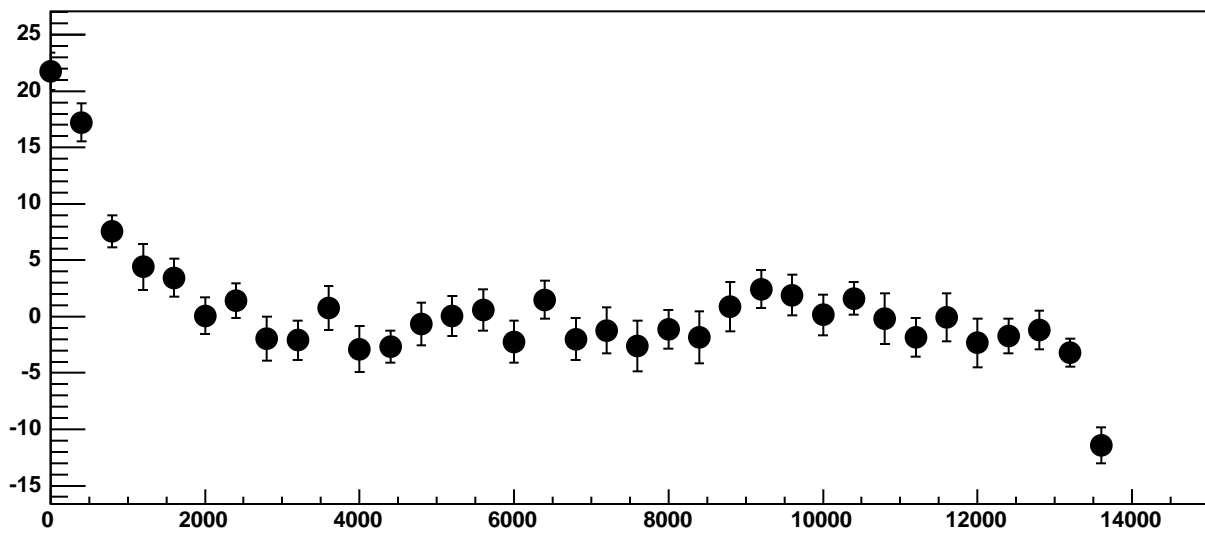


χ^2 / ndf 28.91 / 23
p0 -694.8 ± 0.8175
p1 0.02161 ± 0.0001243

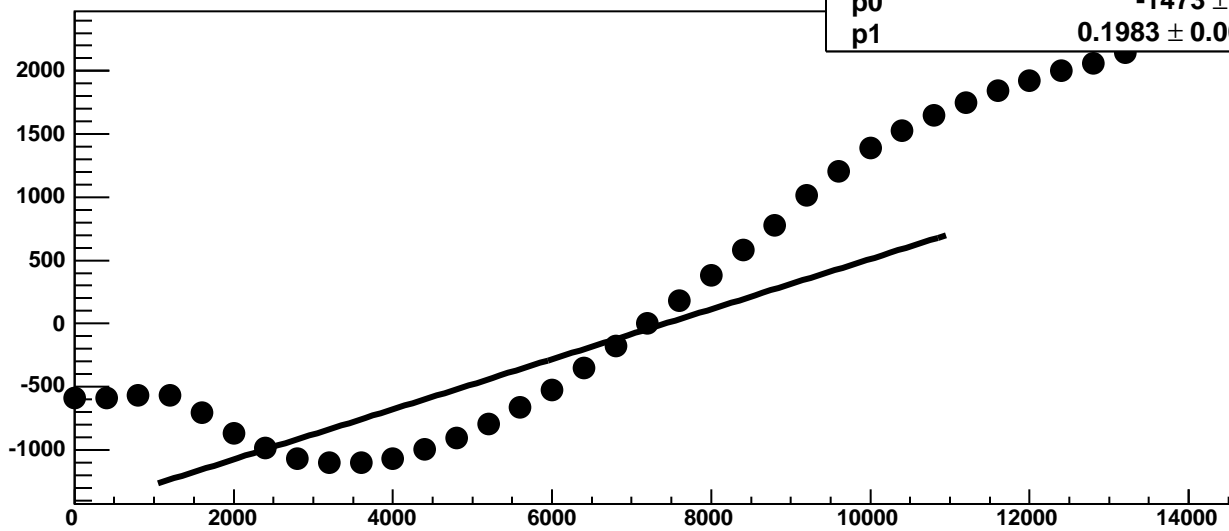
Chip 1, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



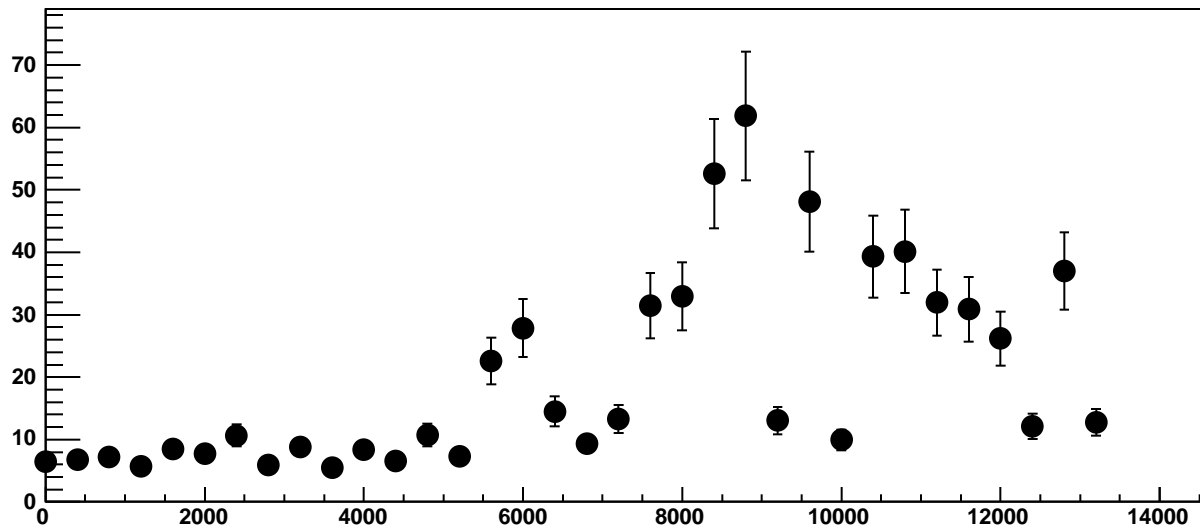
Chip 1, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



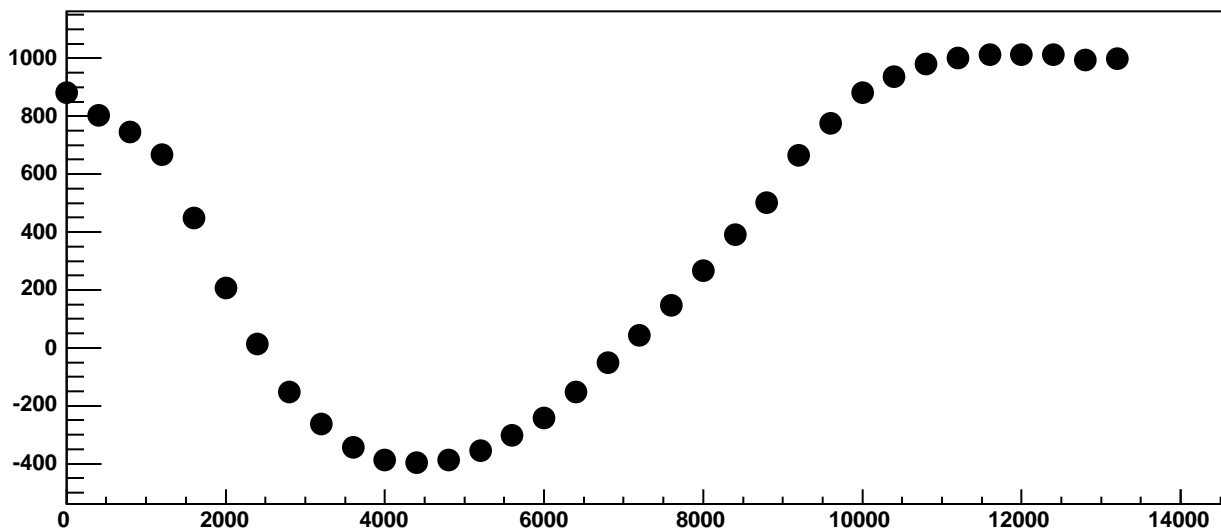
Chip 1, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC



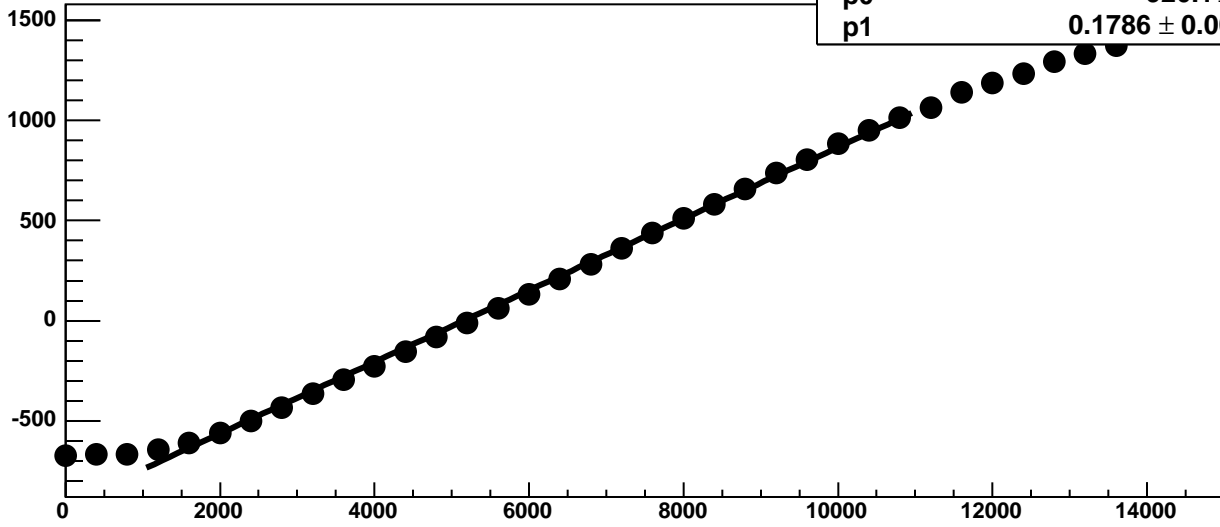
Chip 1, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



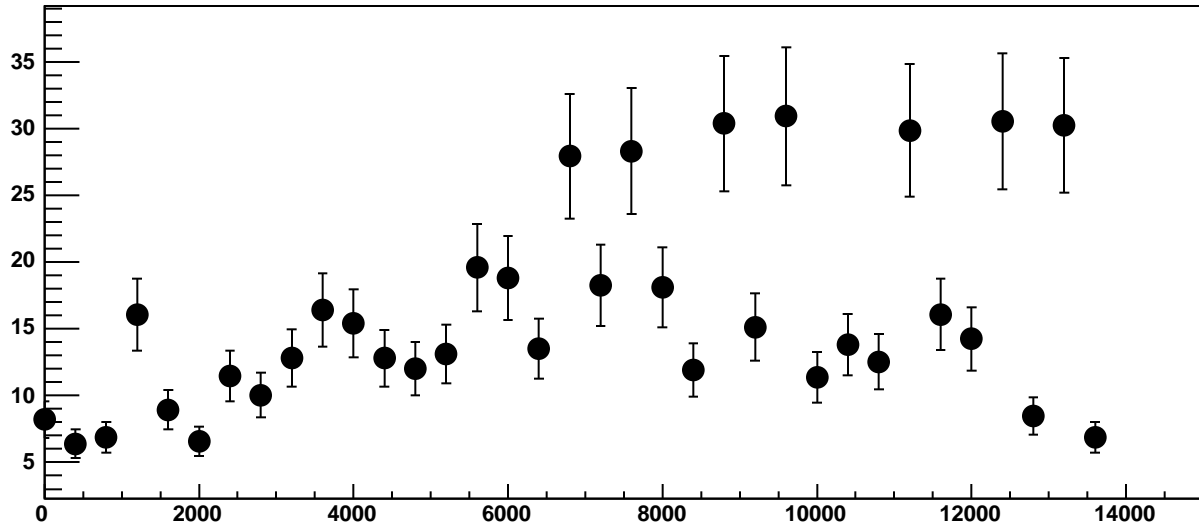
Chip 1, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC



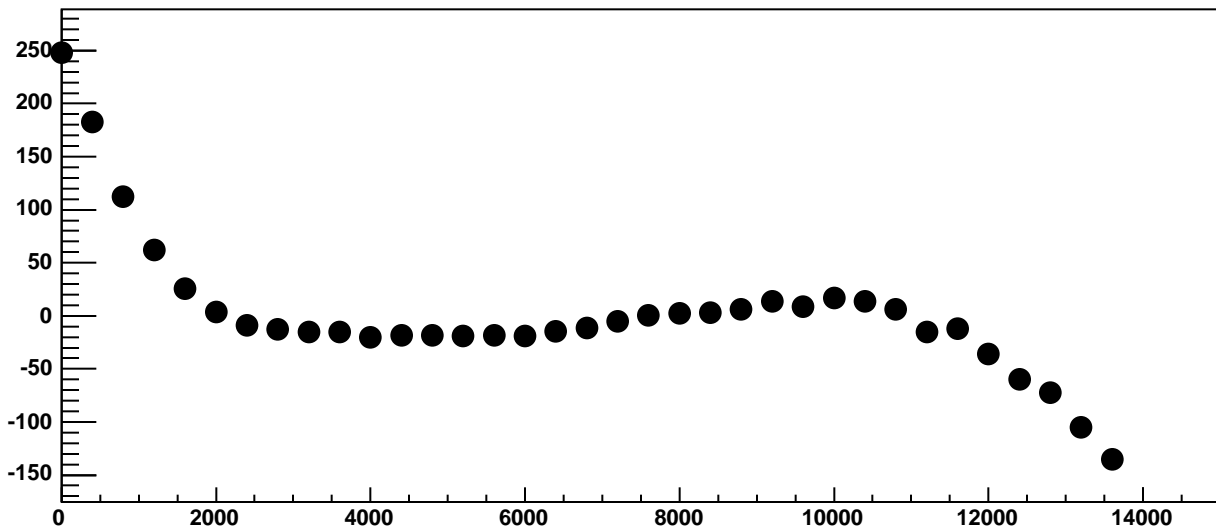
Chip 1, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



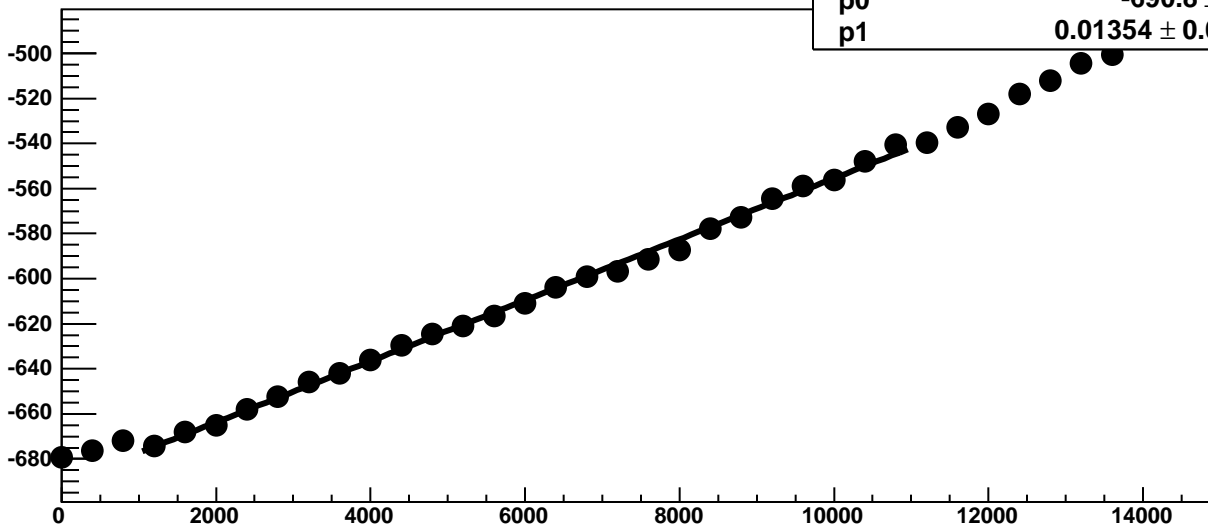
Chip 1, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

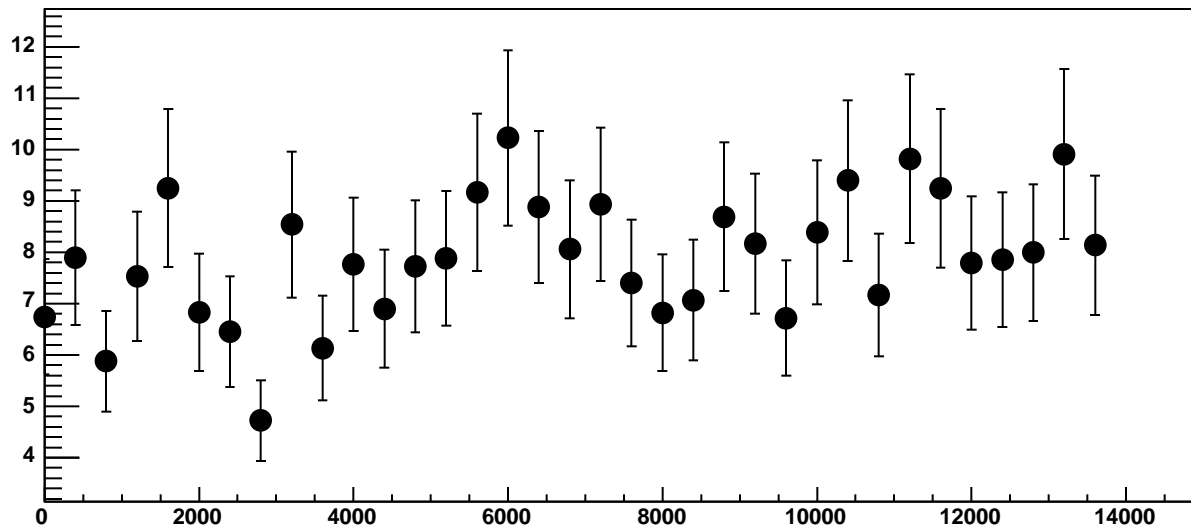


Chip 1, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

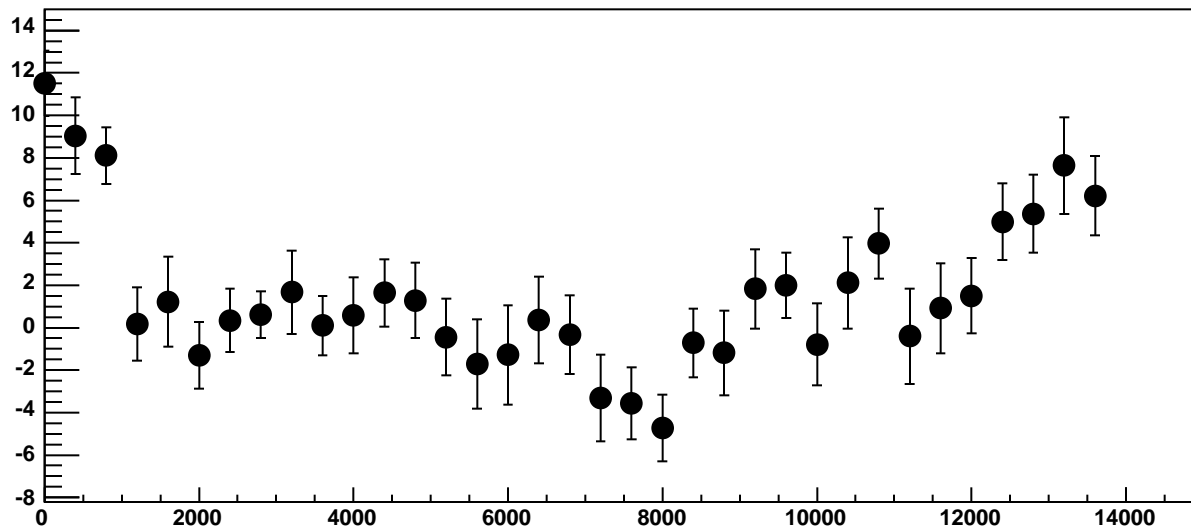


χ^2 / ndf 31.13 / 23
p0 -690.8 ± 0.7491
p1 0.01354 ± 0.0001171

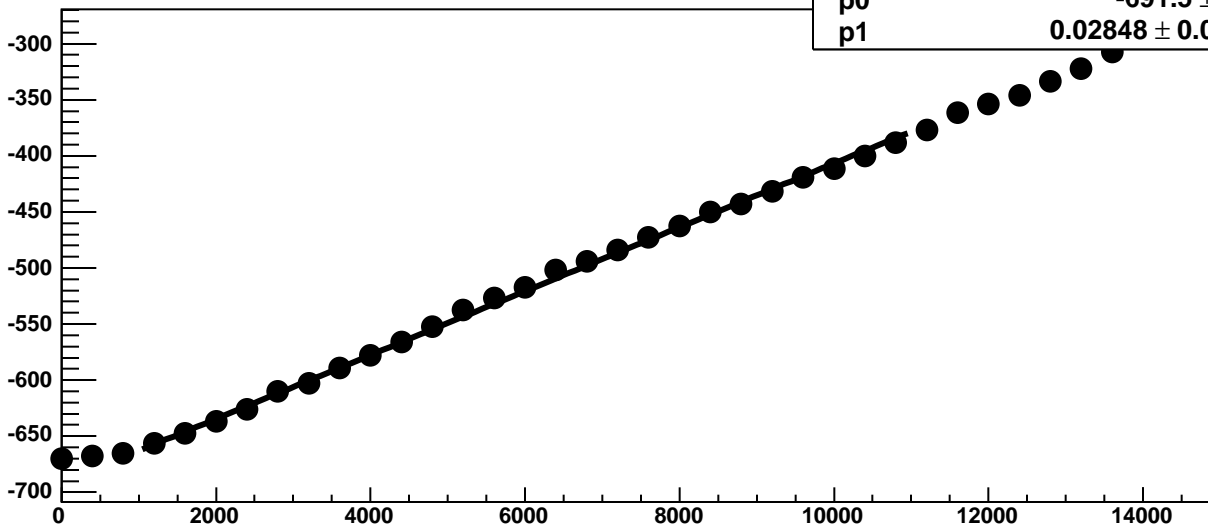
Chip 1, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

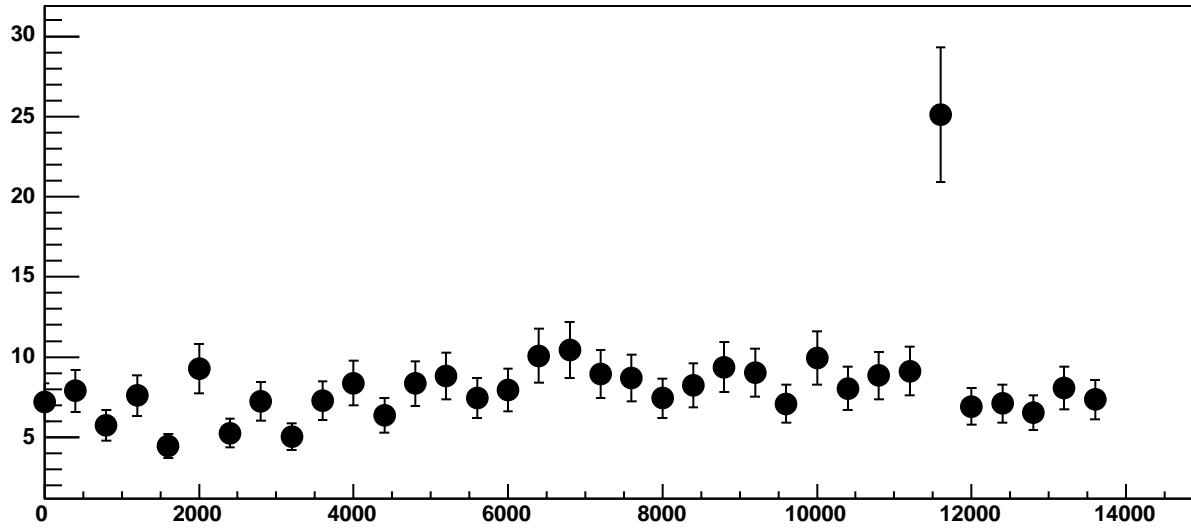


Chip 1, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC

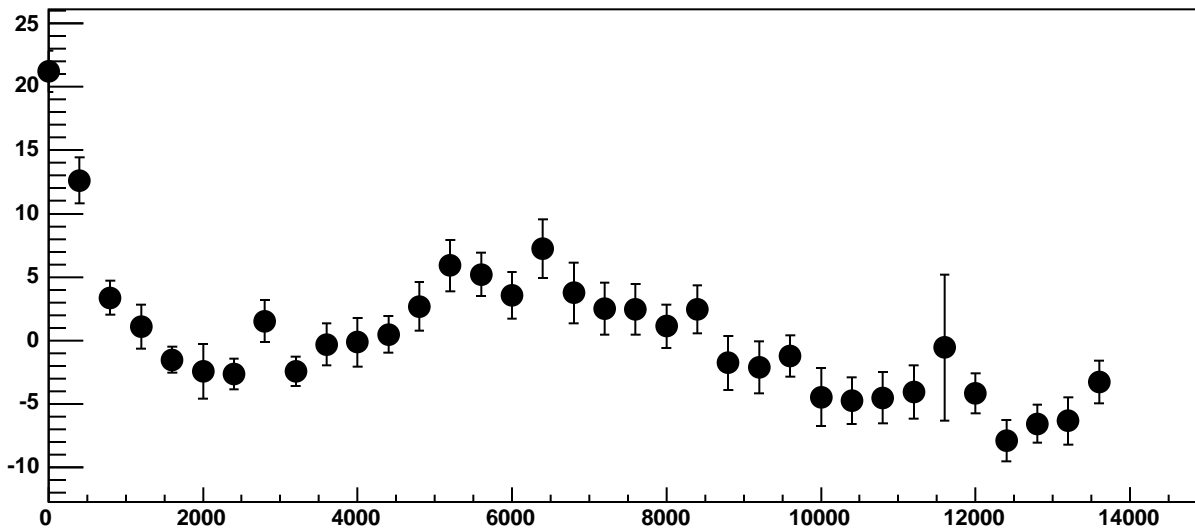


χ^2 / ndf 72.99 / 23
p0 -691.5 ± 0.6895
p1 0.02848 ± 0.0001156

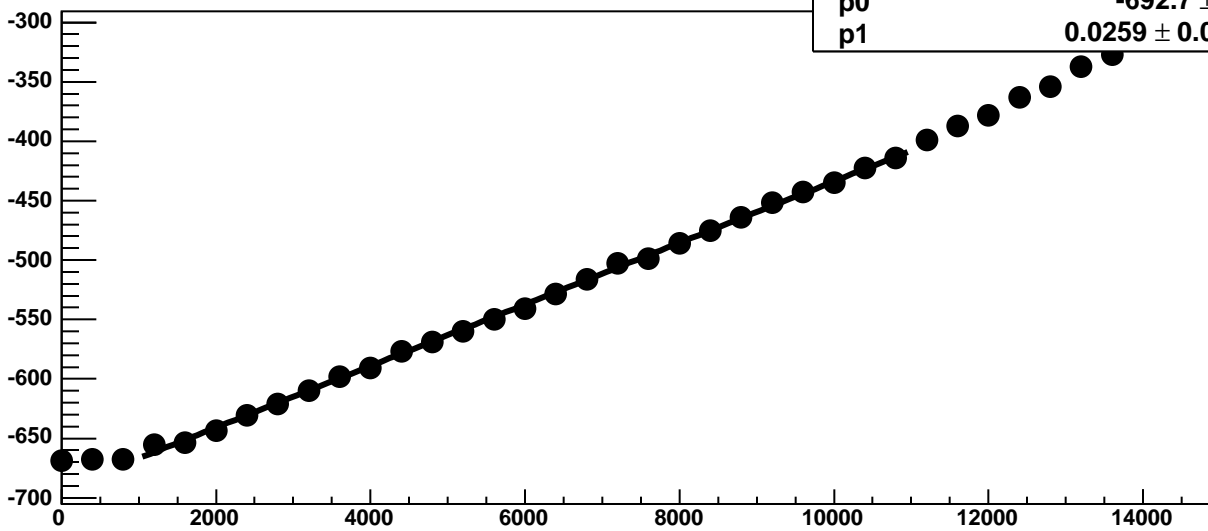
Chip 1, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC

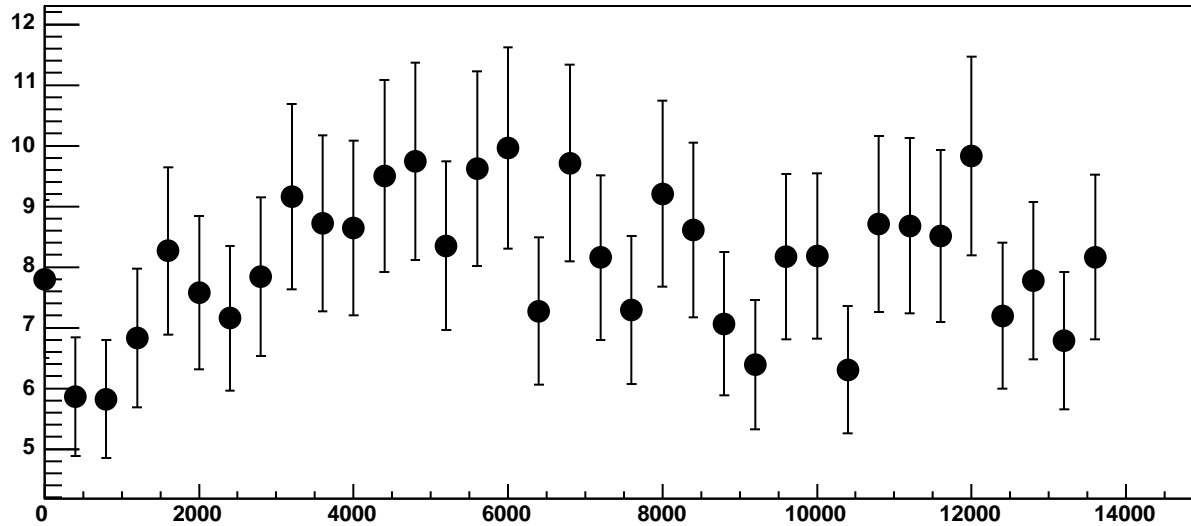


Chip 1, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

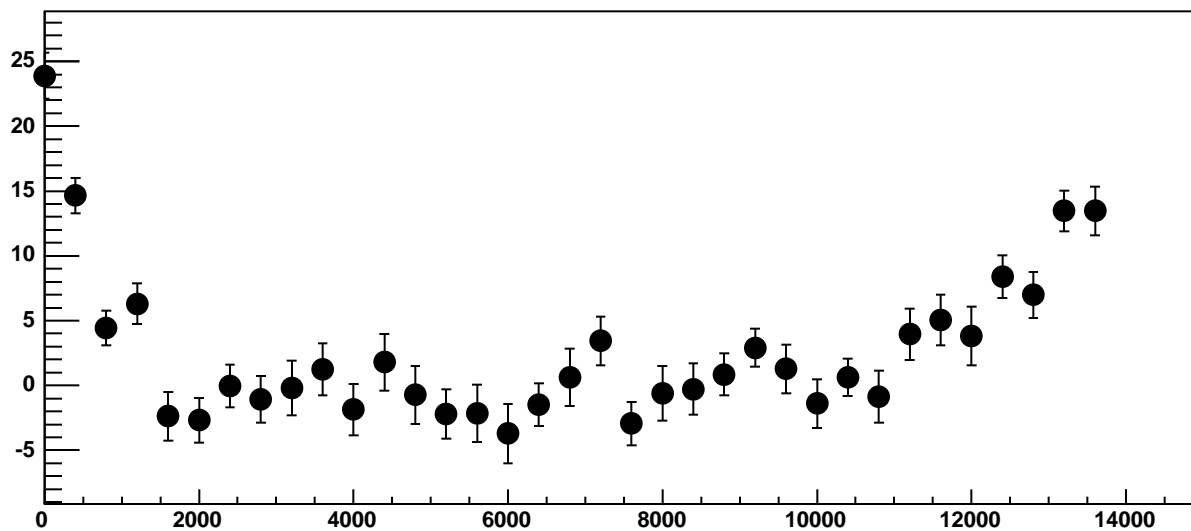


χ^2 / ndf 40.32 / 23
p0 -692.7 ± 0.8284
p1 0.0259 ± 0.0001212

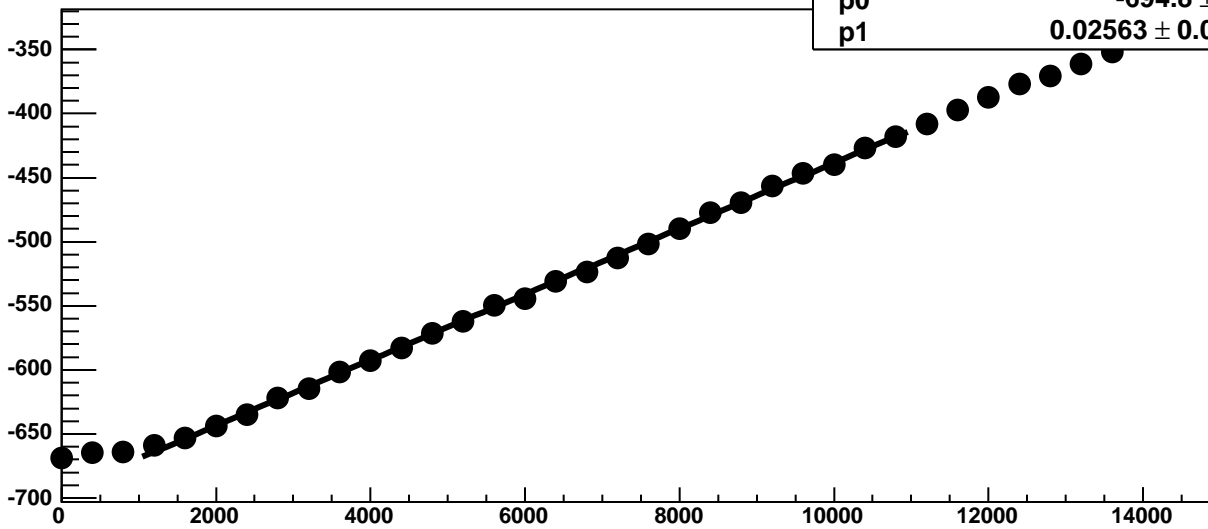
Chip 1, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

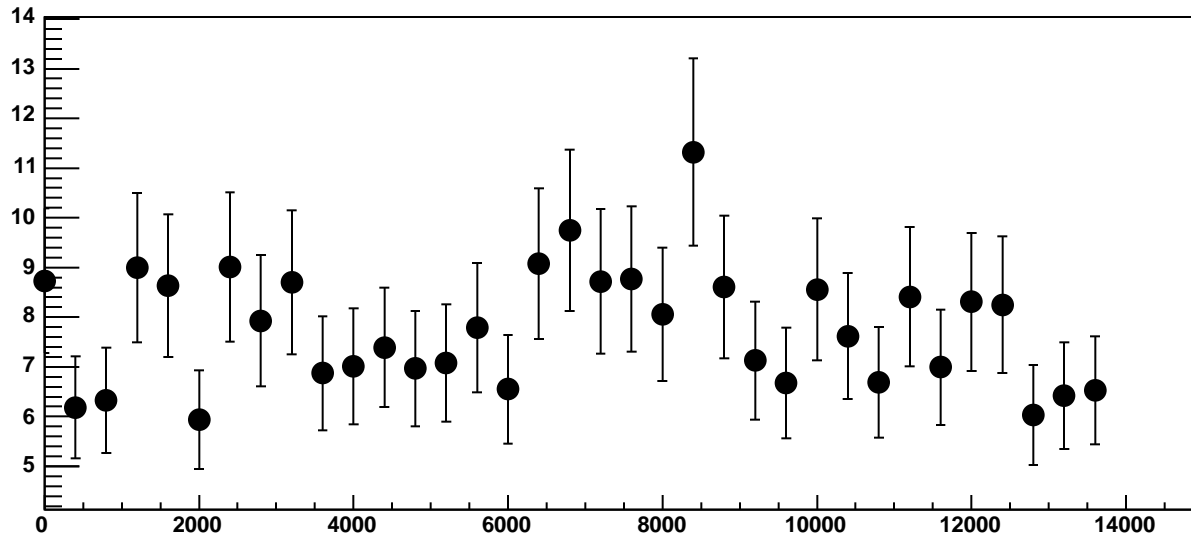


Chip 1, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

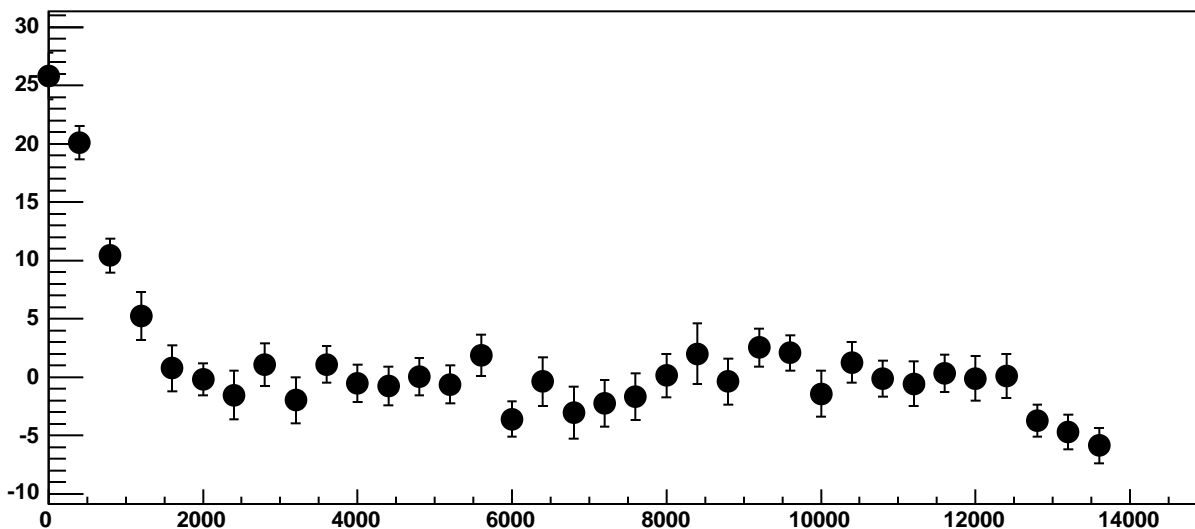


χ^2 / ndf 25.98 / 23
p0 -694.8 ± 0.8079
p1 0.02563 ± 0.0001217

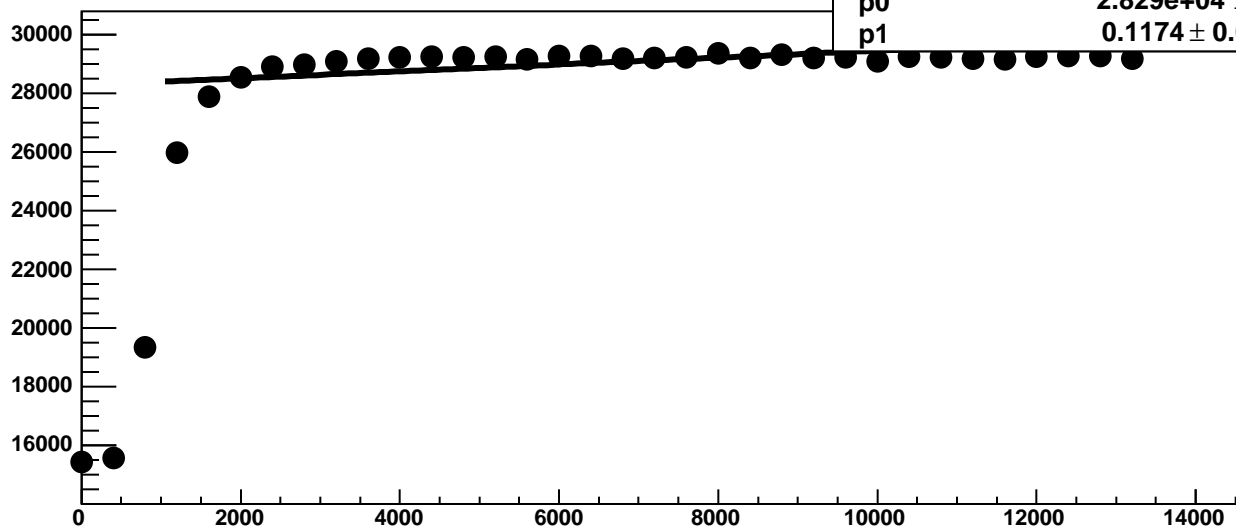
Chip 1, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



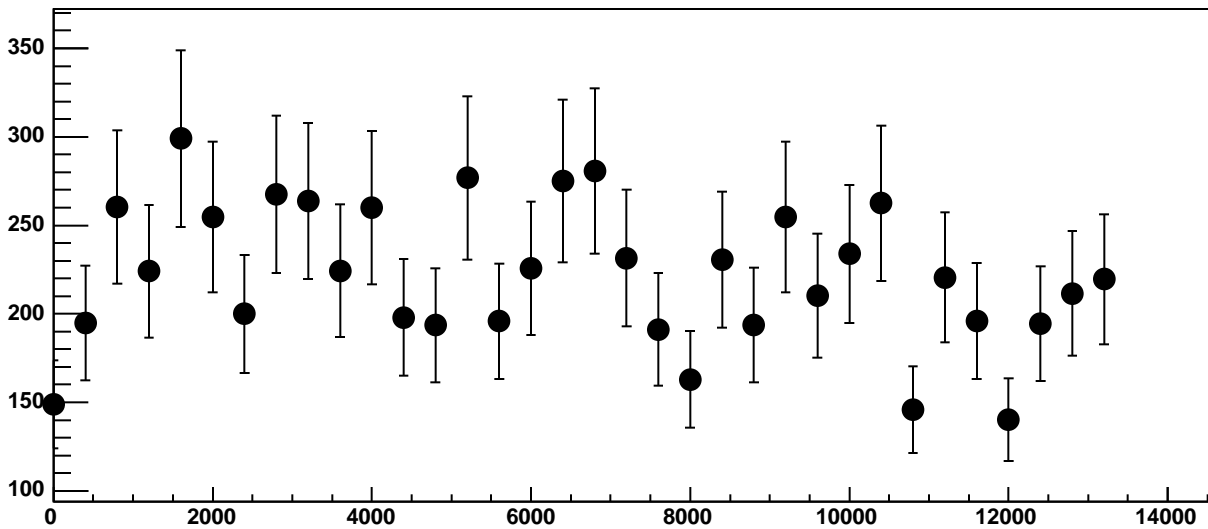
Chip 1, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



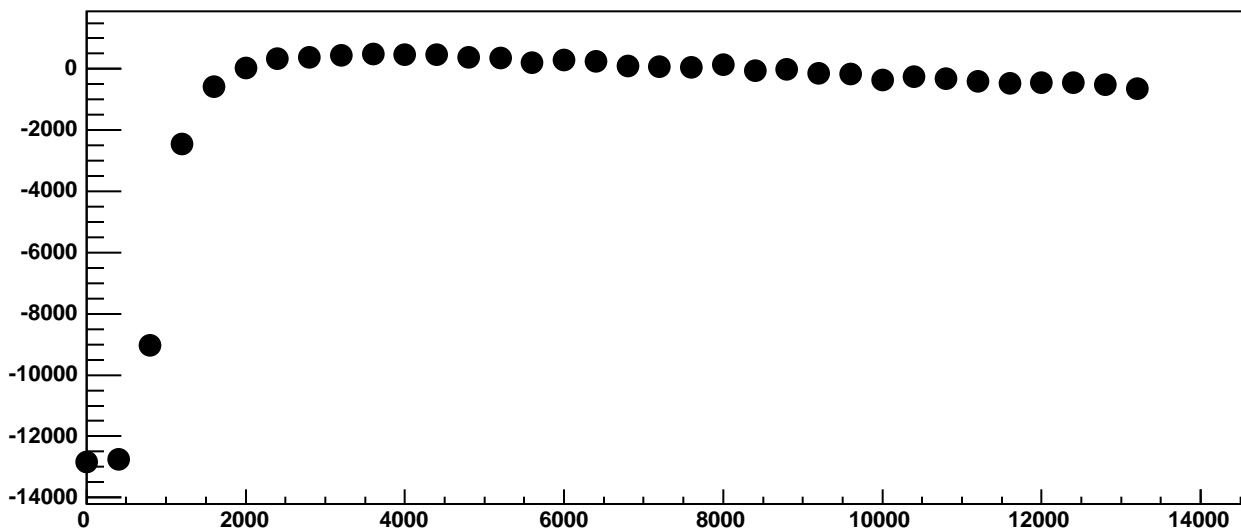
Chip 1, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC



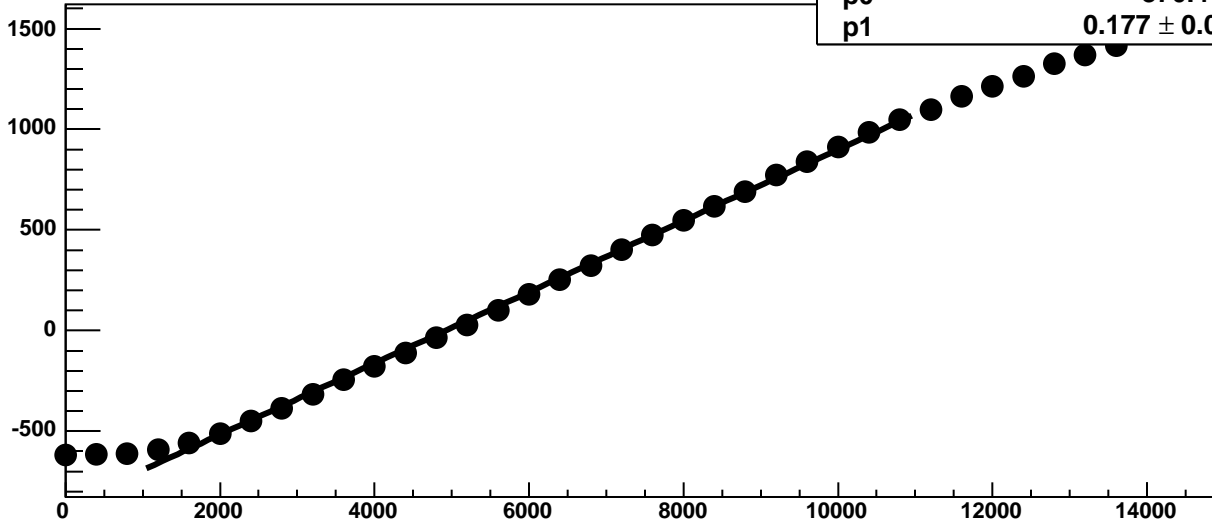
Chip 1, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC



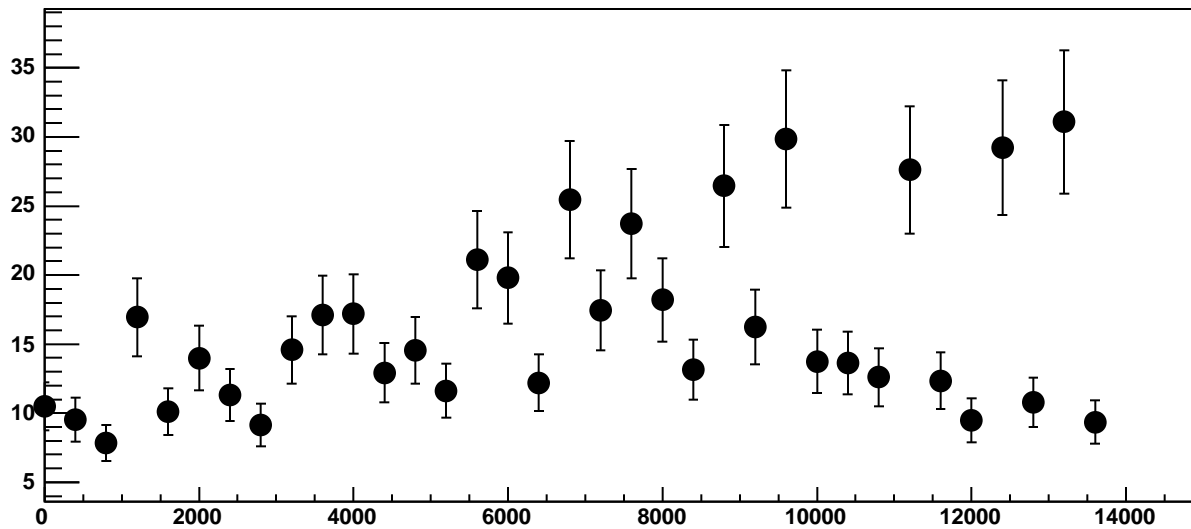
Chip 1, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC



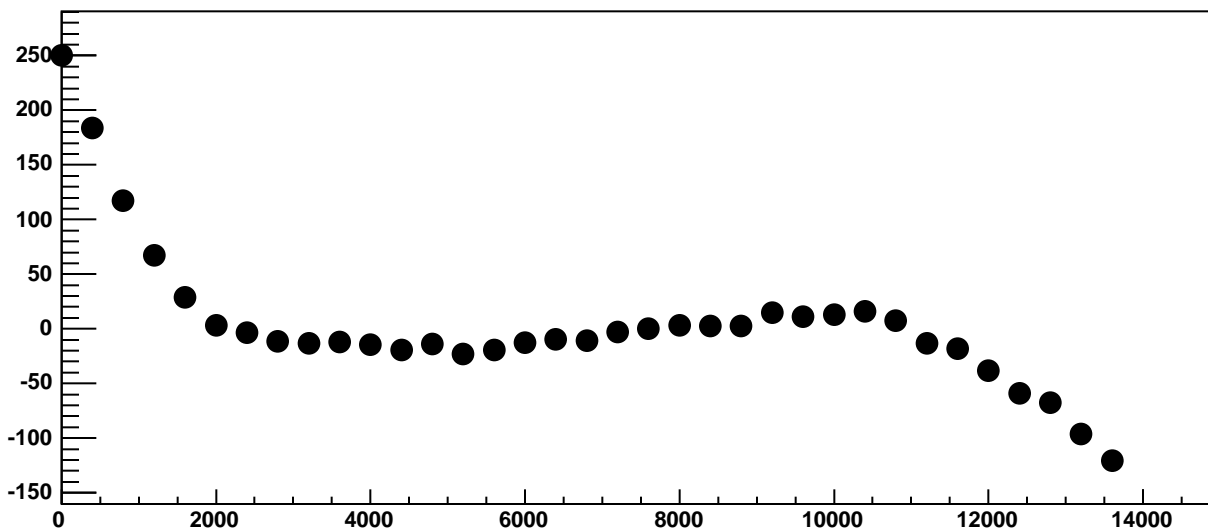
Chip 1, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC



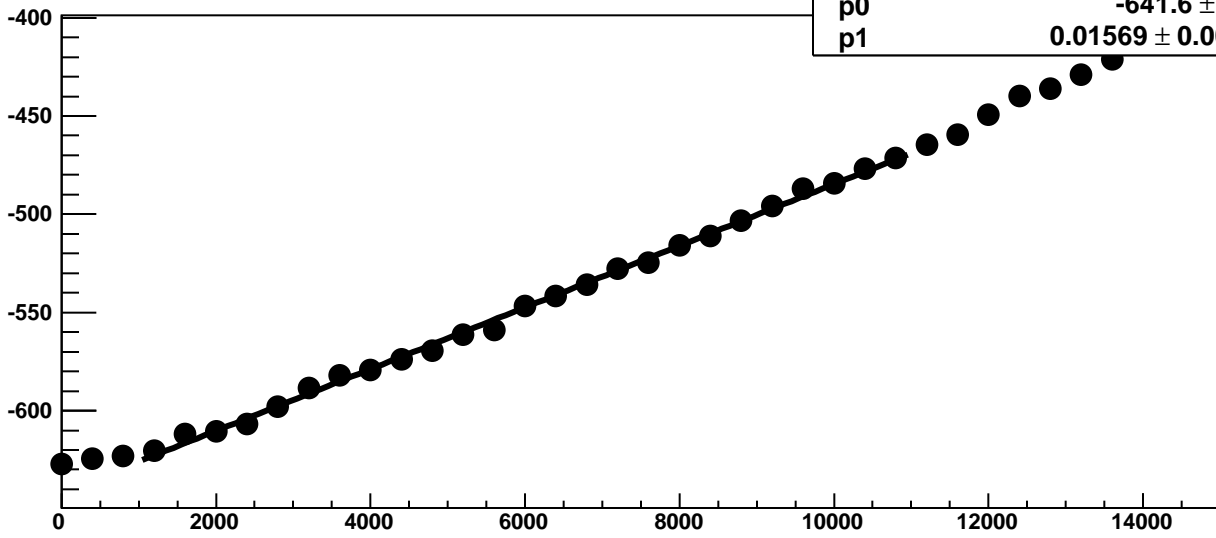
Chip 1, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

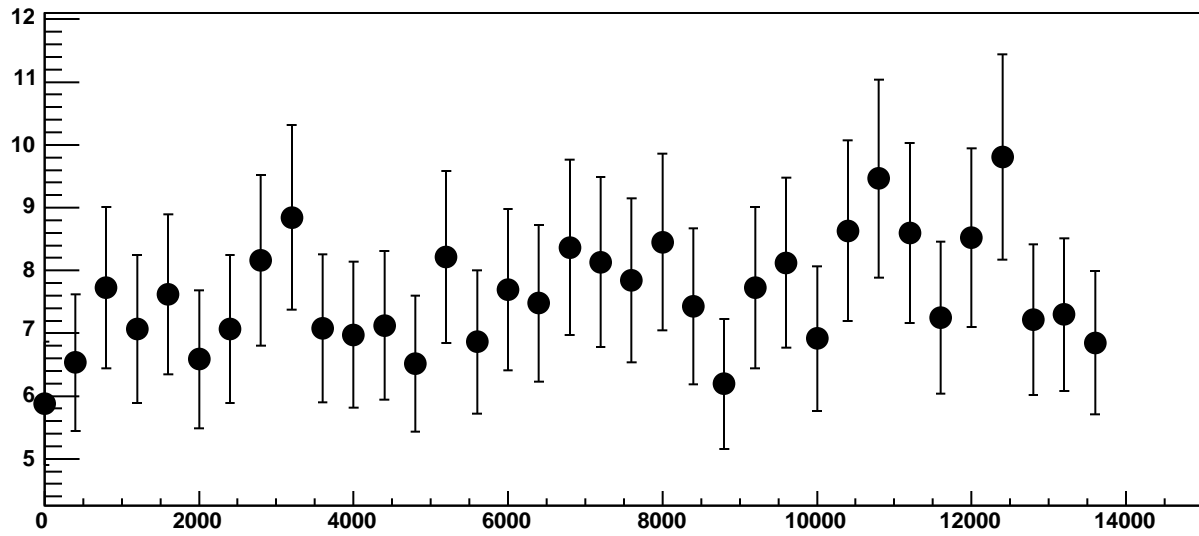


Chip 1, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

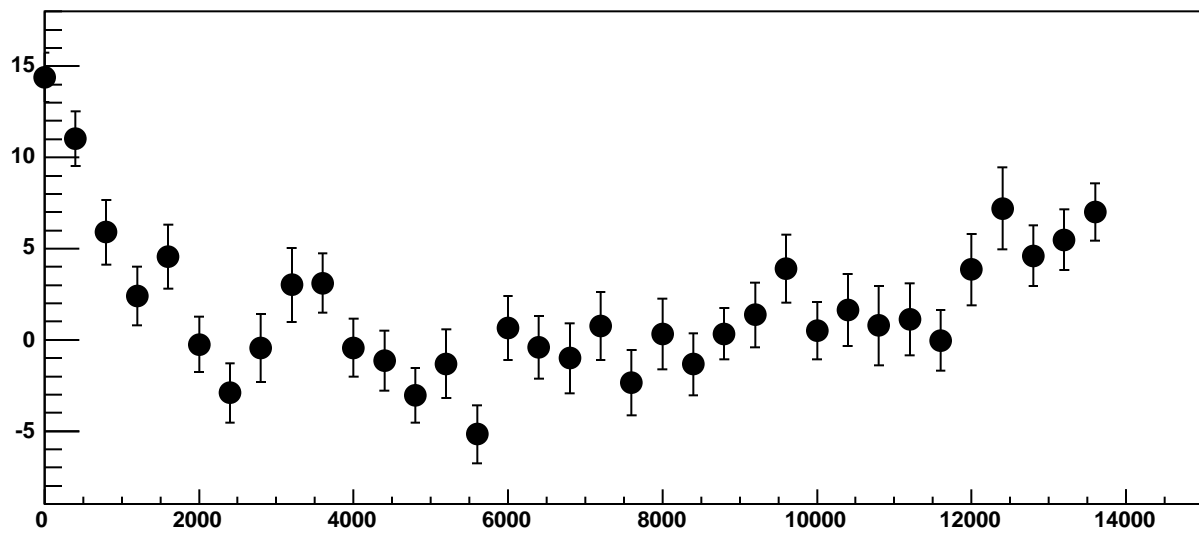


χ^2 / ndf 43.06 / 23
p0 -641.6 ± 0.7824
p1 0.01569 ± 0.0001206

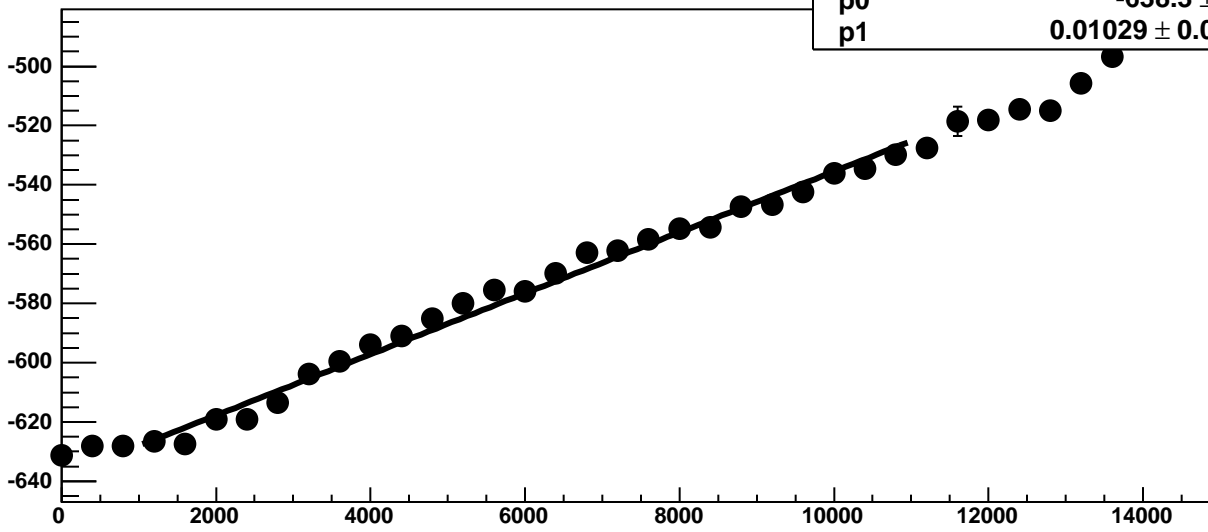
Chip 1, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



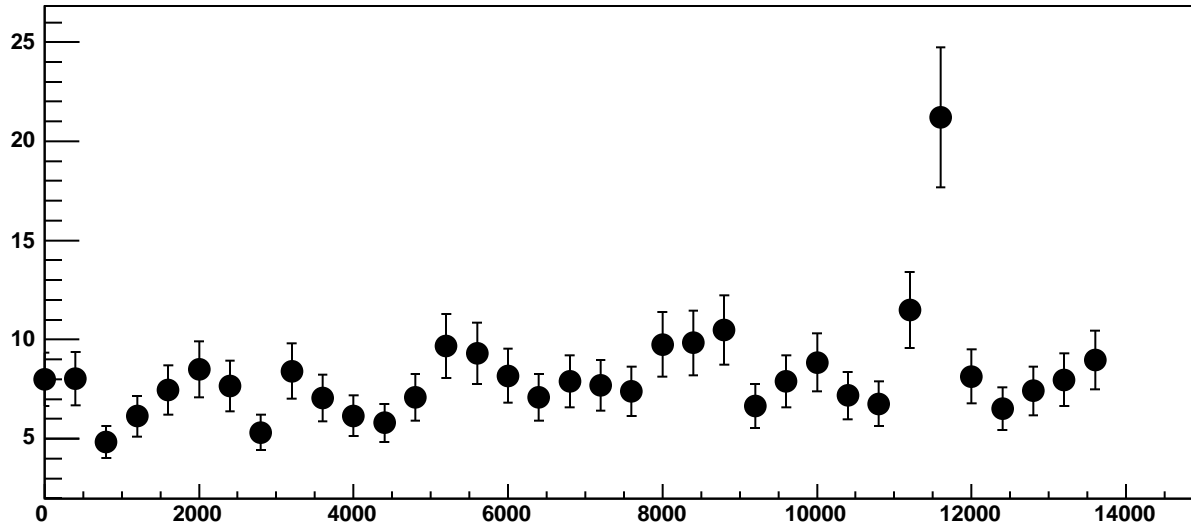
Chip 1, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



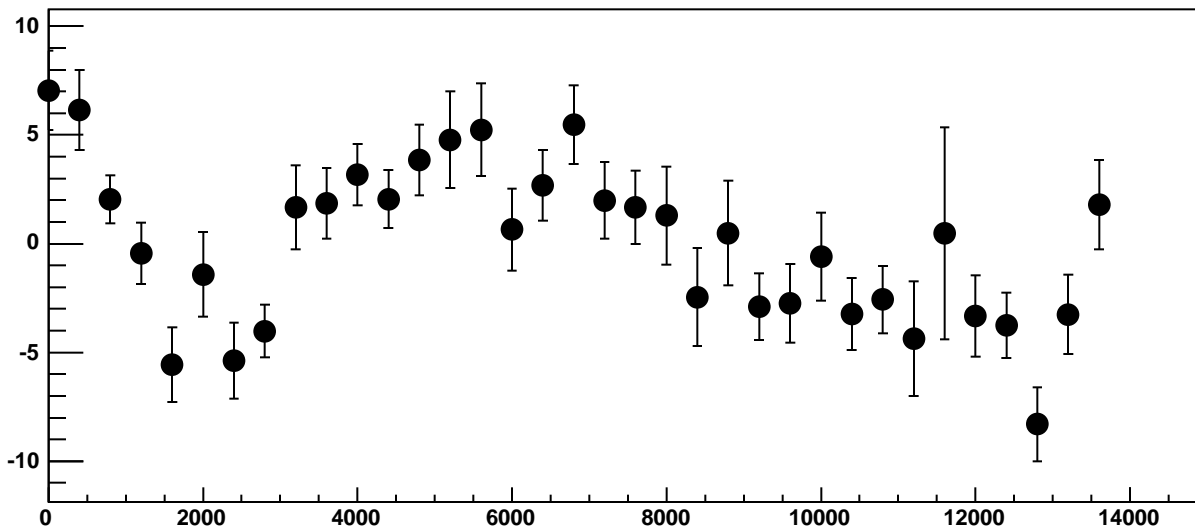
Chip 1, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



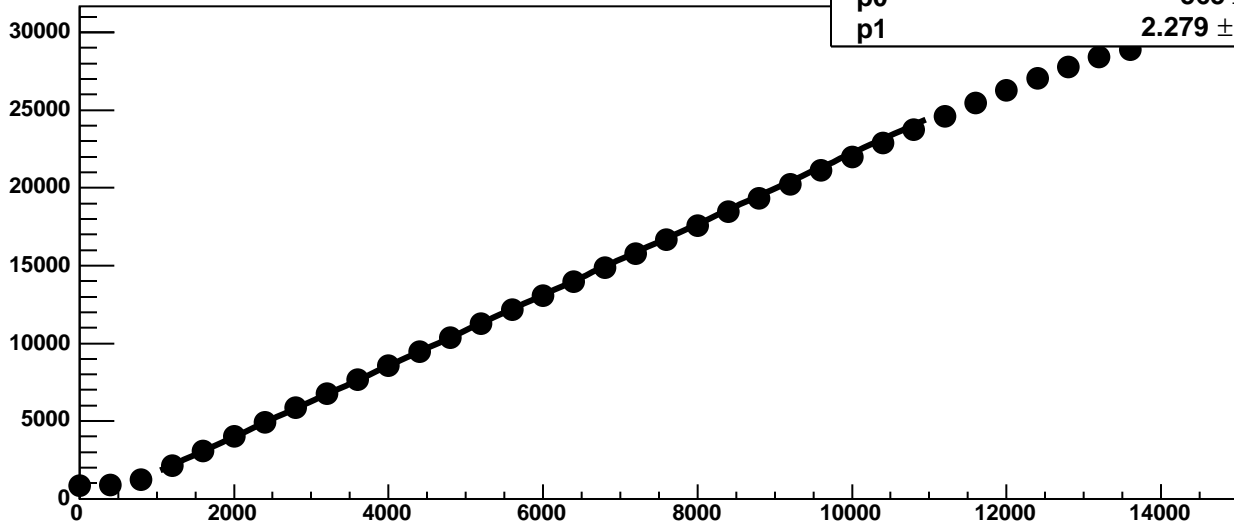
Chip 1, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

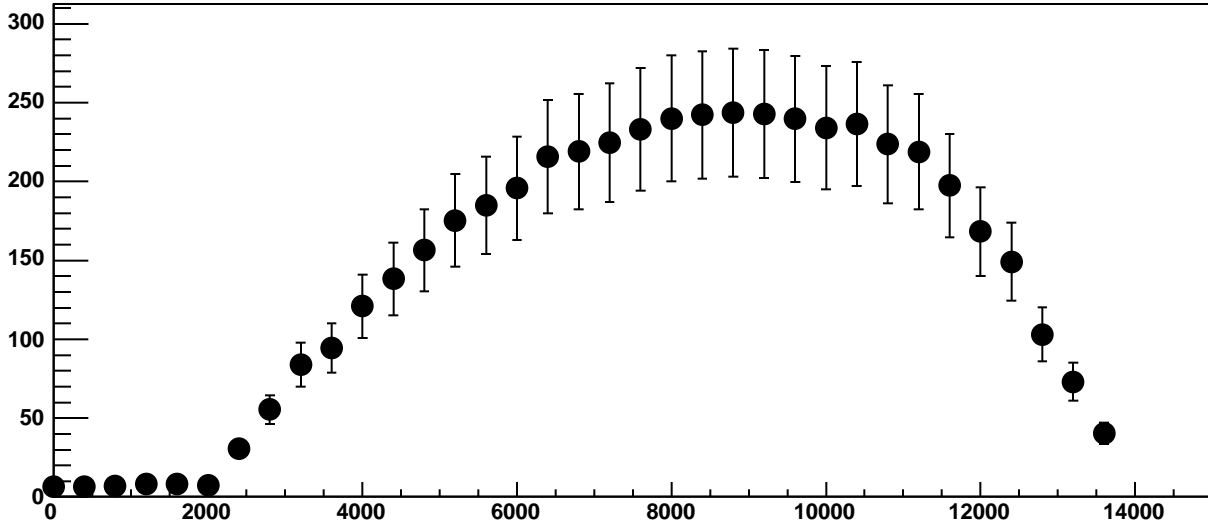


Chip 1, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC

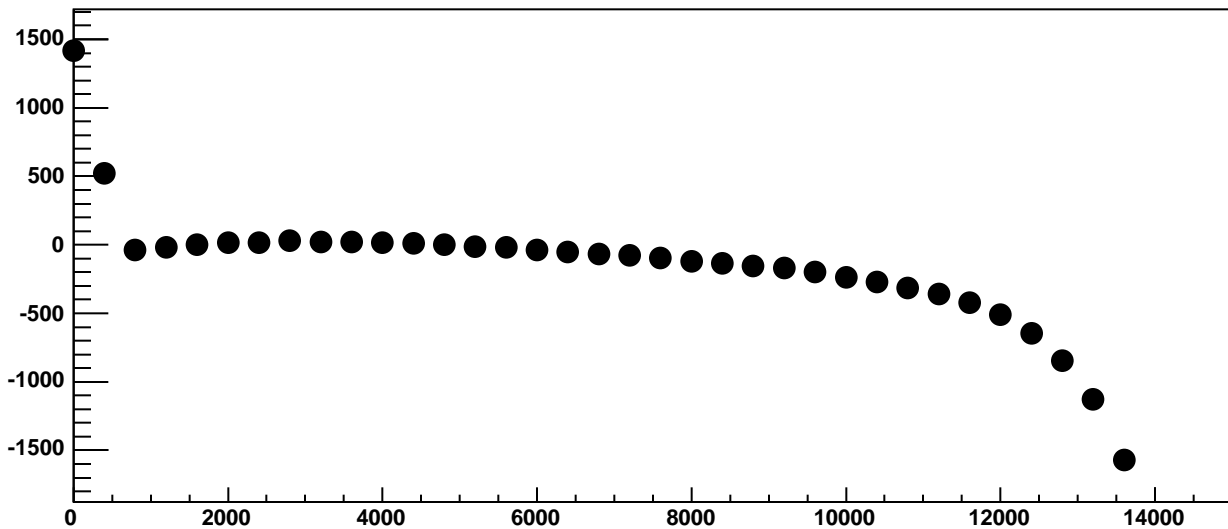


χ^2 / ndf 341.7 / 23
p0 -565 ± 2.905
p1 2.279 ± 0.0016

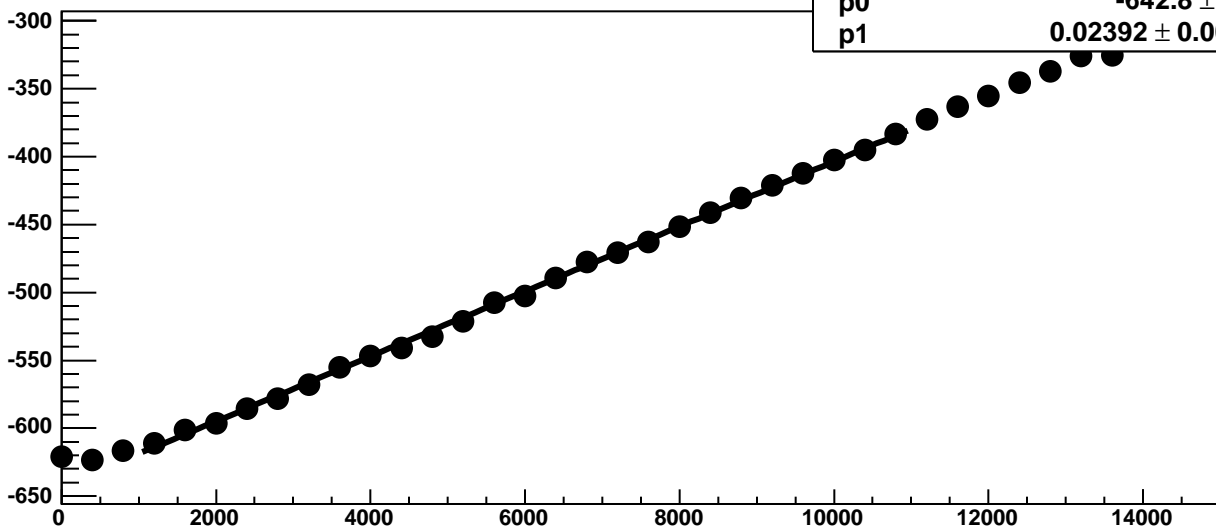
Chip 1, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC

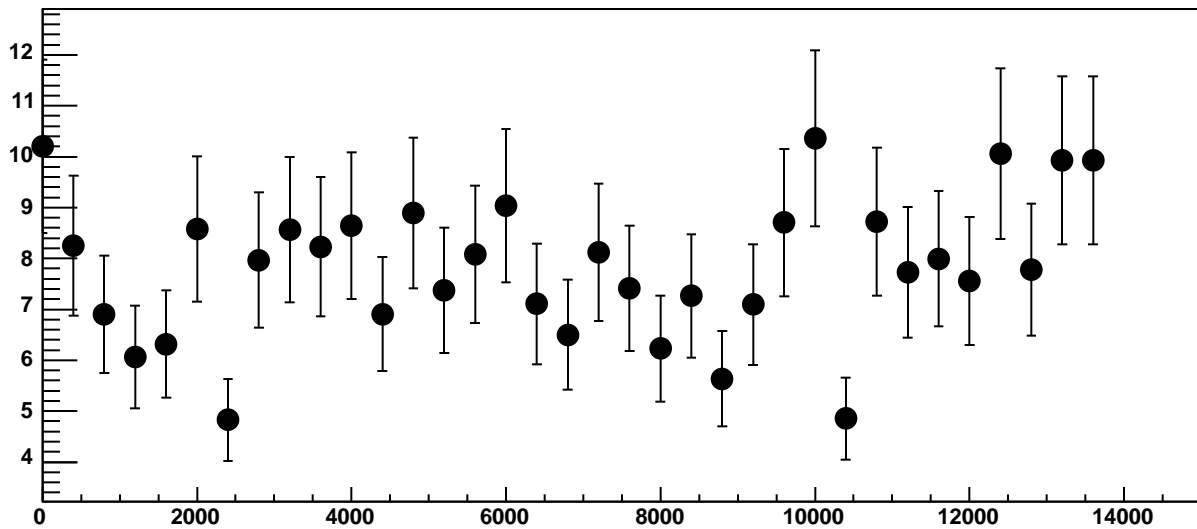


Chip 1, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

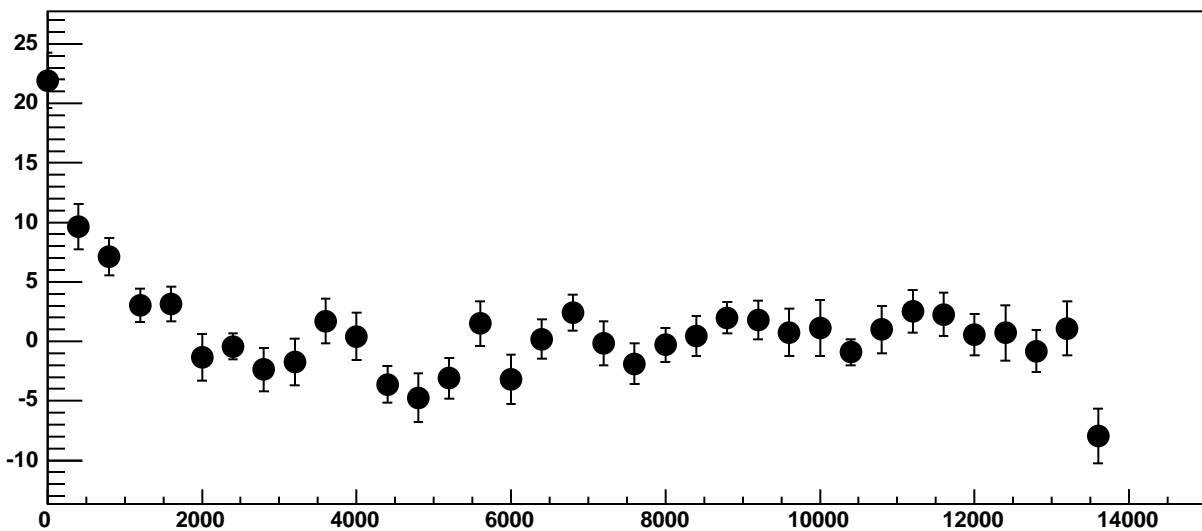


χ^2 / ndf 39.43 / 23
p0 -642.8 ± 0.7182
p1 0.02392 ± 0.0001072

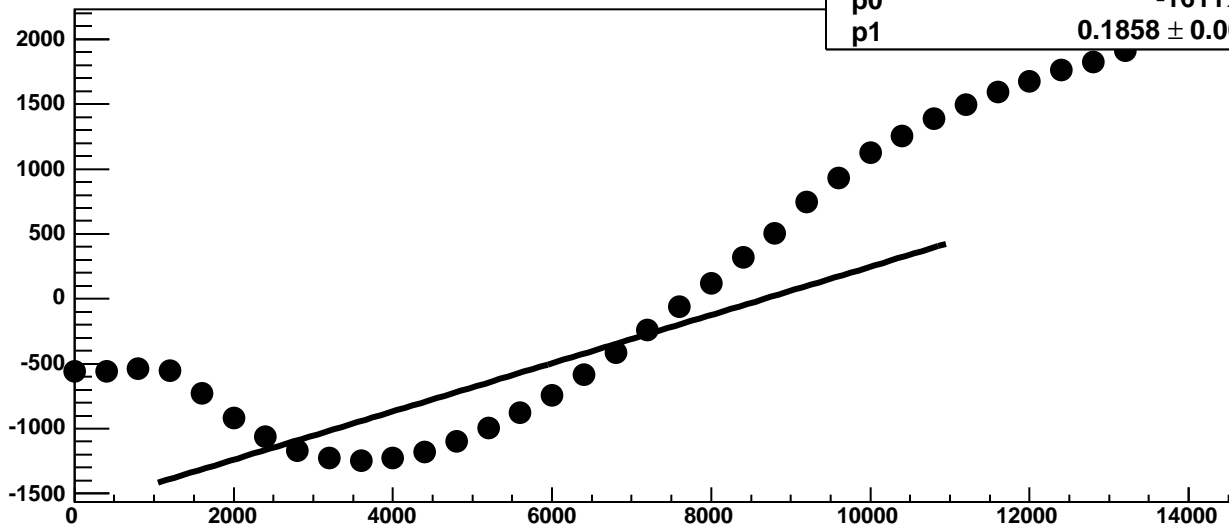
Chip 1, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

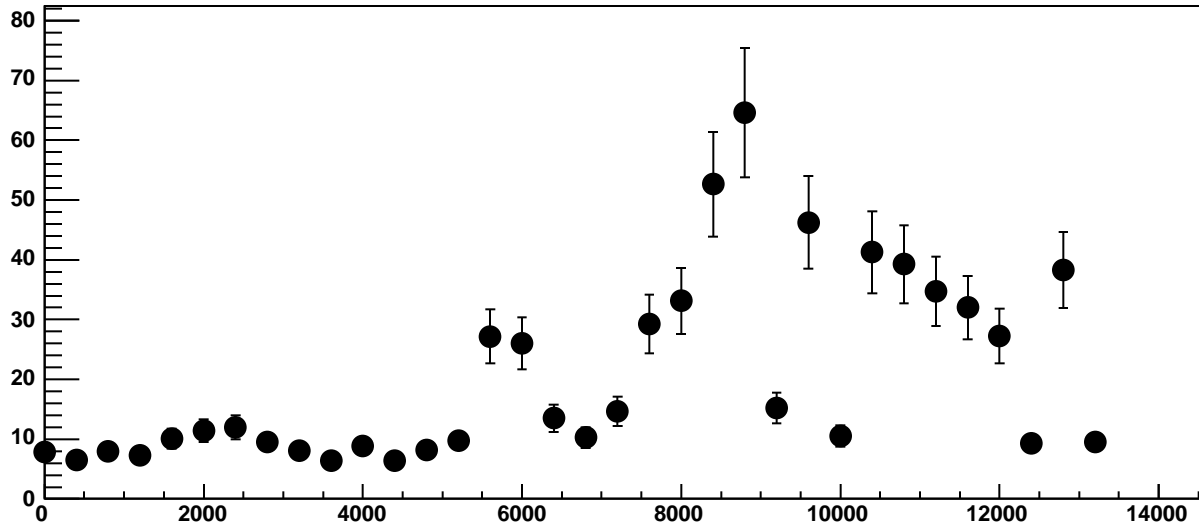


Chip 1, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

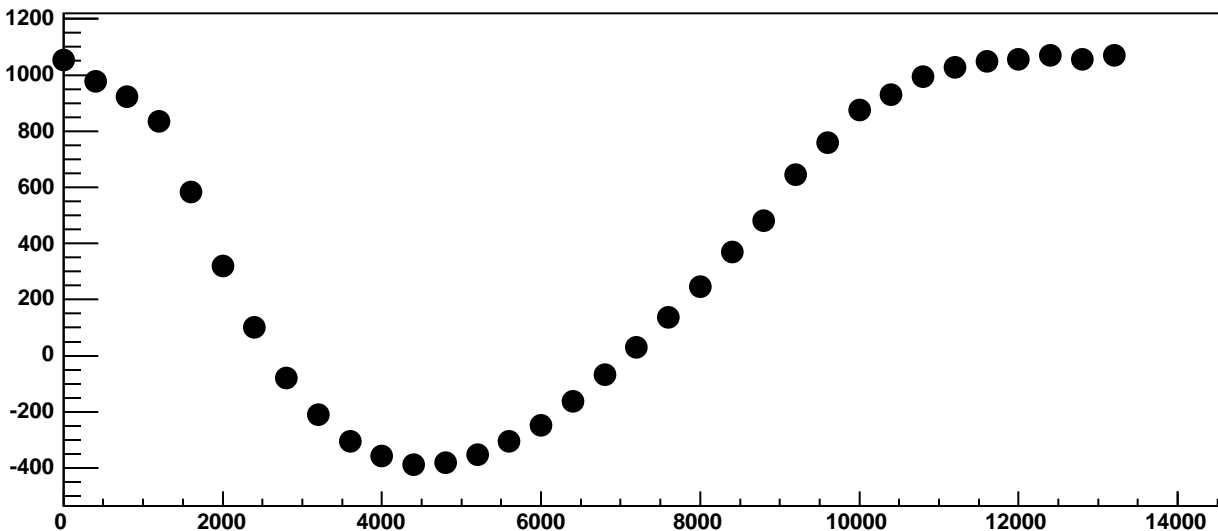


χ^2 / ndf 7.538e+05 / 23
p0 -1611 ± 1.106
p1 0.1858 ± 0.0002286

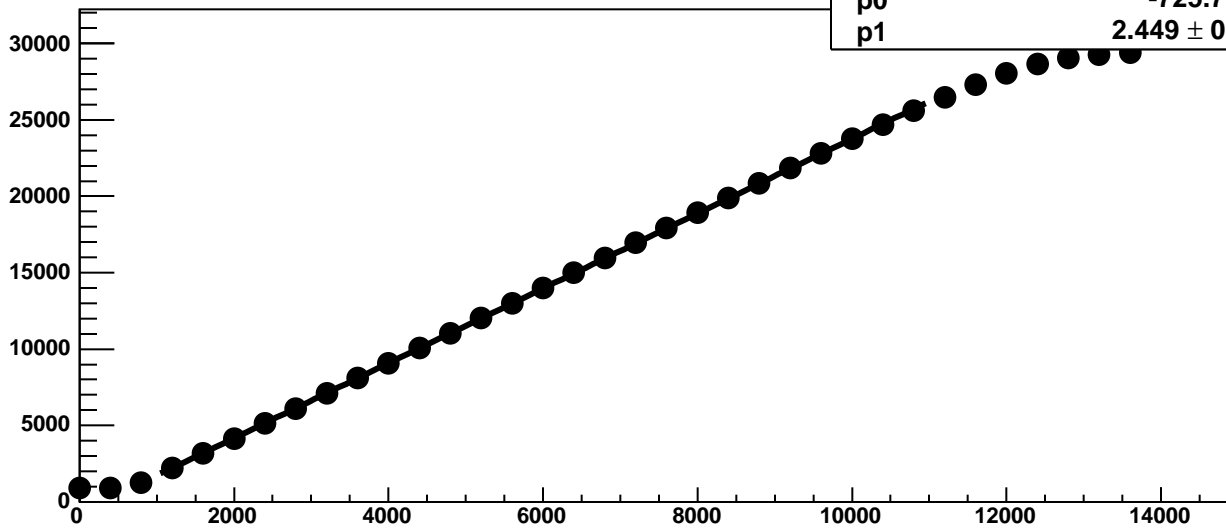
Chip 1, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



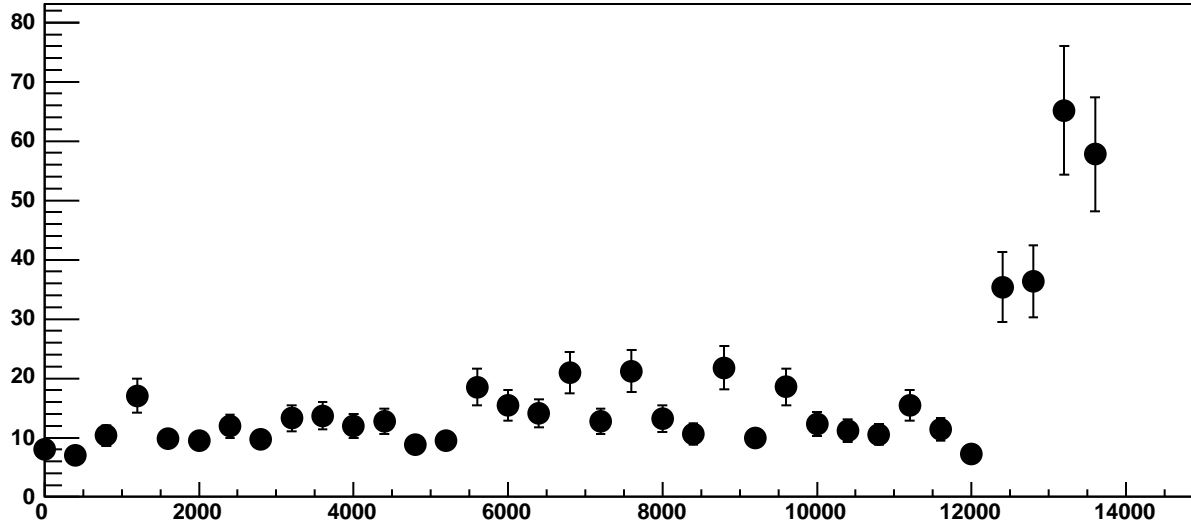
Chip 1, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



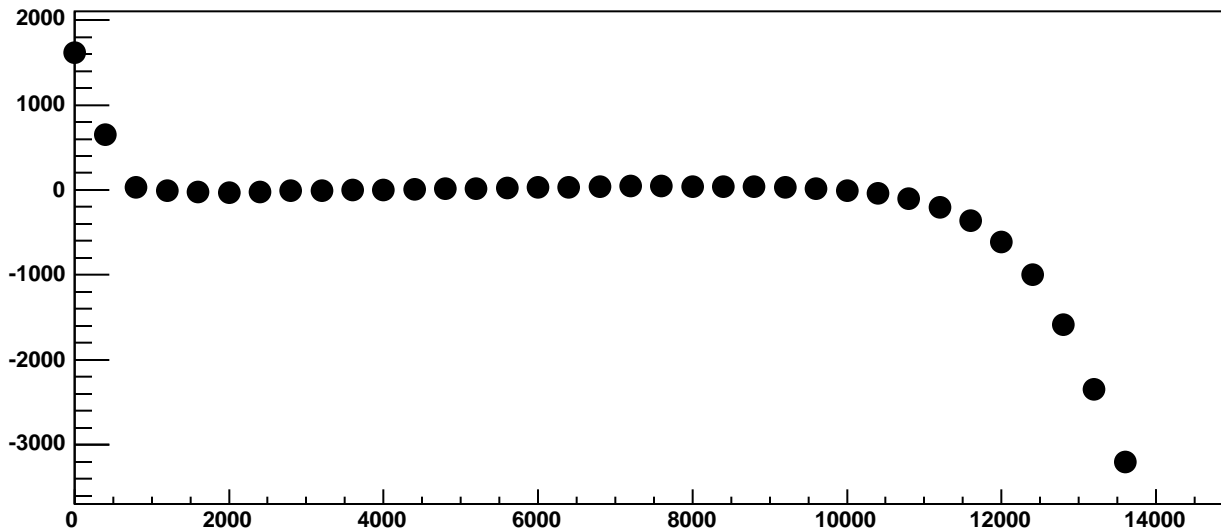
Chip 1, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC



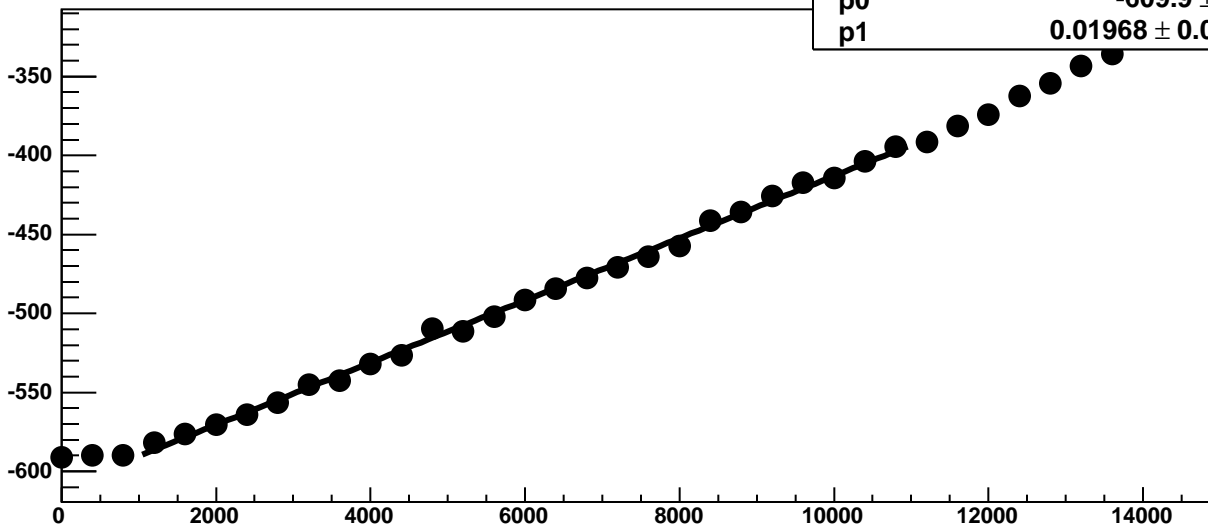
Chip 1, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



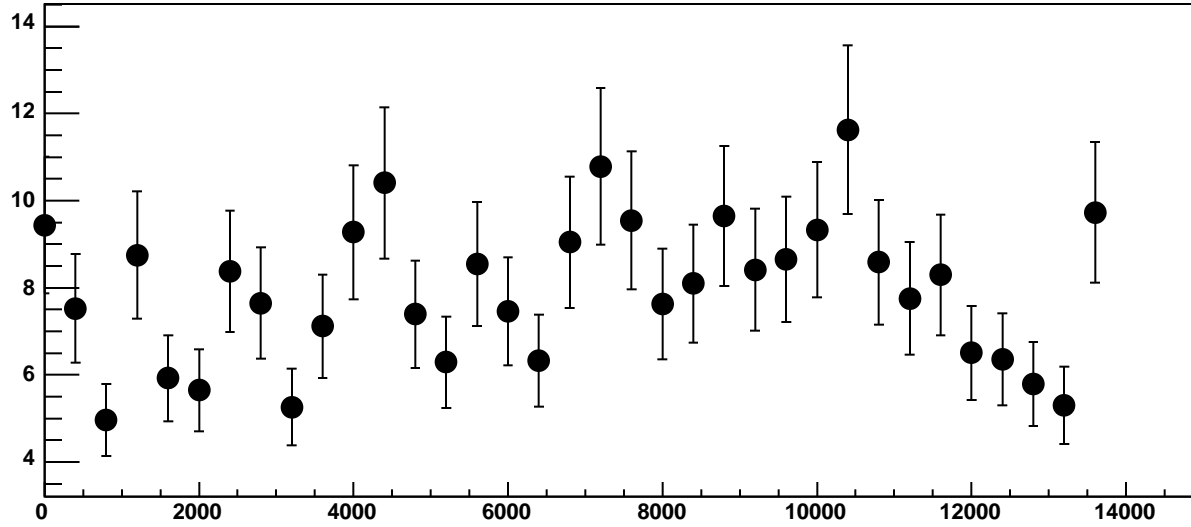
Chip 1, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC



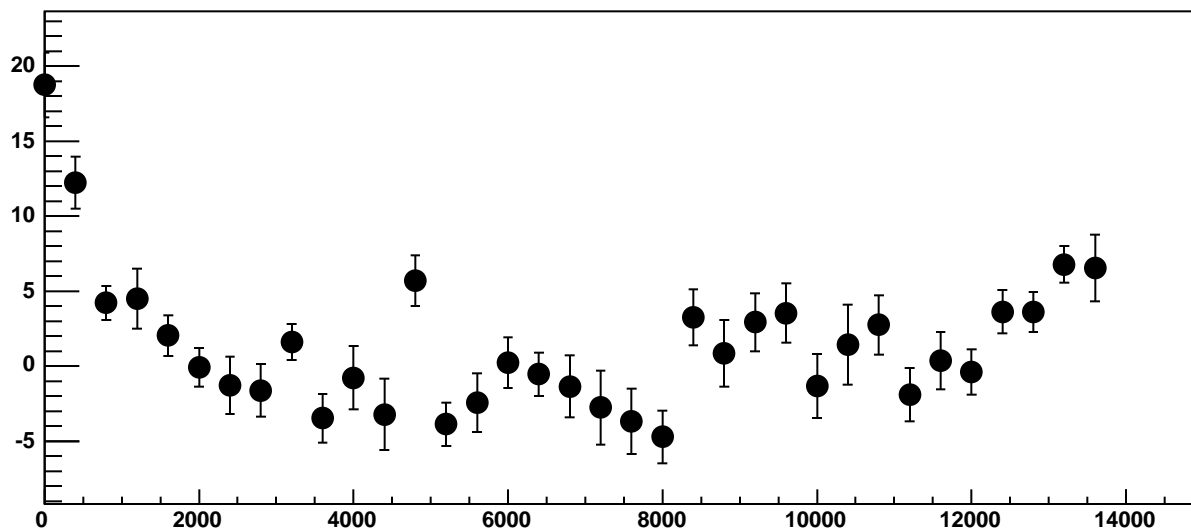
Chip 1, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



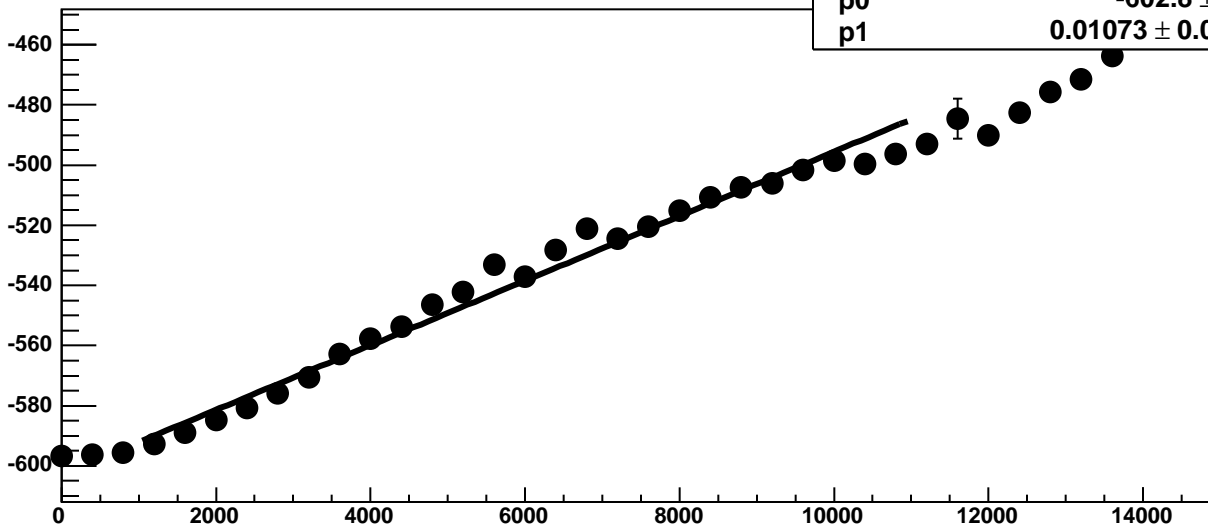
Chip 1, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



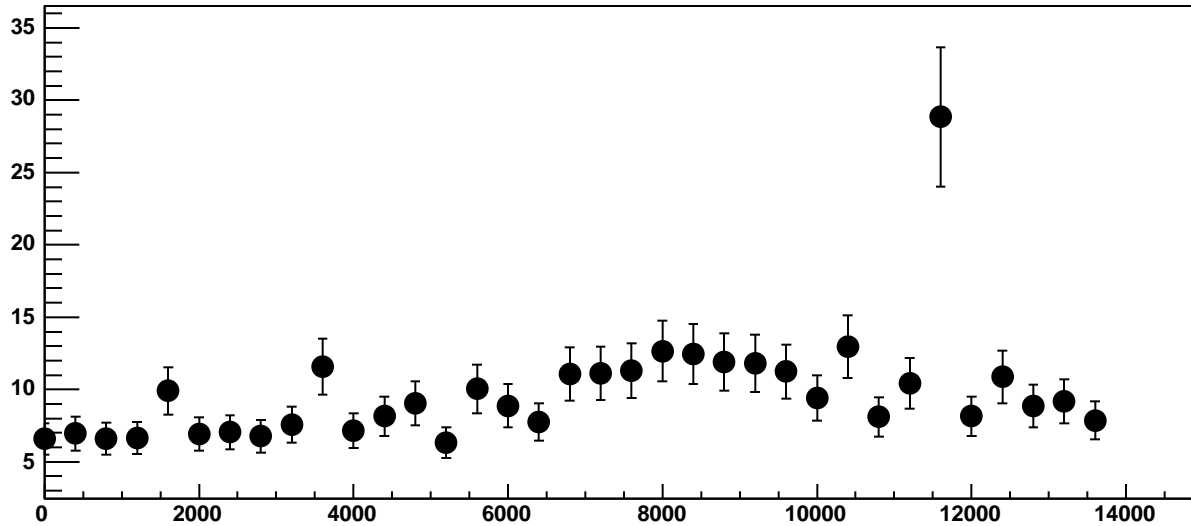
Chip 1, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



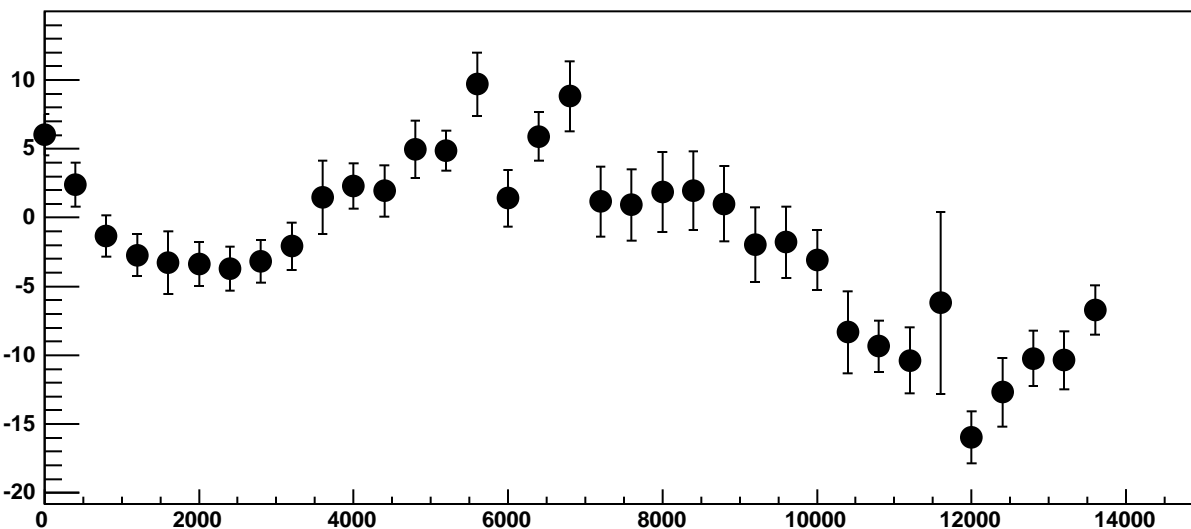
Chip 1, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



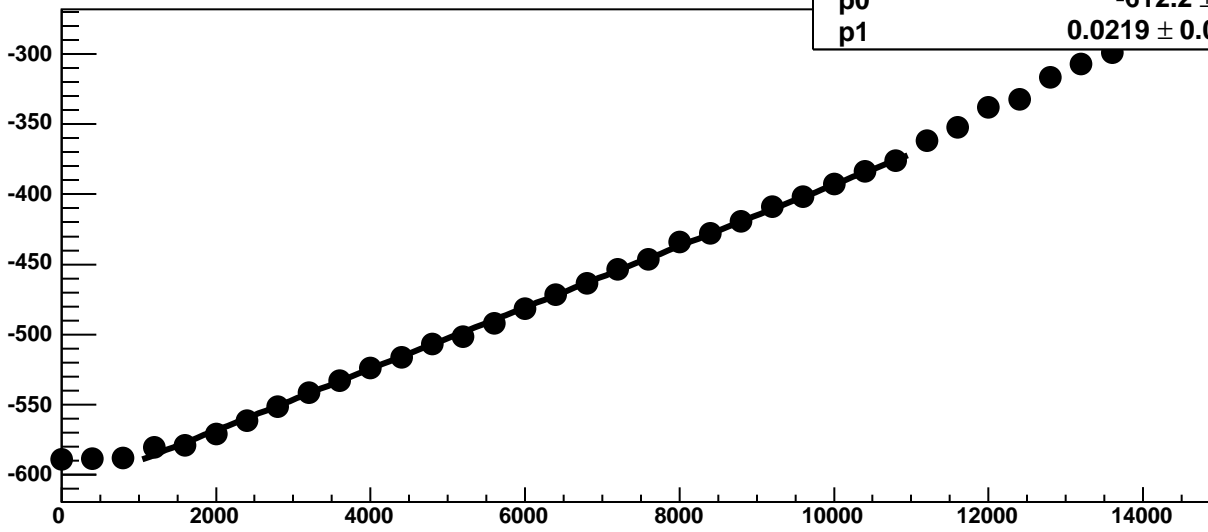
Chip 1, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

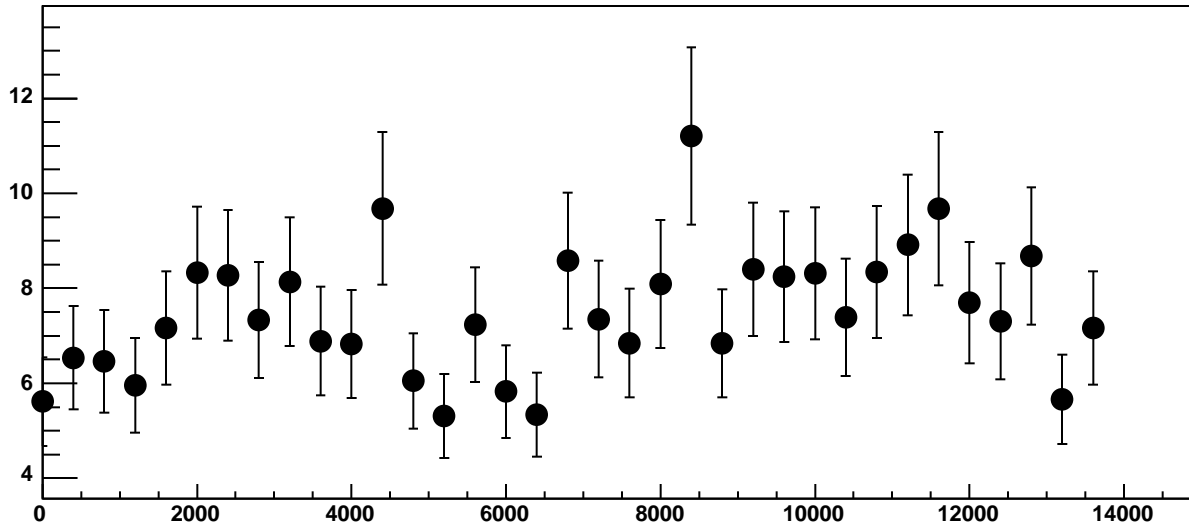


Chip 1, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC

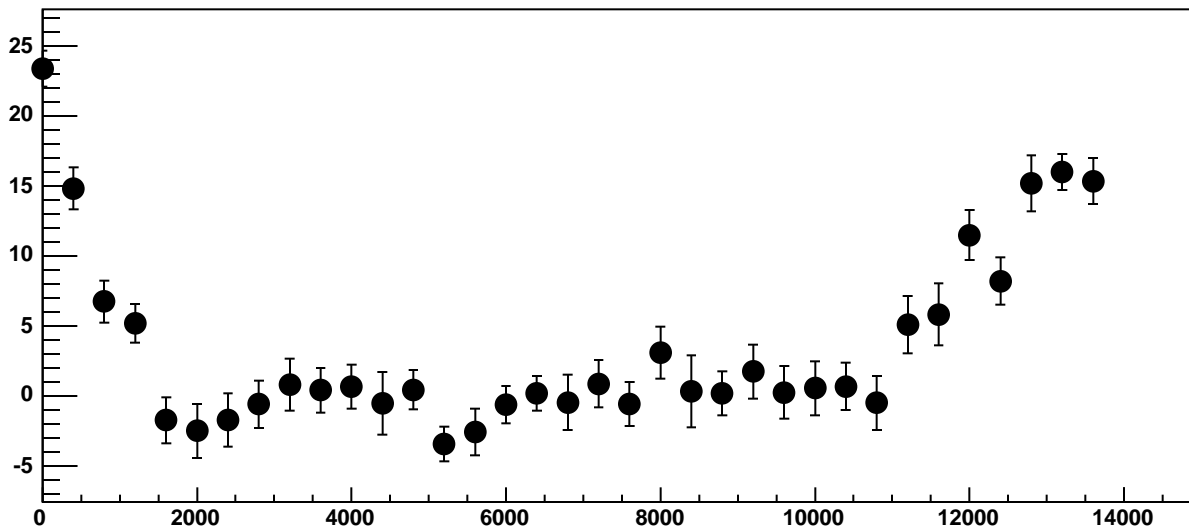


χ^2 / ndf 33.7 / 23
p0 -612.2 ± 0.7662
p1 0.0219 ± 0.0001201

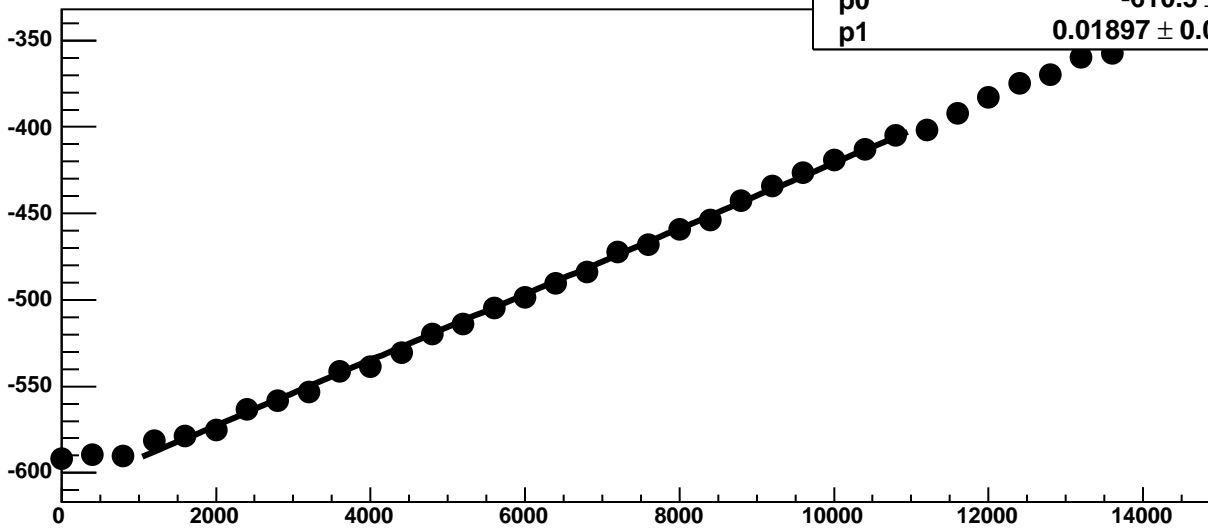
Chip 1, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

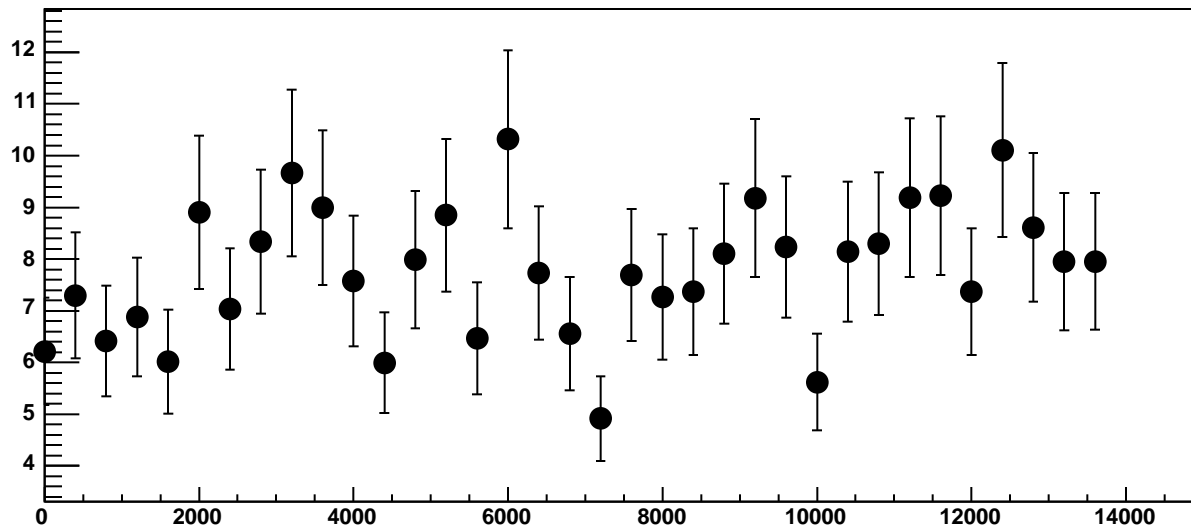


Chip 1, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

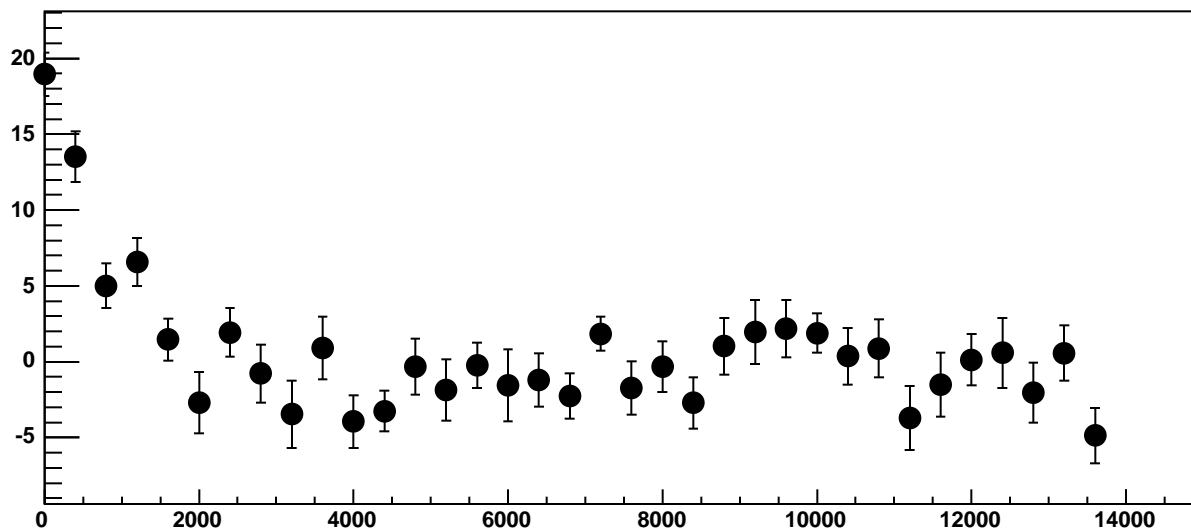


χ^2 / ndf 50.64 / 23
p0 -610.5 ± 0.7811
p1 0.01897 ± 0.0001172

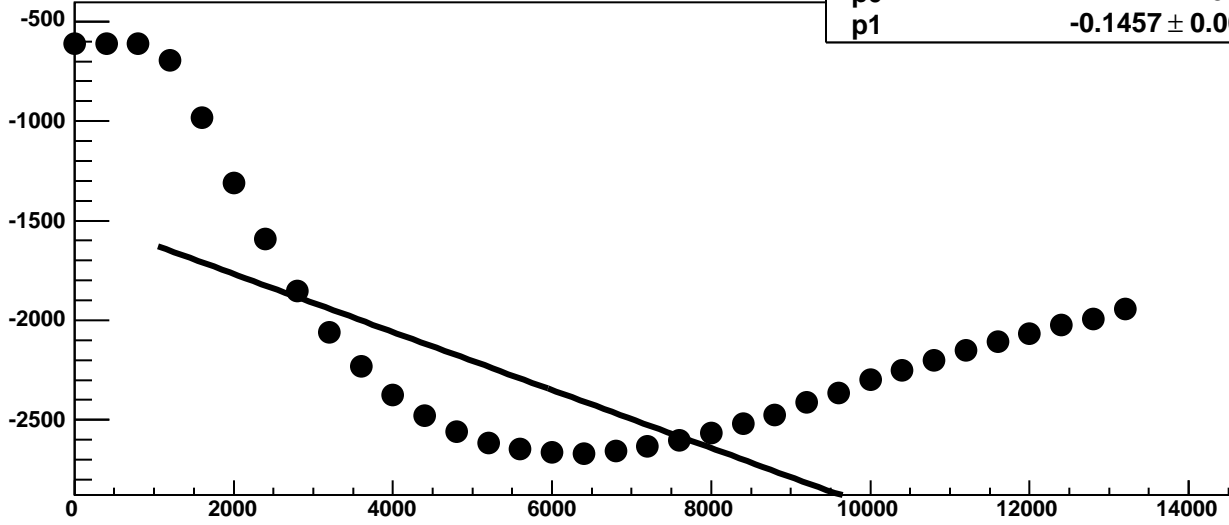
Chip 1, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

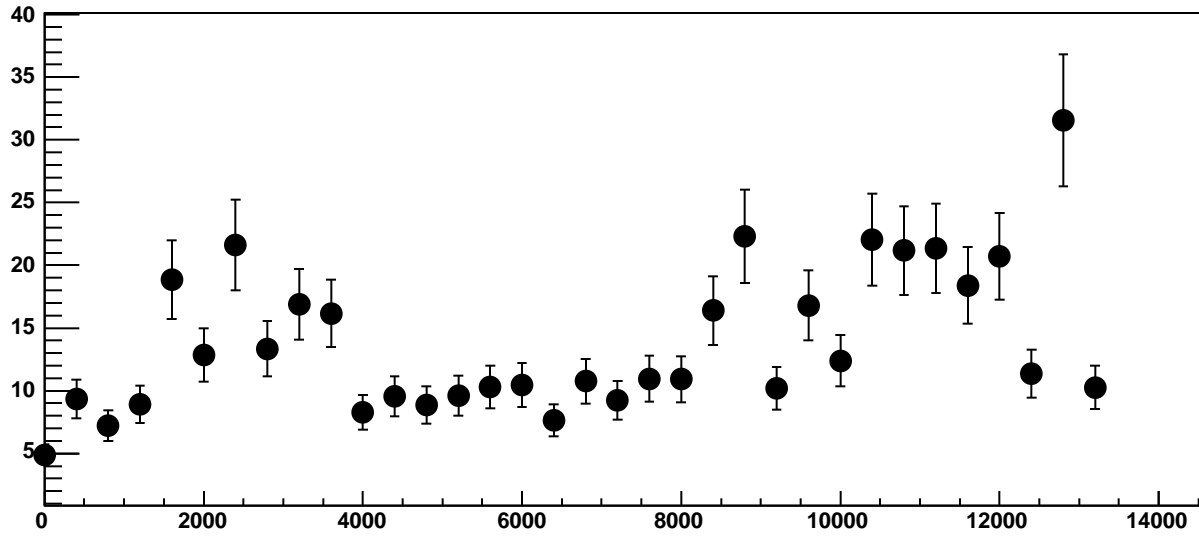


Chip 1, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

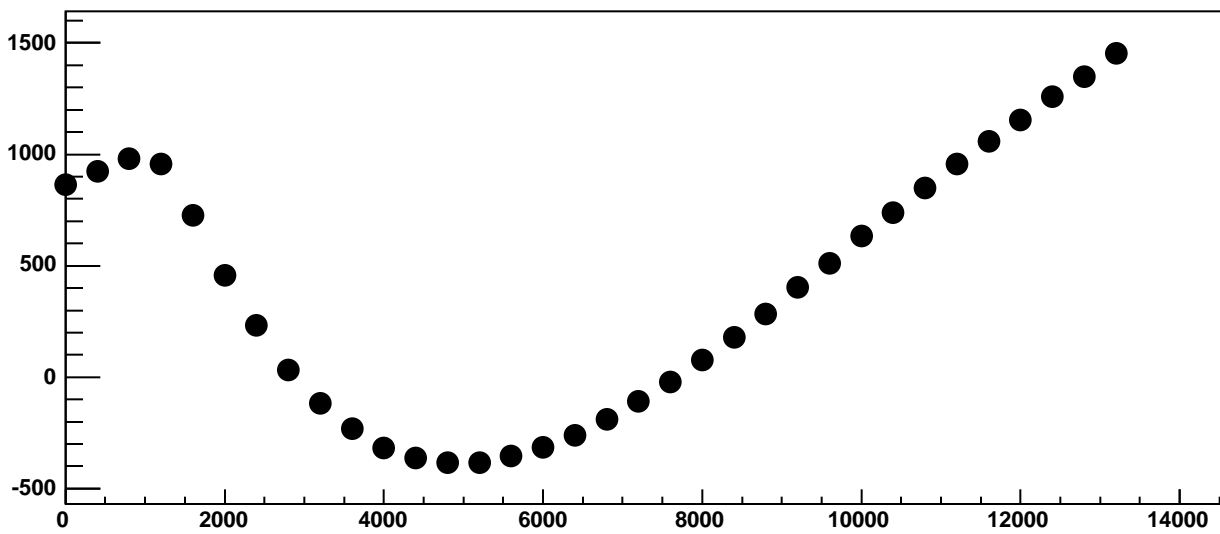


χ^2 / ndf 6.24e+05 / 23
p0 -1475 ± 1.341
p1 -0.1457 ± 0.0002157

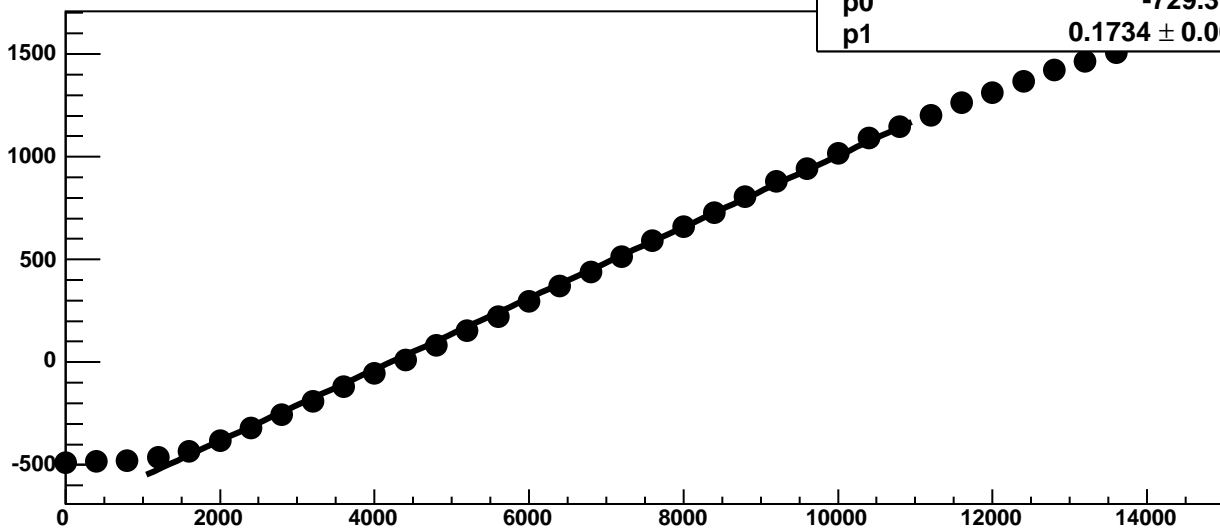
Chip 1, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



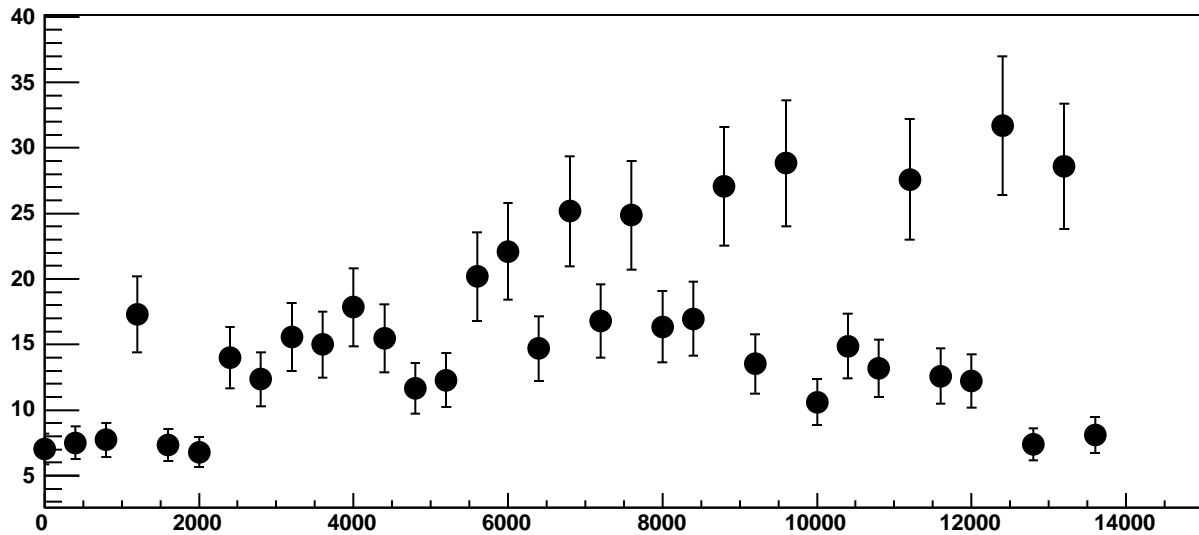
Chip 1, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



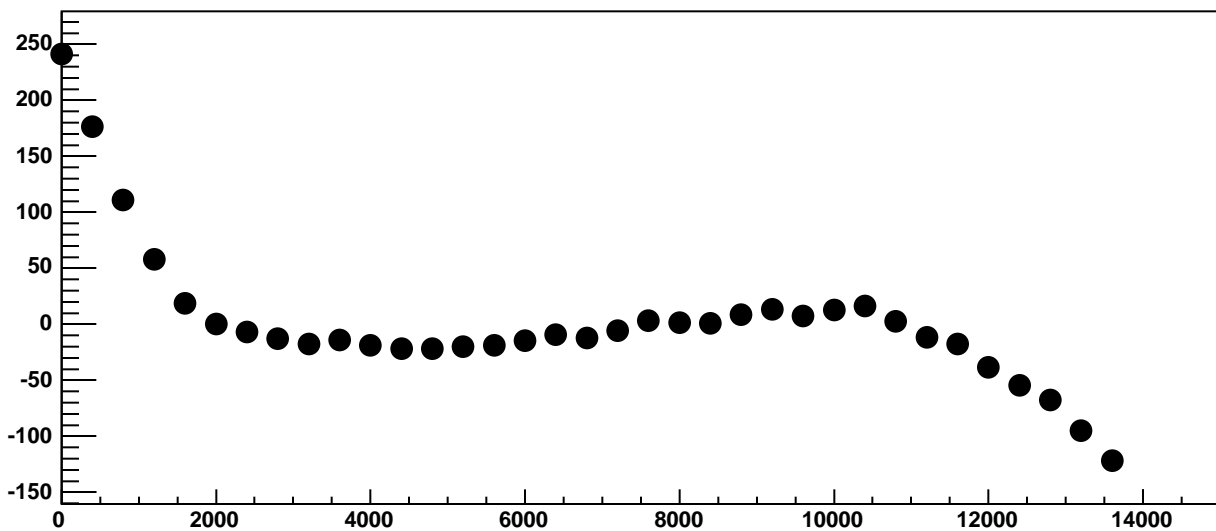
Chip 1, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC



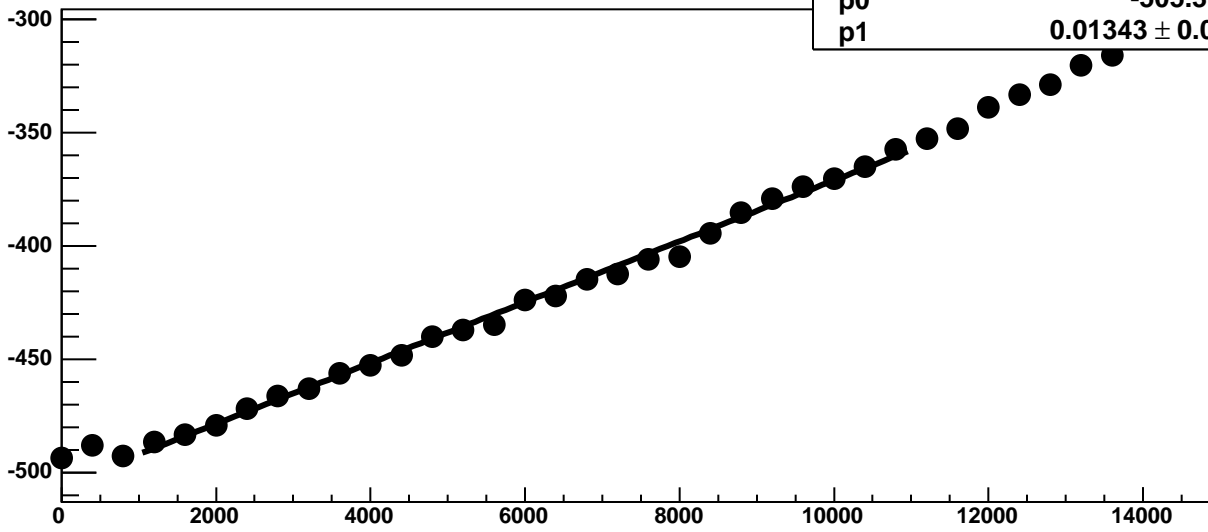
Chip 1, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC

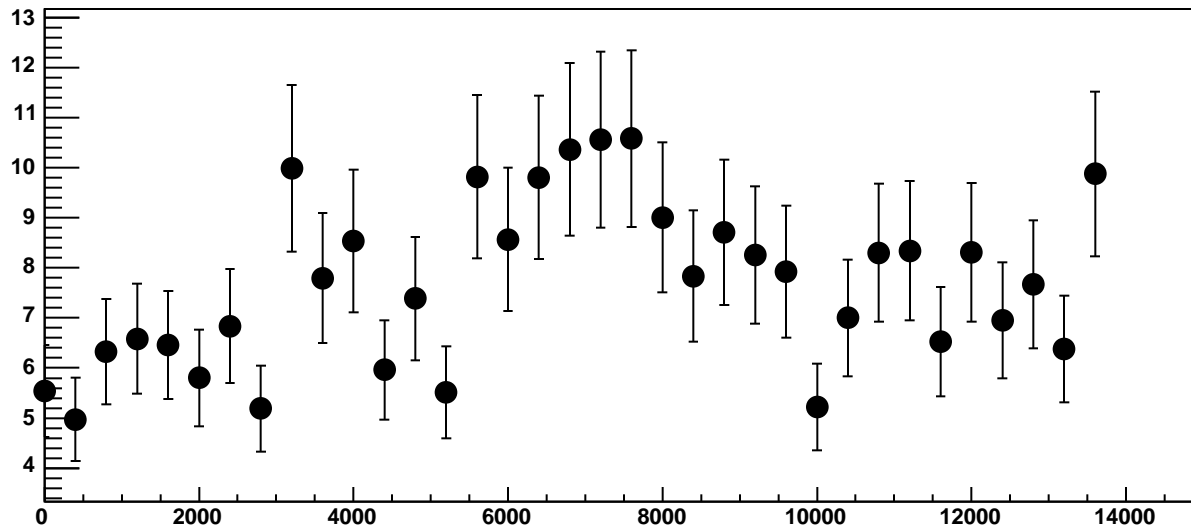


Chip 1, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

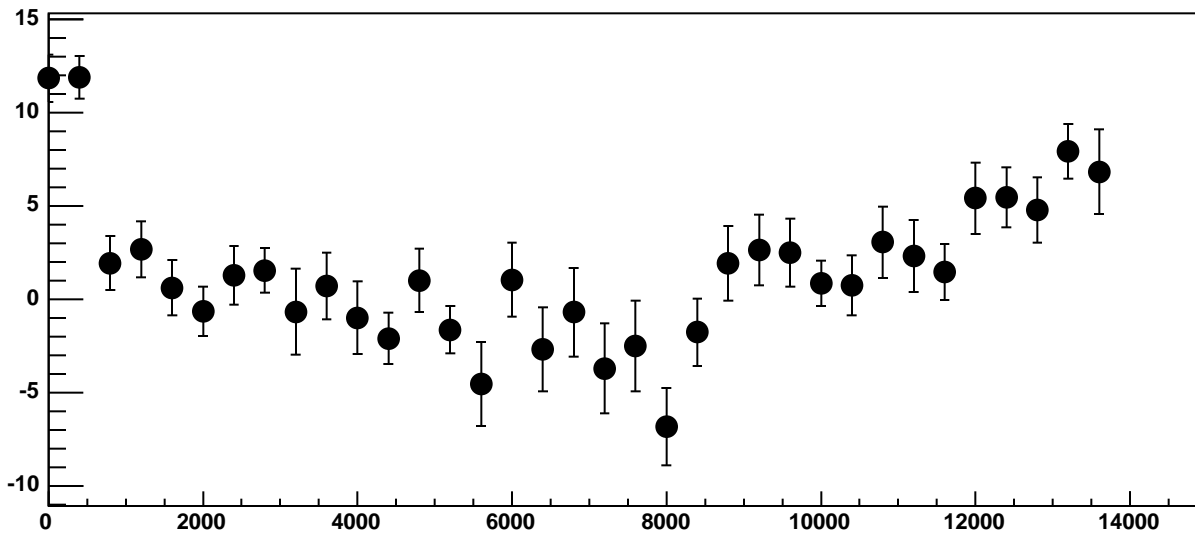


χ^2 / ndf 39.87 / 23
p0 -505.3 ± 0.703
p1 0.01343 ± 0.0001097

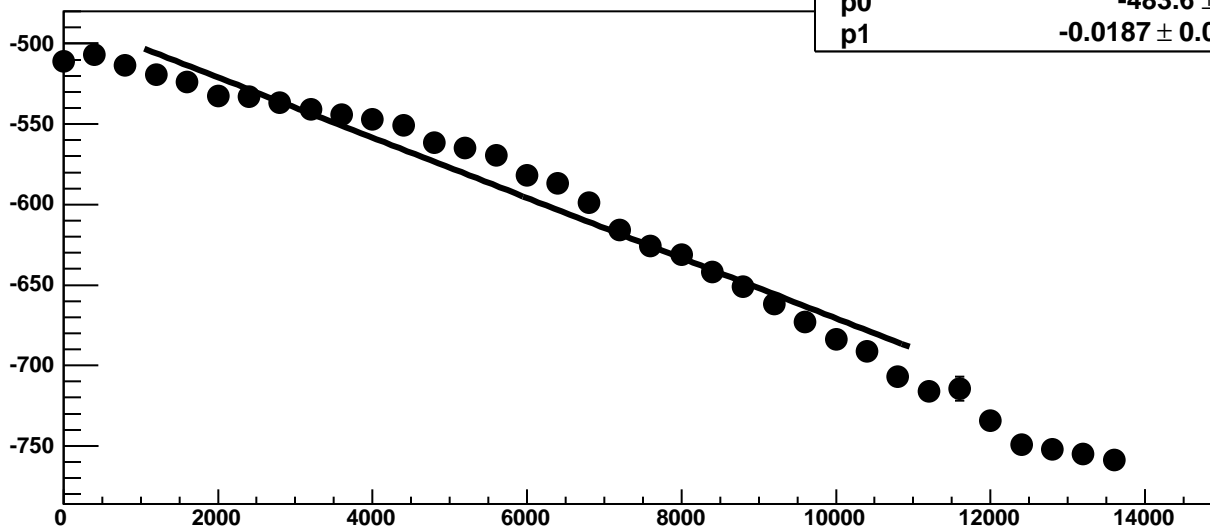
Chip 1, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

770.6 / 23

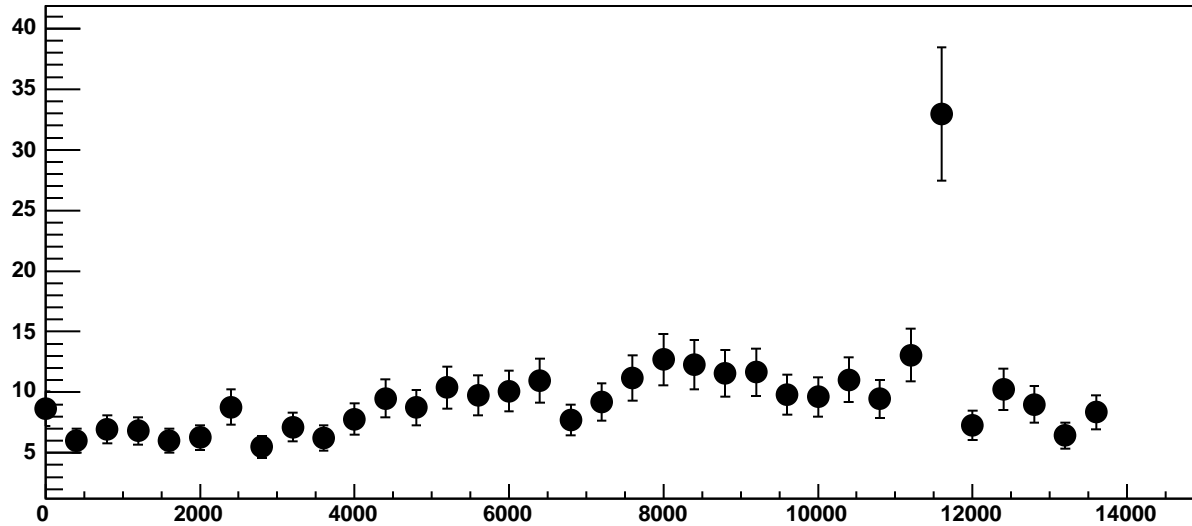
p0

-483.6 ± 0.7634

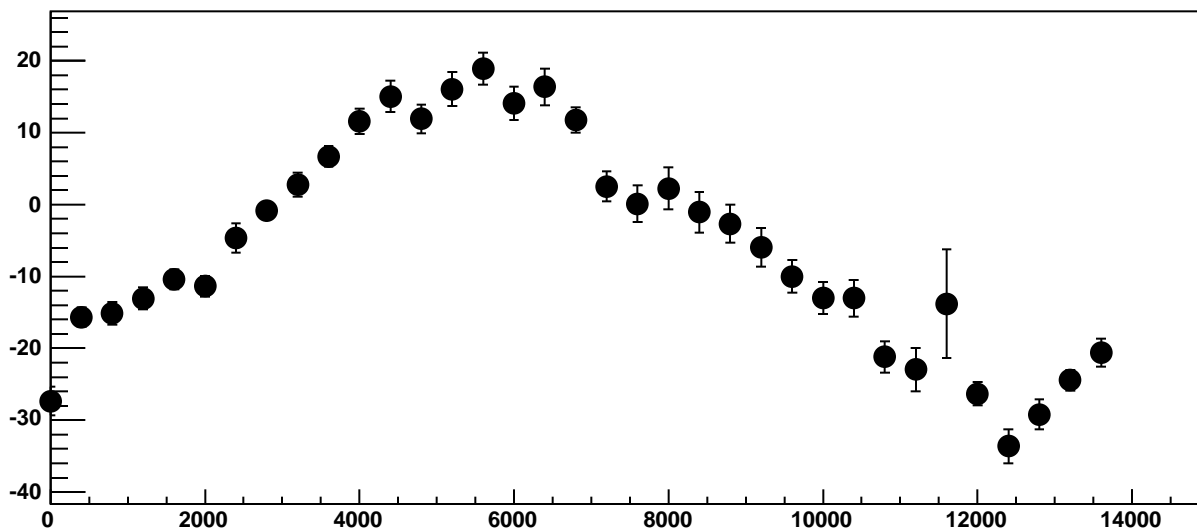
p1

-0.0187 ± 0.0001344

Chip 1, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

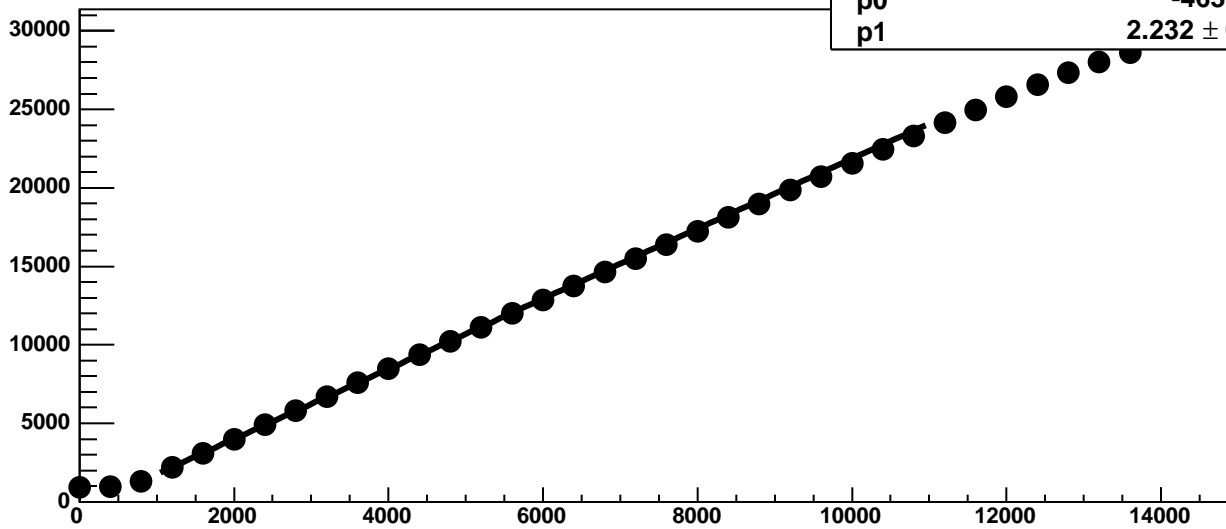


Chip 1, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

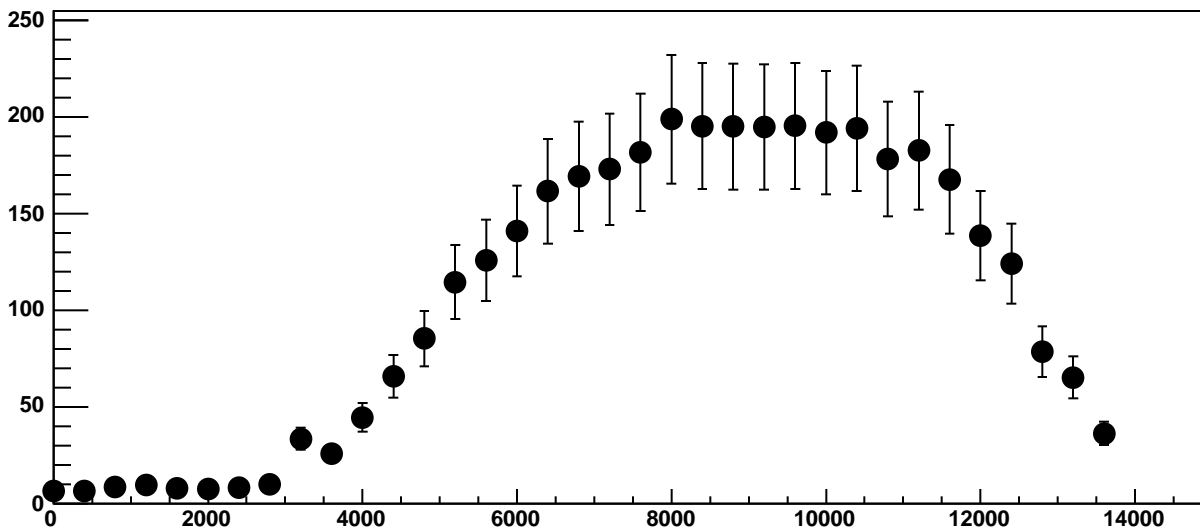


Chip 1, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC

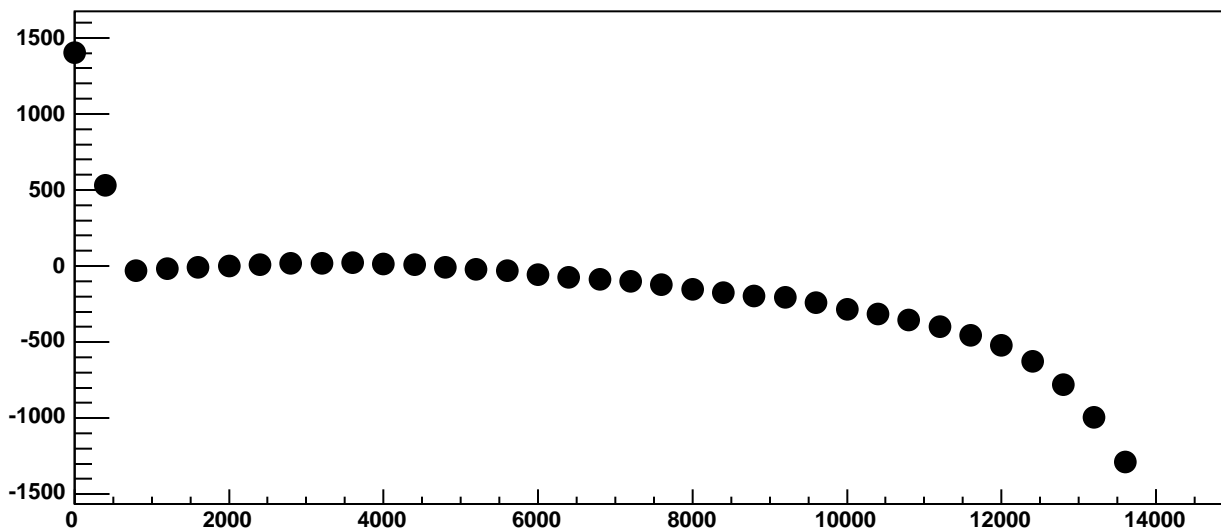
χ^2 / ndf 491.7 / 23
p0 -463 ± 2.445
p1 2.232 ± 0.00109



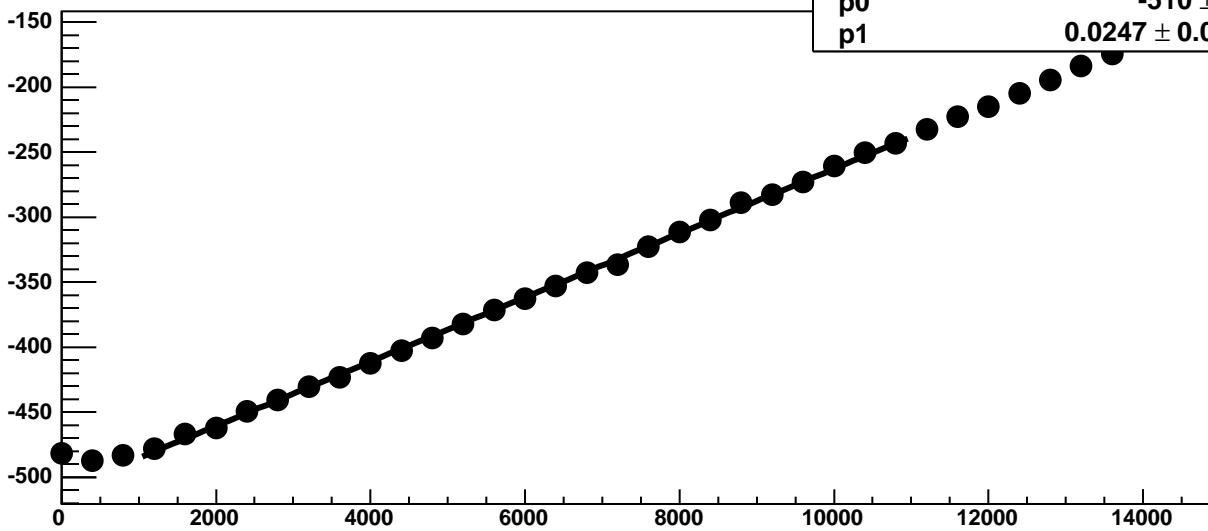
Chip 1, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC

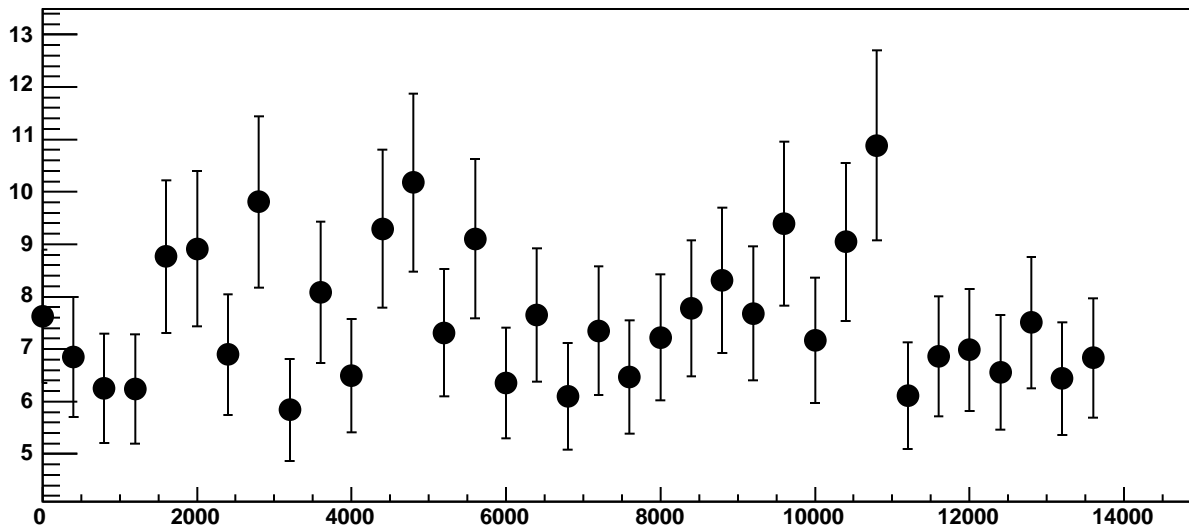


Chip 1, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

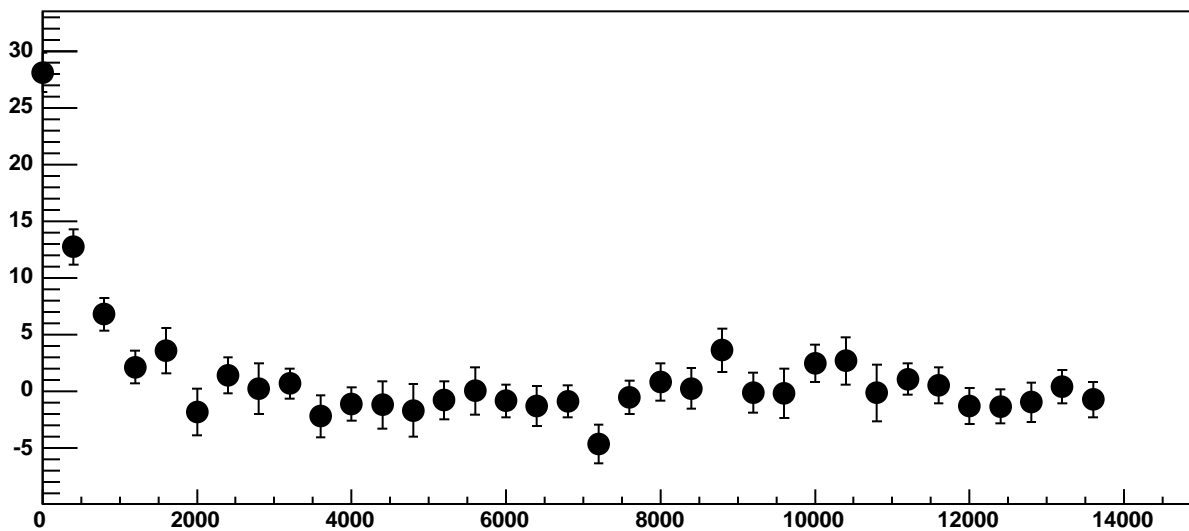


χ^2 / ndf 27.04 / 23
p0 -510 ± 0.8088
p1 0.0247 ± 0.0001256

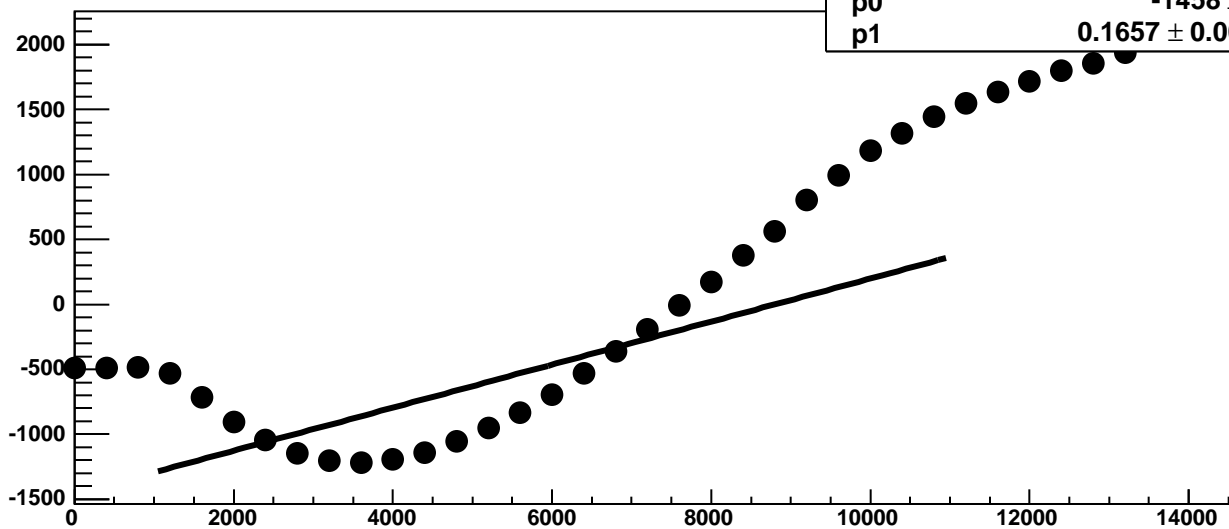
Chip 1, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

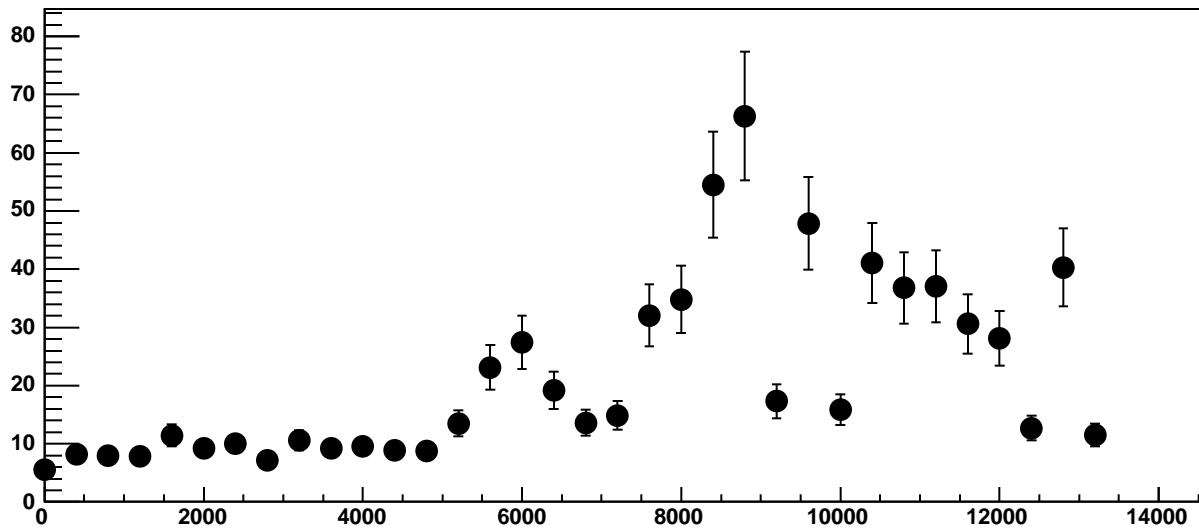


Chip 1, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

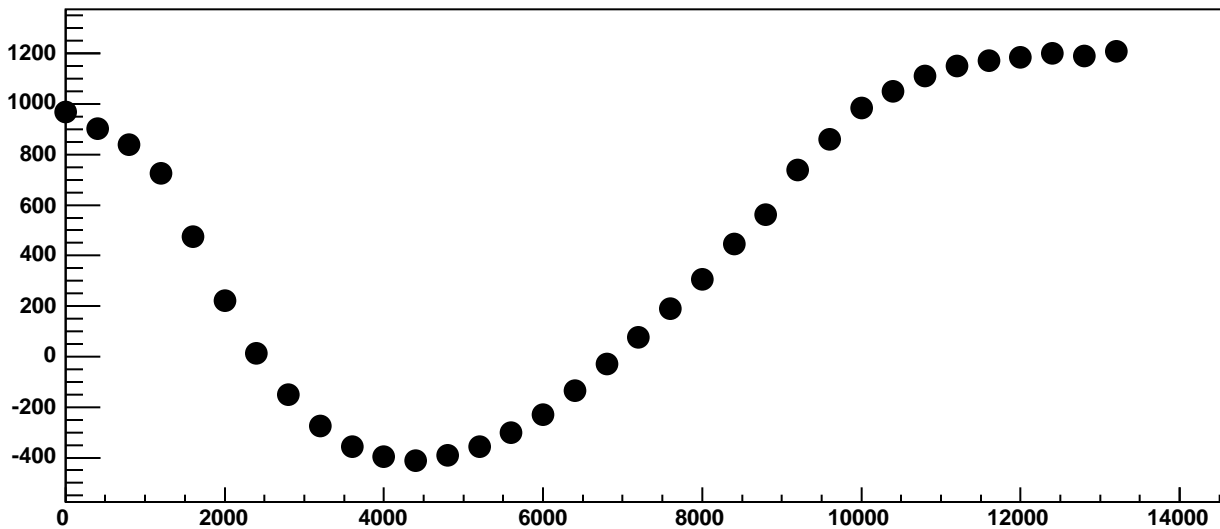


χ^2 / ndf 5.378e+05 / 23
p0 -1458 ± 1.184
p1 0.1657 ± 0.0002638

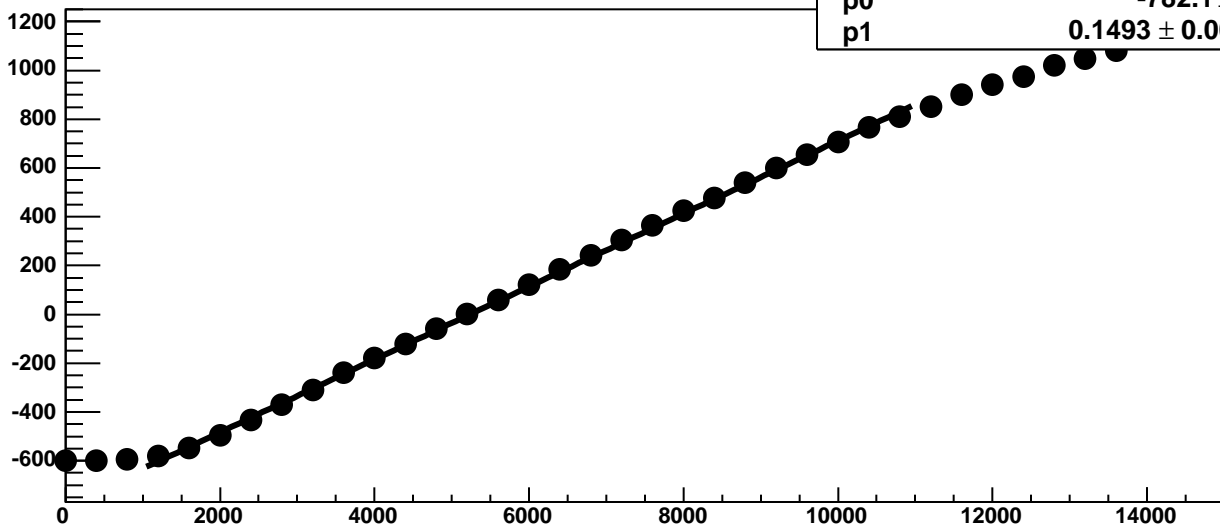
Chip 1, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



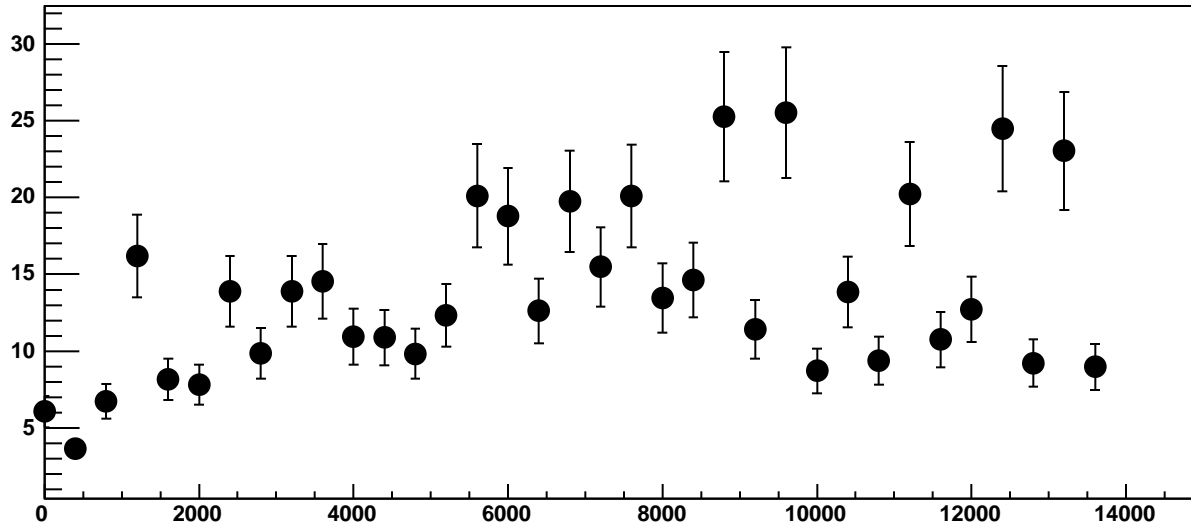
Chip 1, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



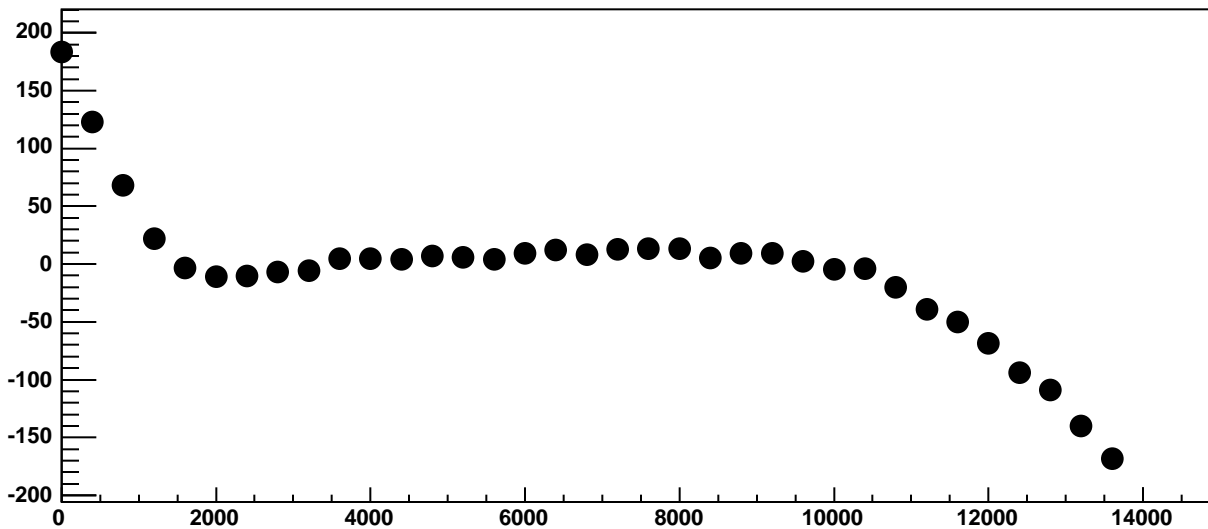
Chip 1, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



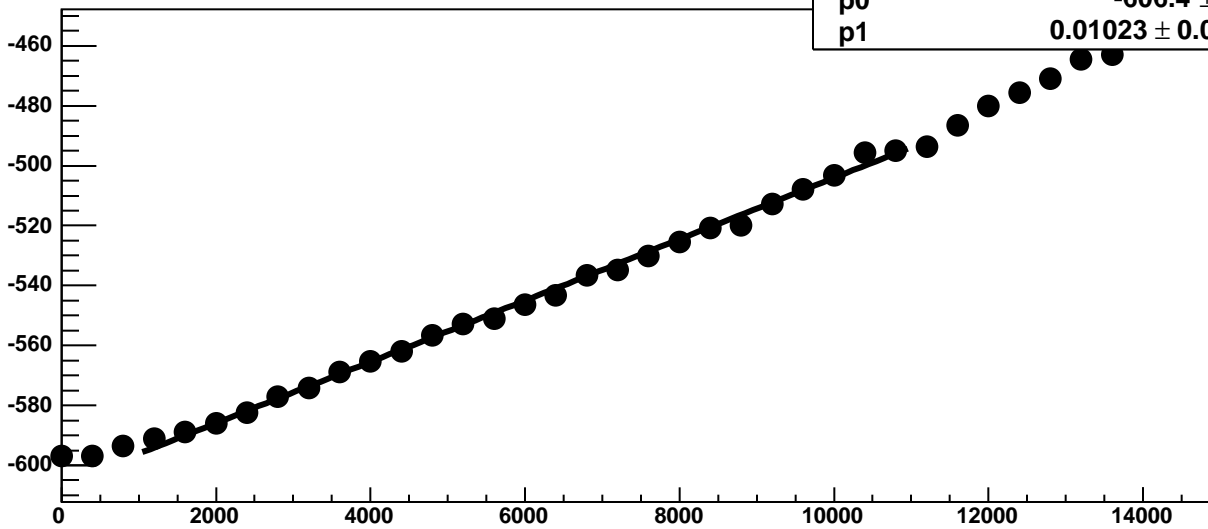
Chip 1, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

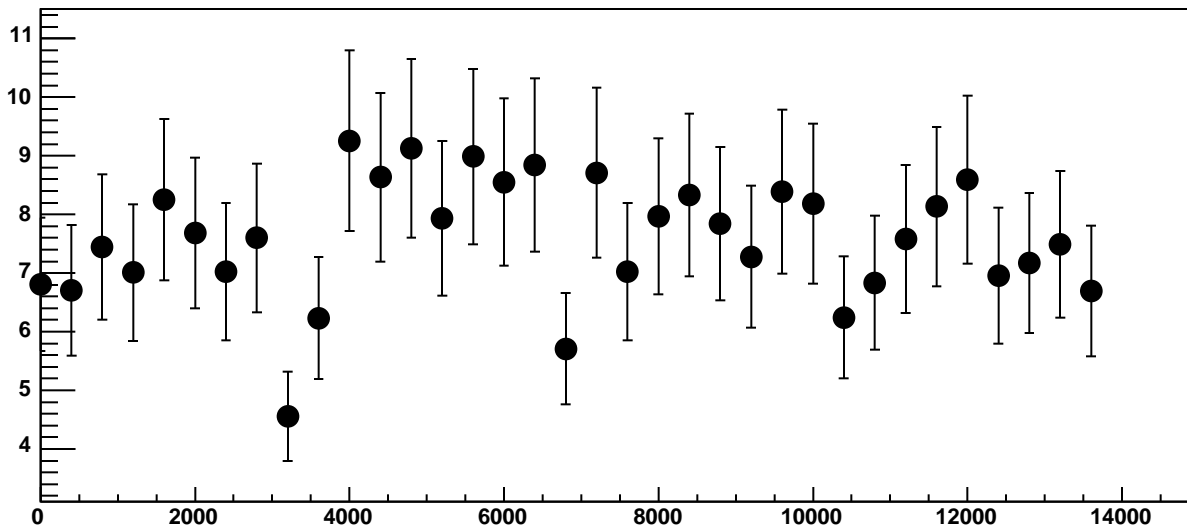


Chip 1, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

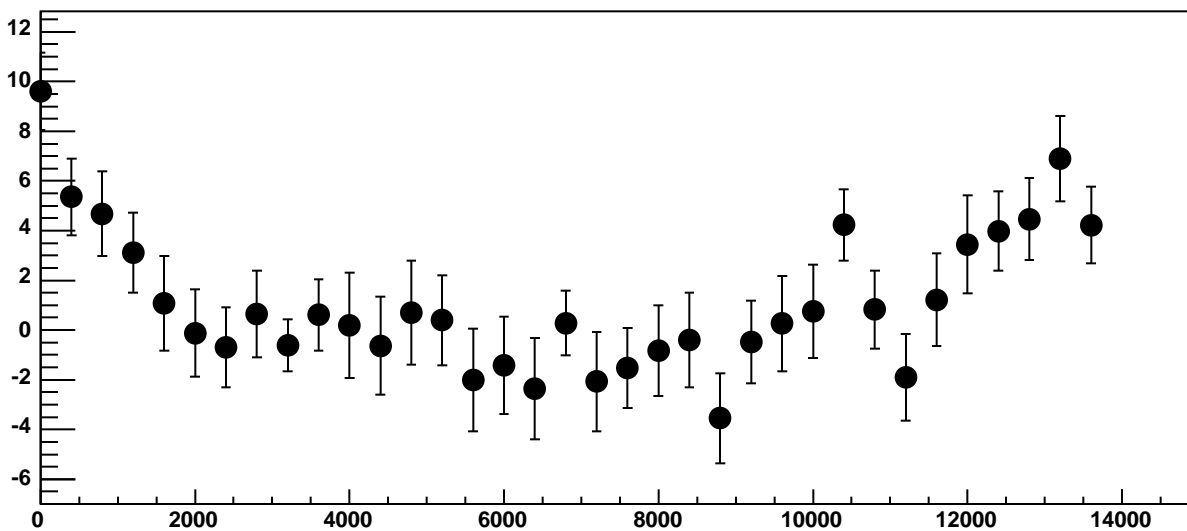


χ^2 / ndf 23.48 / 23
p0 -606.4 ± 0.7494
p1 0.01023 ± 0.0001138

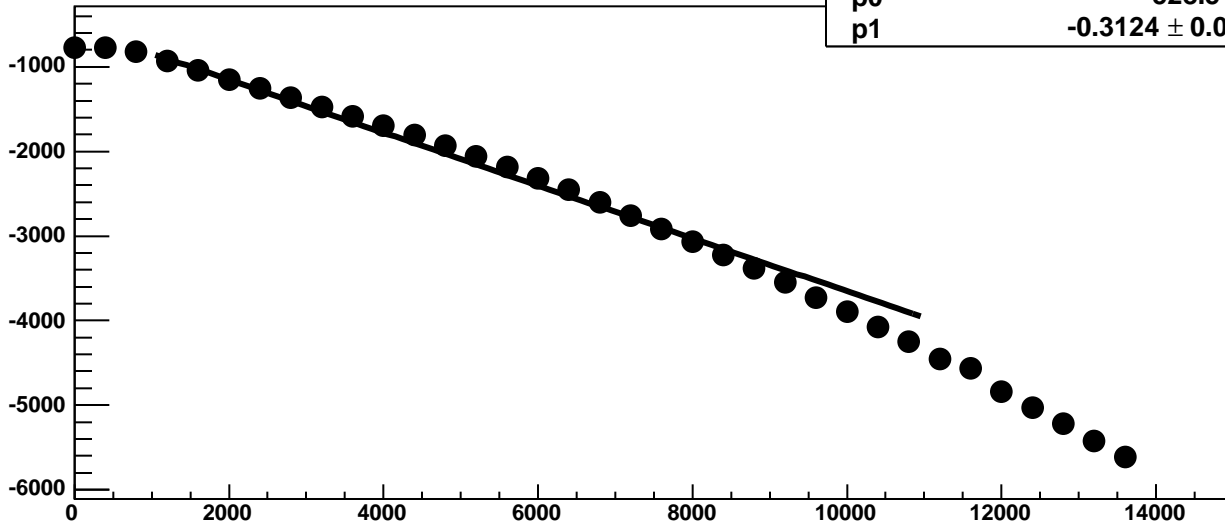
Chip 1, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC

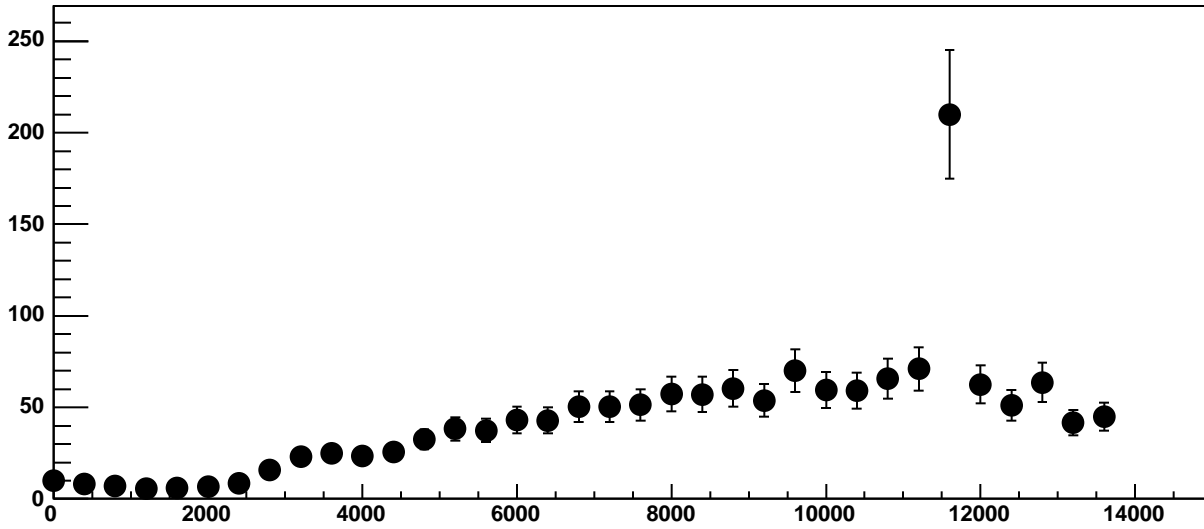


Chip 1, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

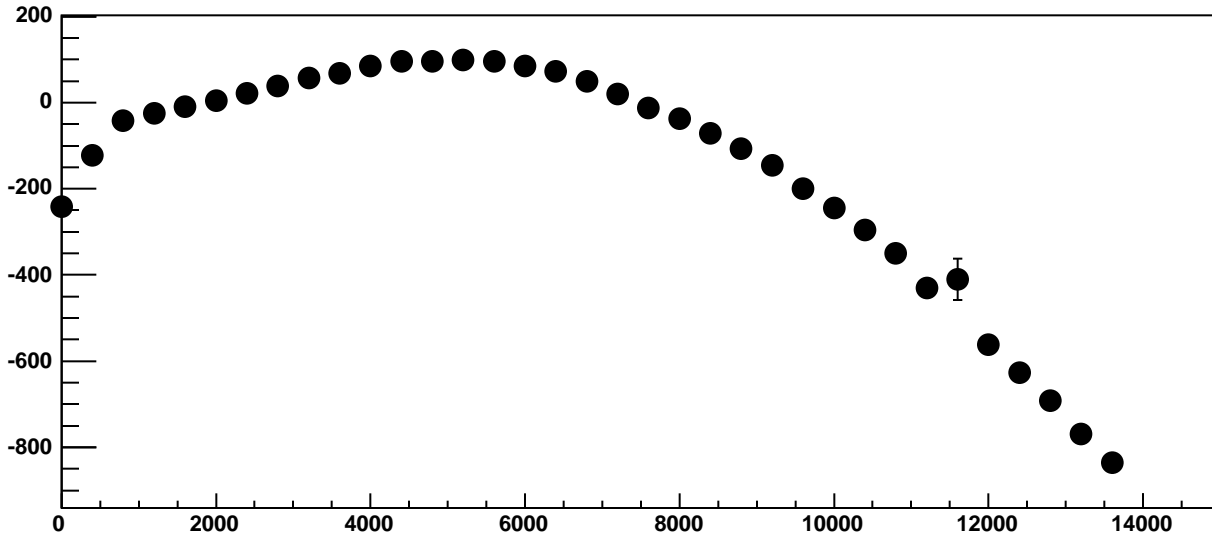


χ^2 / ndf 3686 / 23
p0 -528.5 ± 1.226
p1 -0.3124 ± 0.0004648

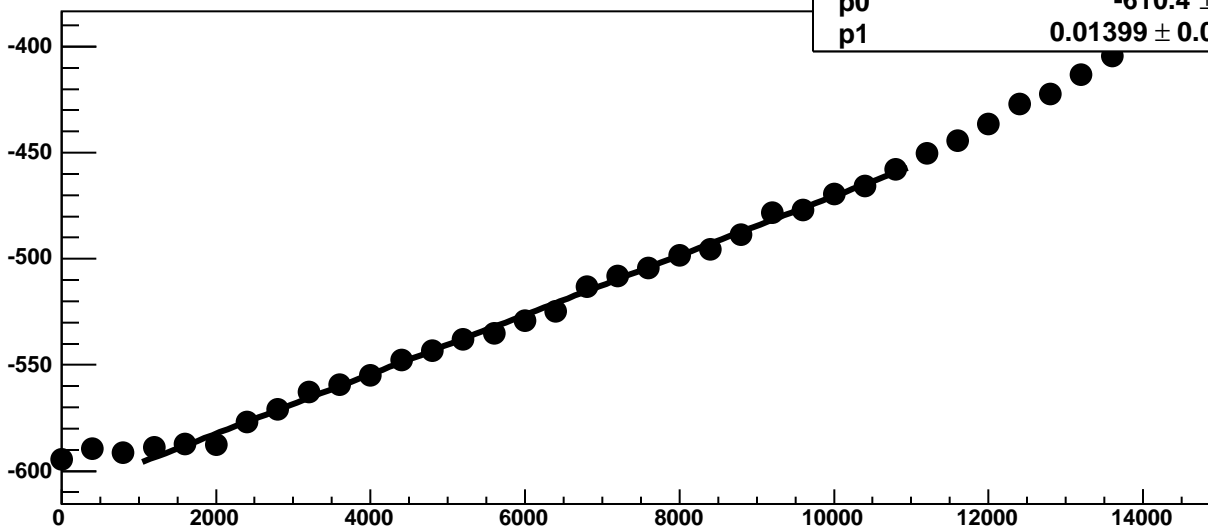
Chip 1, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



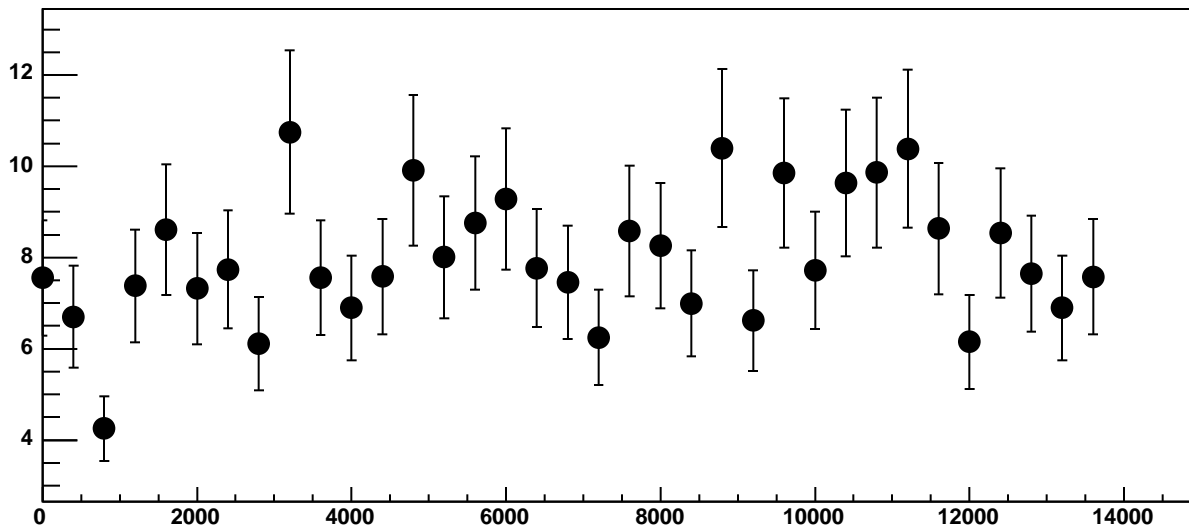
Chip 1, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



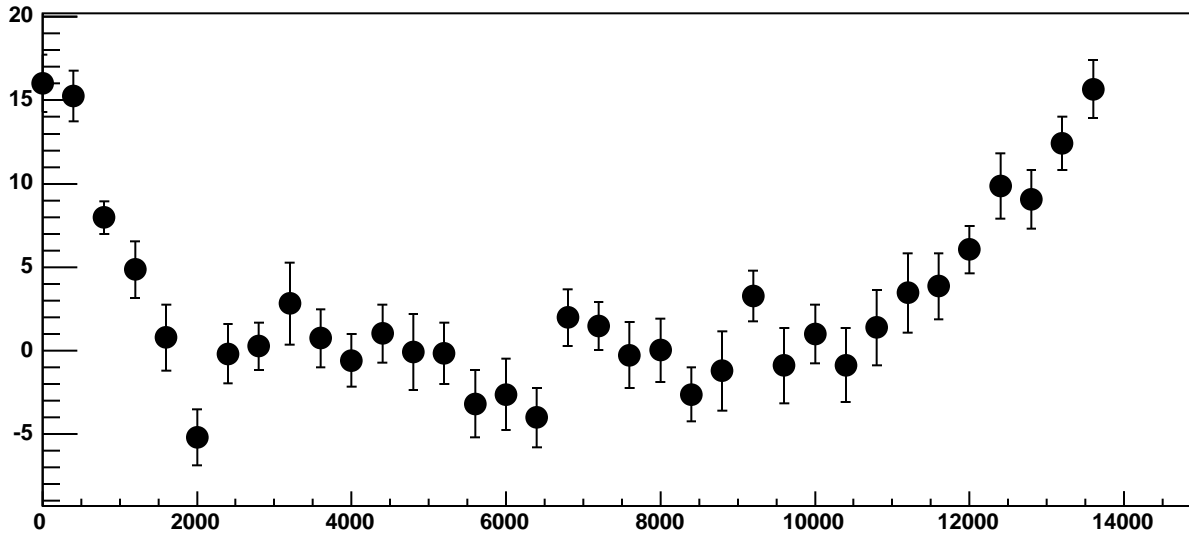
Chip 1, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



Chip 1, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC

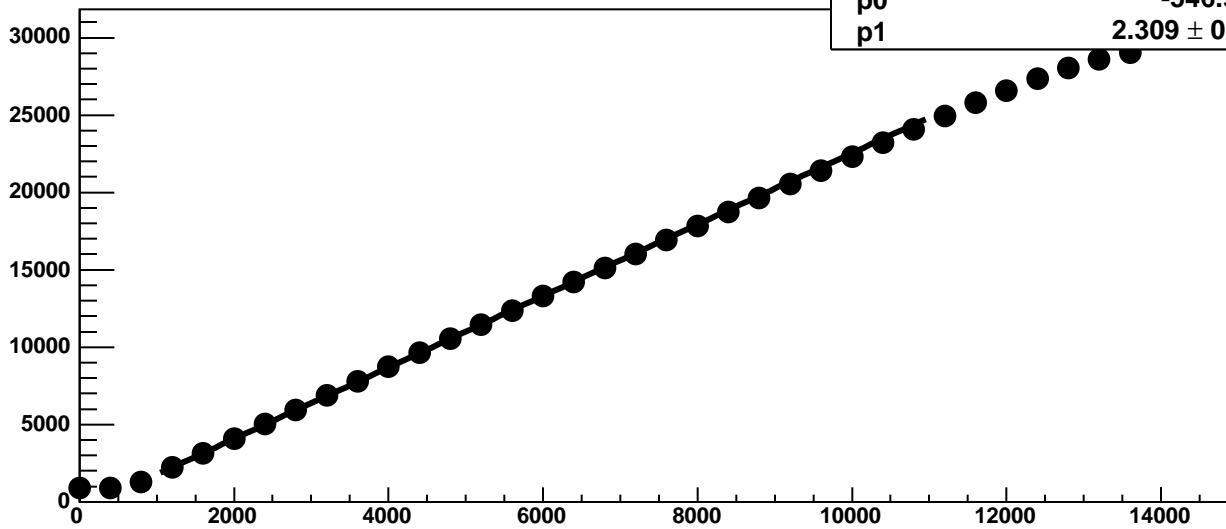


Chip 1, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

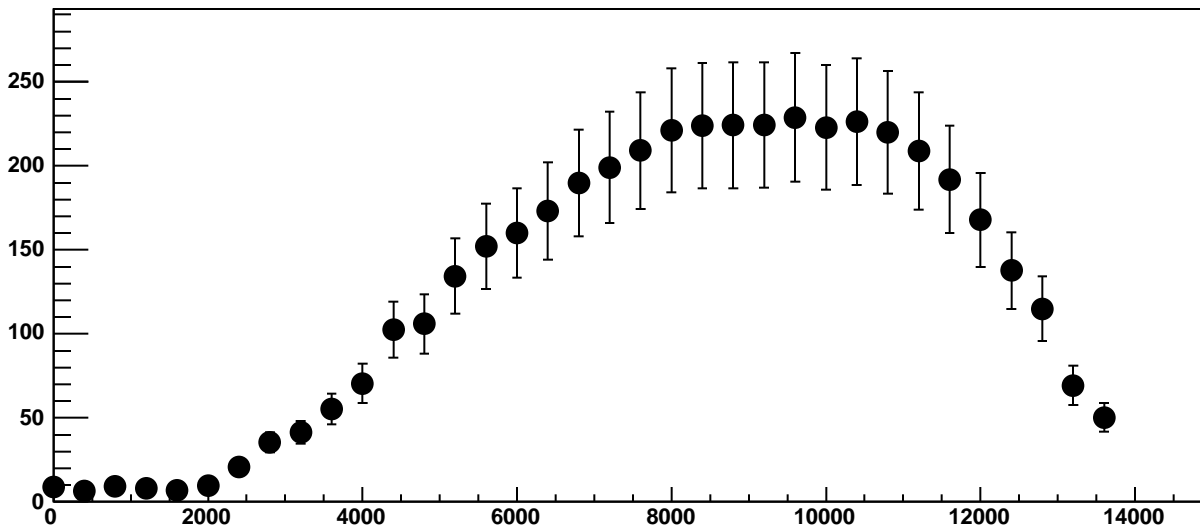


Chip 1, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC

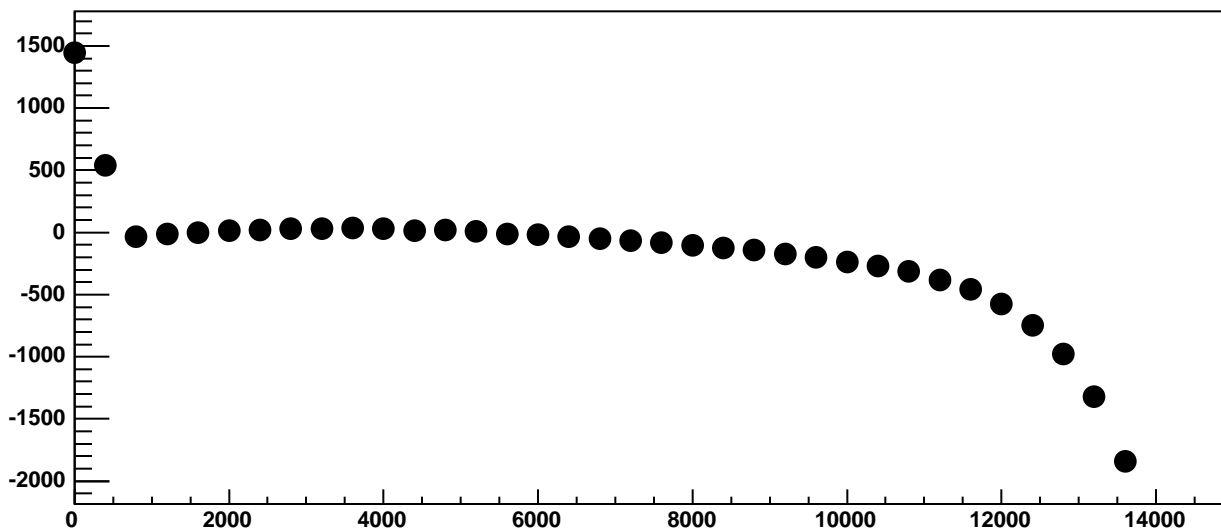
χ^2 / ndf 288.2 / 23
p0 -546.9 ± 2.61
p1 2.309 ± 0.001402



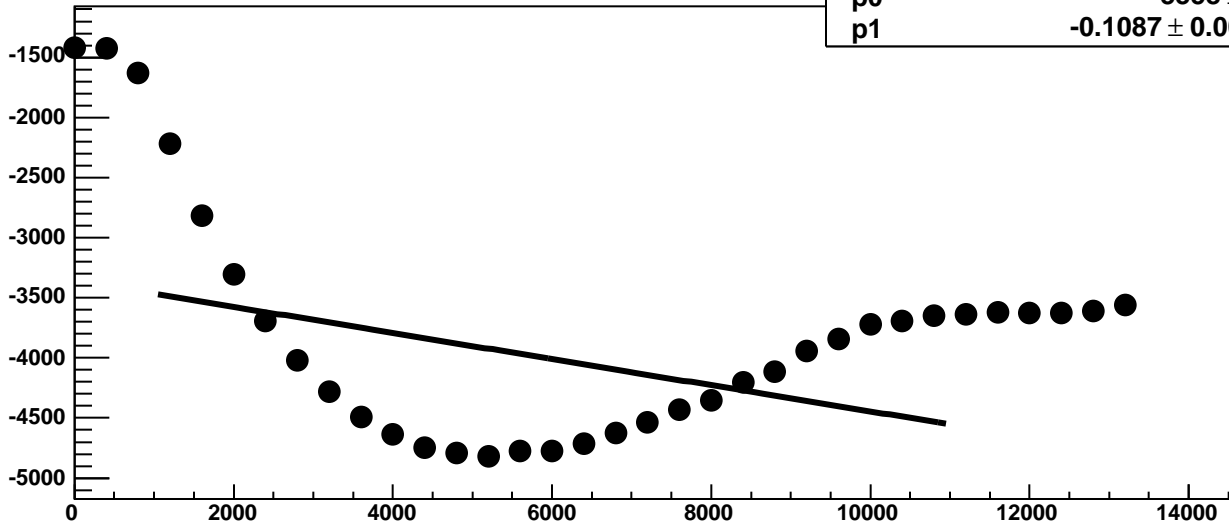
Chip 1, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC

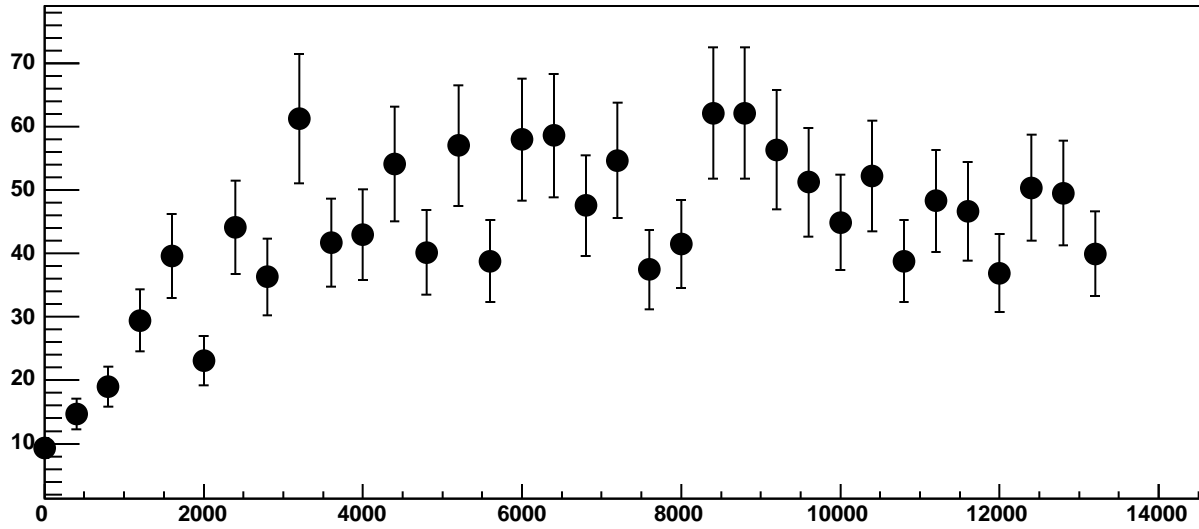


Chip 1, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

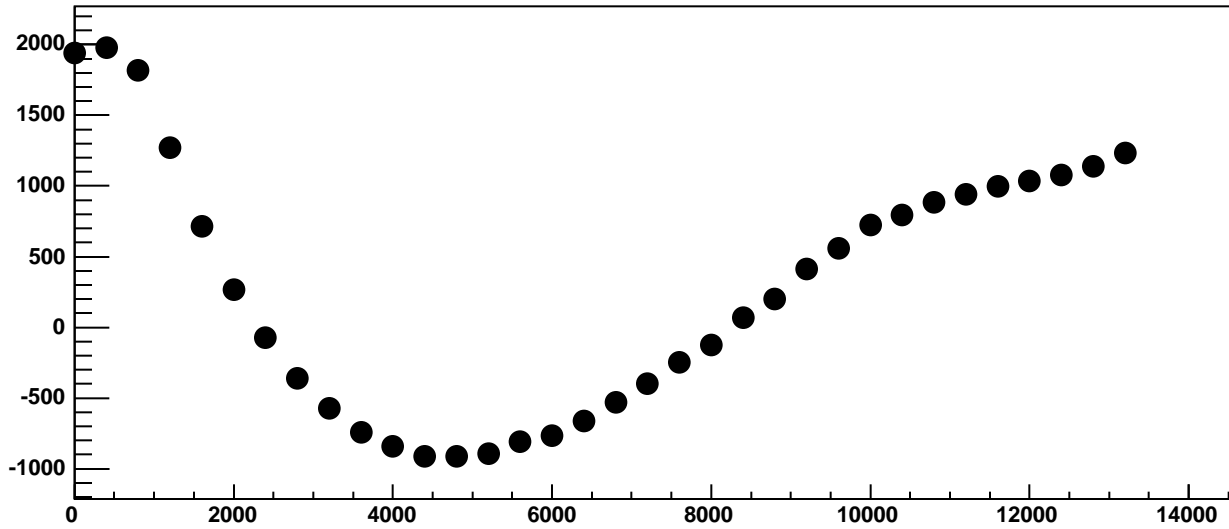


χ^2 / ndf	1.22e+05 / 23
p0	-3358 ± 3.822
p1	-0.1087 ± 0.0006353

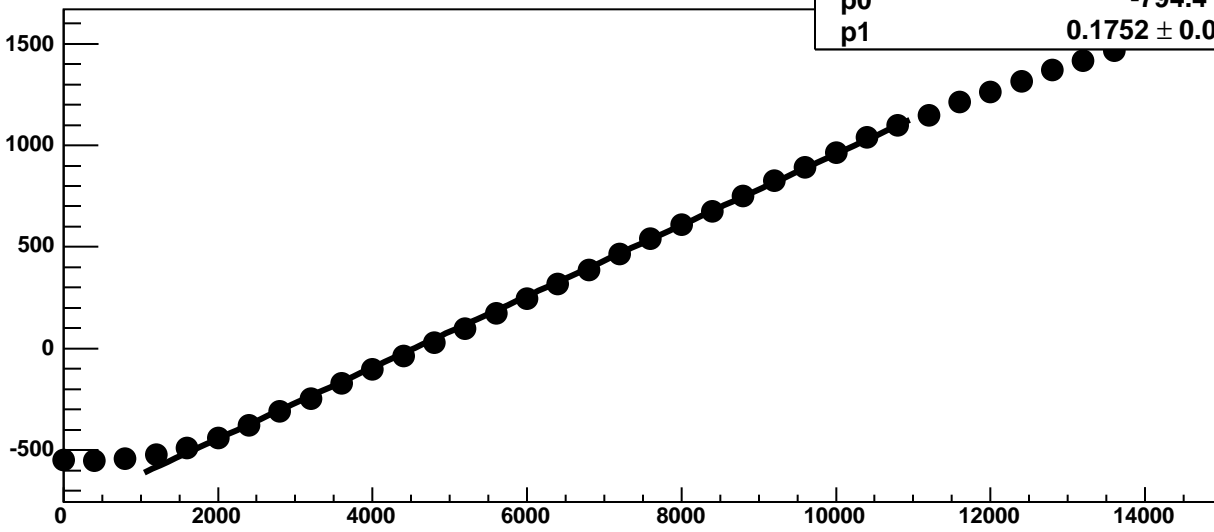
Chip 1, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

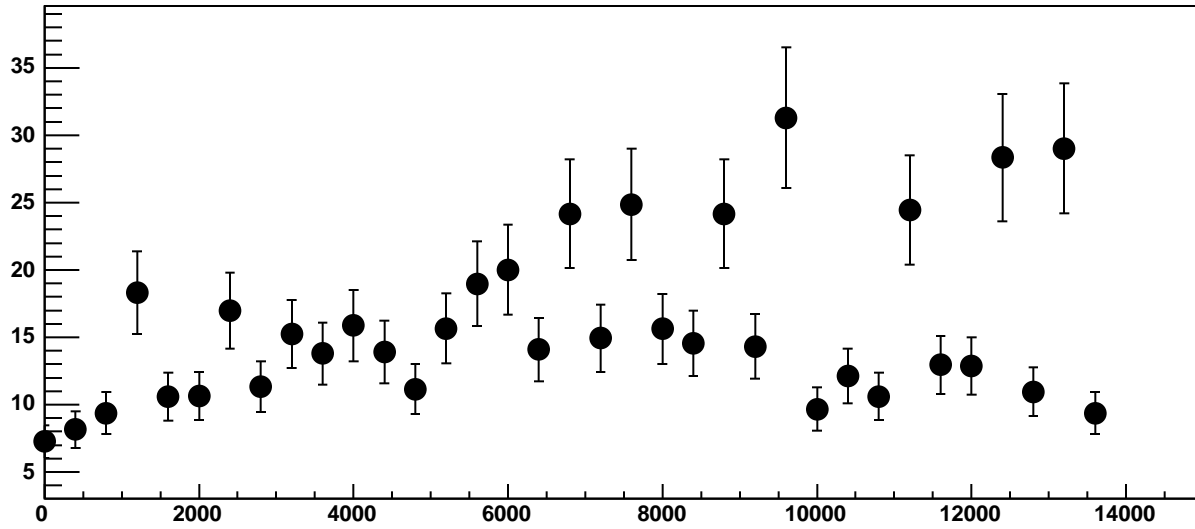


Chip 1, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

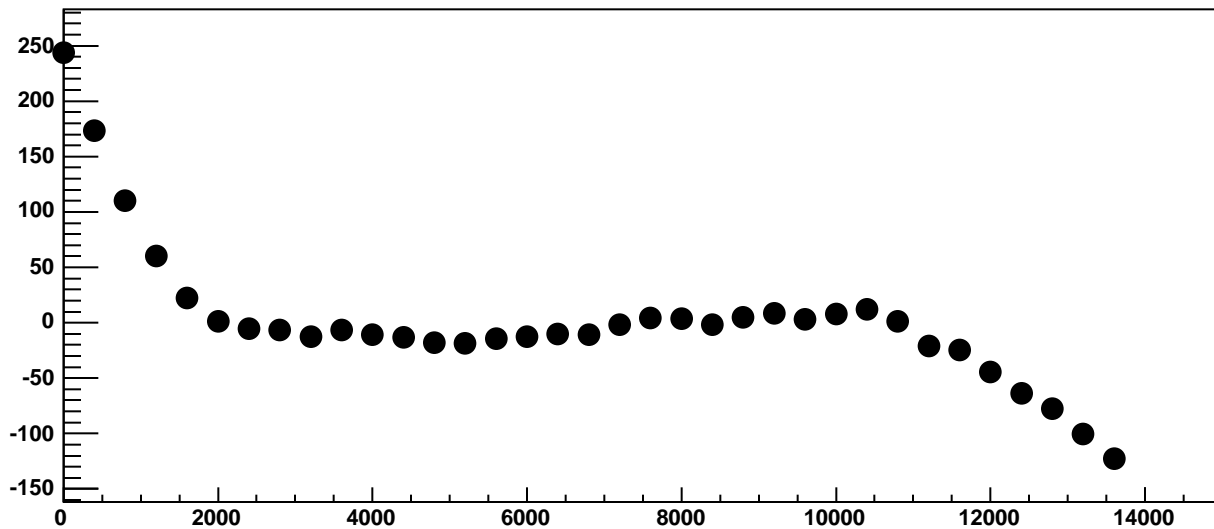


χ^2 / ndf 492.2 / 23
p0 -794.4 ± 1.376
p1 0.1752 ± 0.0002049

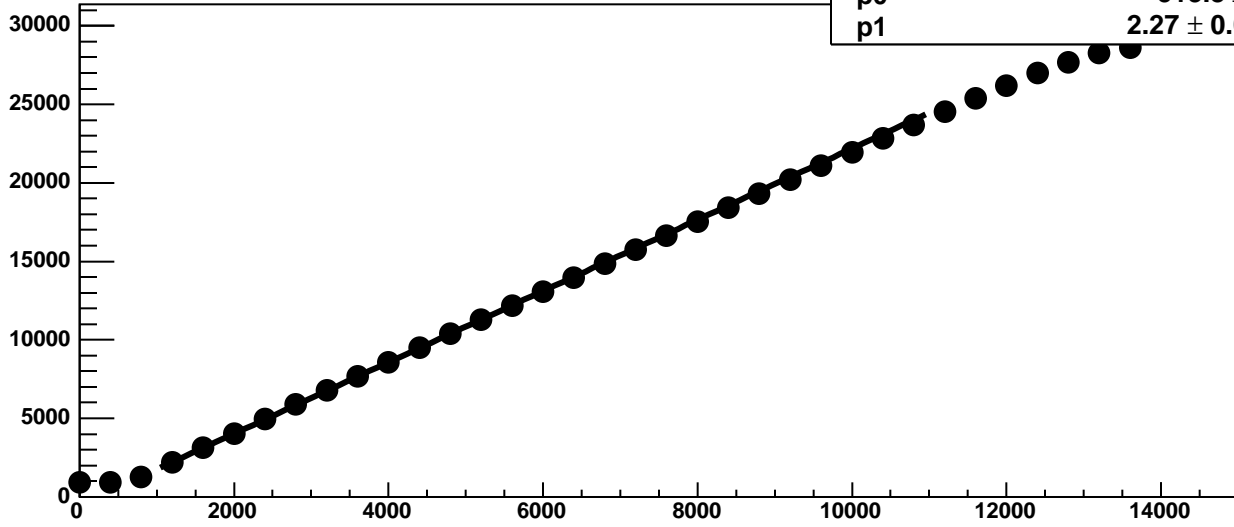
Chip 1, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



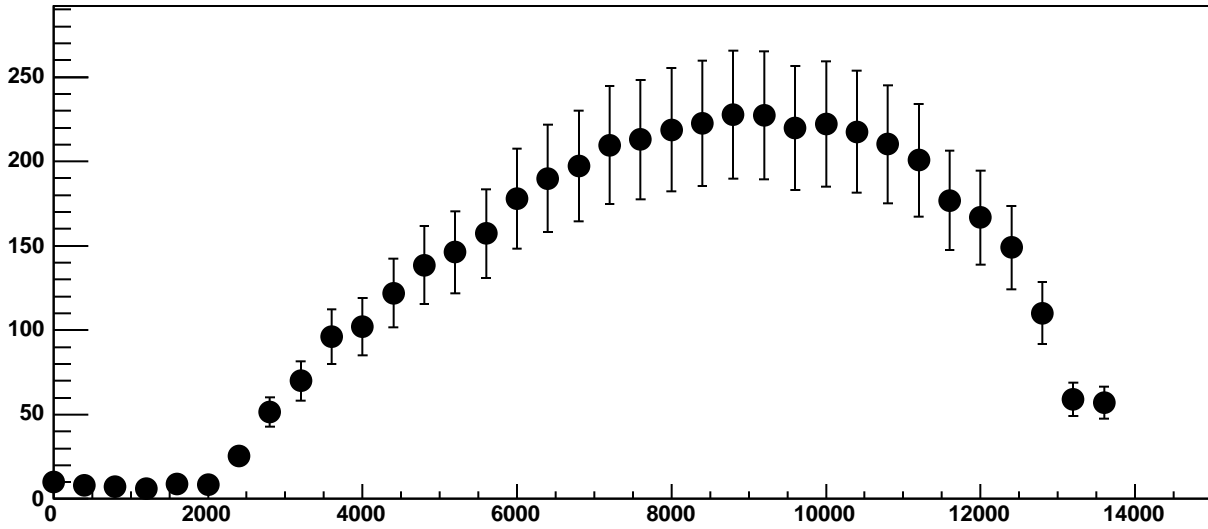
Chip 1, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



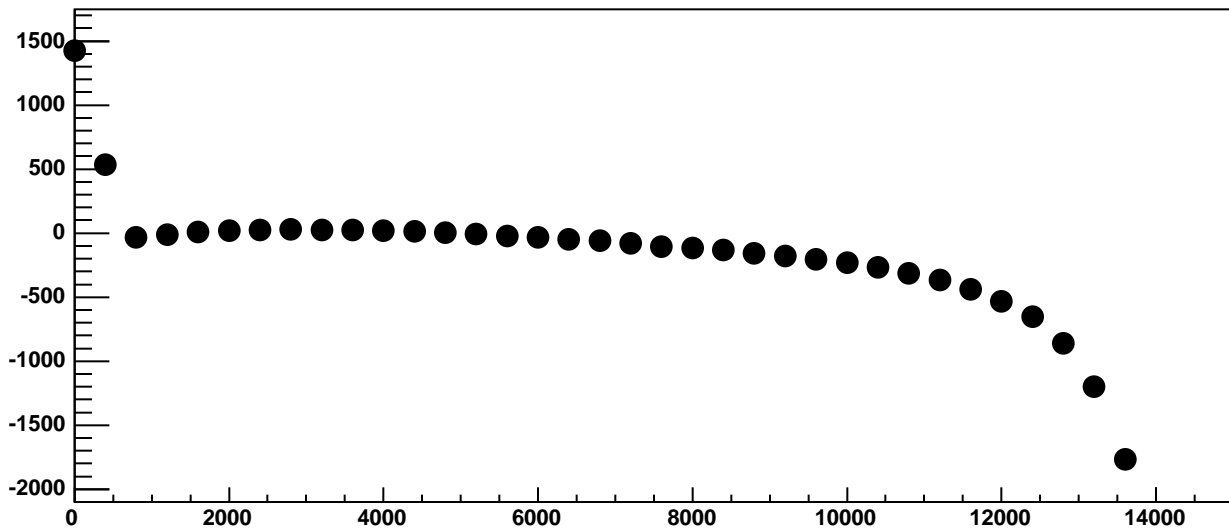
Chip 1, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC



Chip 1, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC

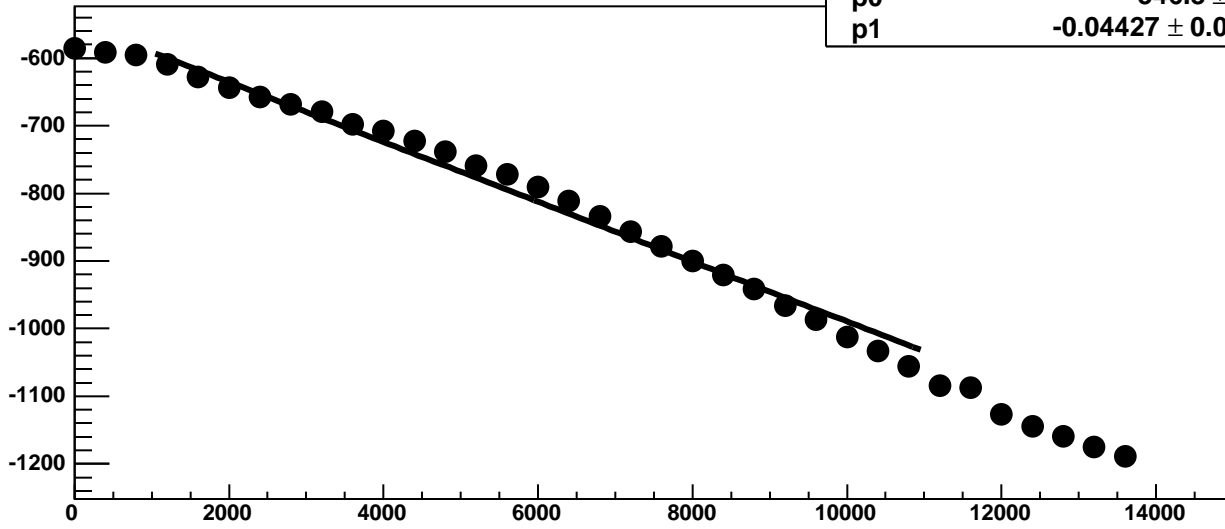


Chip 1, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC

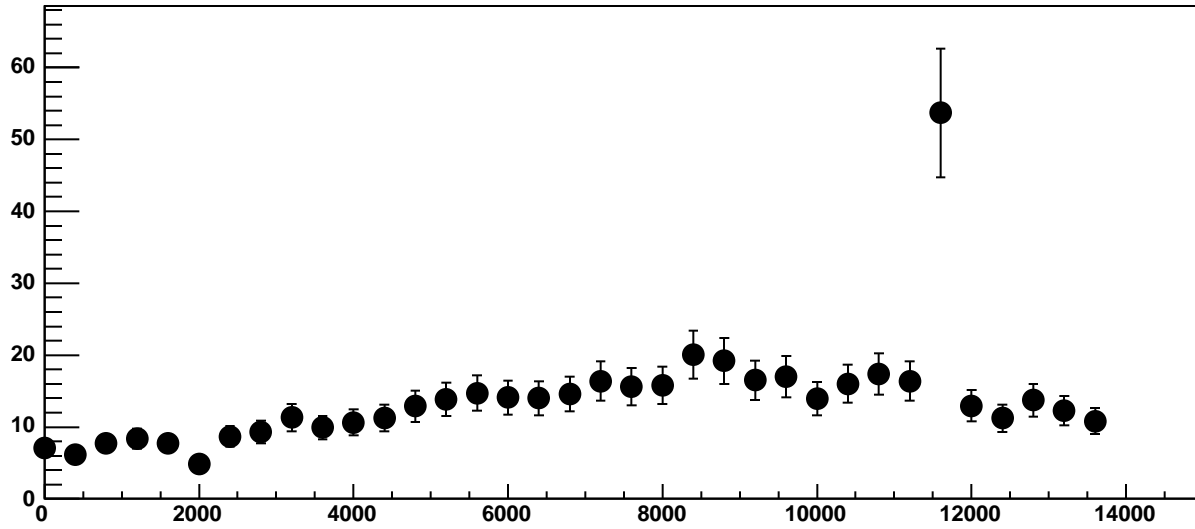


Chip 1, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

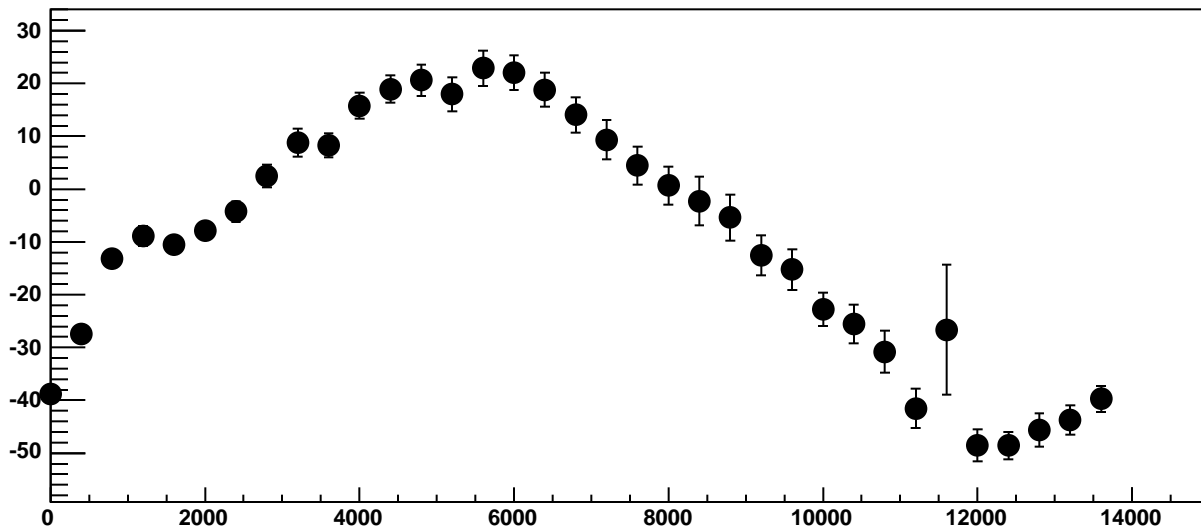
χ^2 / ndf 650.1 / 23
p0 -546.8 ± 0.9319
p1 -0.04427 ± 0.0001868



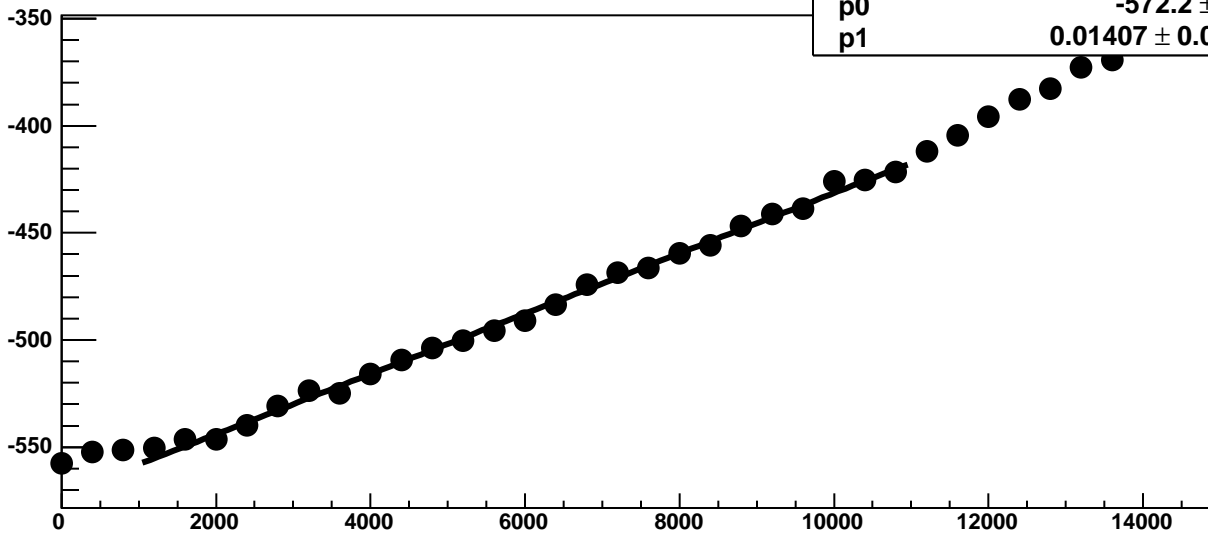
Chip 1, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

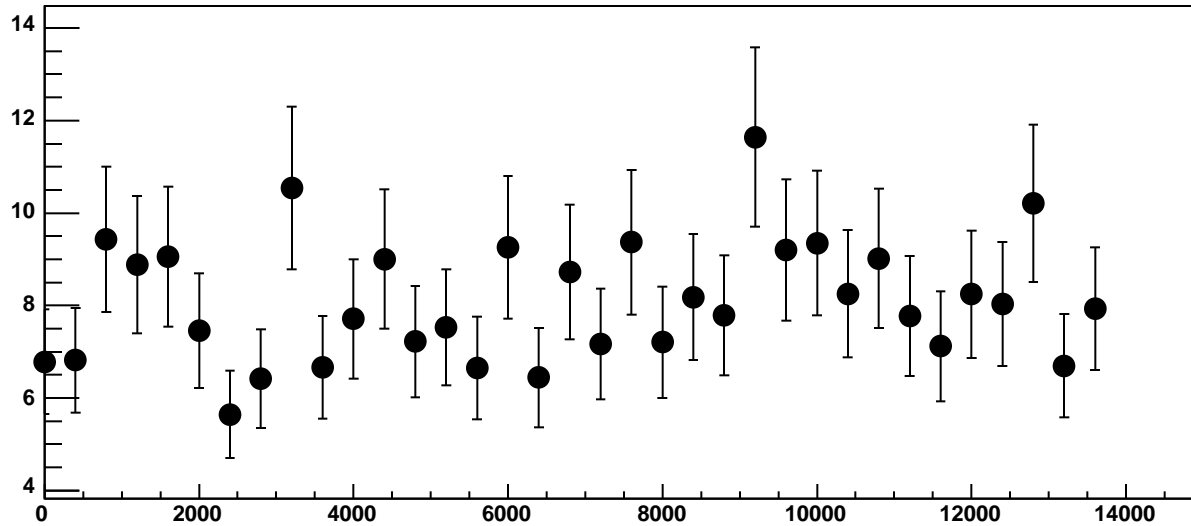


Chip 1, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

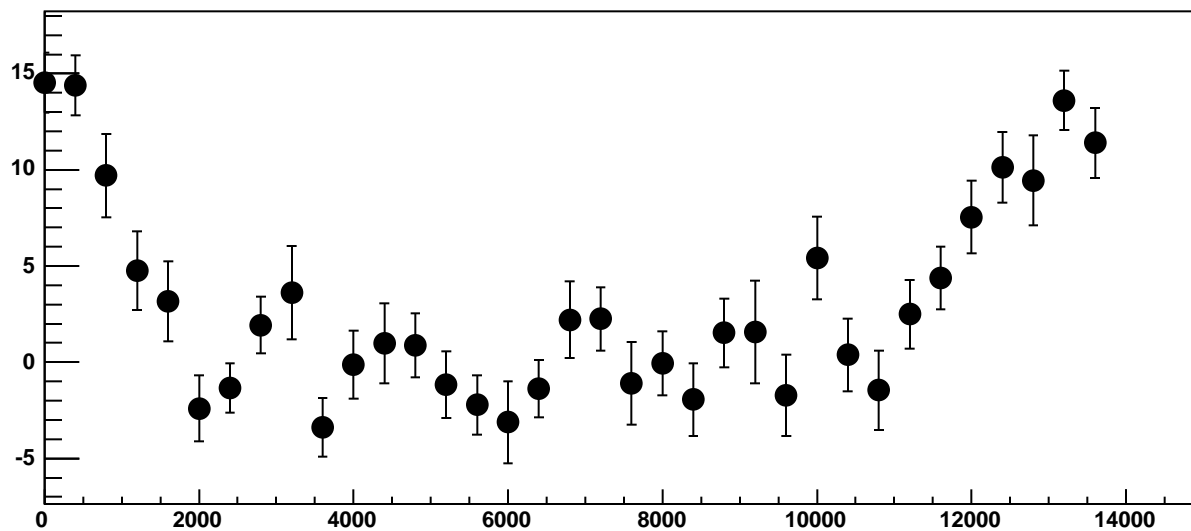


χ^2 / ndf 38.93 / 23
p0 -572.2 ± 0.8199
p1 0.01407 ± 0.0001302

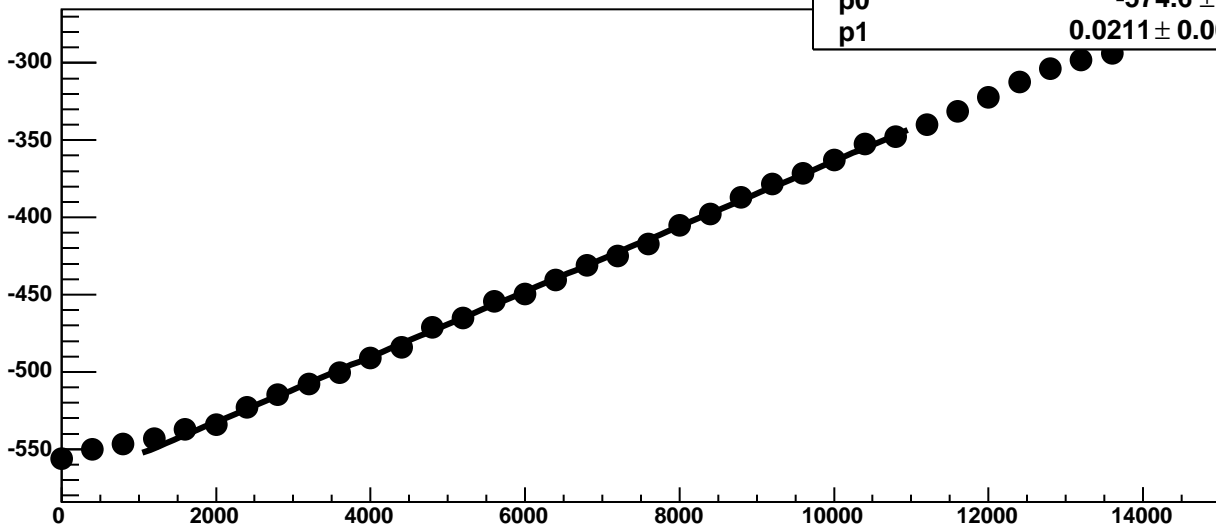
Chip 1, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



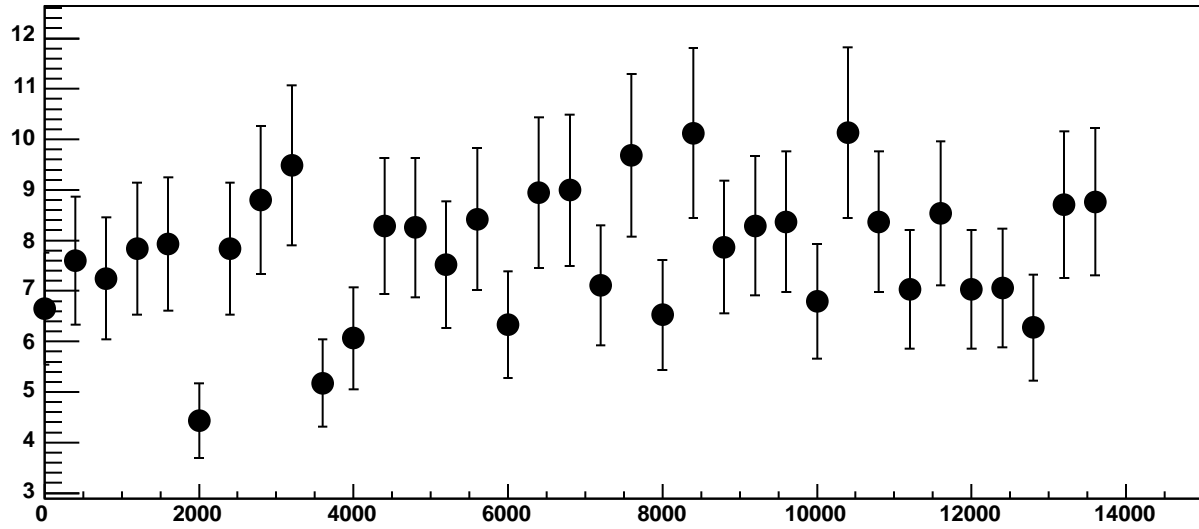
Chip 1, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



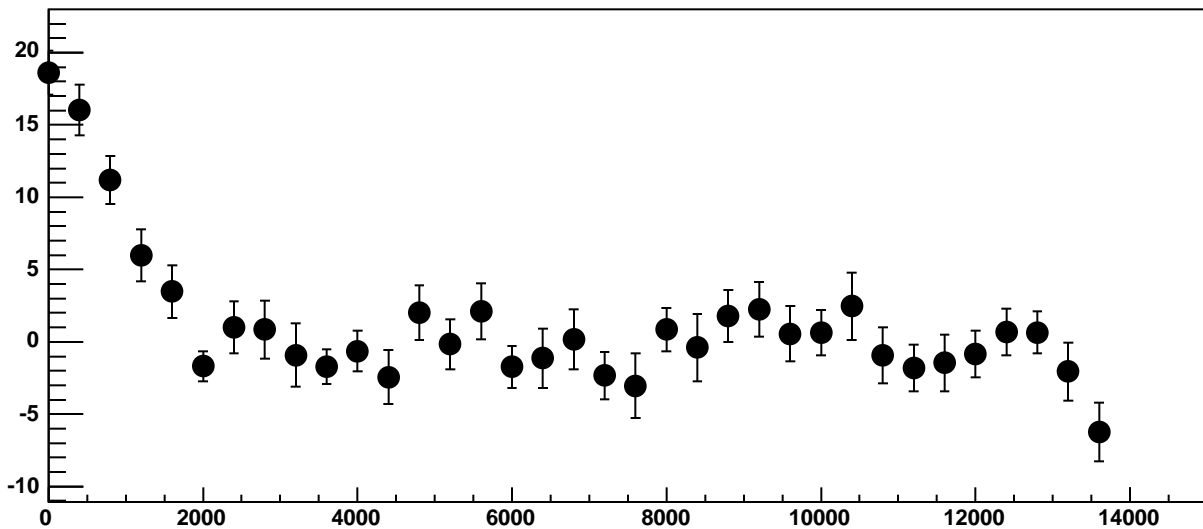
Chip 1, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC



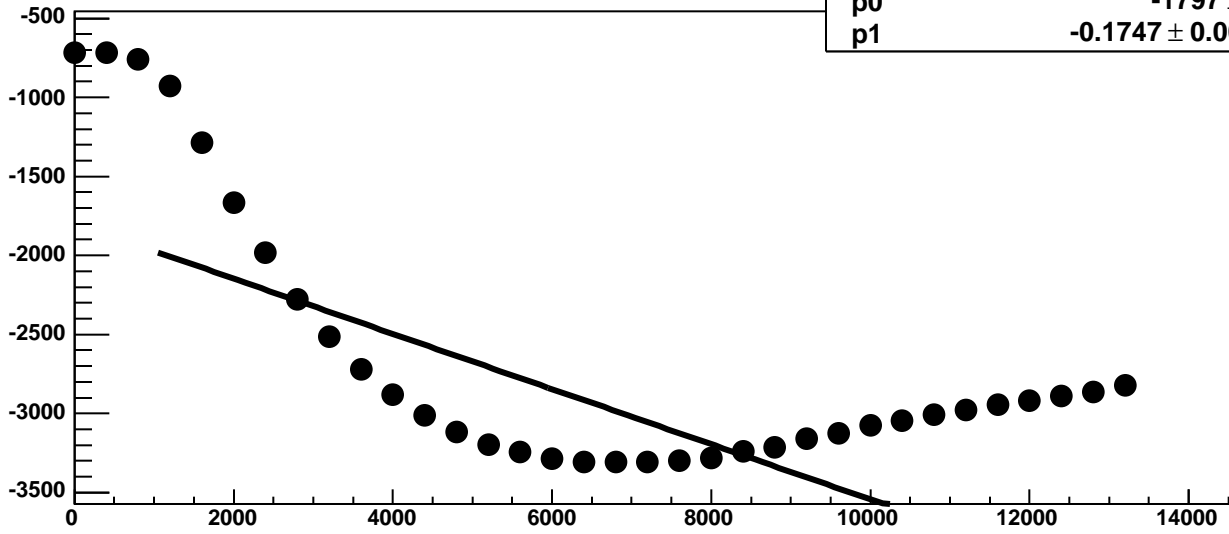
Chip 1, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC

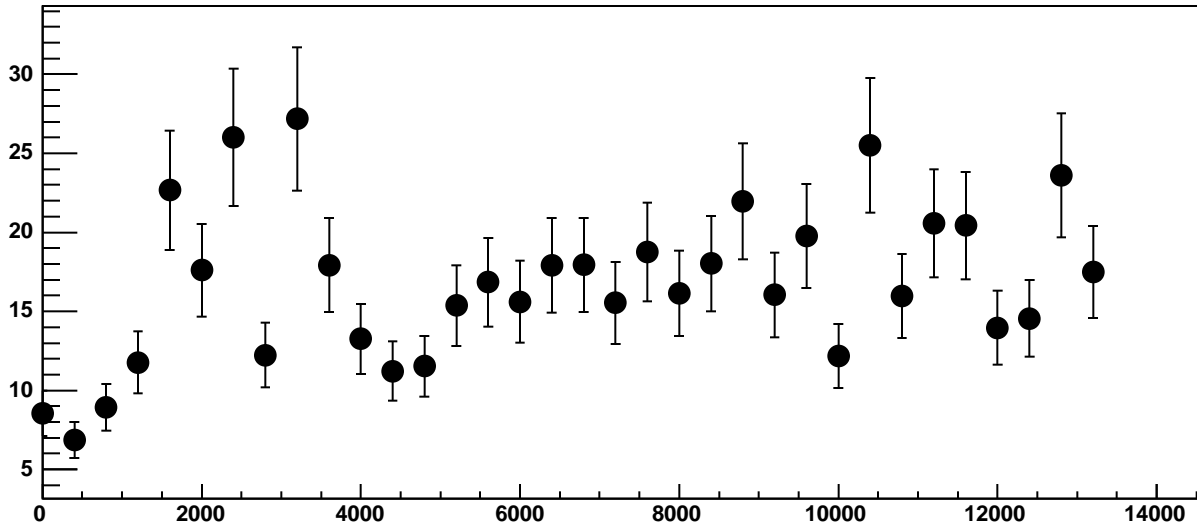


Chip 1, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

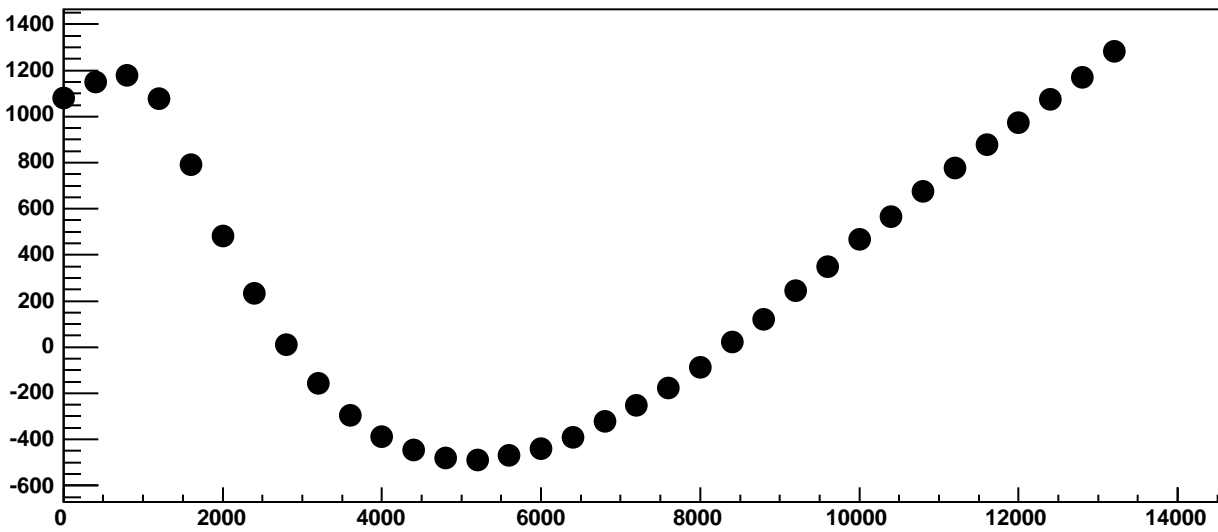


χ^2 / ndf	4.376e+05 / 23
p0	-1797 ± 1.657
p1	-0.1747 ± 0.0002582

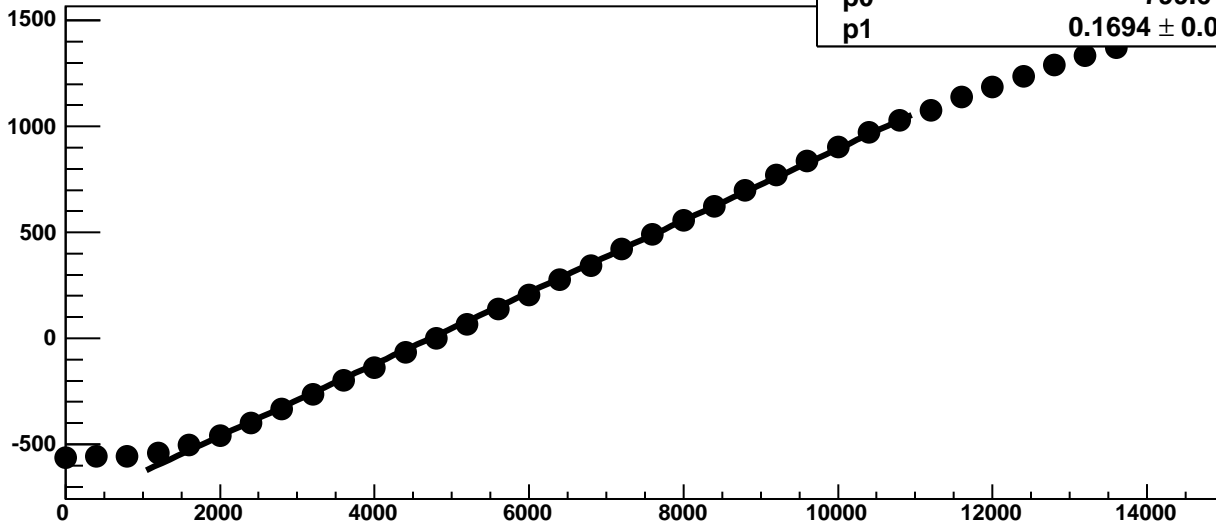
Chip 1, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



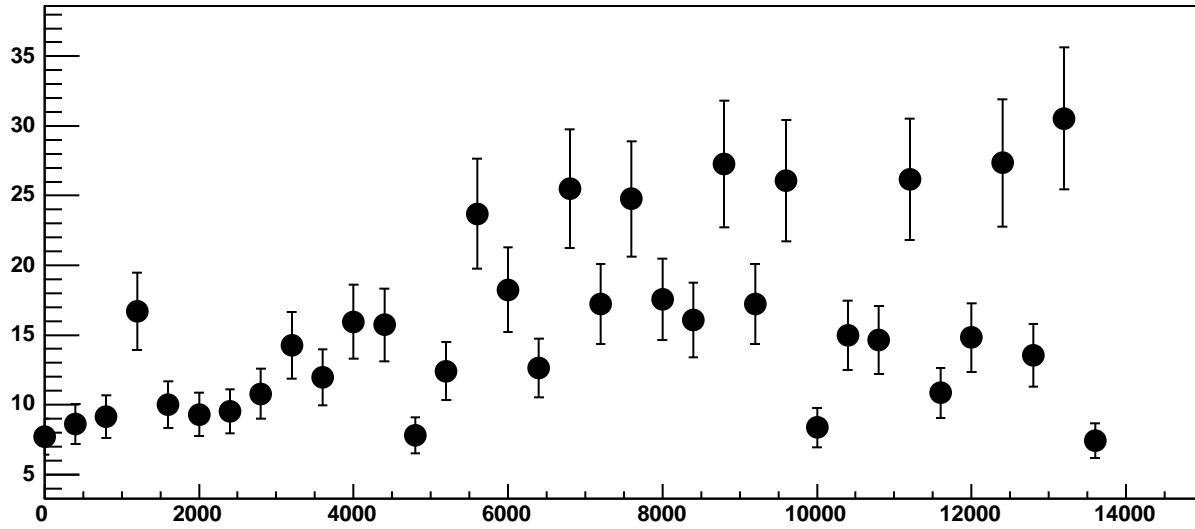
Chip 1, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



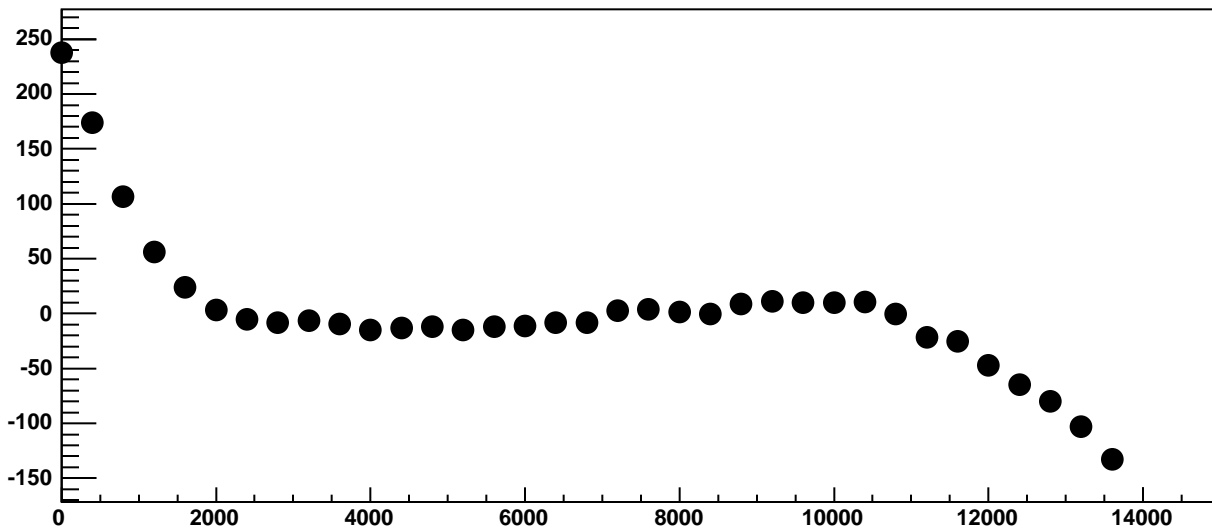
Chip 1, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



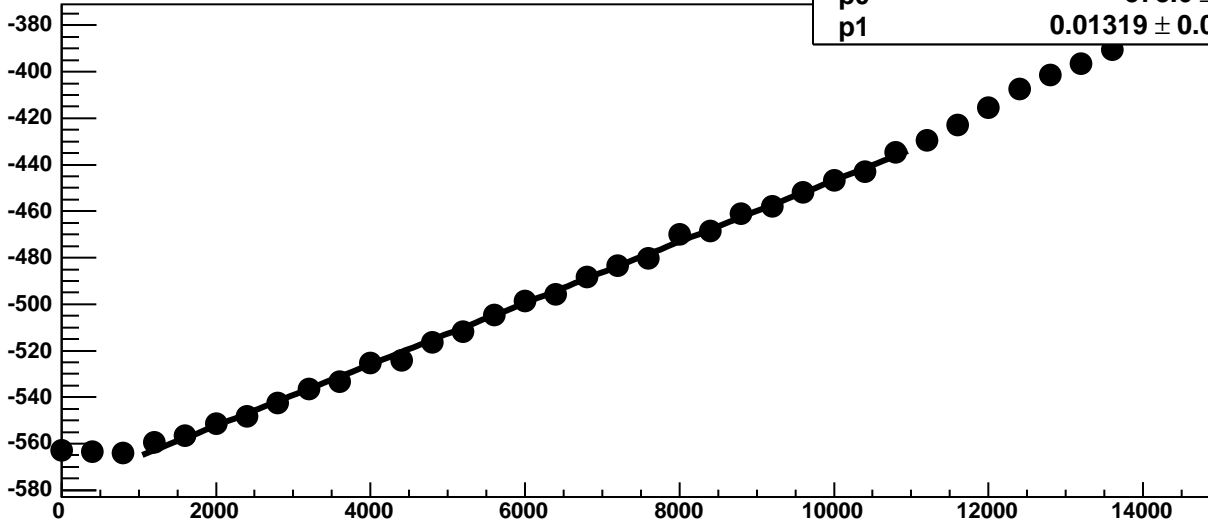
Chip 1, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



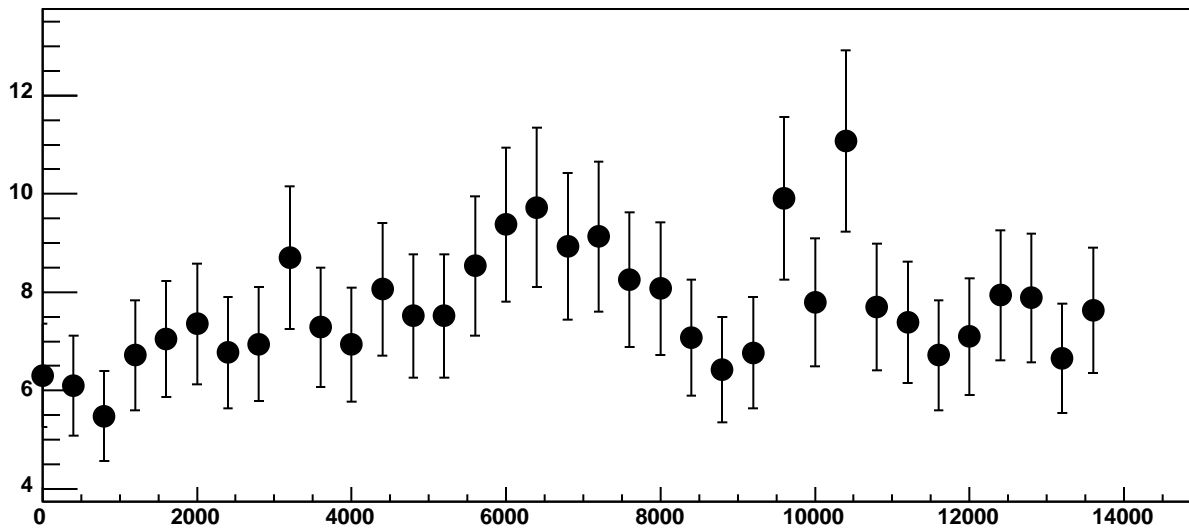
Chip 1, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



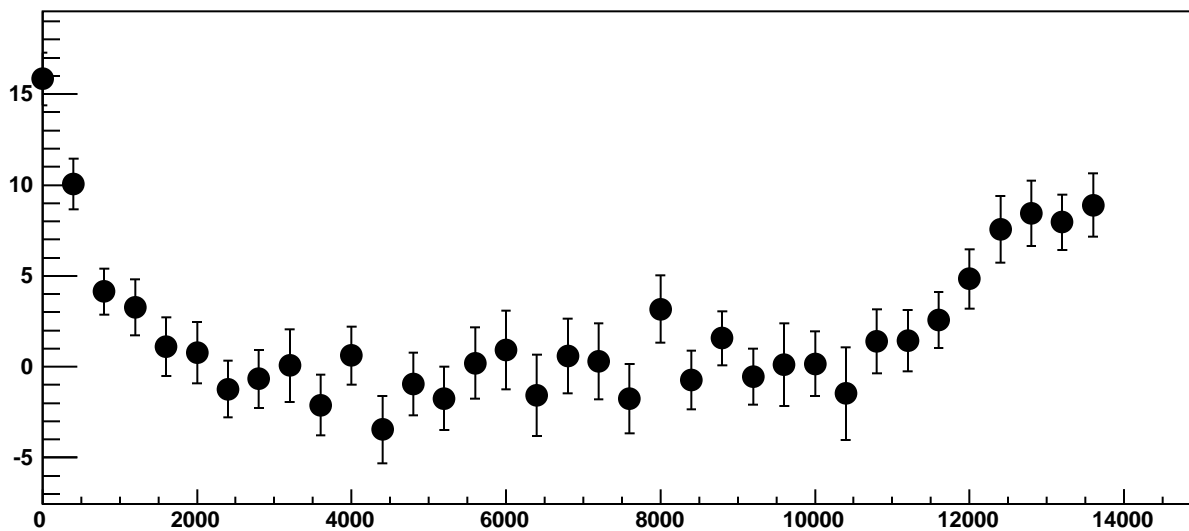
Chip 1, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC



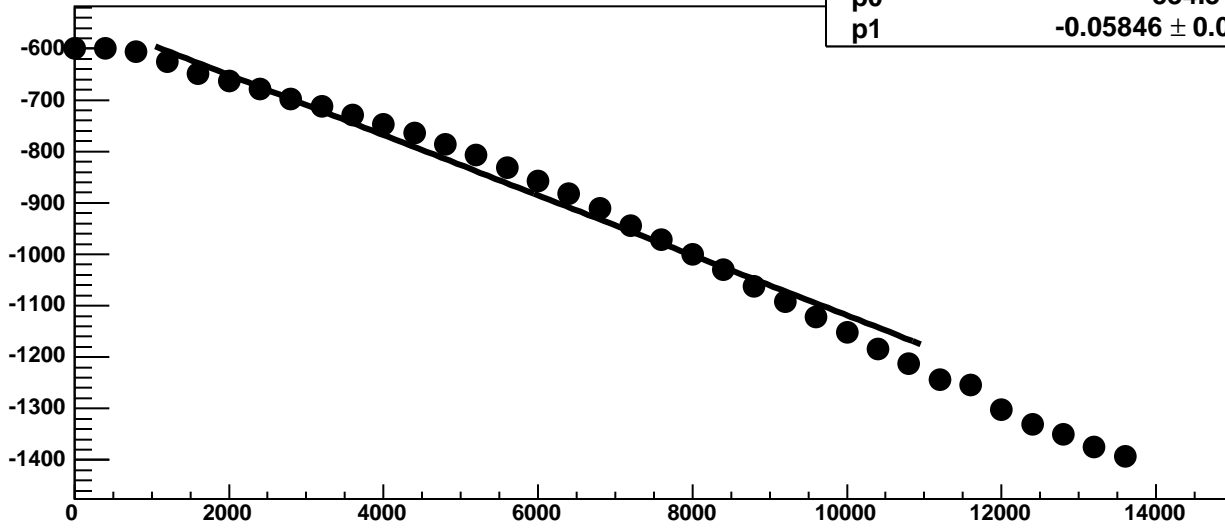
Chip 1, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC

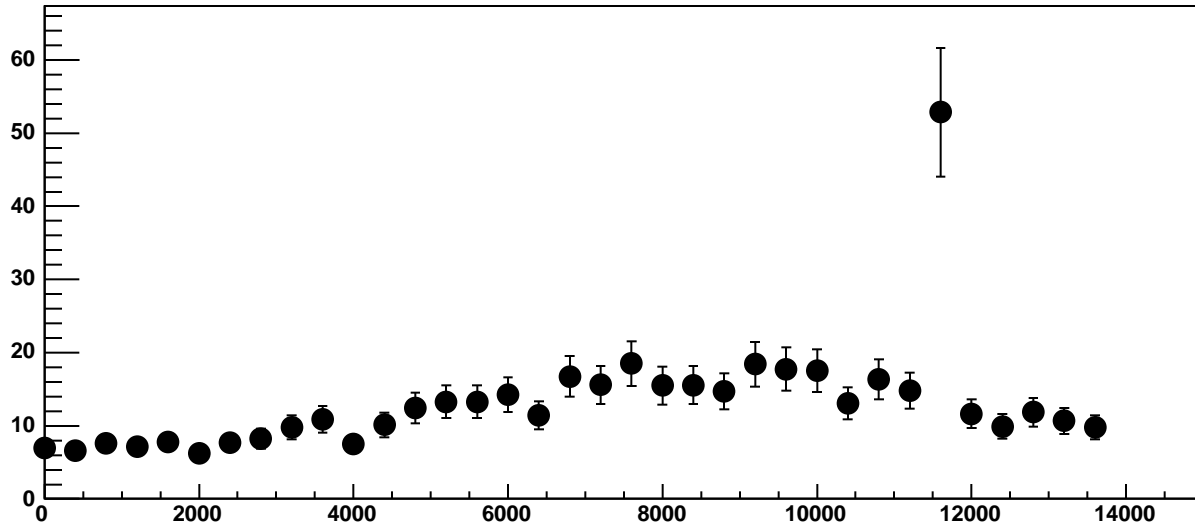


Chip 1, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

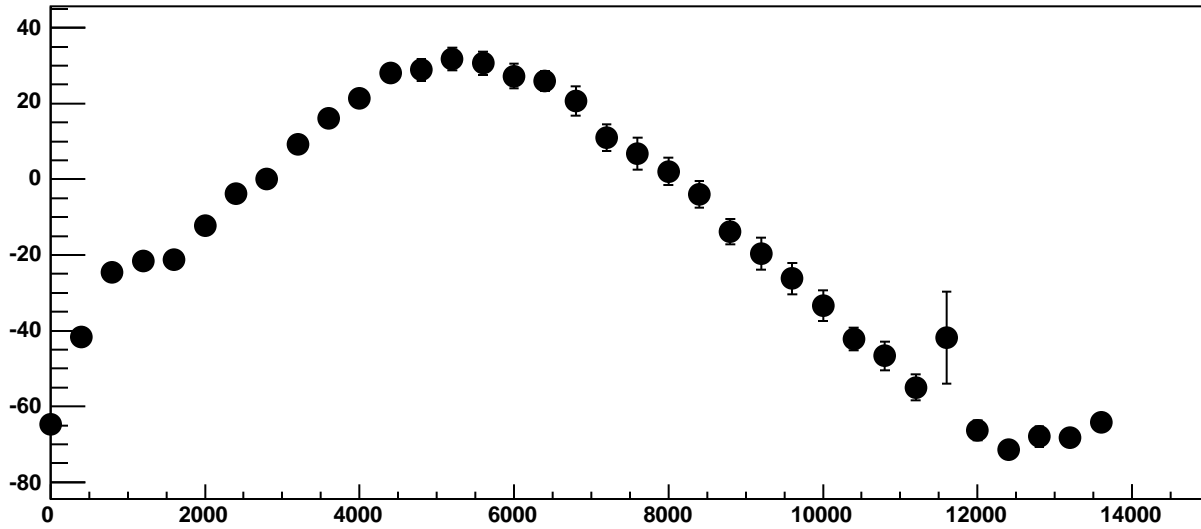


χ^2 / ndf	1771 / 23
p0	-534.3 ± 0.927
p1	-0.05846 ± 0.0001831

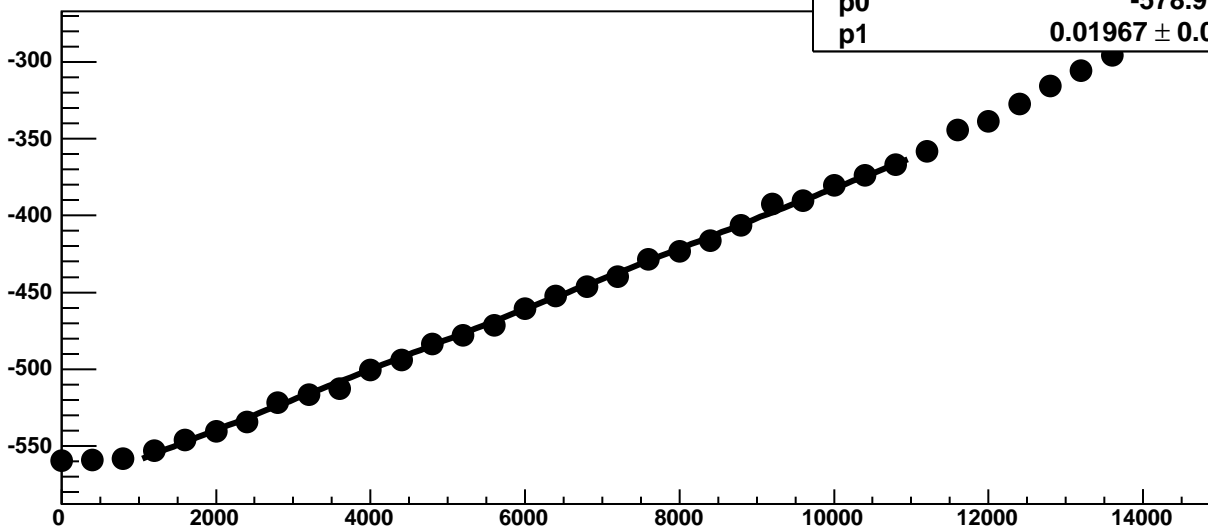
Chip 1, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

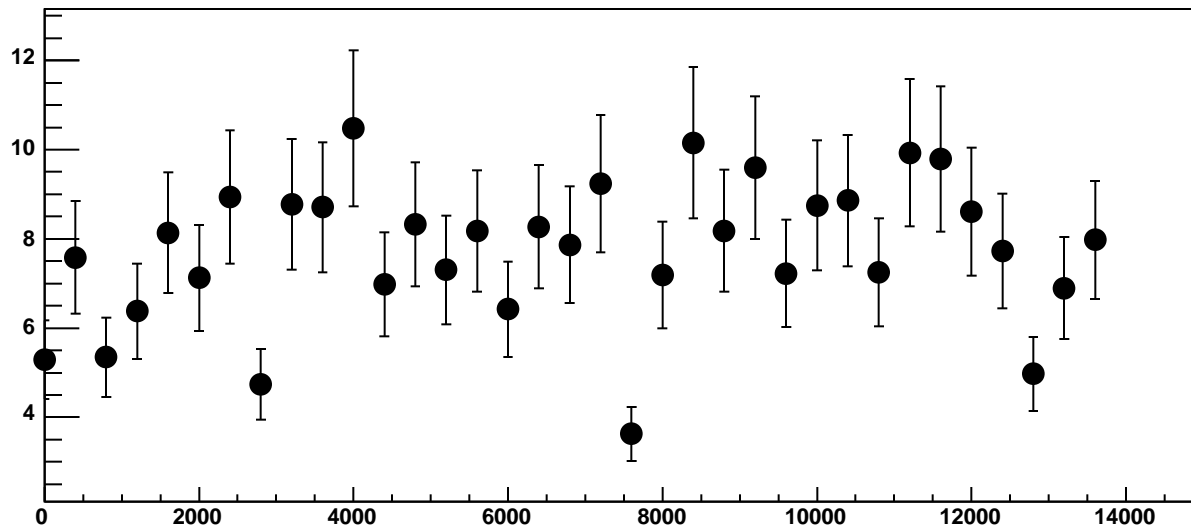


Chip 1, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

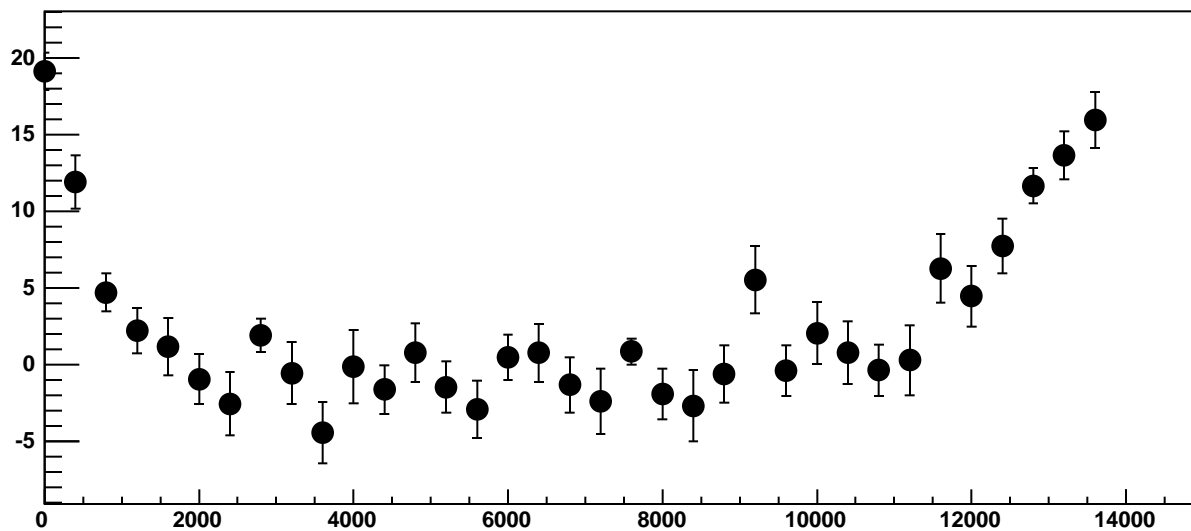


χ^2 / ndf 30.46 / 23
p0 -578.9 ± 0.768
p1 0.01967 ± 0.0001174

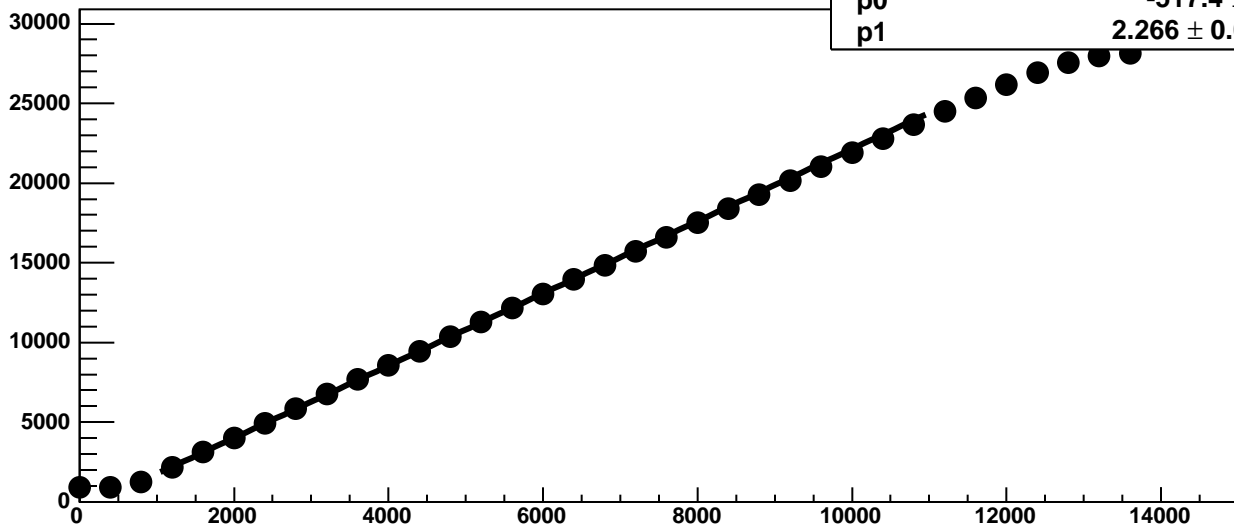
Chip 1, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



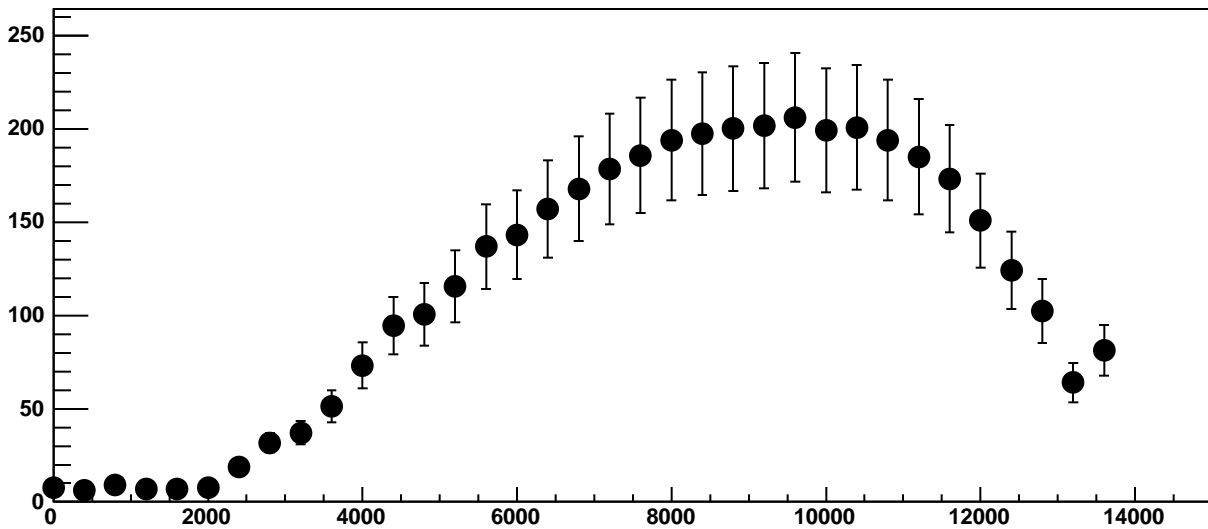
Chip 1, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



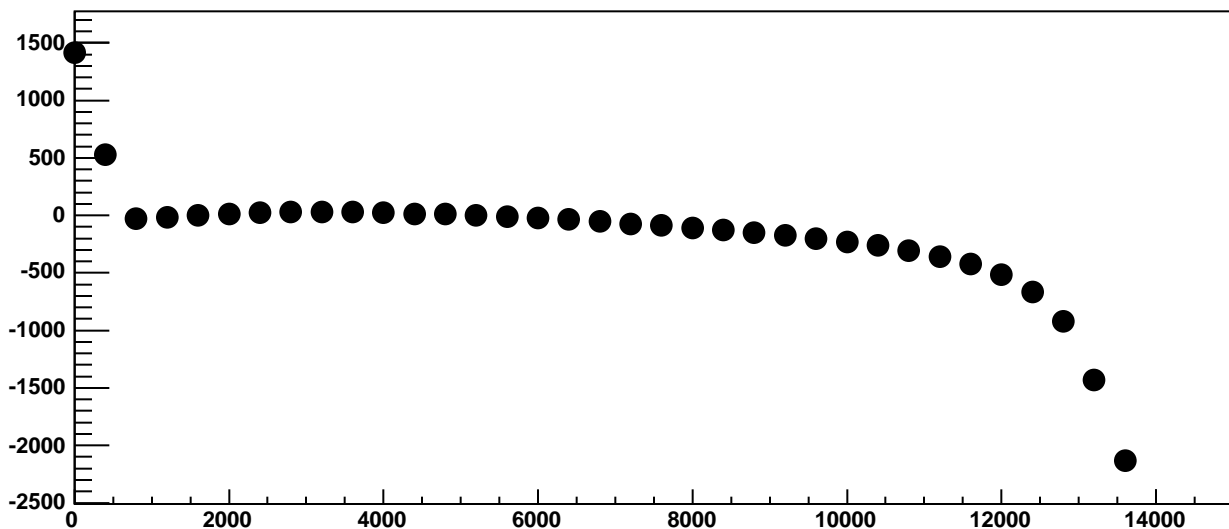
Chip 1, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC



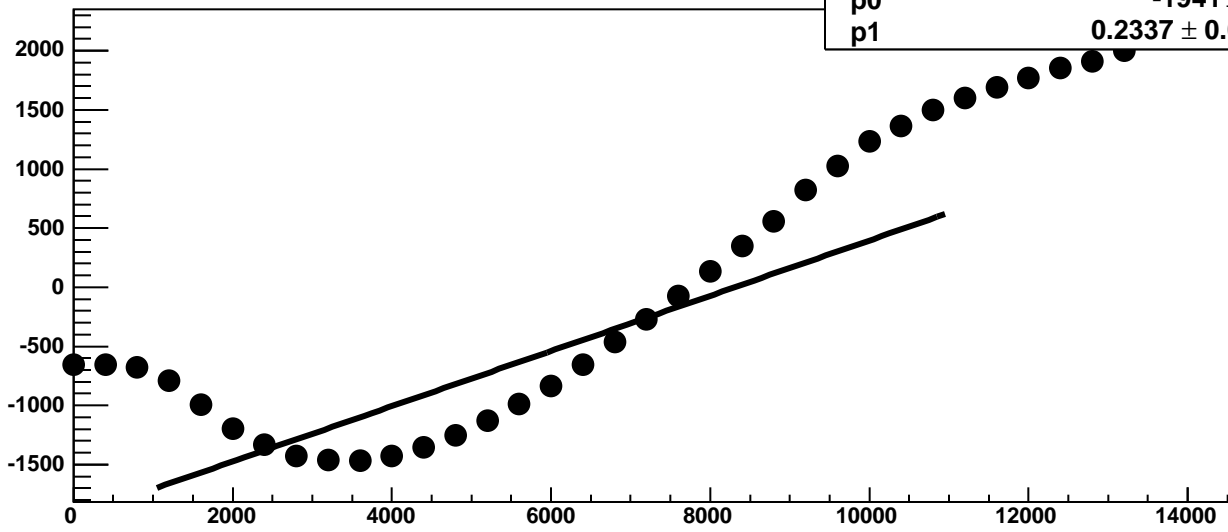
Chip 1, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC

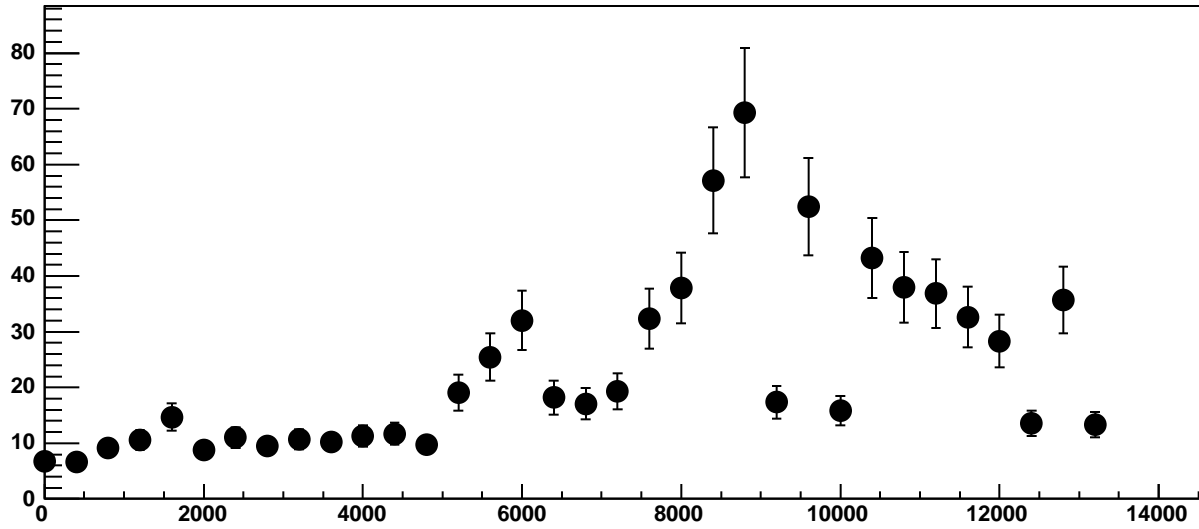


Chip 1, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

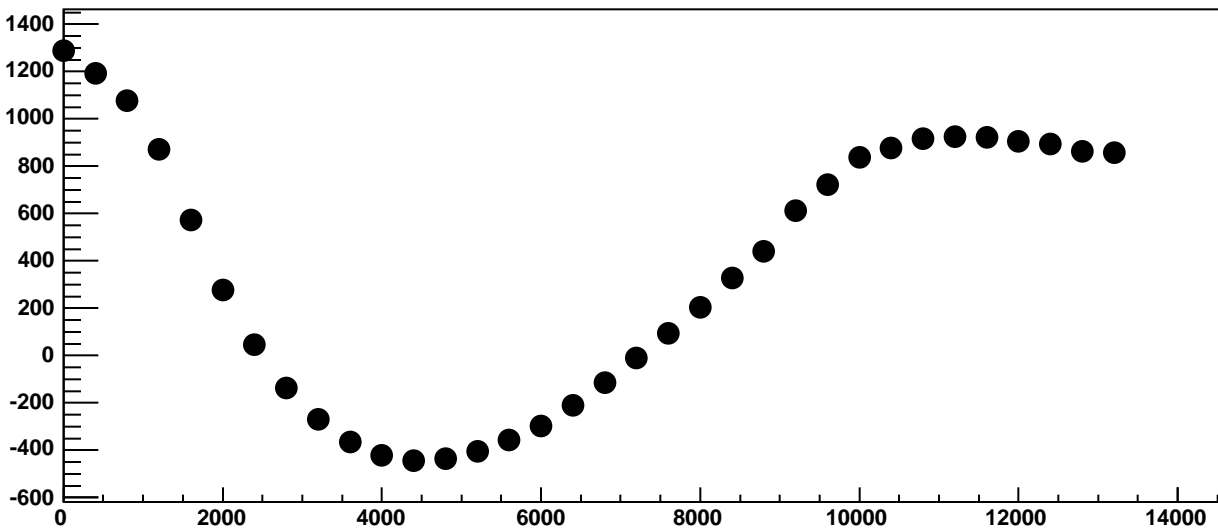


χ^2 / ndf 4.282e+05 / 23
p0 -1941 ± 1.362
p1 0.2337 ± 0.000292

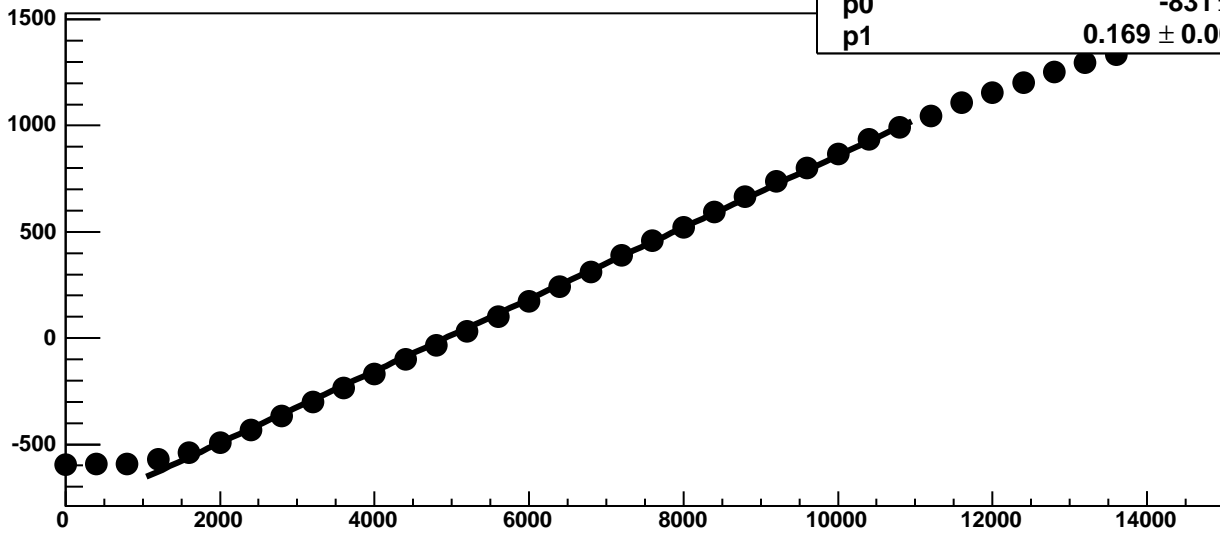
Chip 1, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



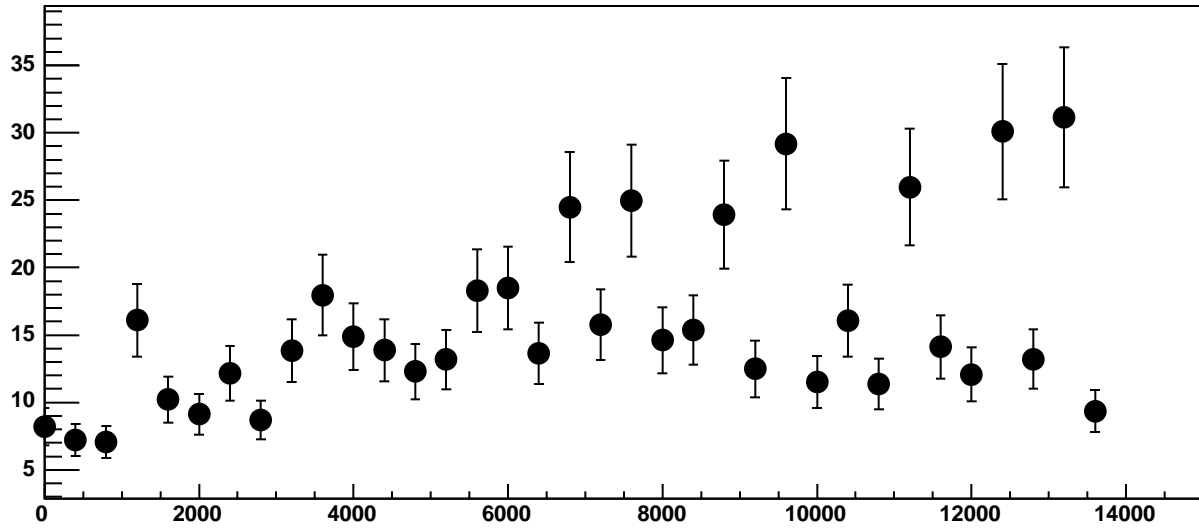
Chip 1, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



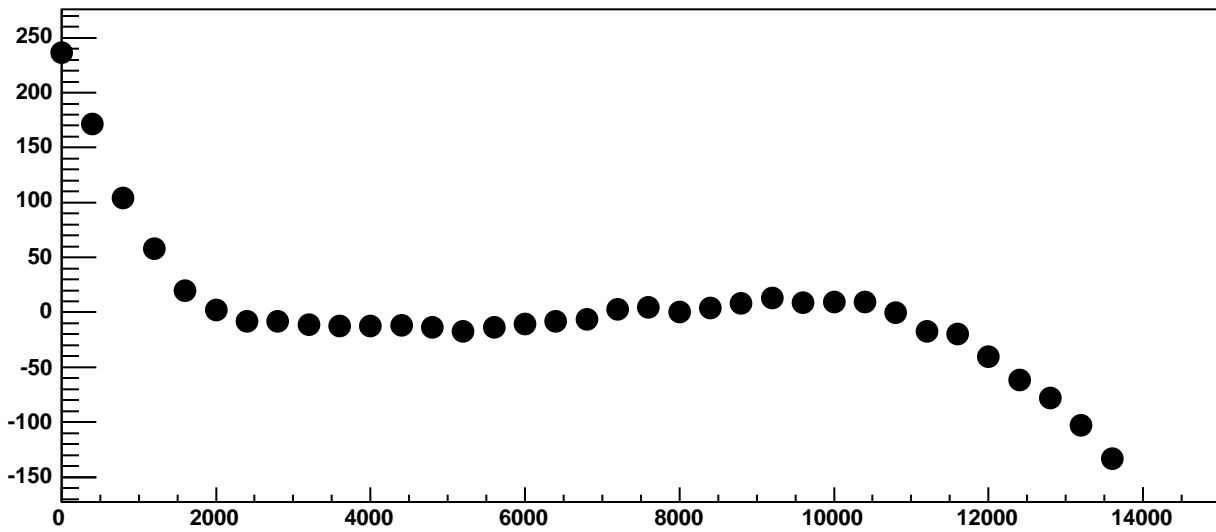
Chip 1, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



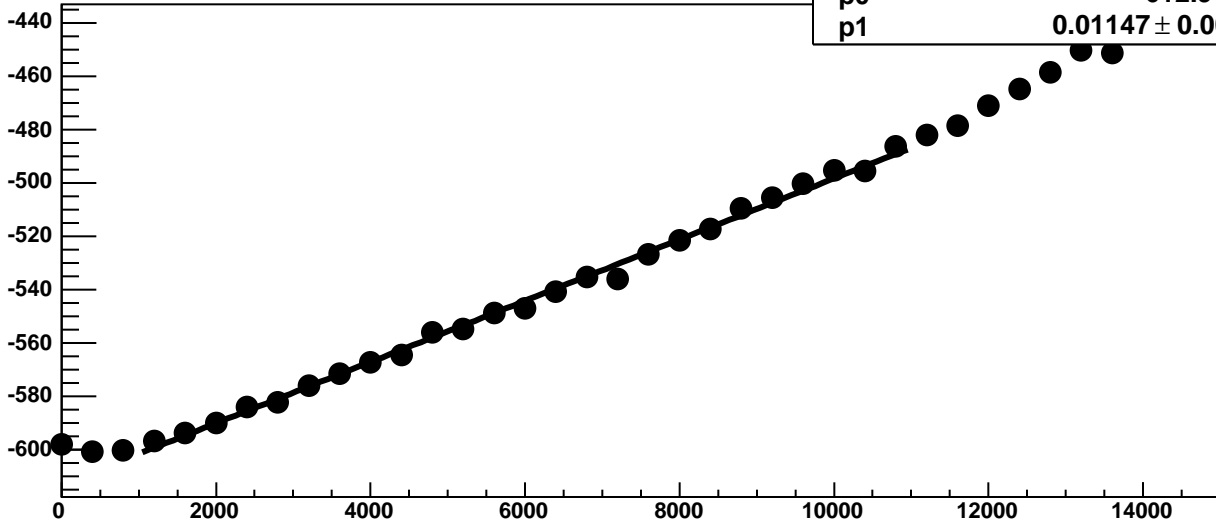
Chip 1, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



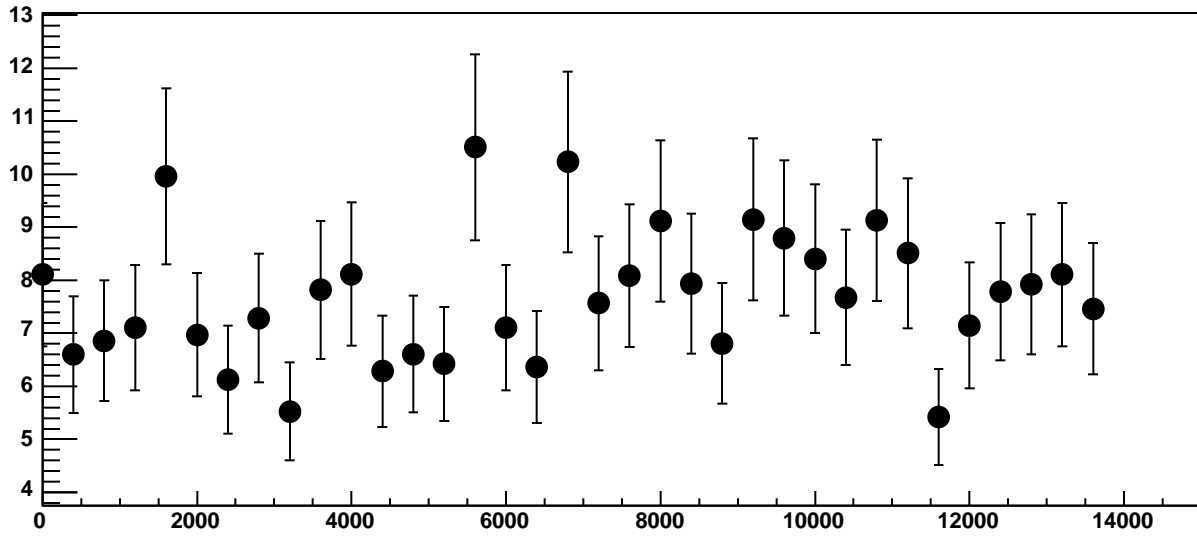
Chip 1, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



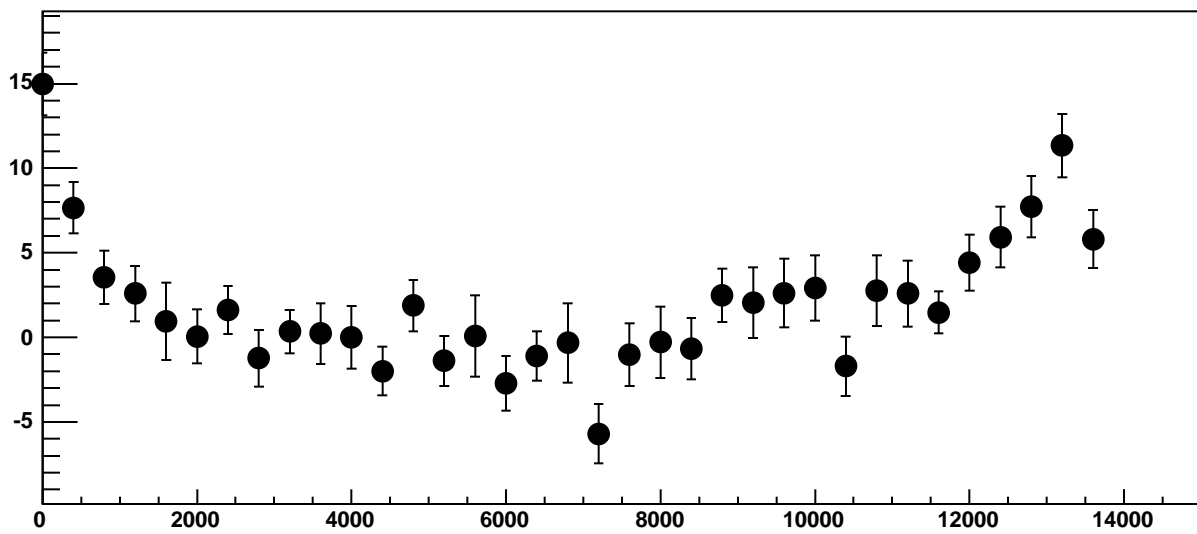
Chip 1, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



Chip 1, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

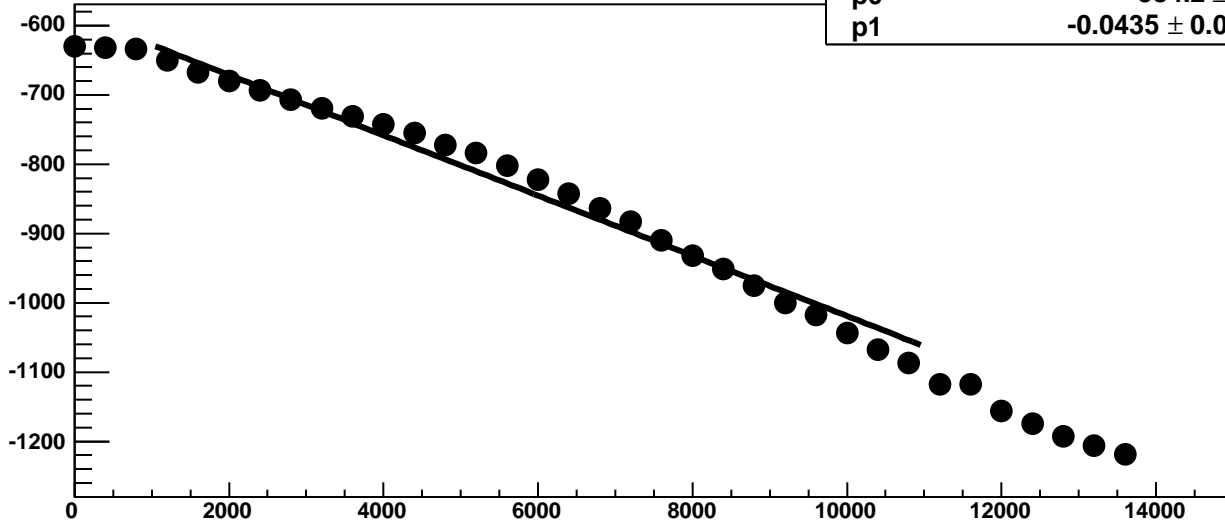


Chip 1, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

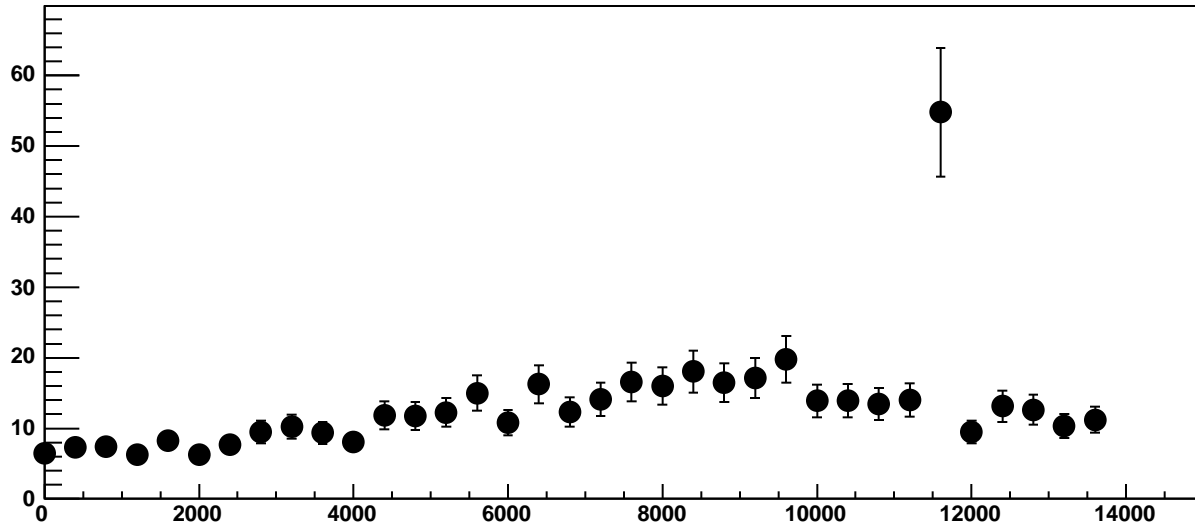


Chip 1, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

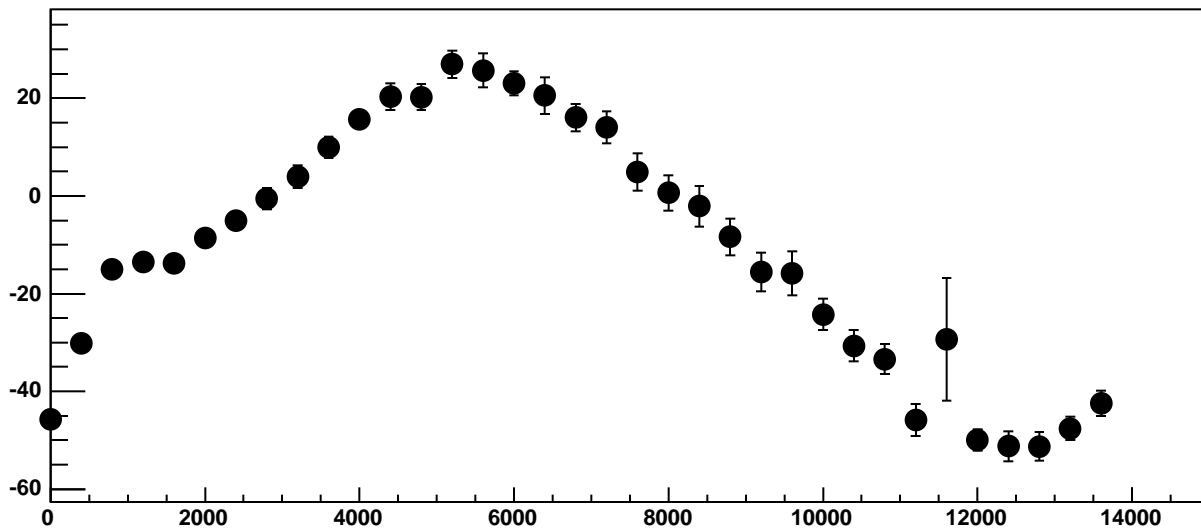
χ^2 / ndf 1008 / 23
p0 -584.2 ± 0.9088
p1 -0.0435 ± 0.0001764



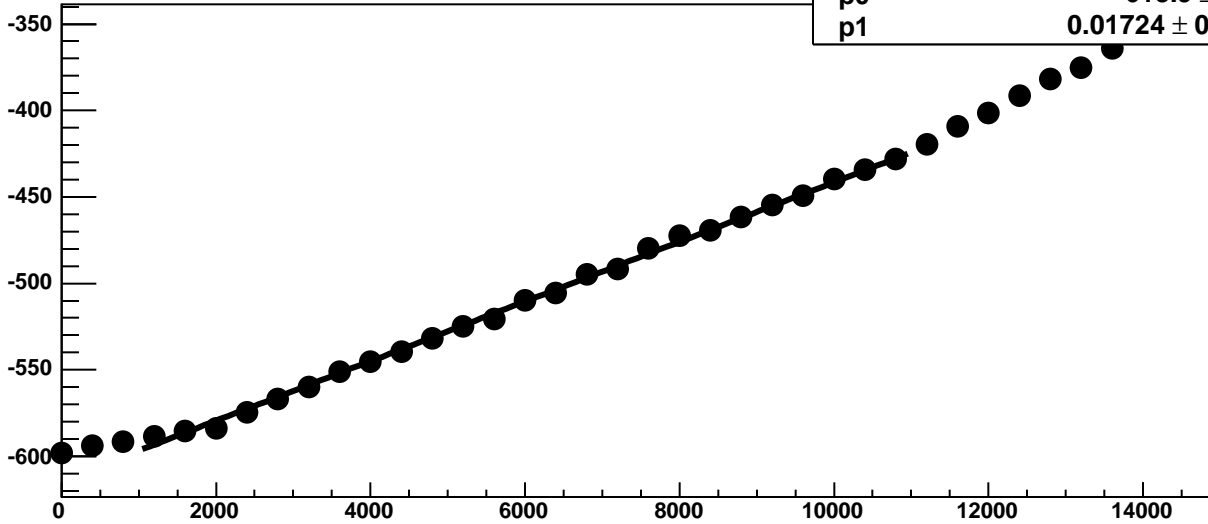
Chip 1, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



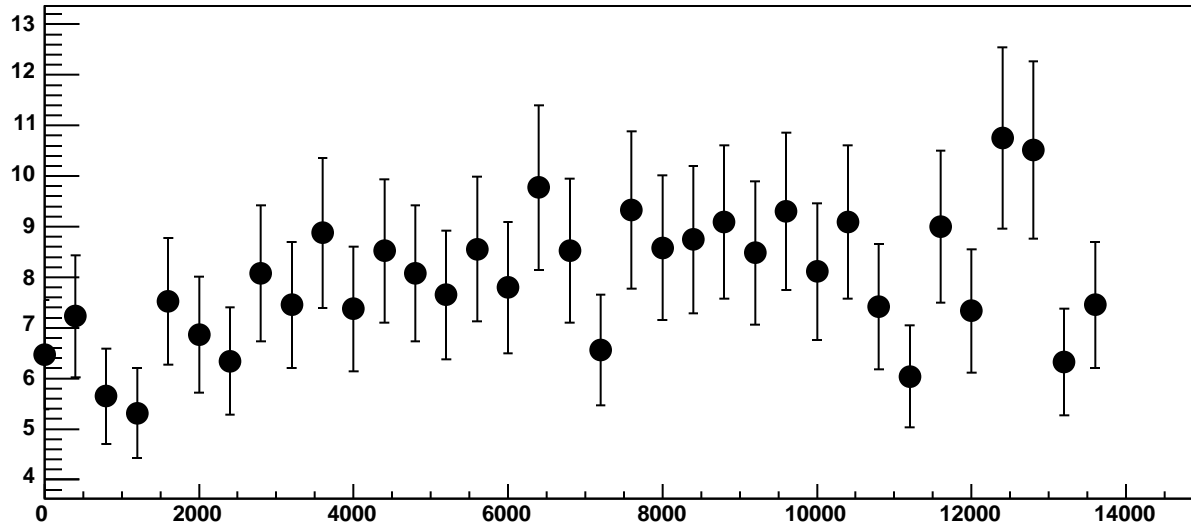
Chip 1, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC



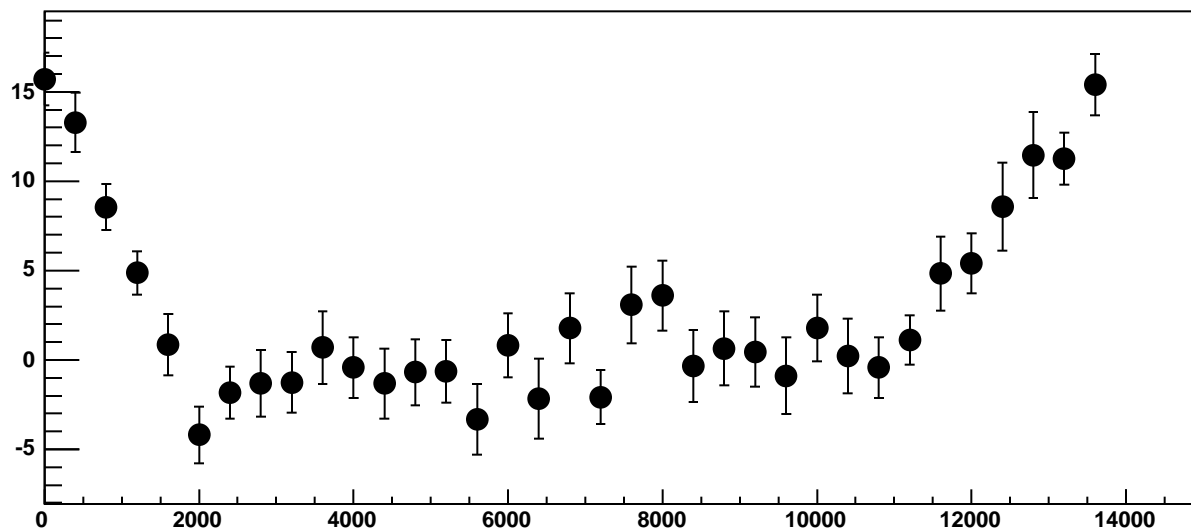
Chip 1, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



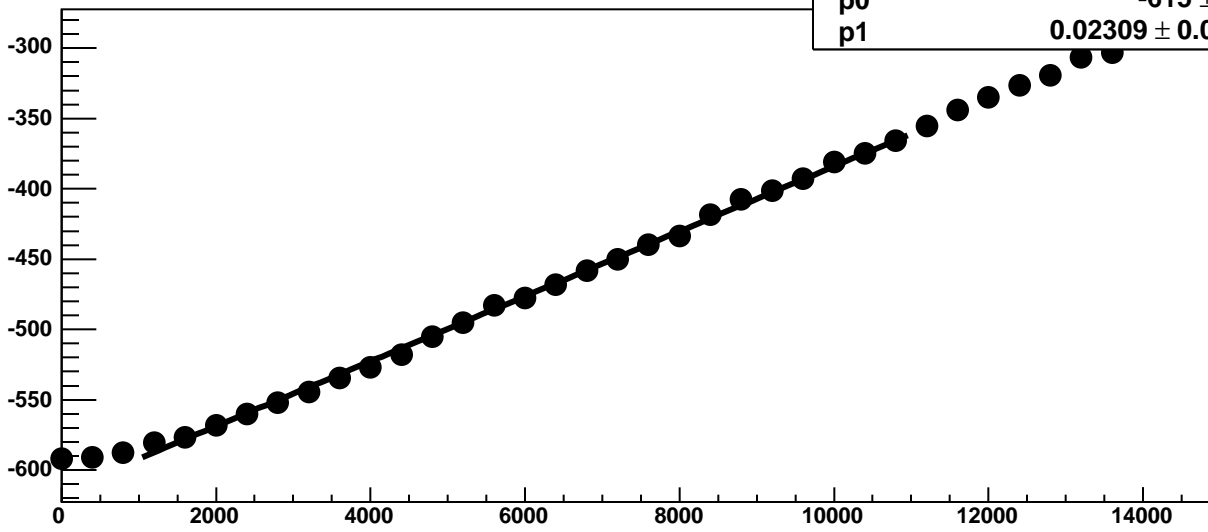
Chip 1, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



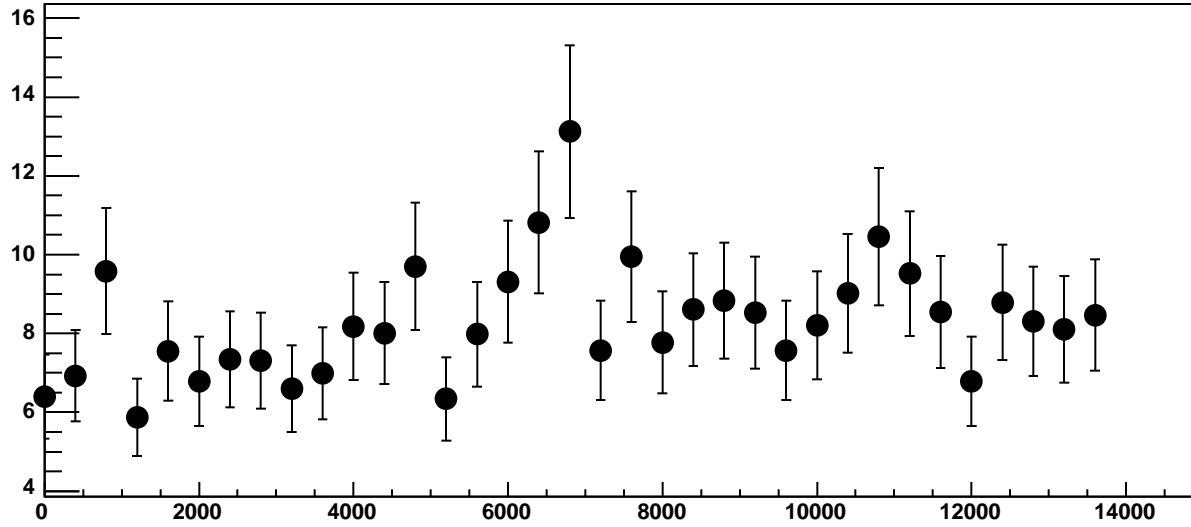
Chip 1, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



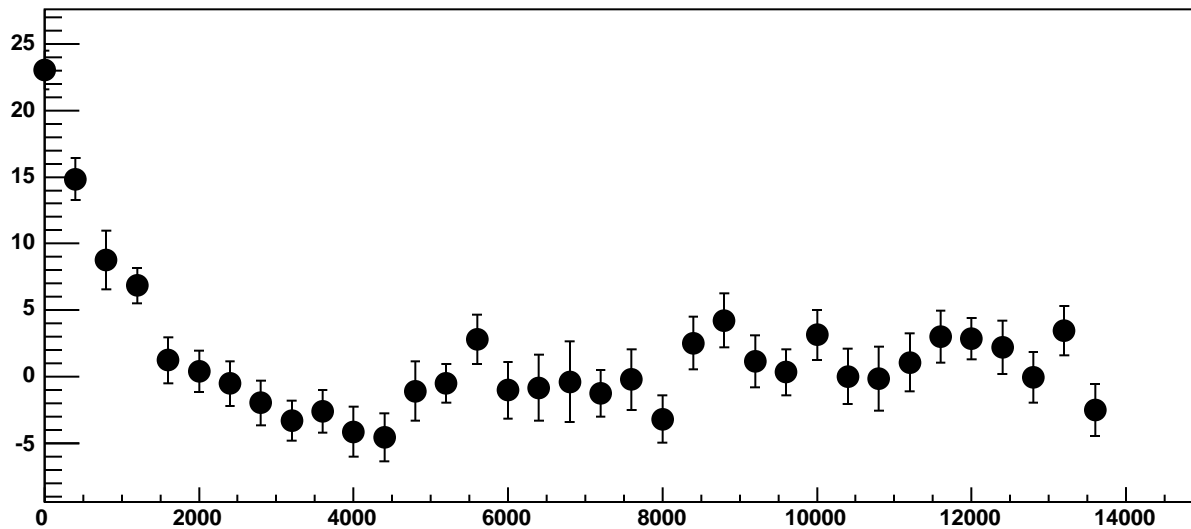
Chip 1, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



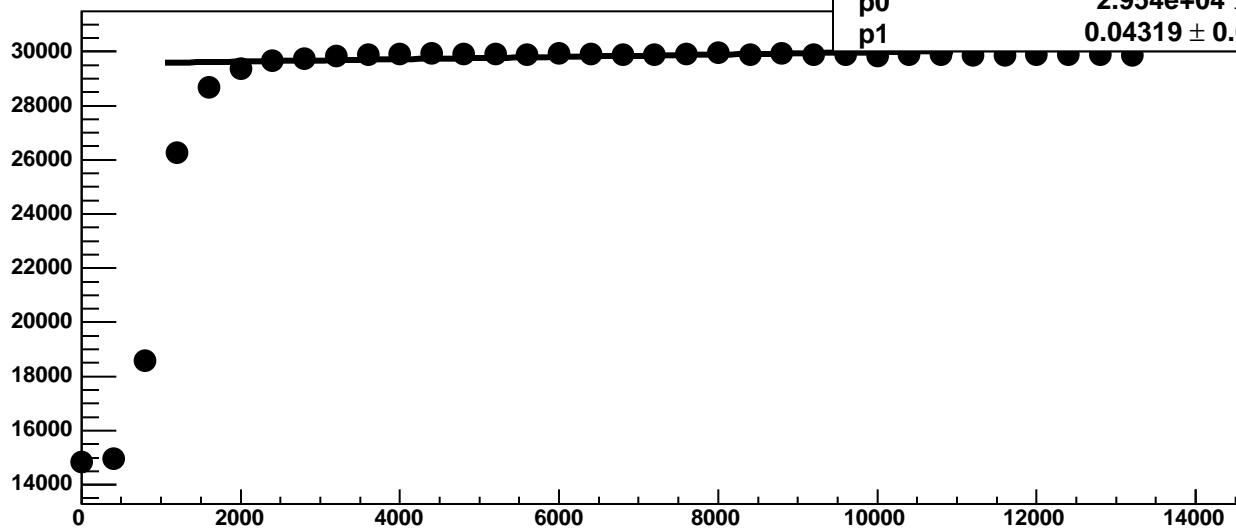
Chip 1, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



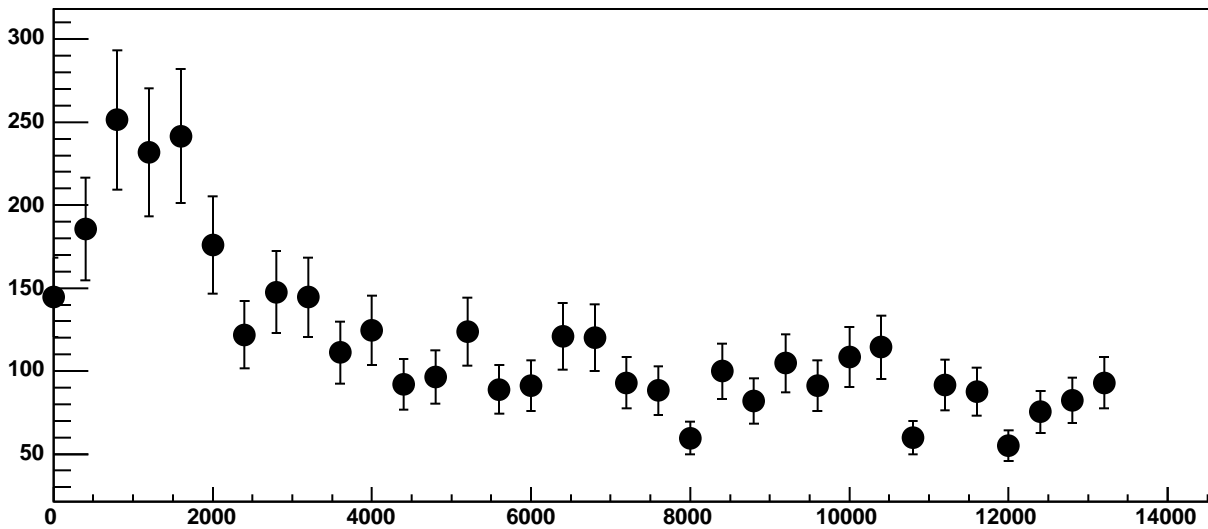
Chip 1, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



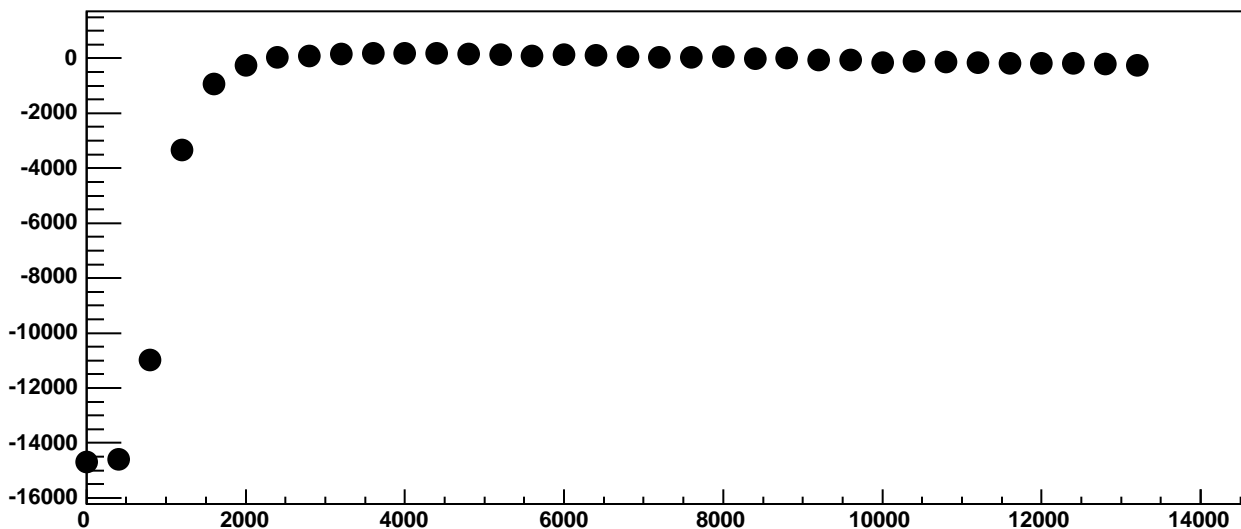
Chip 1, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC



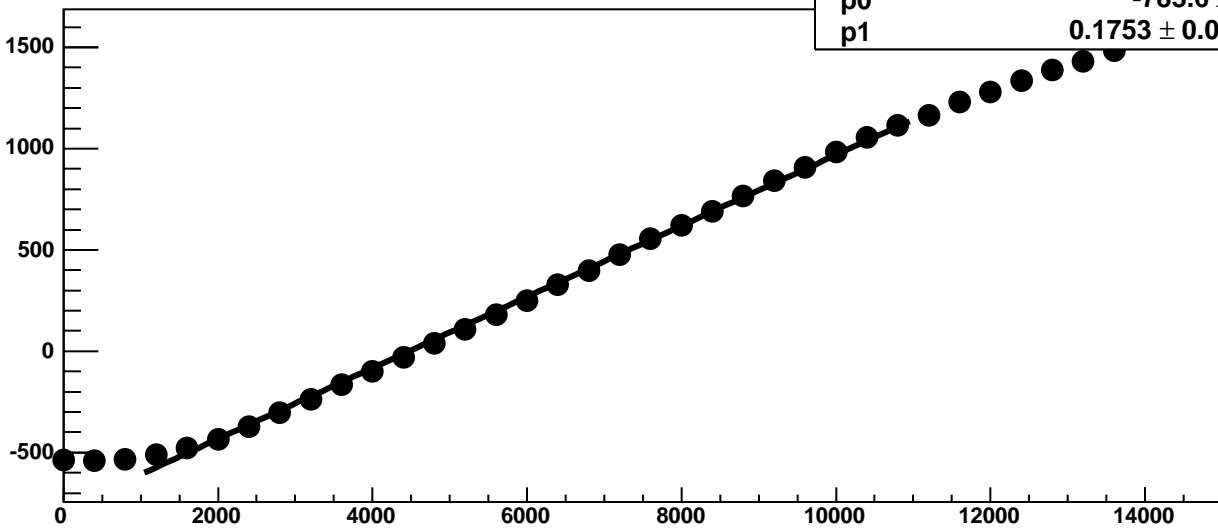
Chip 1, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC



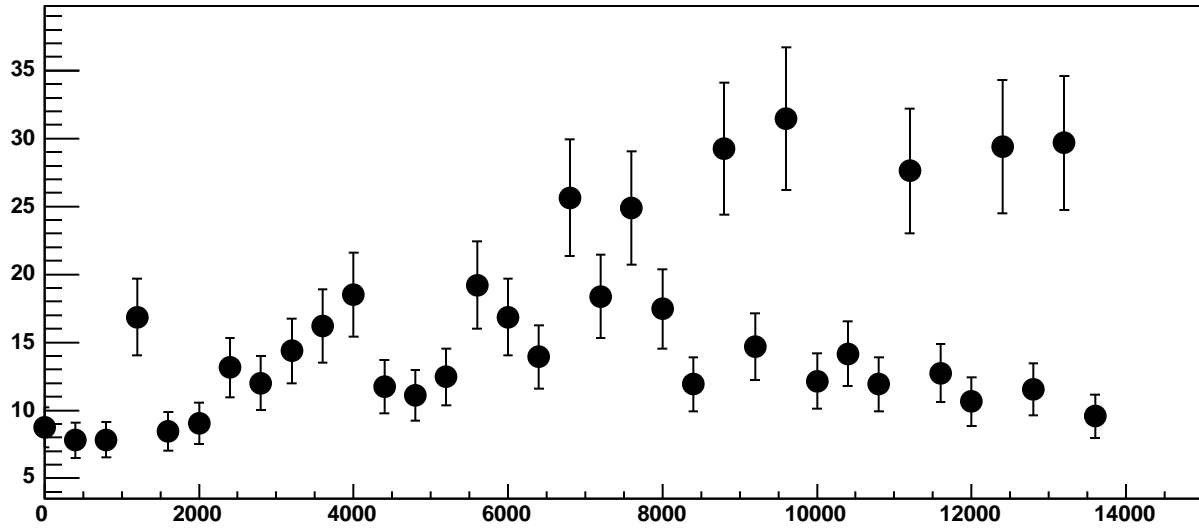
Chip 1, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC



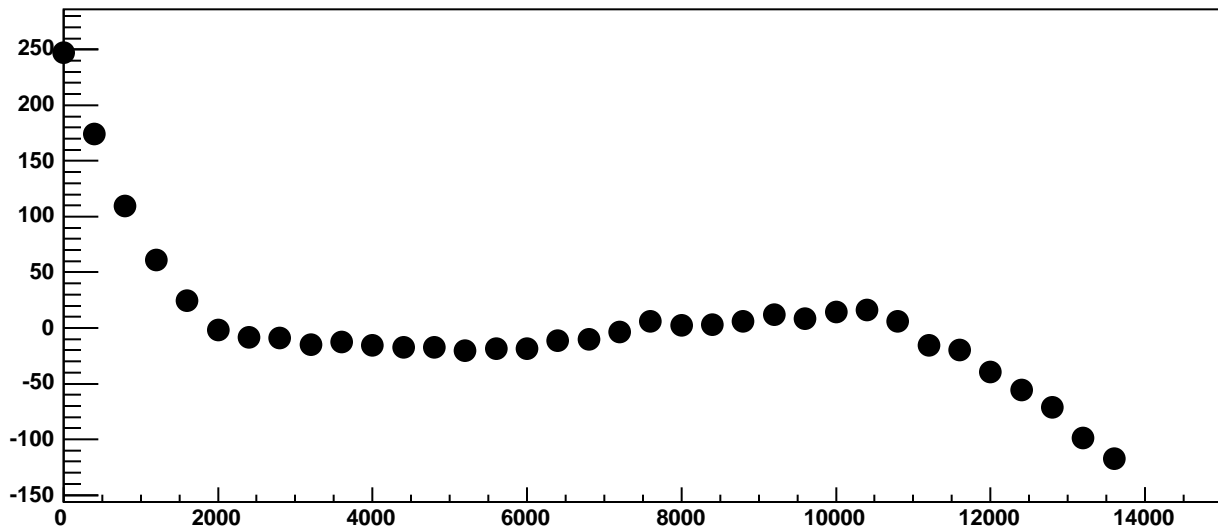
Chip 1, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



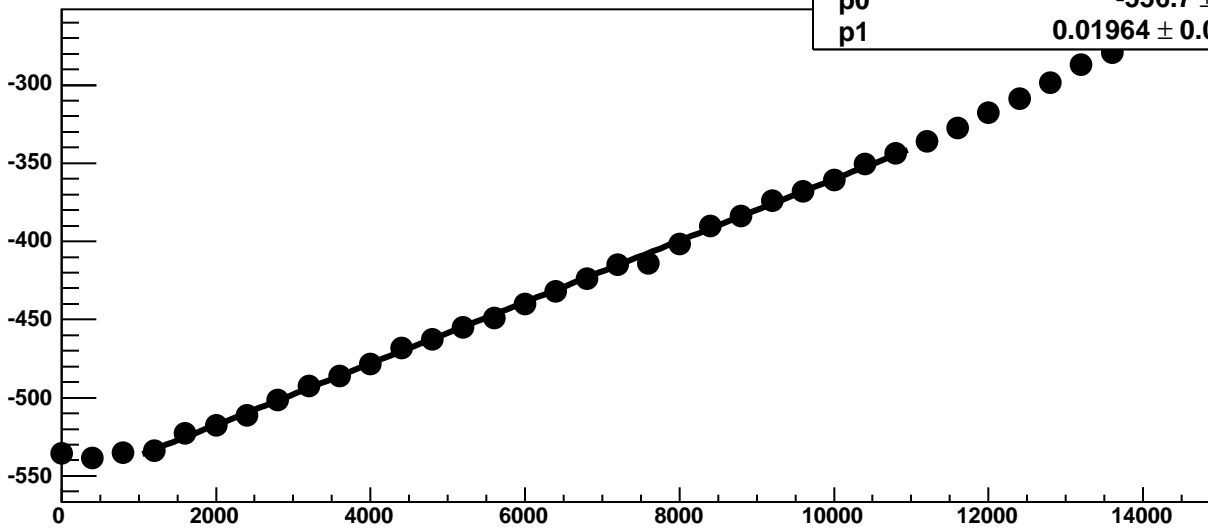
Chip 1, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



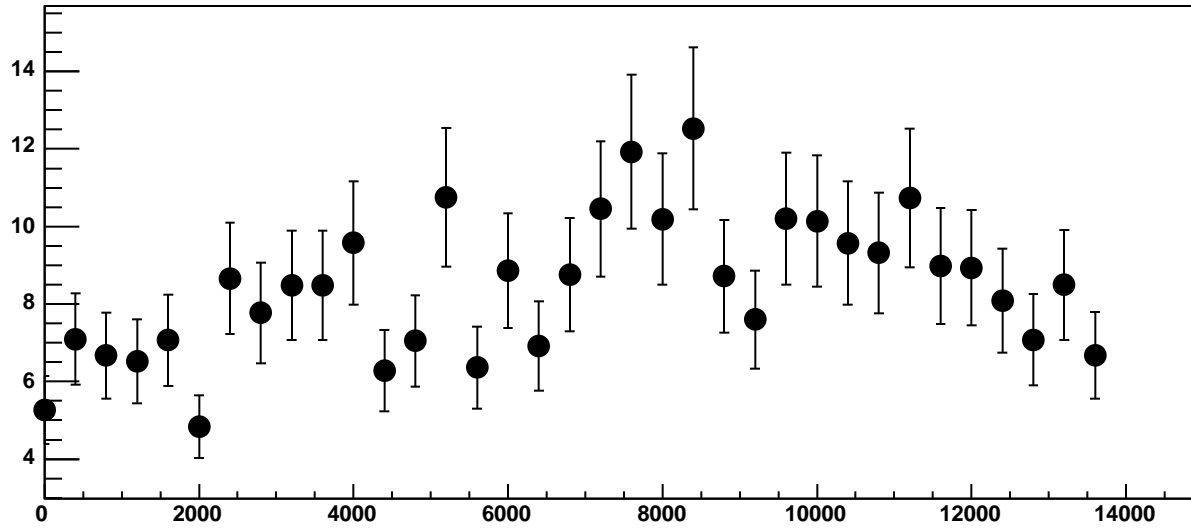
Chip 1, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



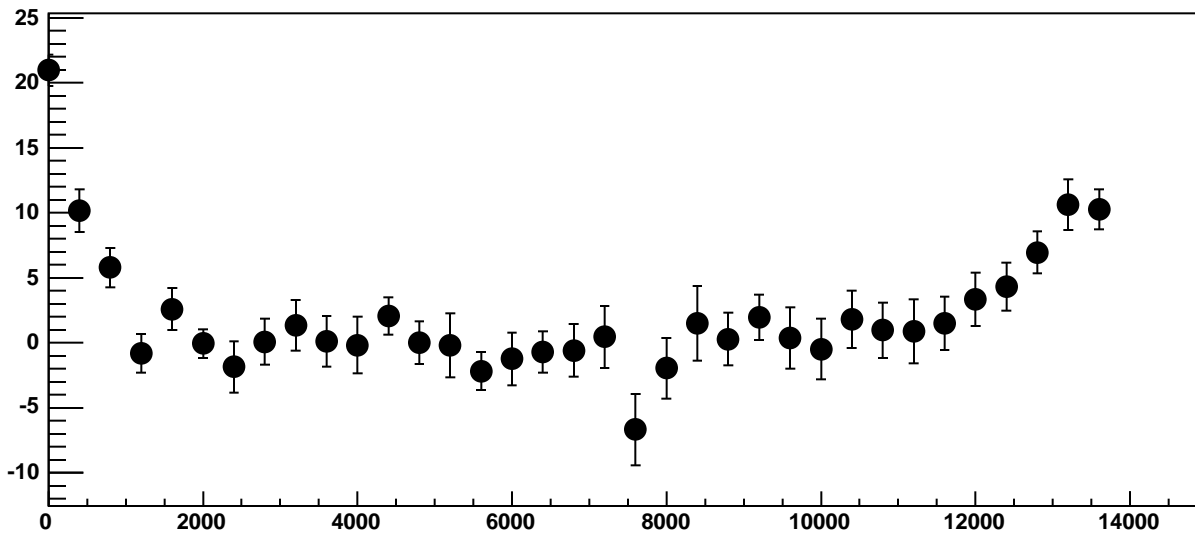
Chip 1, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



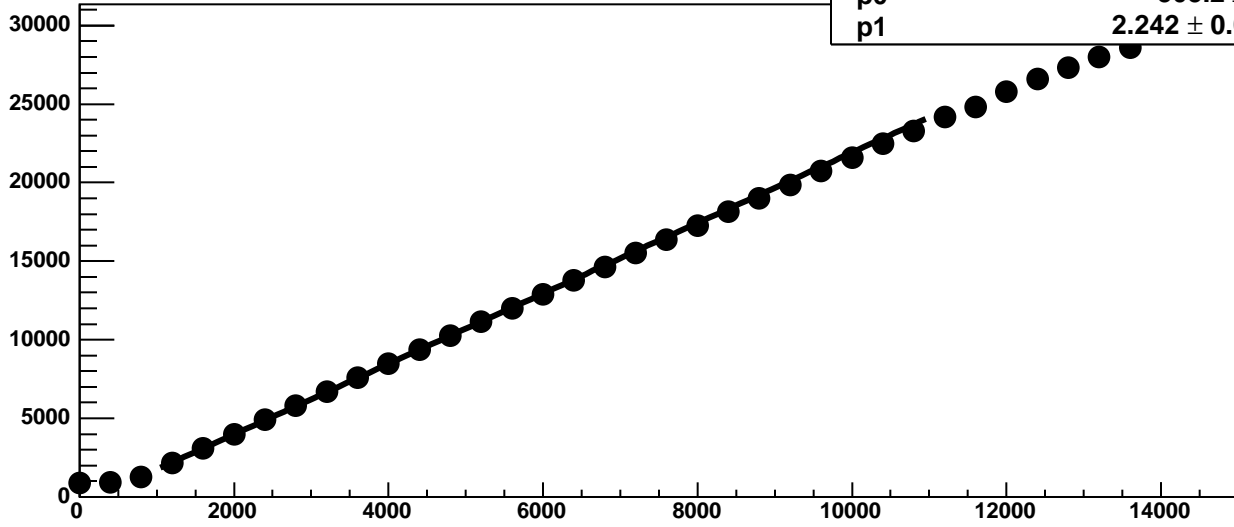
Chip 1, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



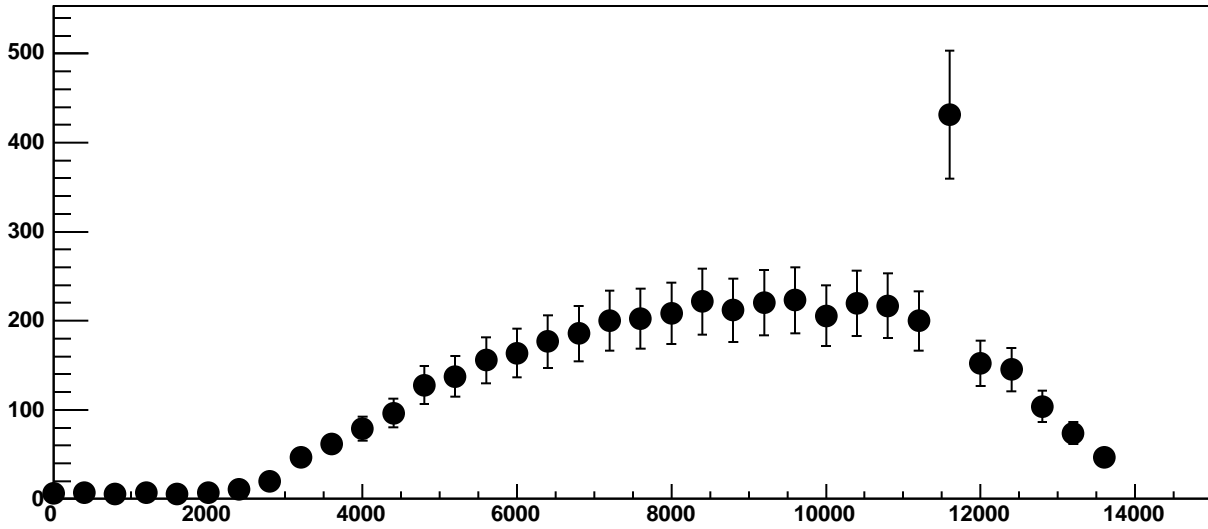
Chip 1, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



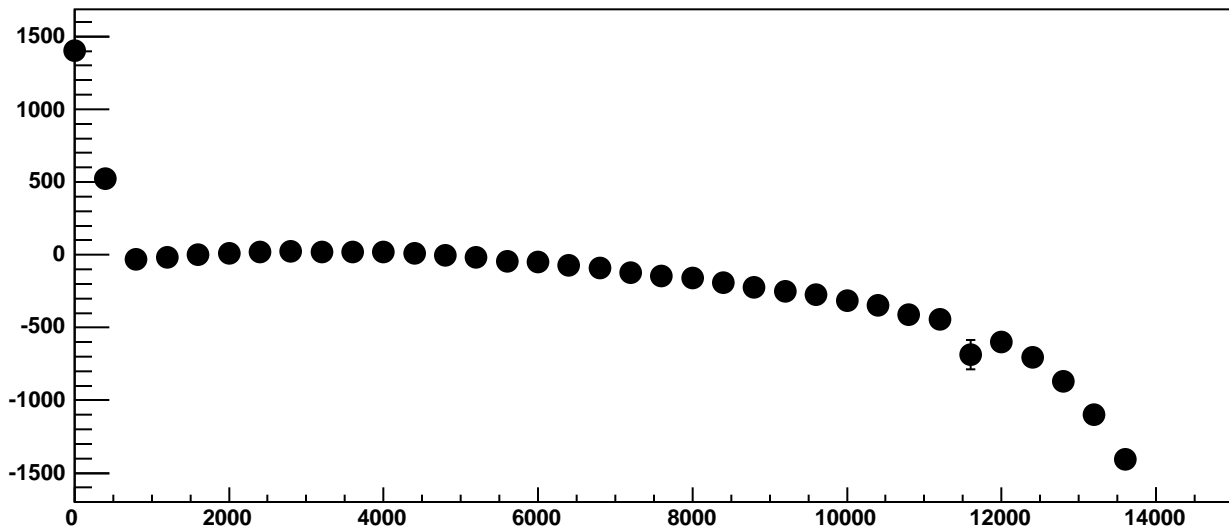
Chip 1, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC



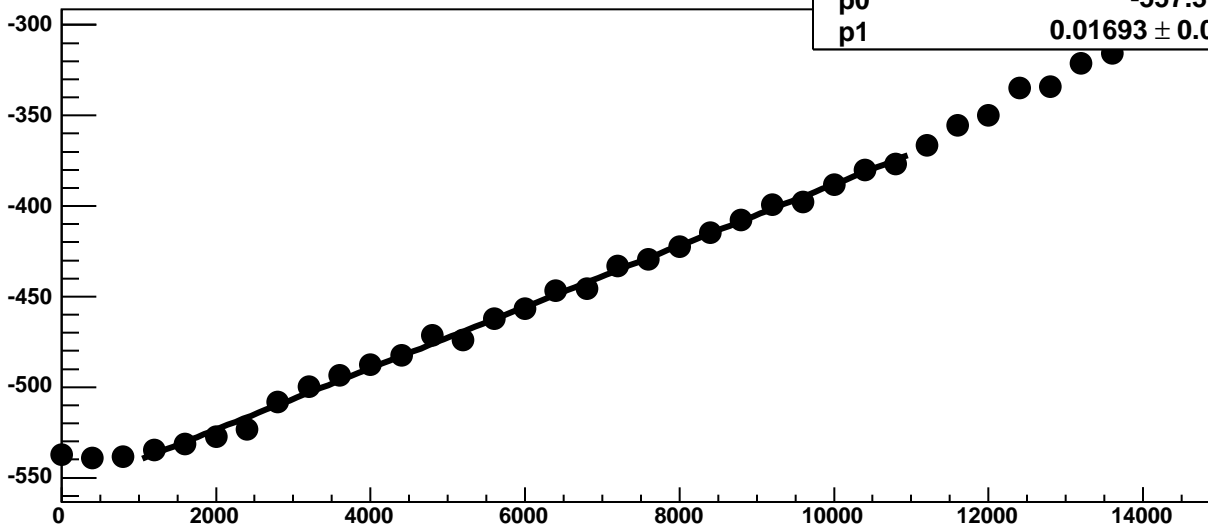
Chip 1, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC



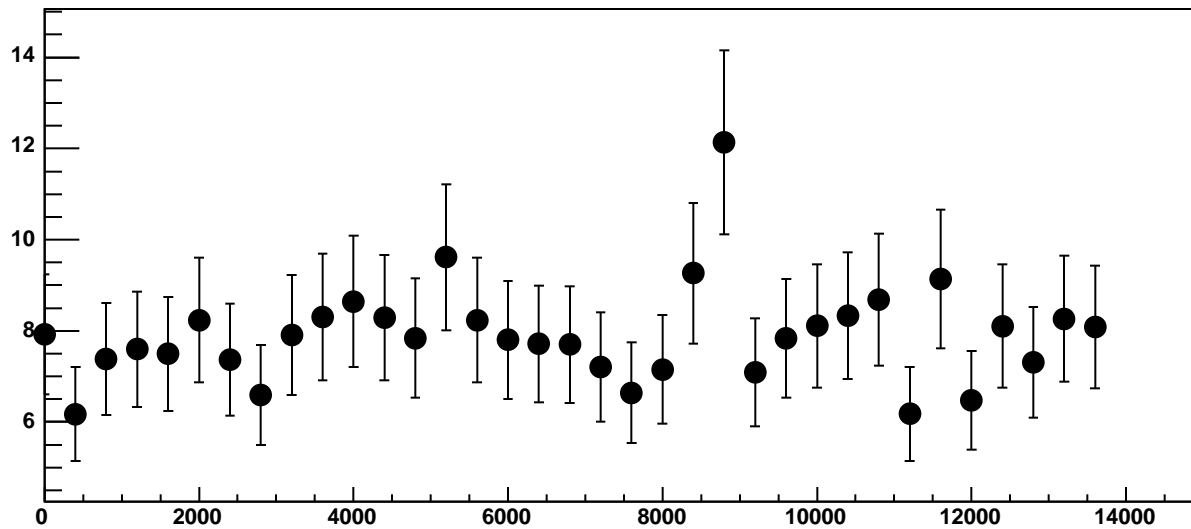
Chip 1, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC



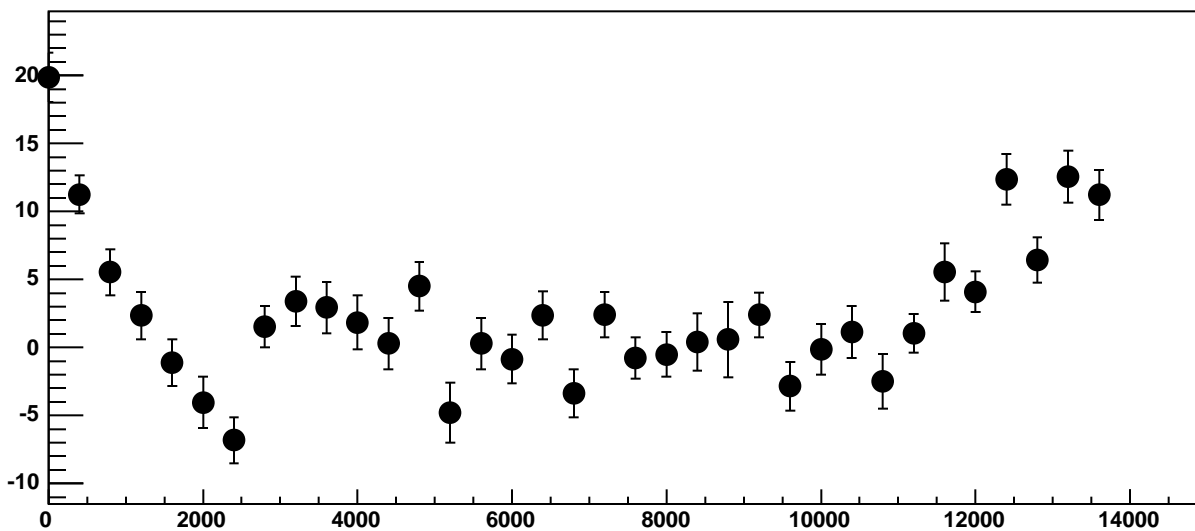
Chip 1, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



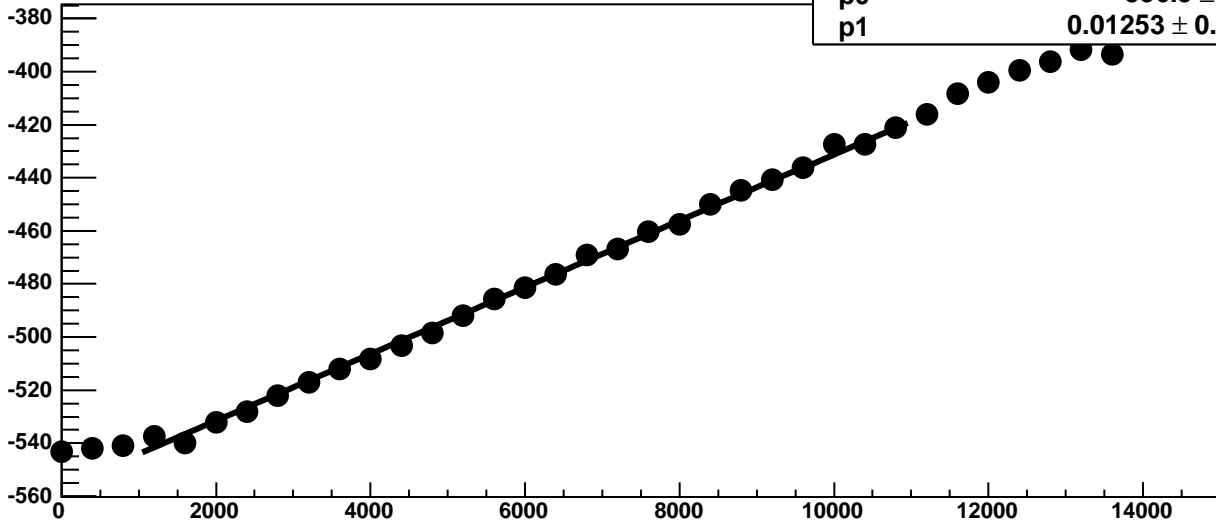
Chip 1, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



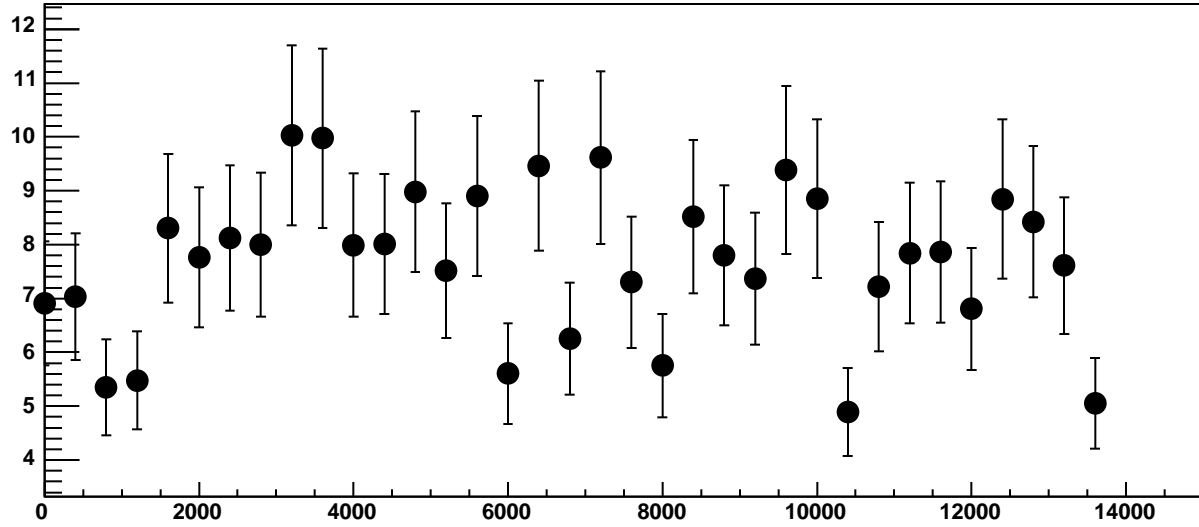
Chip 1, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



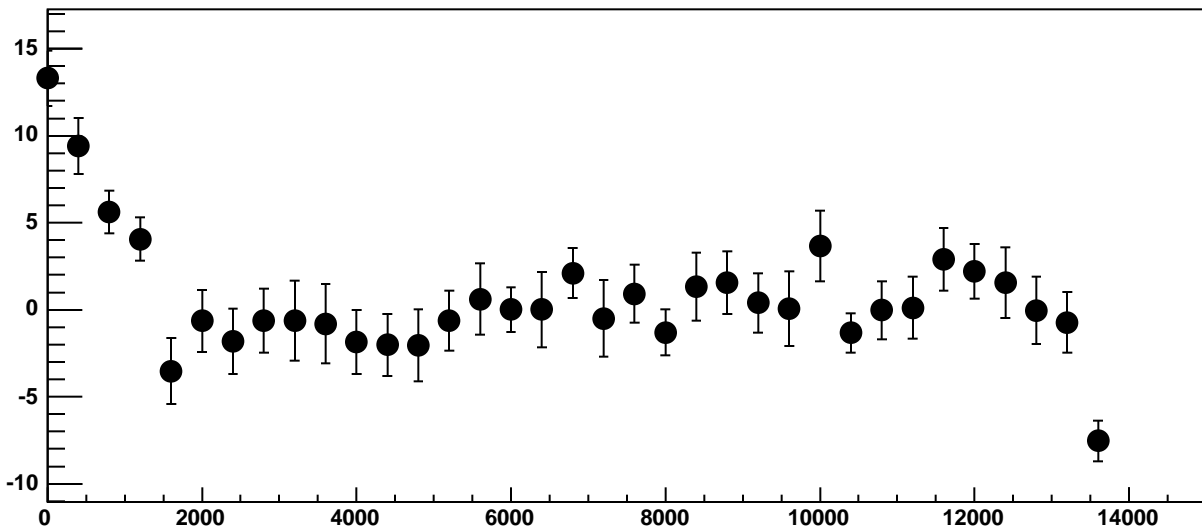
Chip 1, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



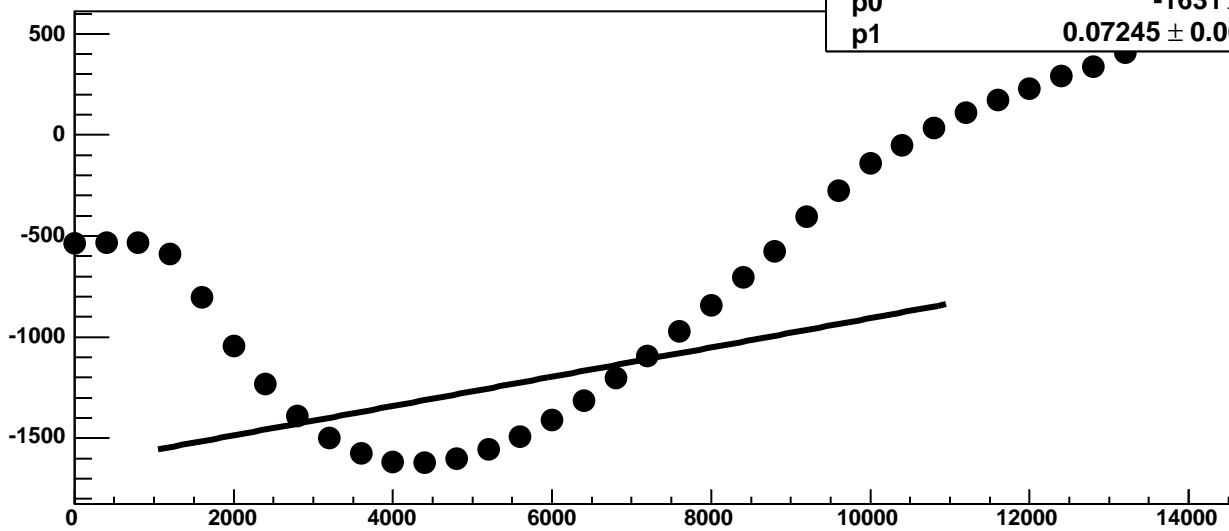
Chip 1, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

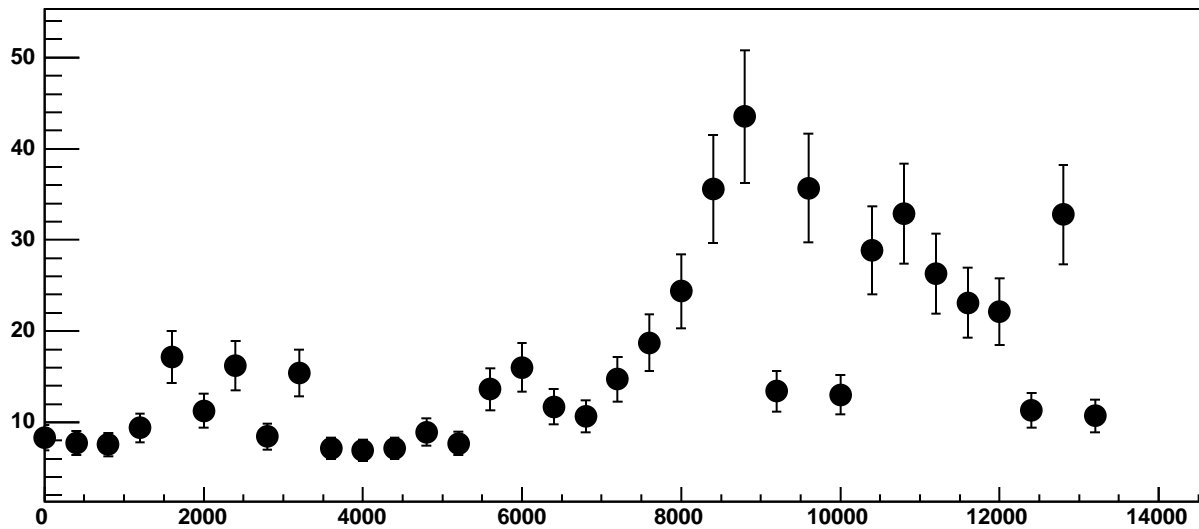


Chip 1, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC

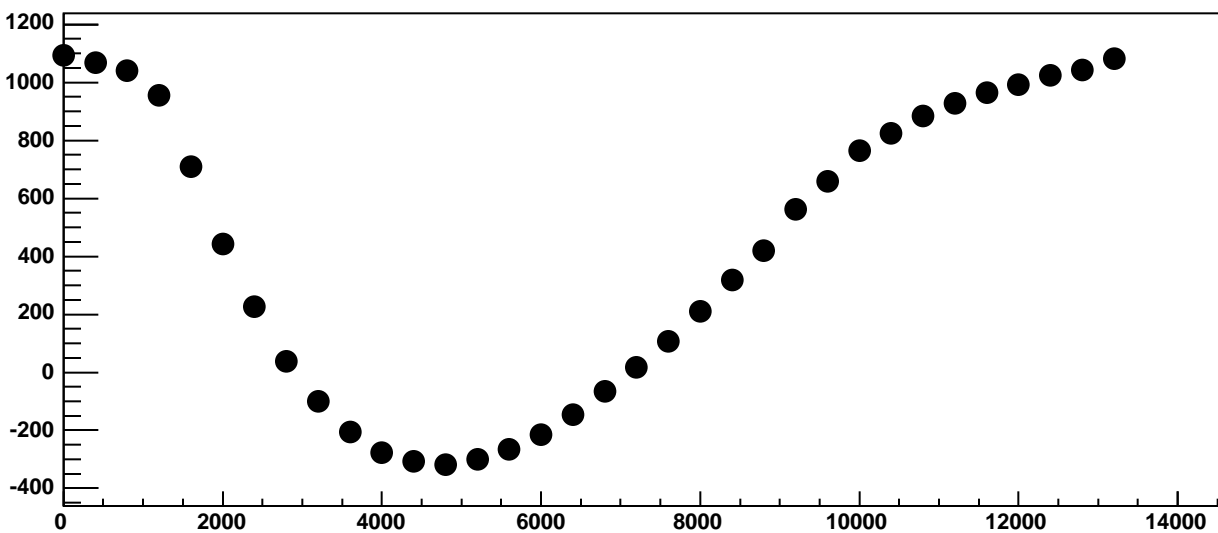


χ^2 / ndf 5.527e+05 / 23
p0 -1631 ± 1.264
p1 0.07245 ± 0.0002442

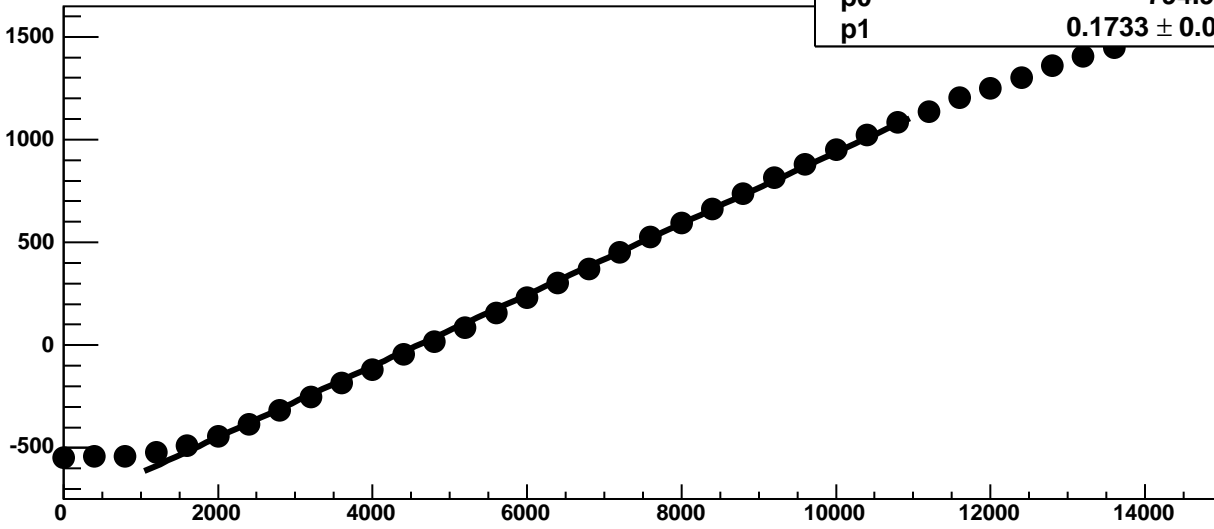
Chip 1, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



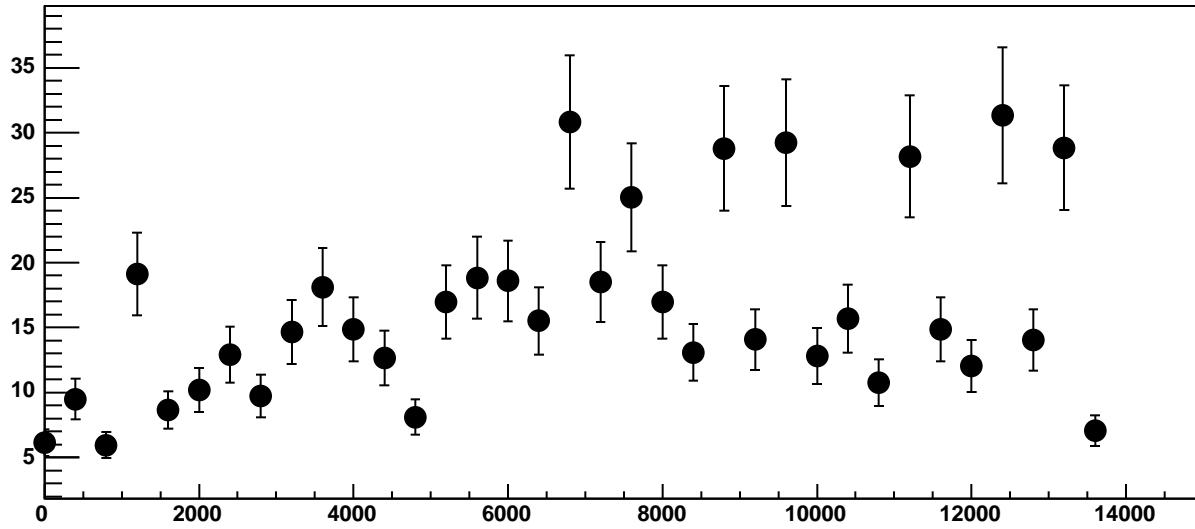
Chip 1, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC



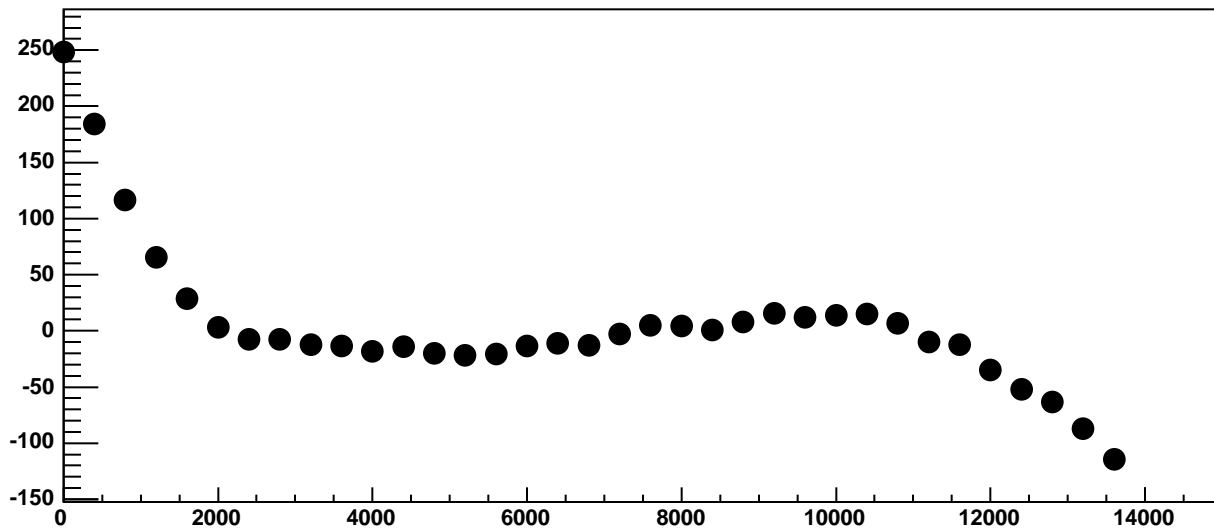
Chip 1, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



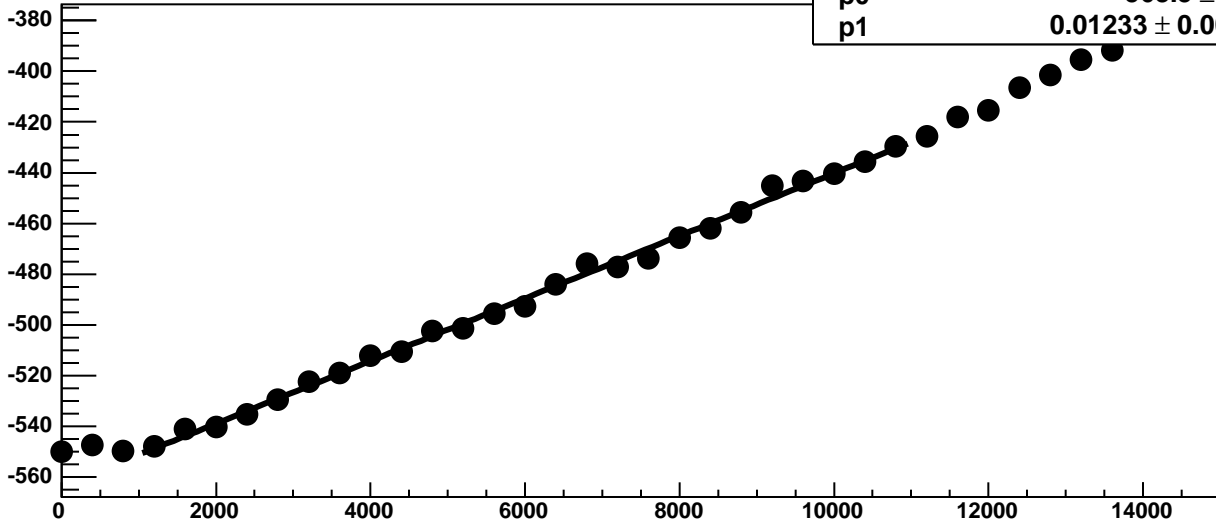
Chip 1, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

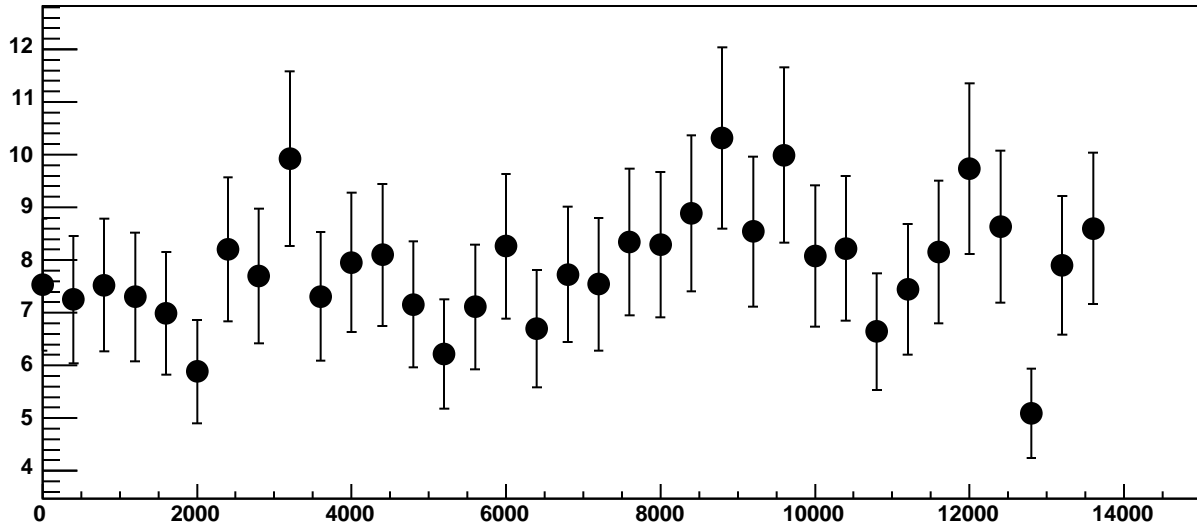


Chip 1, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

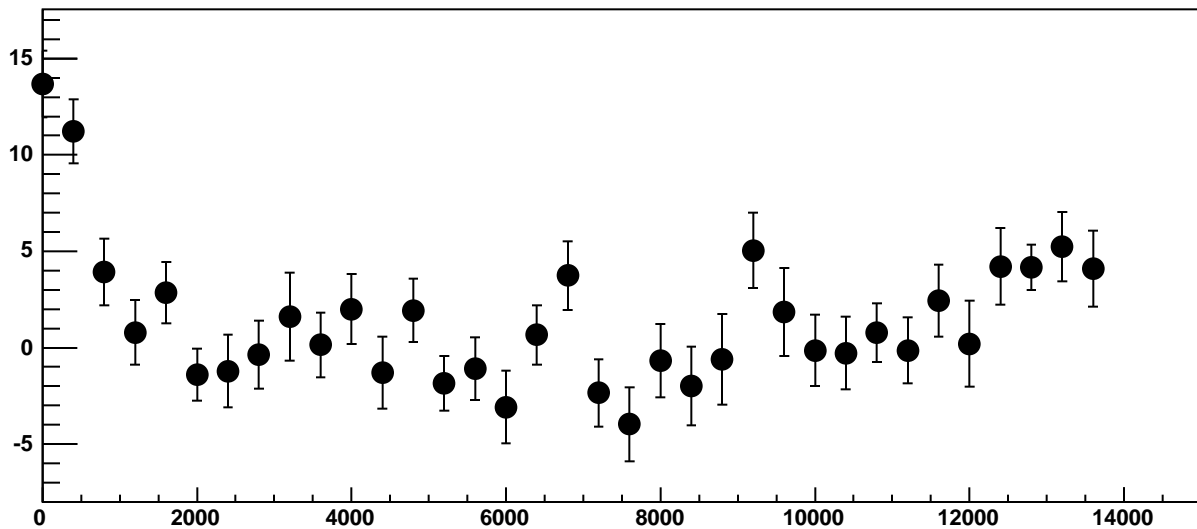


χ^2 / ndf 32.75 / 23
p0 -563.5 ± 0.7795
p1 0.01233 ± 0.0001215

Chip 1, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

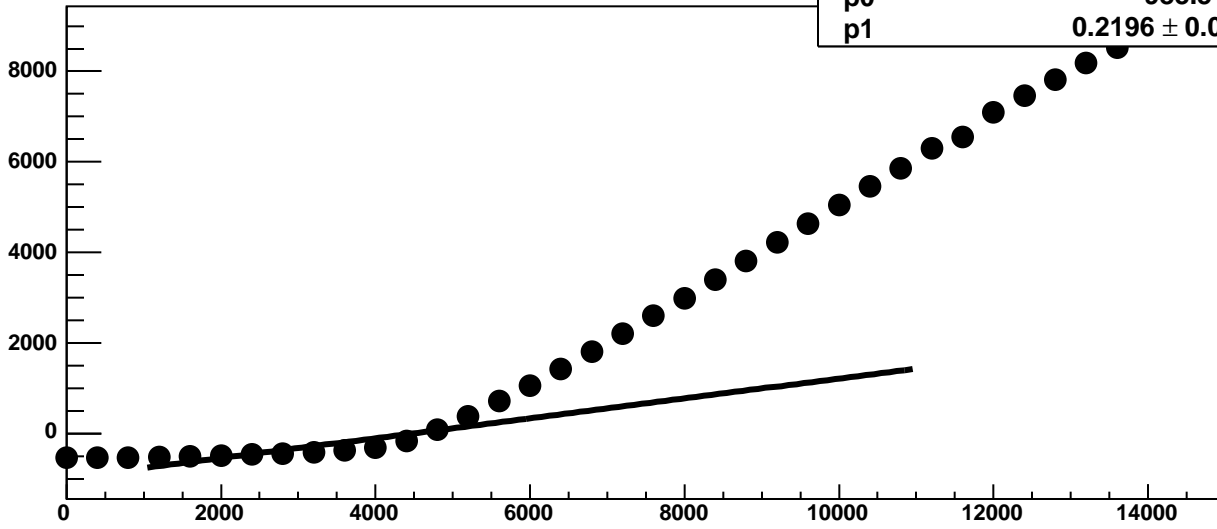


Chip 1, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

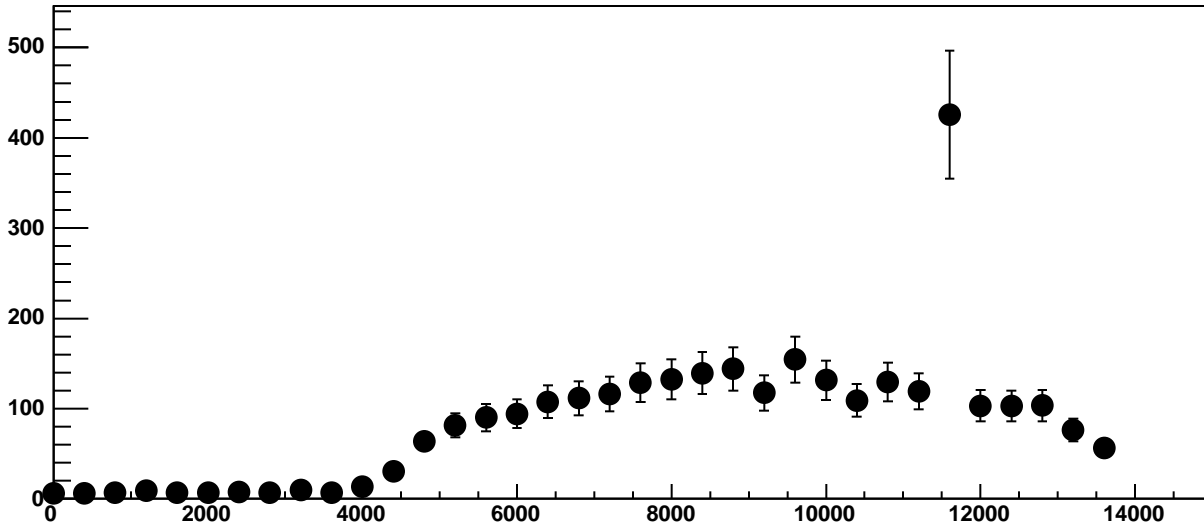


Chip 1, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC

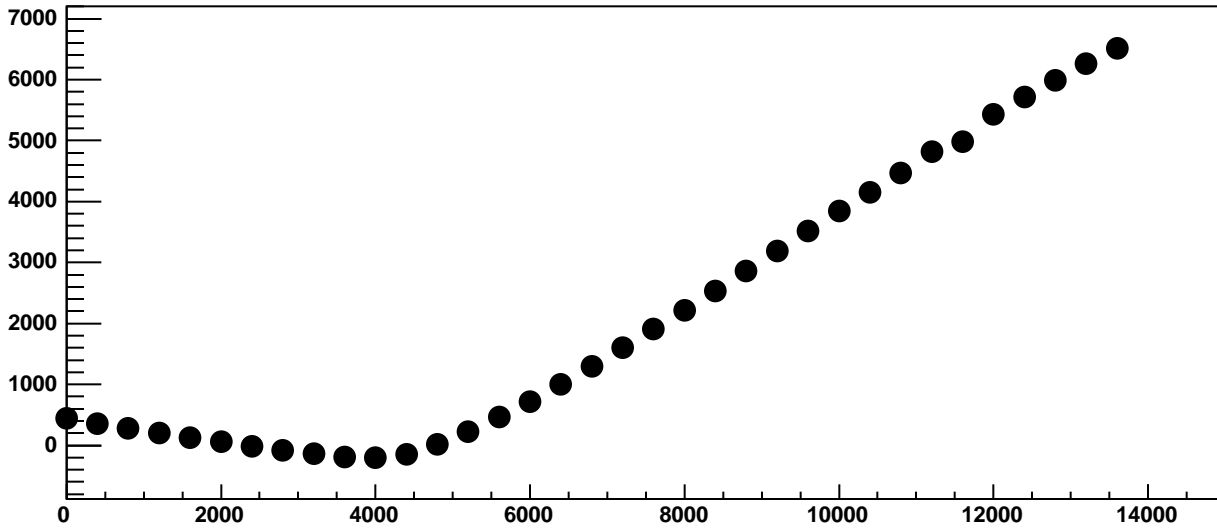
χ^2 / ndf 1.654e+05 / 23
p0 -983.5 ± 1.777
p1 0.2196 ± 0.0006471



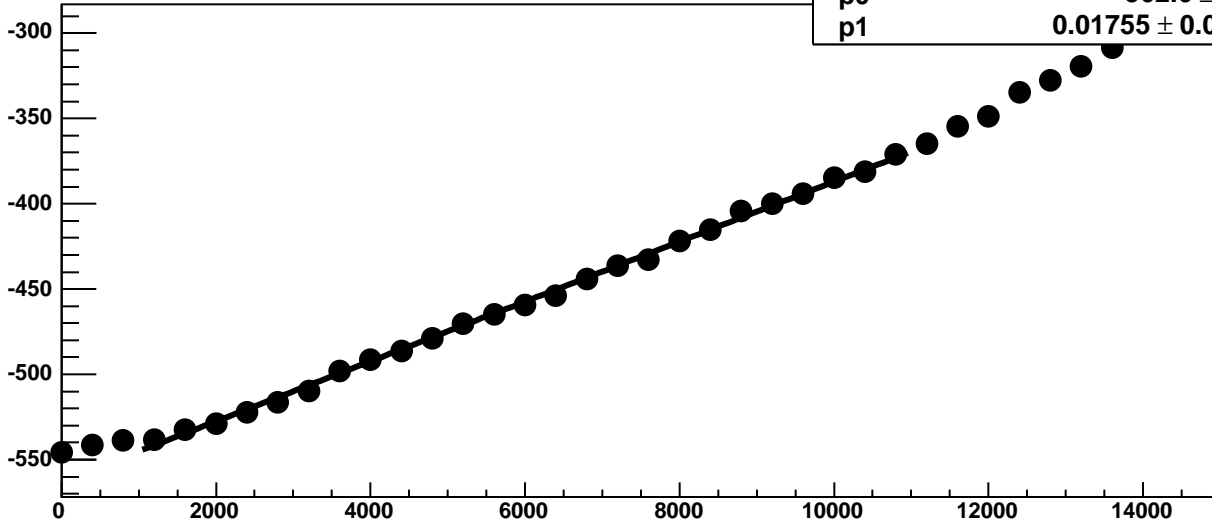
Chip 1, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC

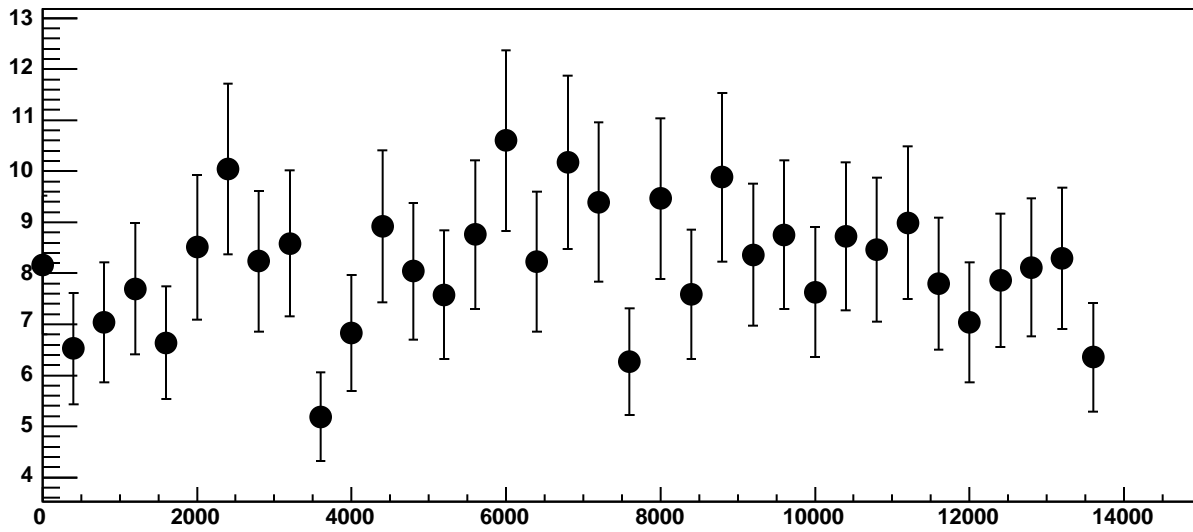


Chip 1, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

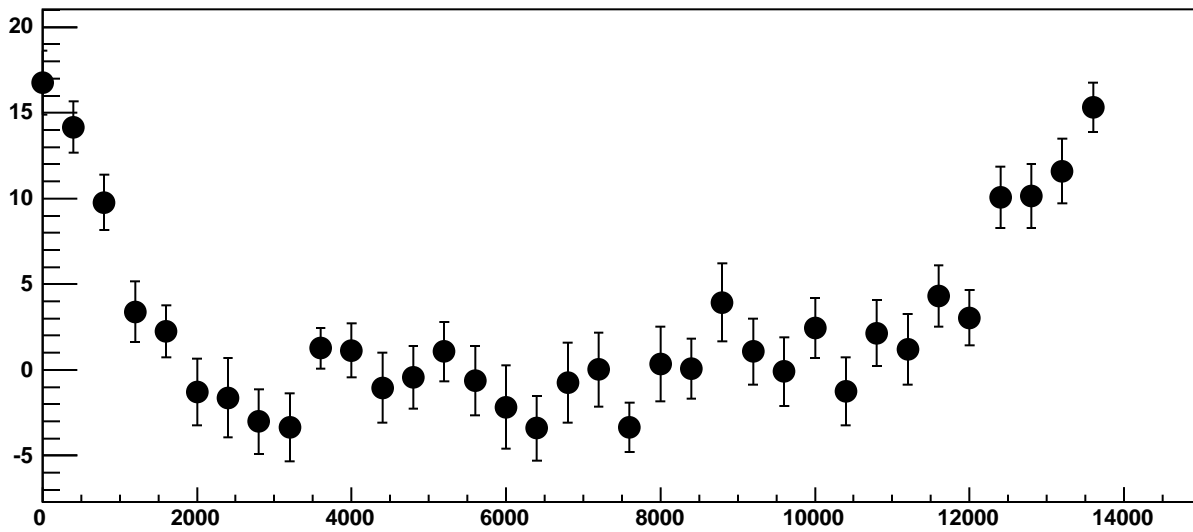


χ^2 / ndf 31.29 / 23
p0 -562.6 ± 0.8174
p1 0.01755 ± 0.0001271

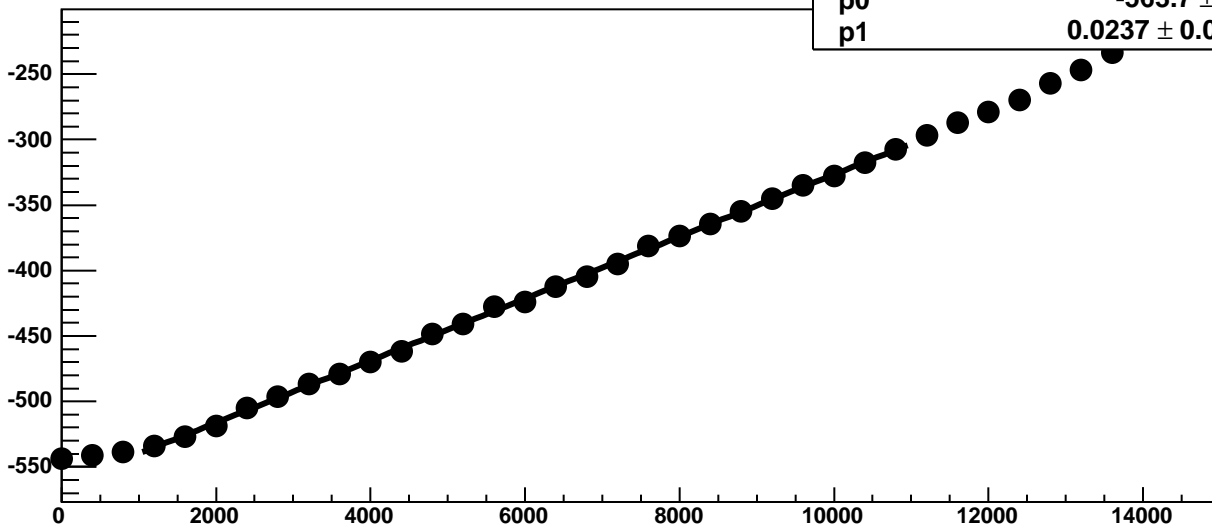
Chip 1, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

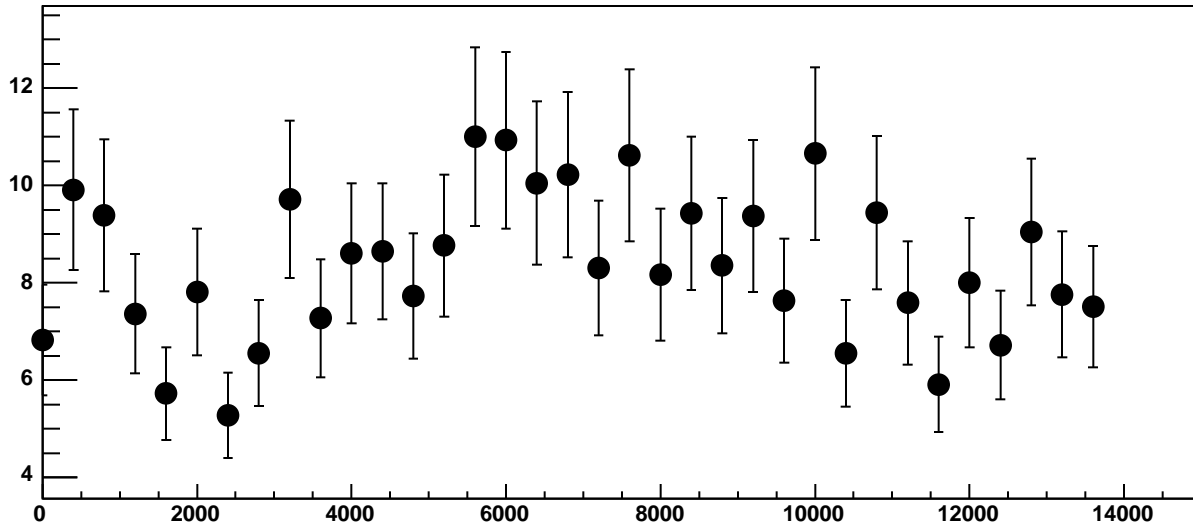


Chip 1, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

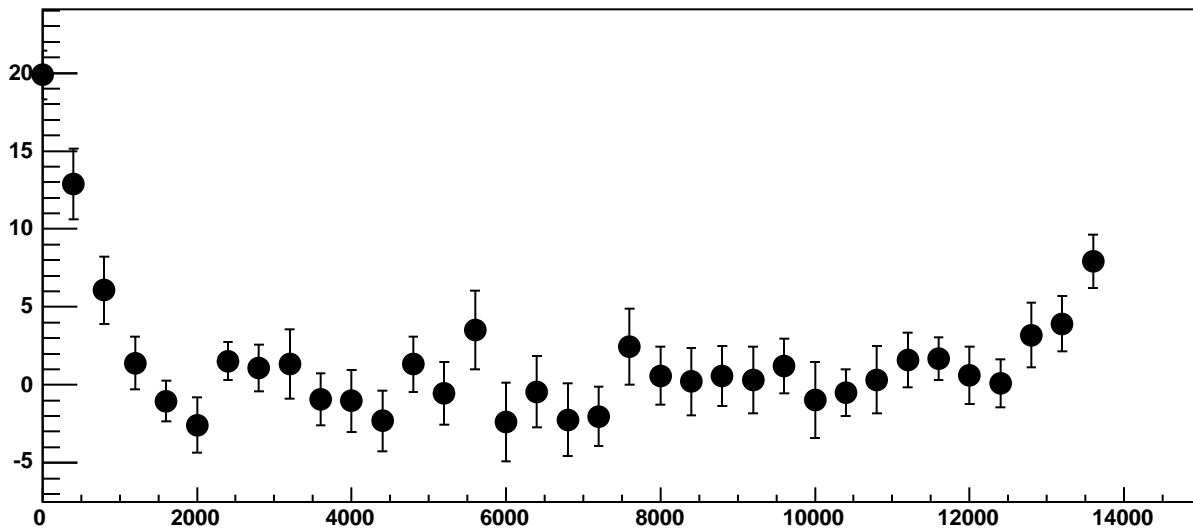


χ^2 / ndf 15.38 / 23
p0 -563.7 ± 0.7496
p1 0.0237 ± 0.0001201

Chip 1, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

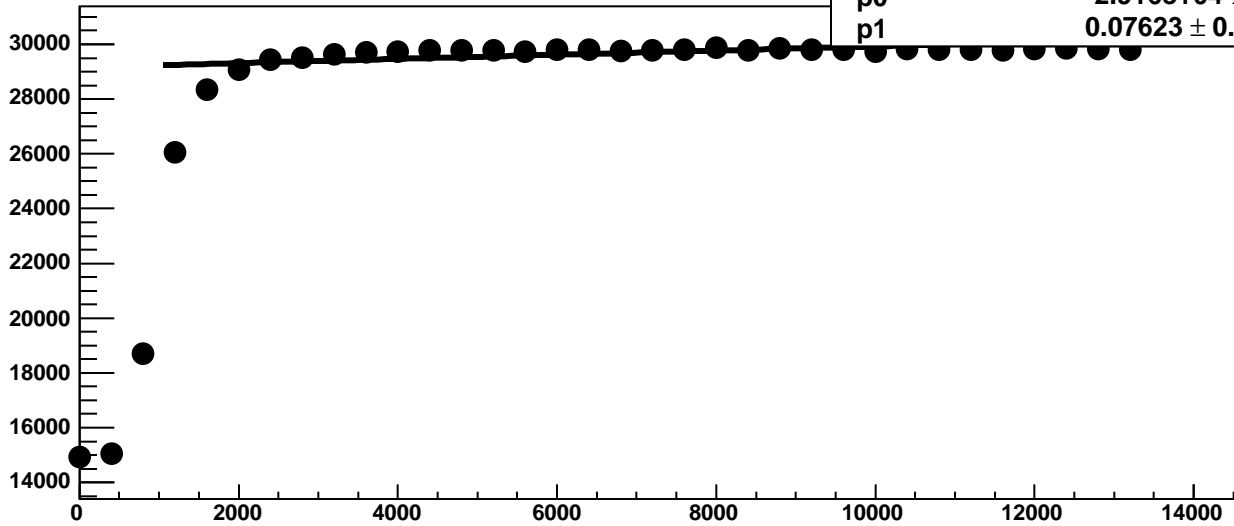
4676 / 23

p0

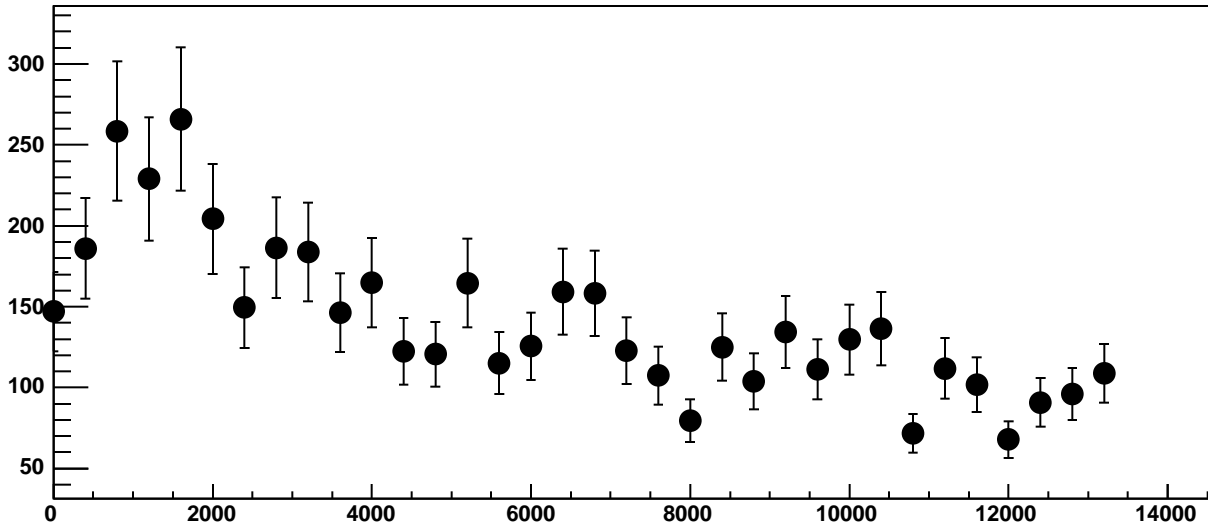
$2.916\text{e}+04 \pm 16.88$

p1

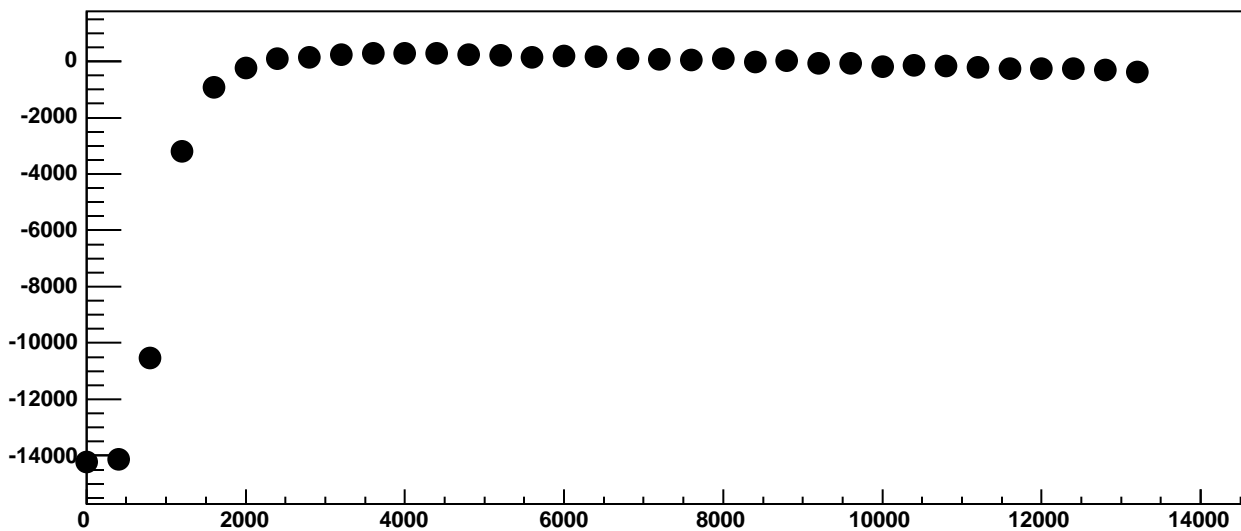
0.07623 ± 0.002201



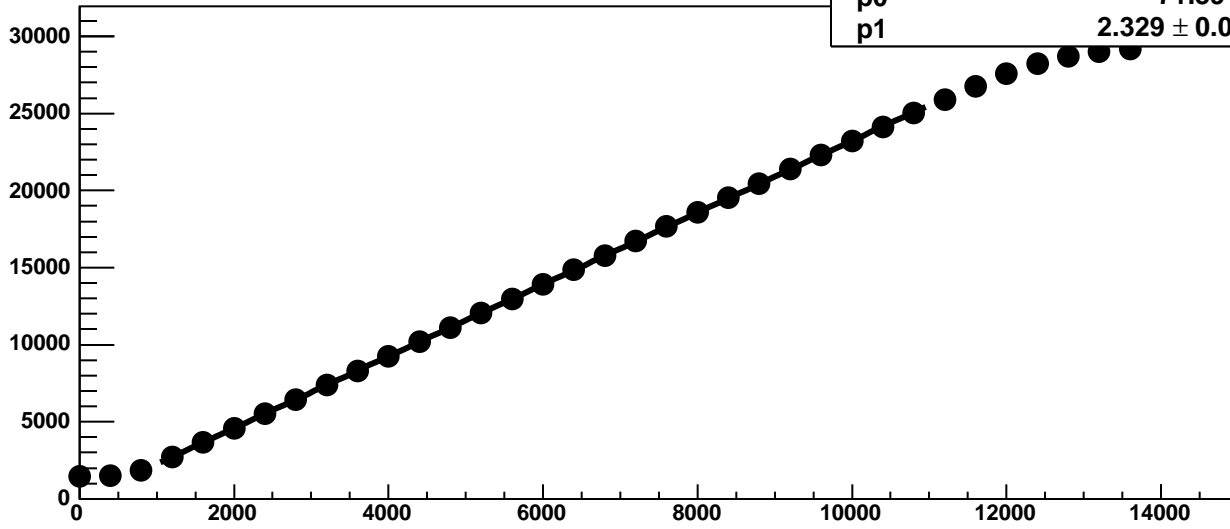
Chip 1, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC

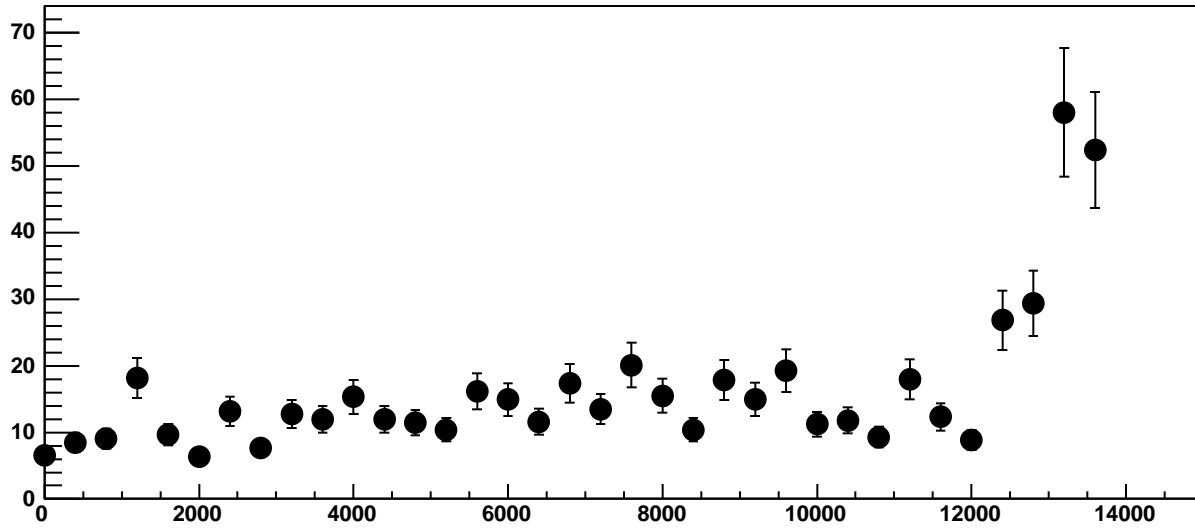


Chip 2, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC

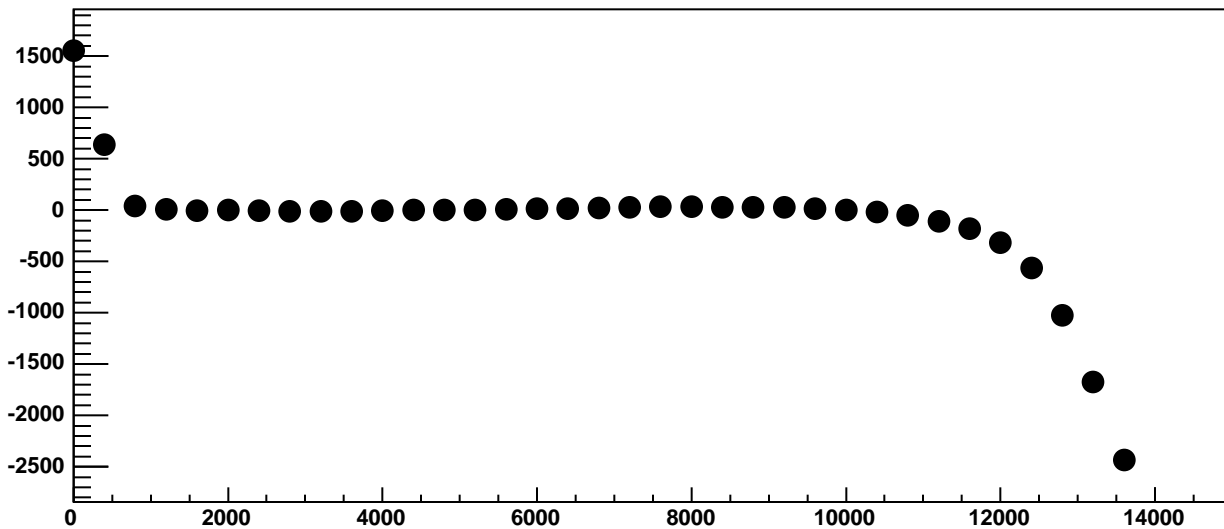


χ^2 / ndf 1179 / 23
p0 -71.39 ± 1.084
p1 2.329 ± 0.0001745

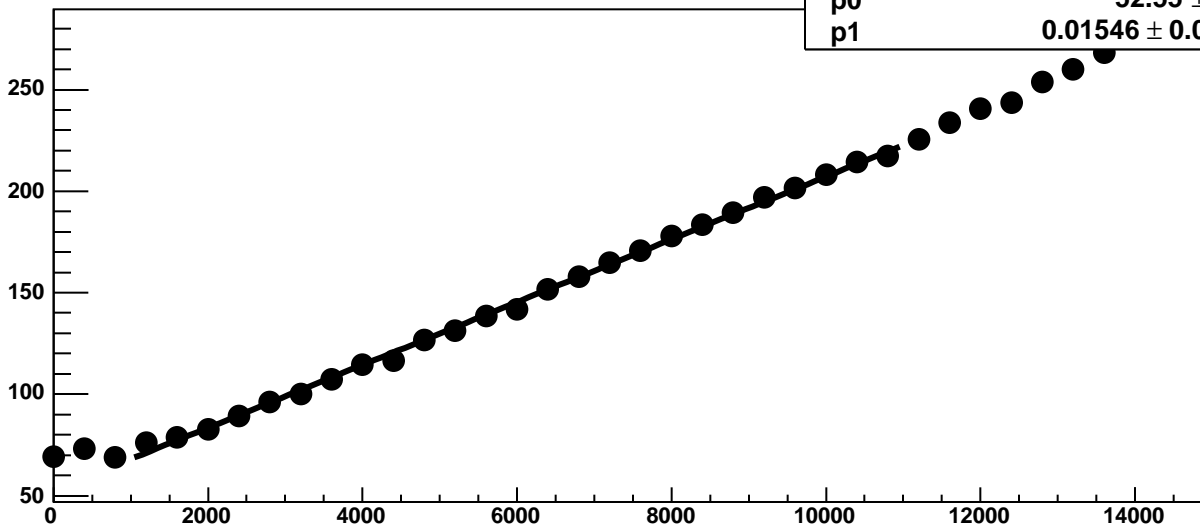
Chip 2, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC

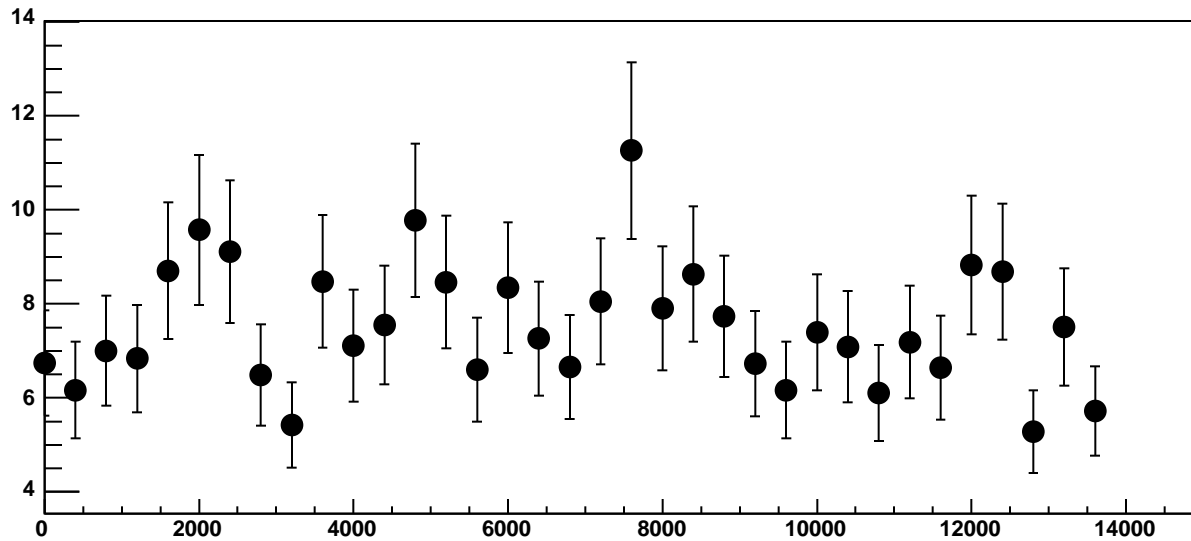


Chip 2, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

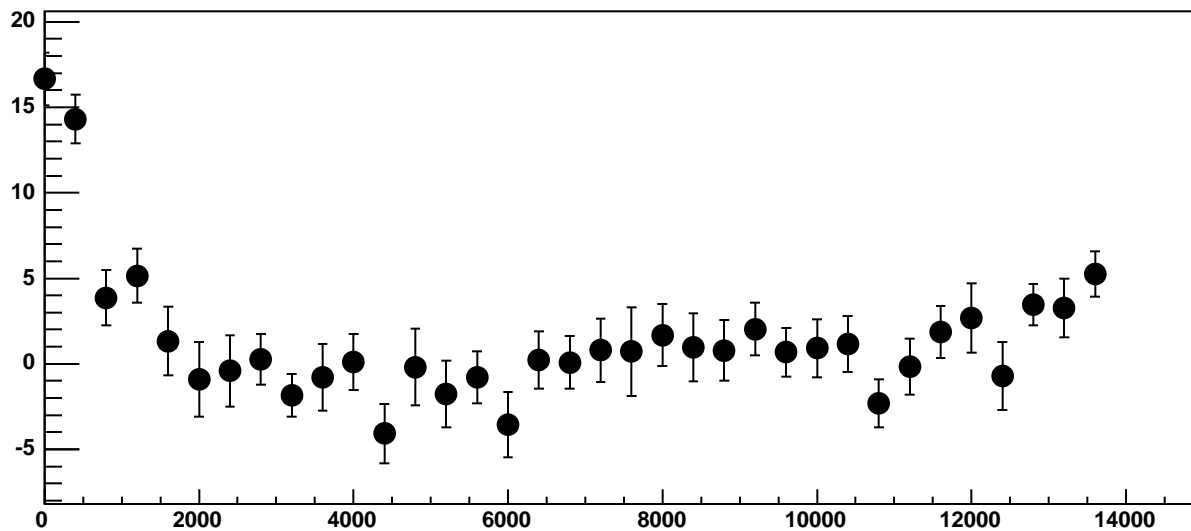


χ^2 / ndf 30.84 / 23
p0 52.55 ± 0.7836
p1 0.01546 ± 0.0001147

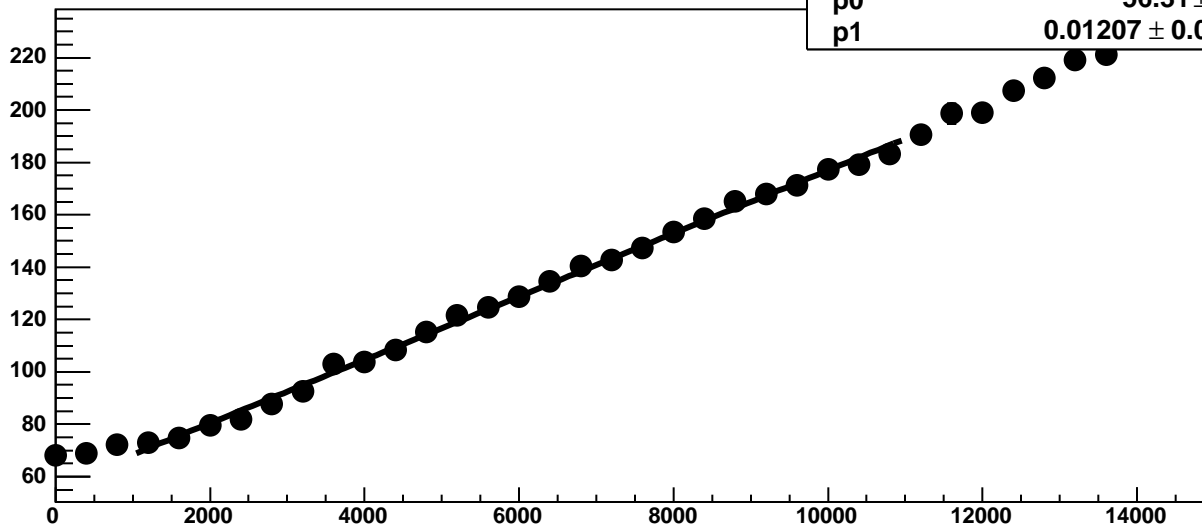
Chip 2, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

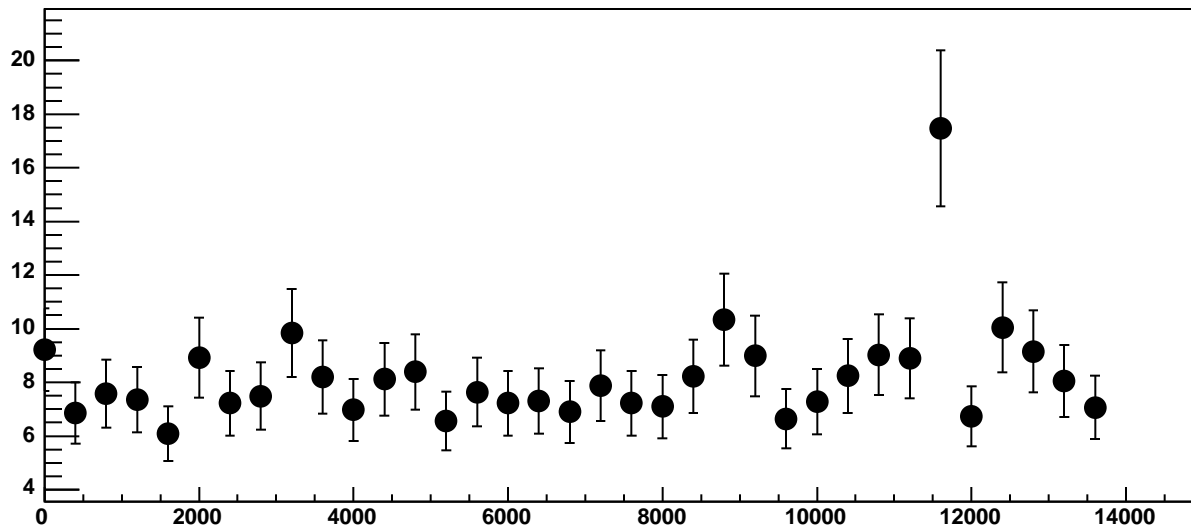


Chip 2, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

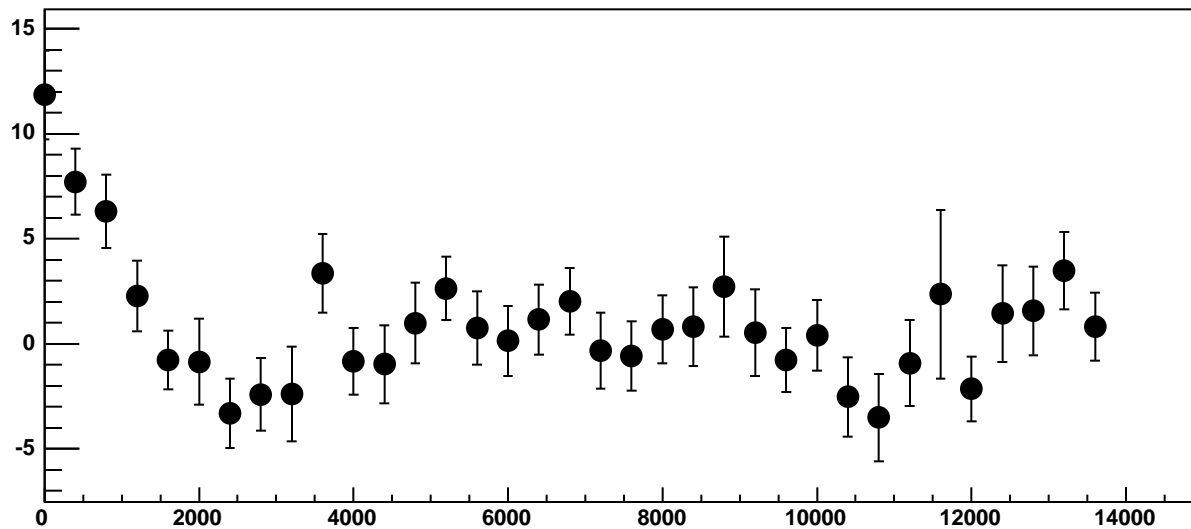


χ^2 / ndf 25.62 / 23
p0 56.31 ± 0.7979
p1 0.01207 ± 0.0001224

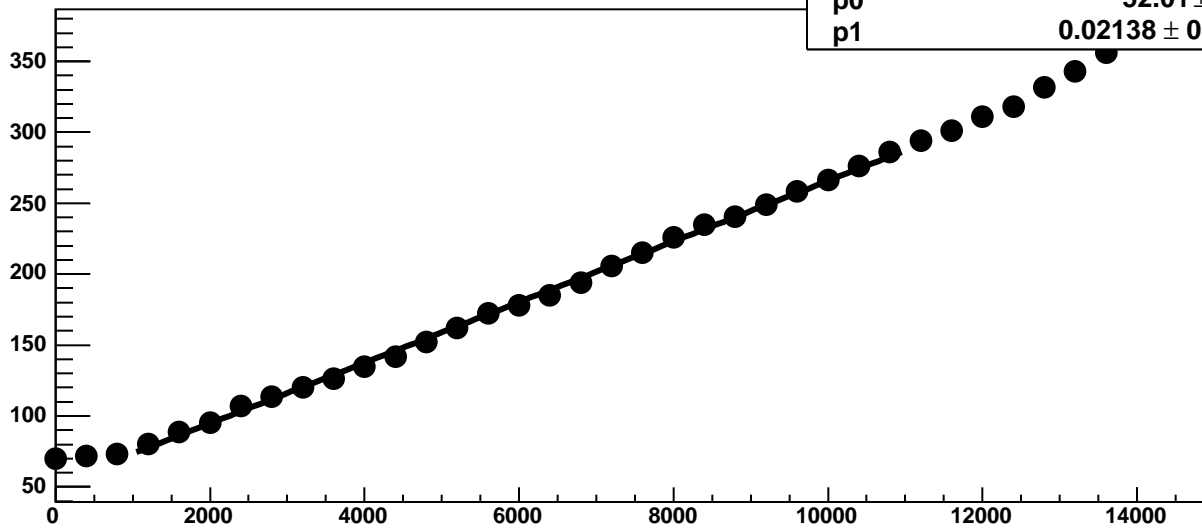
Chip 2, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

42.16 / 23

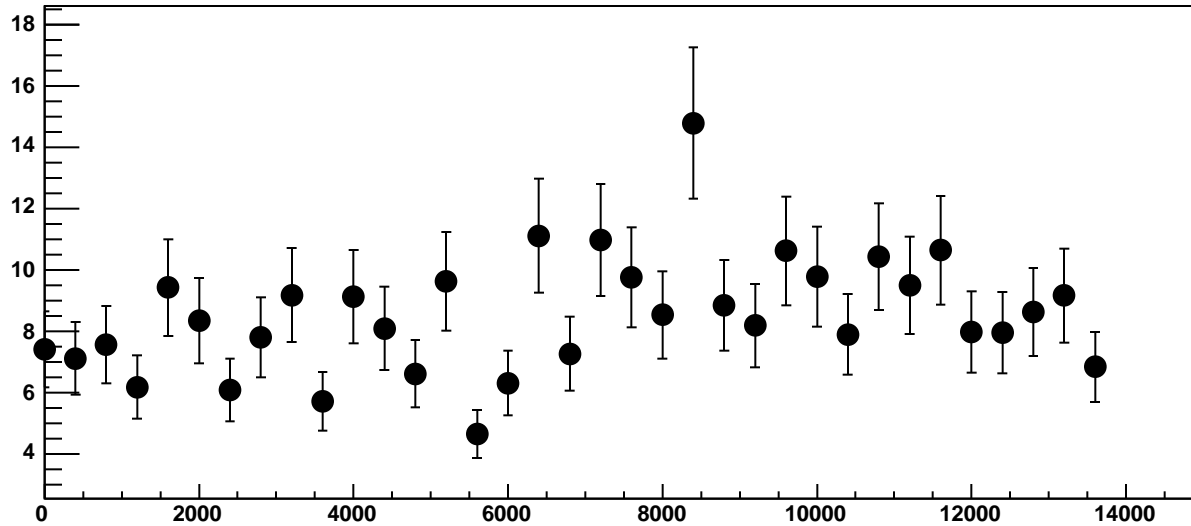
p0

52.01 ± 0.8009

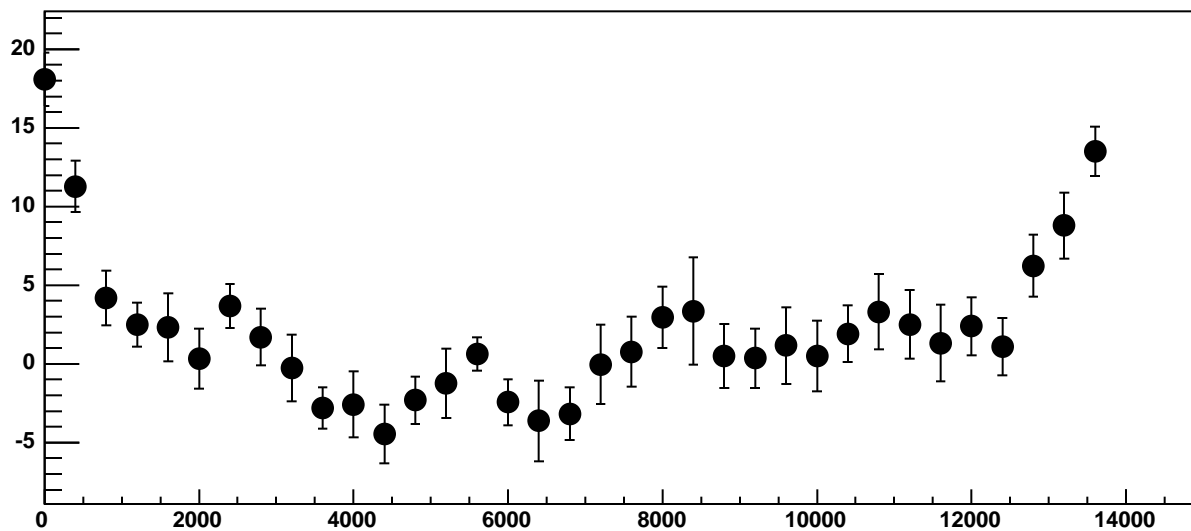
p1

0.02138 ± 0.000132

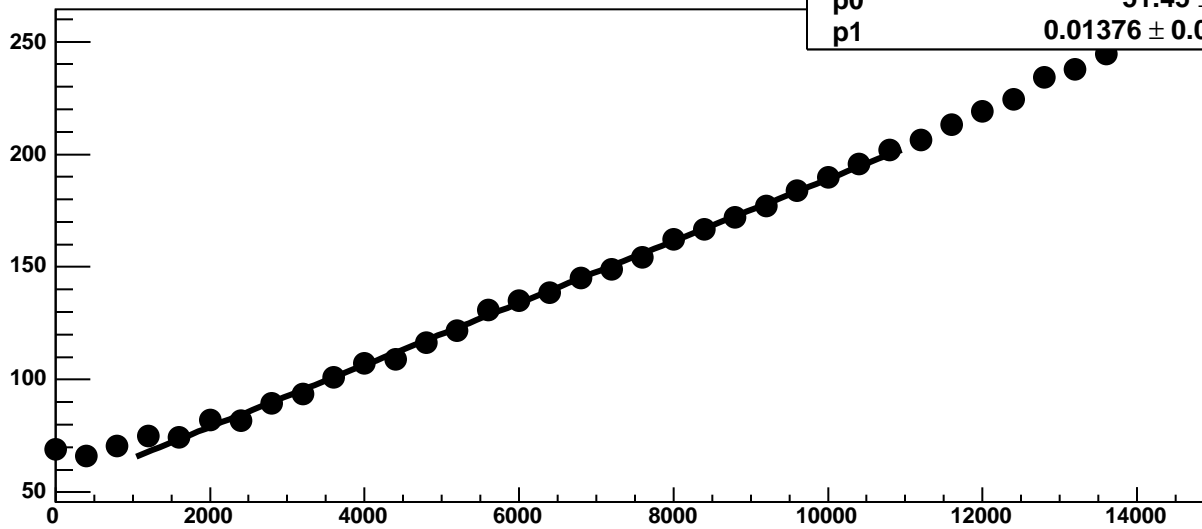
Chip 2, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC

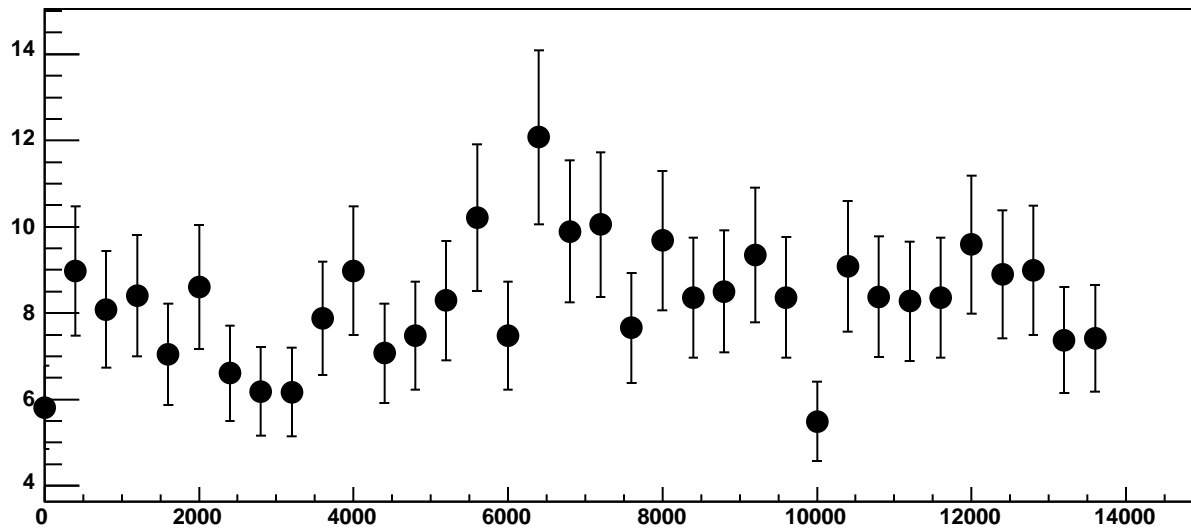


Chip 2, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

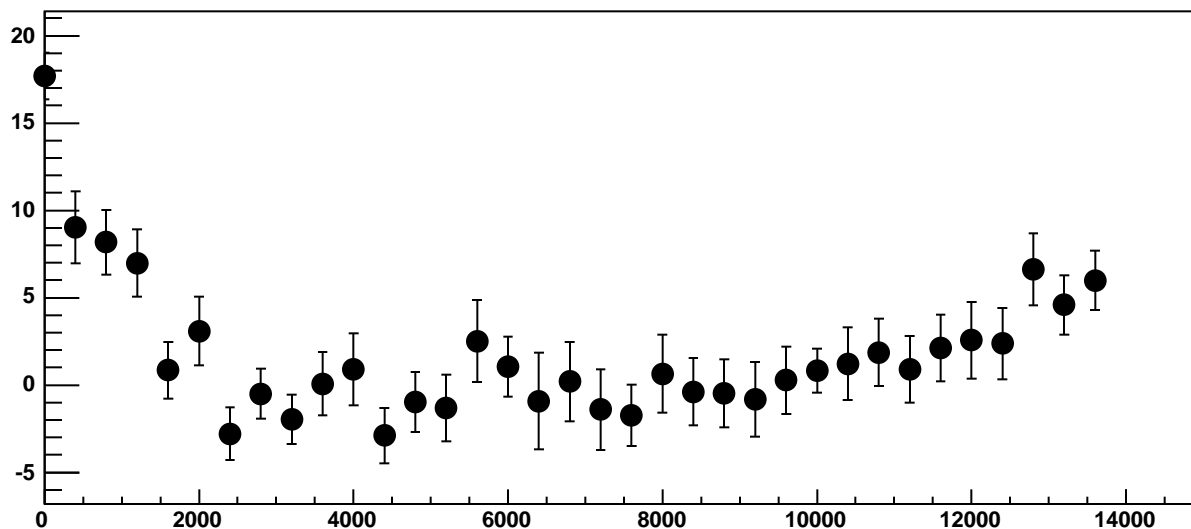


χ^2 / ndf 30.59 / 23
p0 51.45 ± 0.7841
p1 0.01376 ± 0.0001208

Chip 2, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

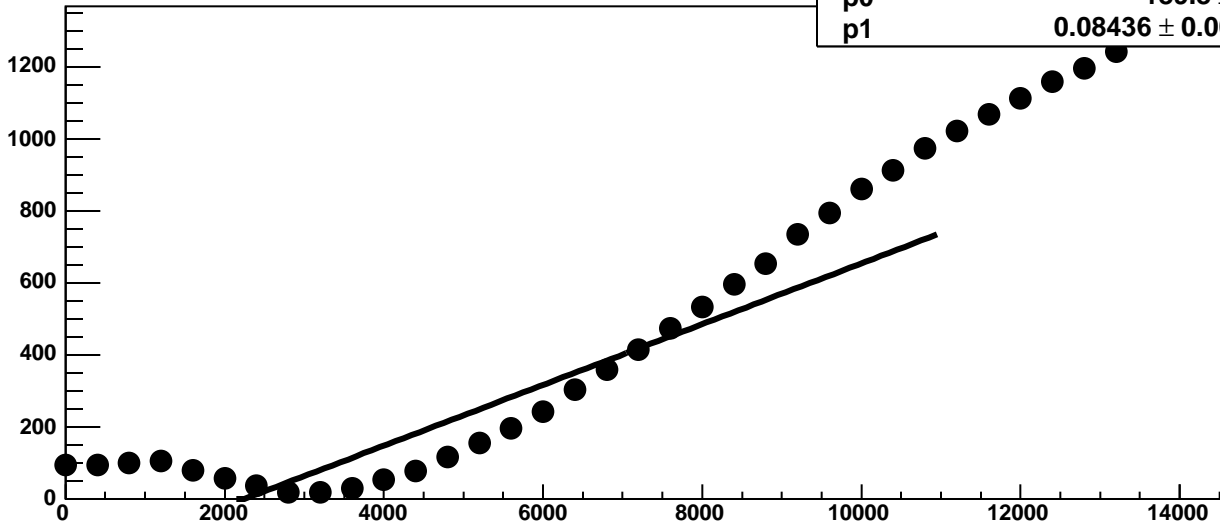


Chip 2, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

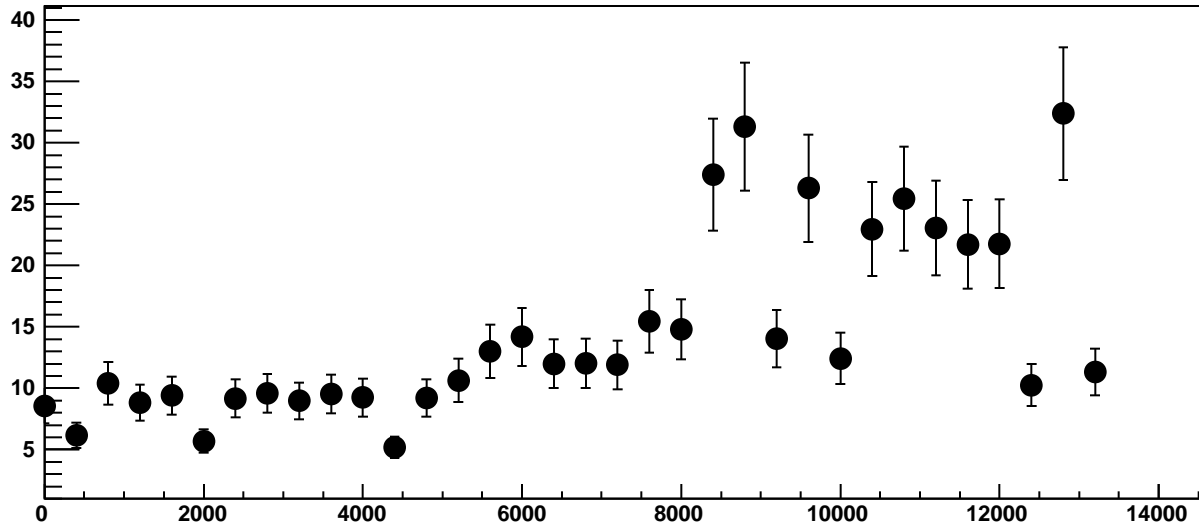


Chip 2, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

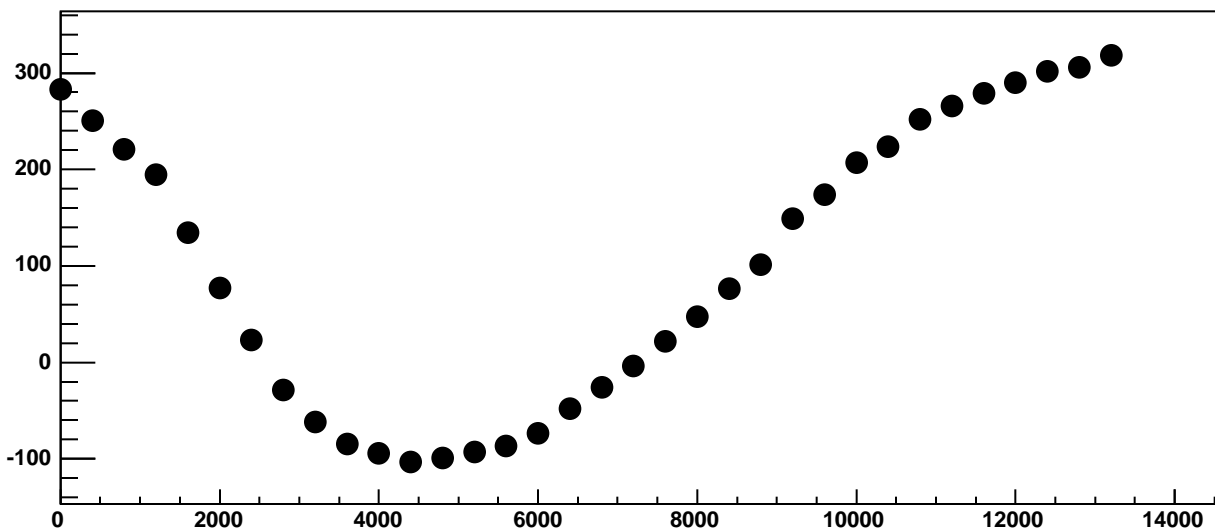
χ^2 / ndf 4.673e+04 / 23
p0 -189.8 ± 1.002
p1 0.08436 ± 0.0002023



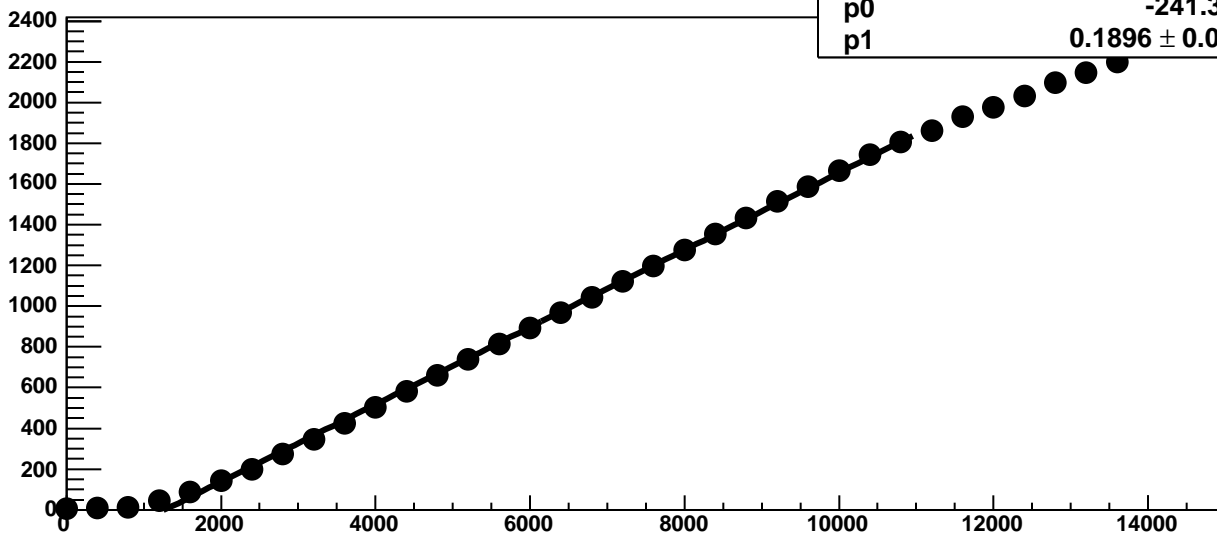
Chip 2, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

675 / 23

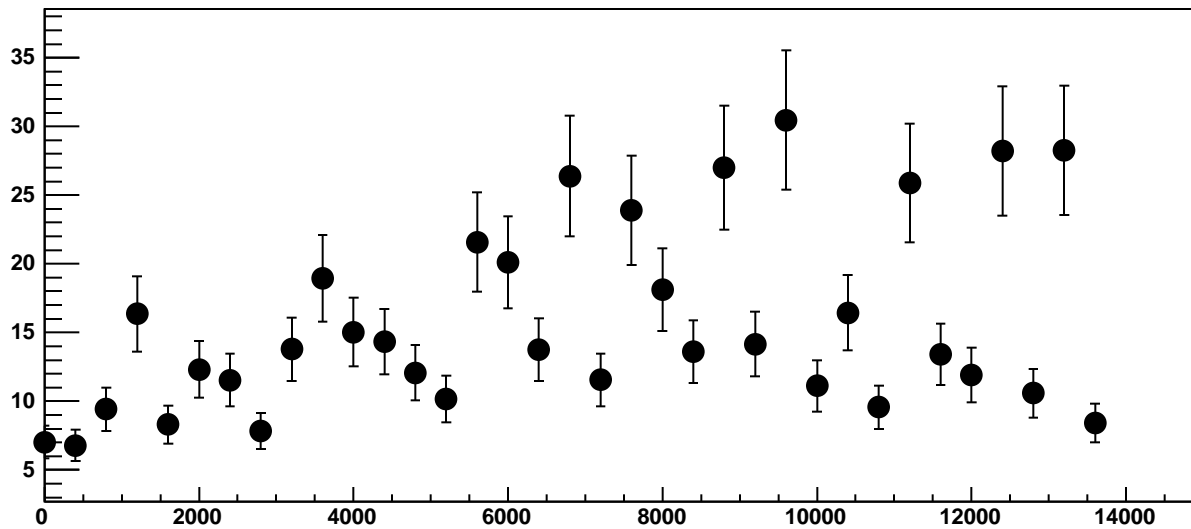
p0

-241.3 ± 1.22

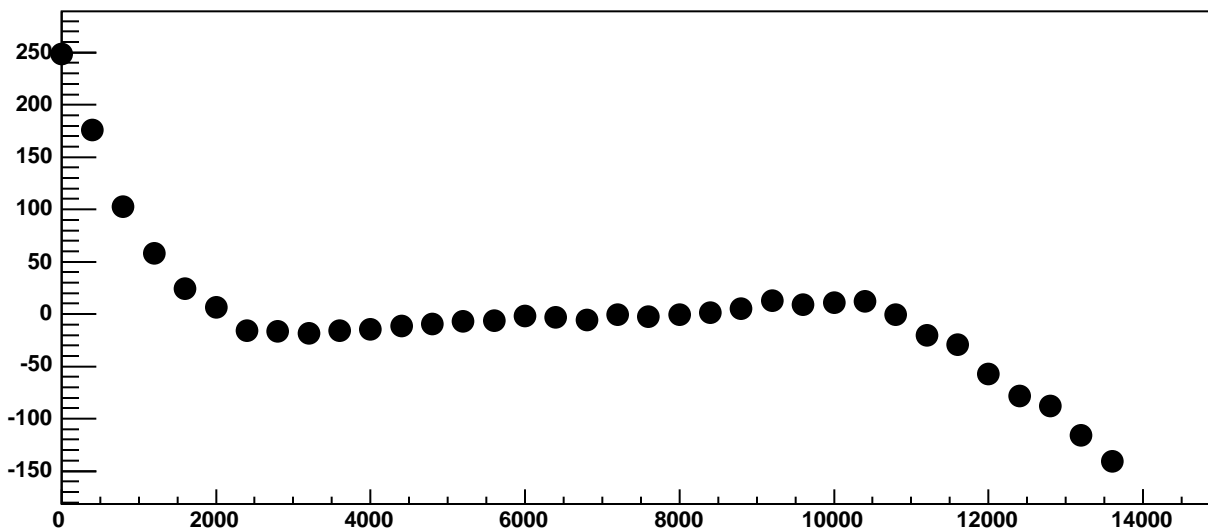
p1

0.1896 ± 0.0001952

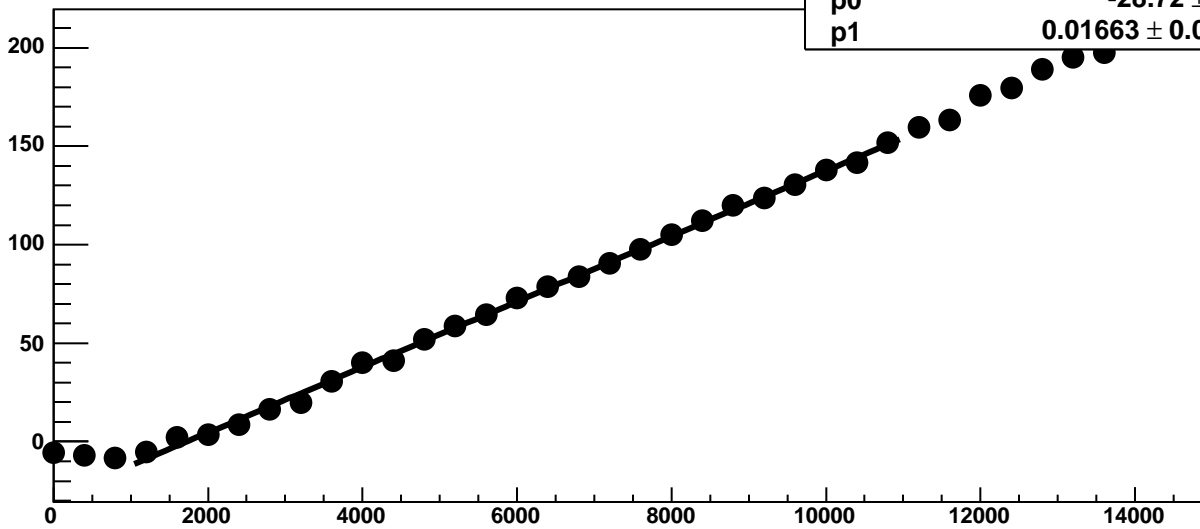
Chip 2, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC

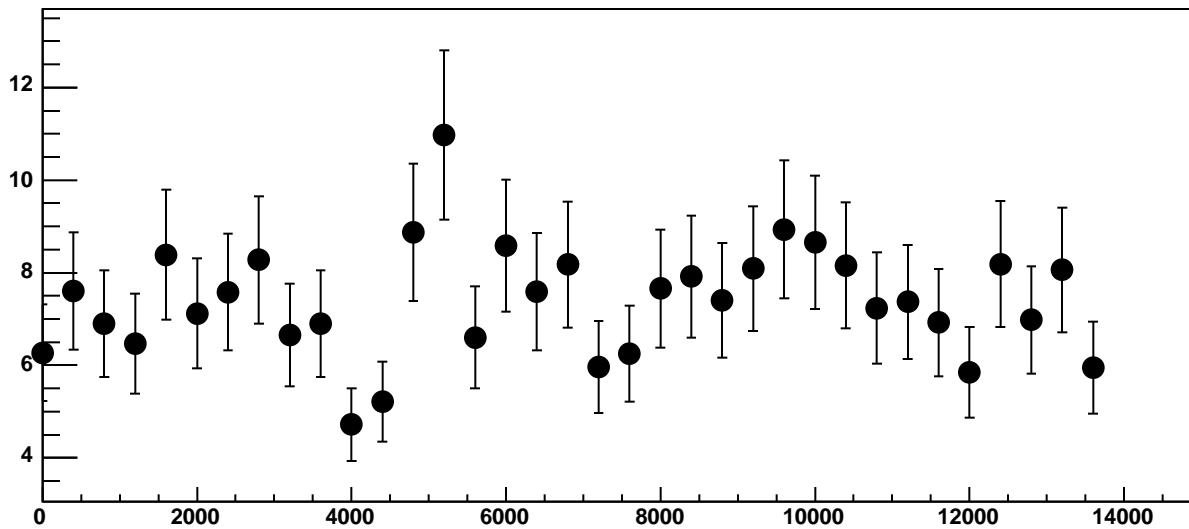


Chip 2, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

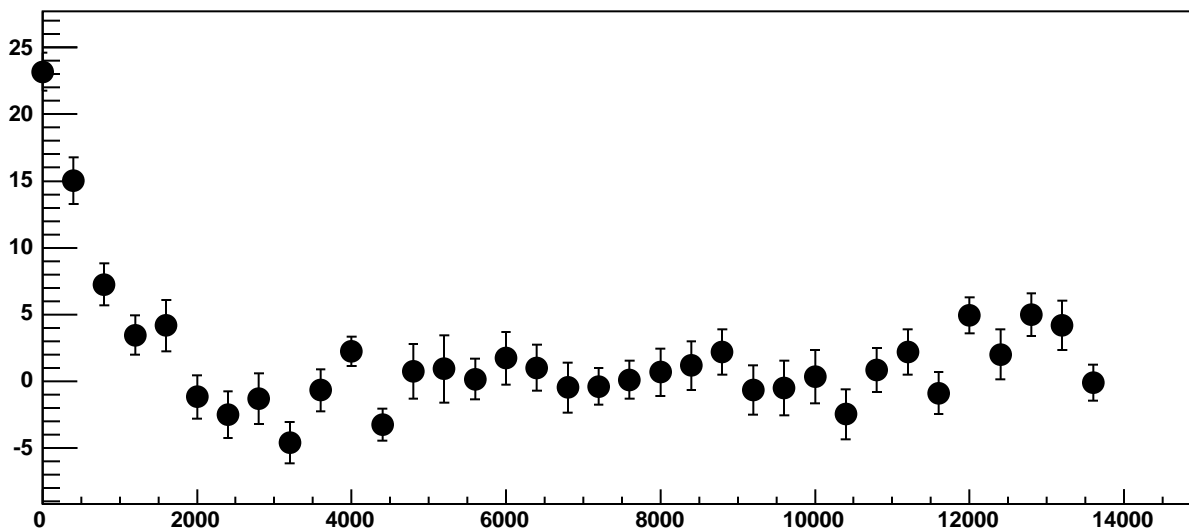


χ^2 / ndf 40.11 / 23
p0 -28.72 ± 0.7522
p1 0.01663 ± 0.0001183

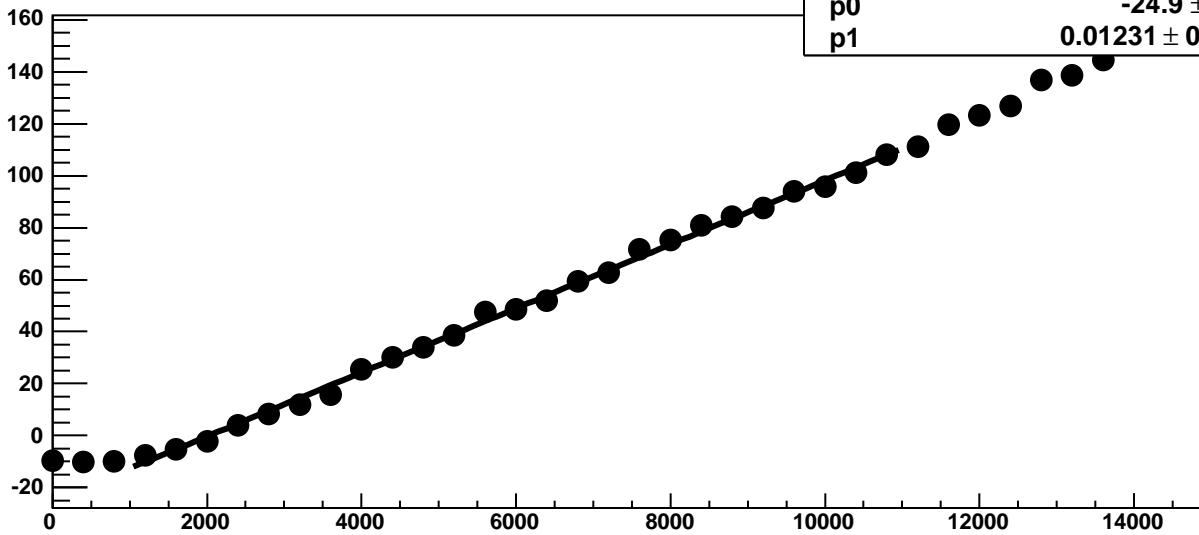
Chip 2, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

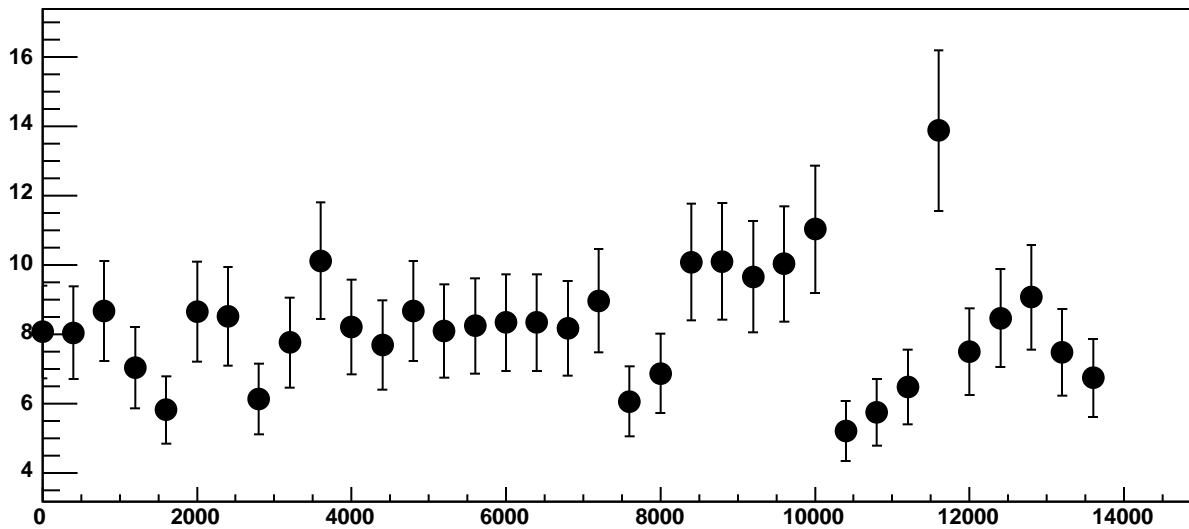


Chip 2, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

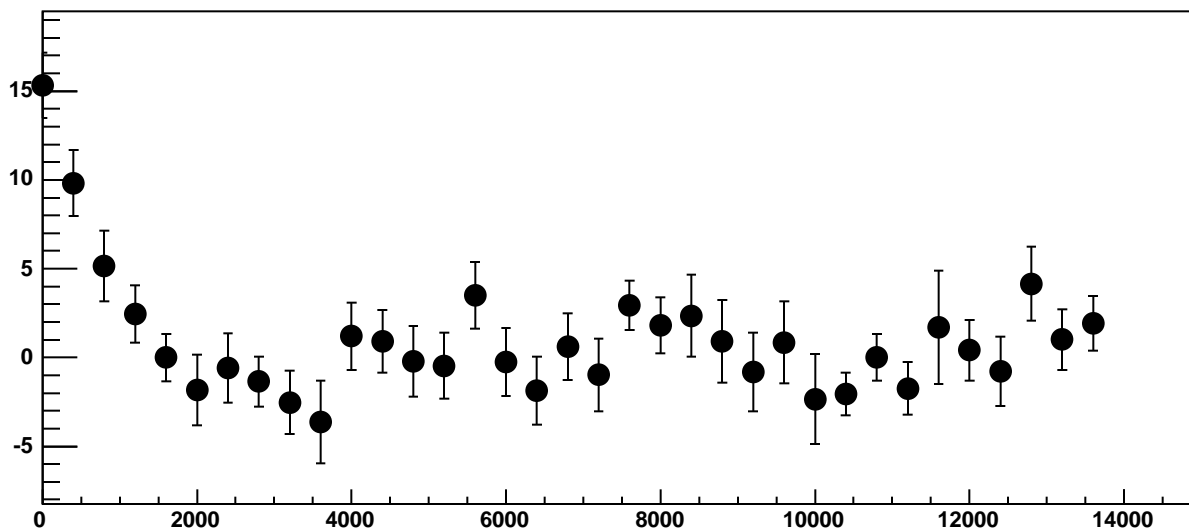


χ^2 / ndf 25.14 / 23
p0 -24.9 ± 0.7649
p1 0.01231 ± 0.000113

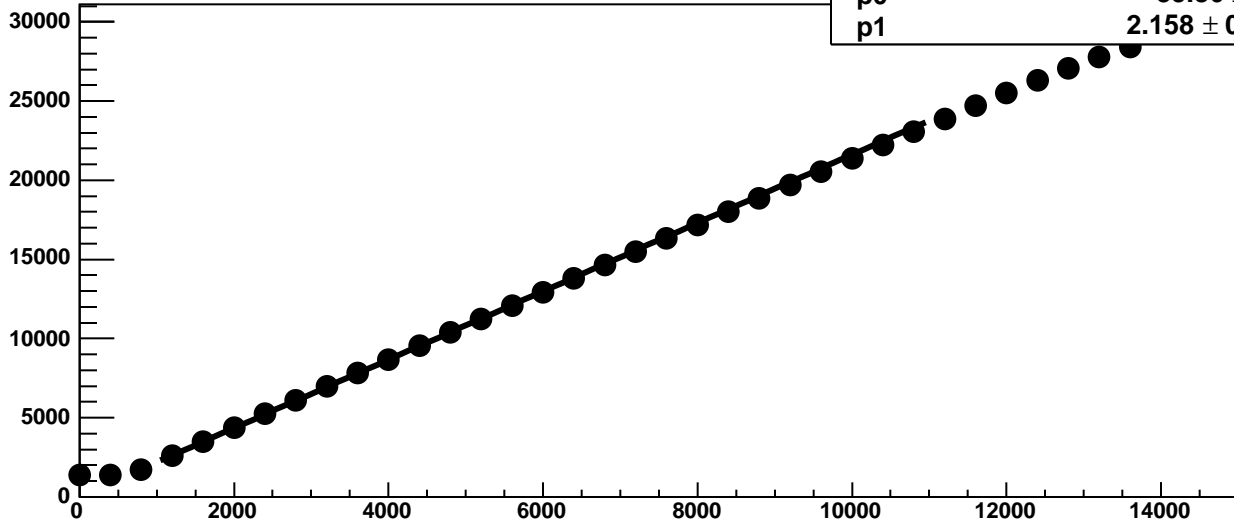
Chip 2, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



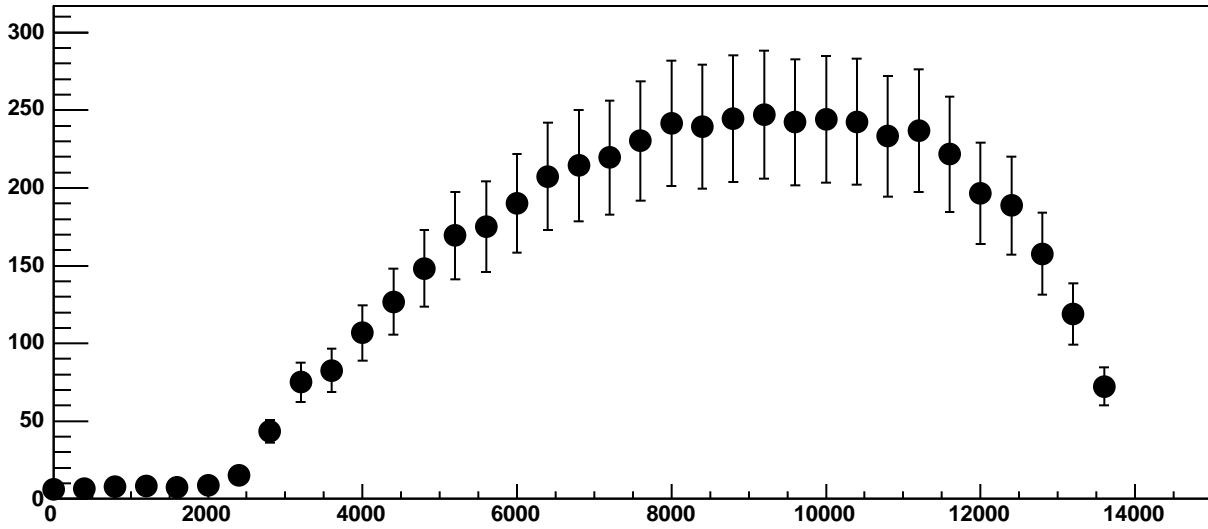
Chip 2, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



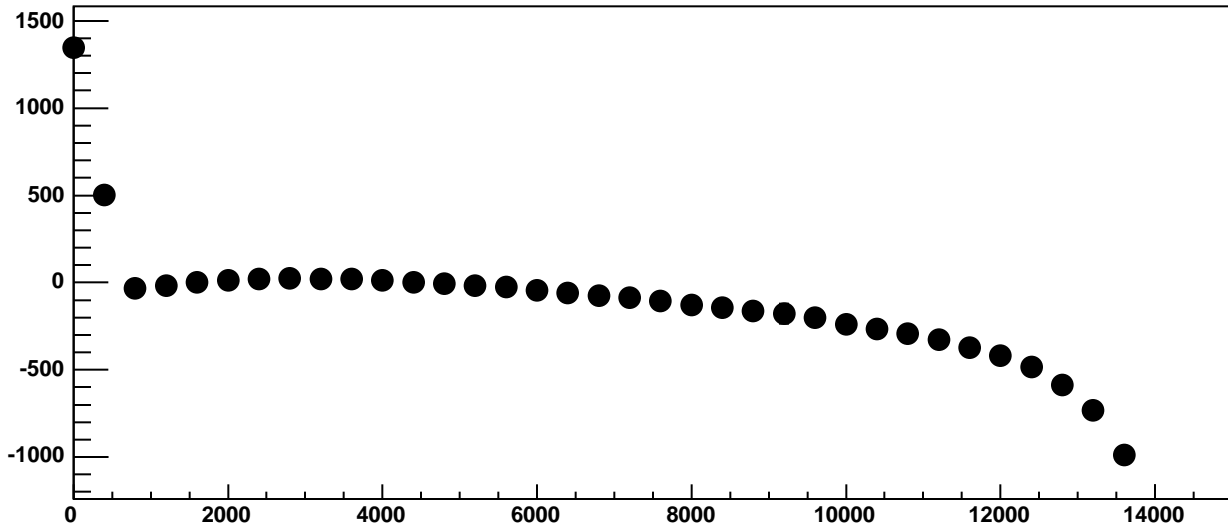
Chip 2, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC



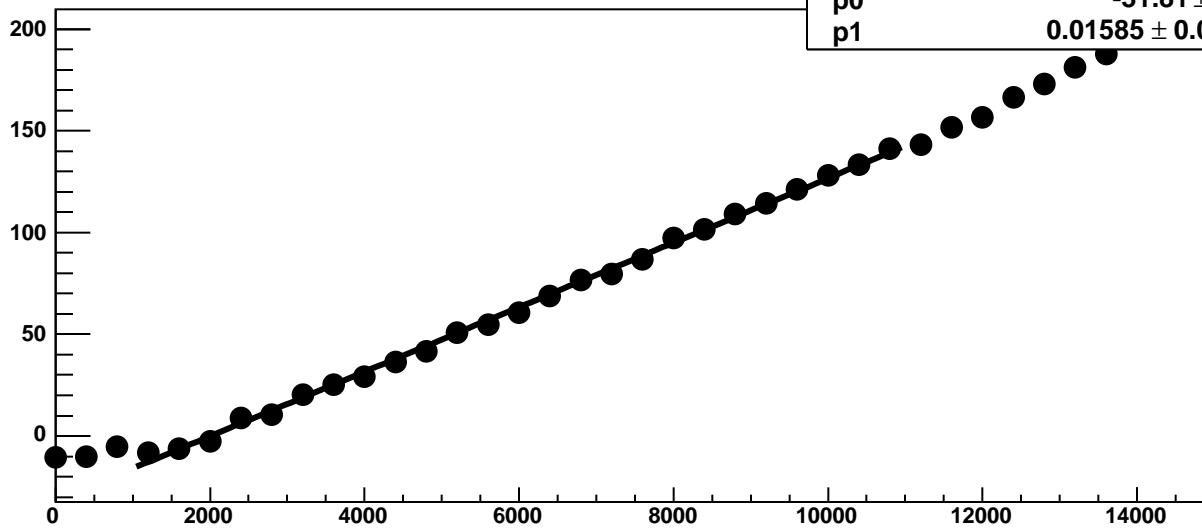
Chip 2, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



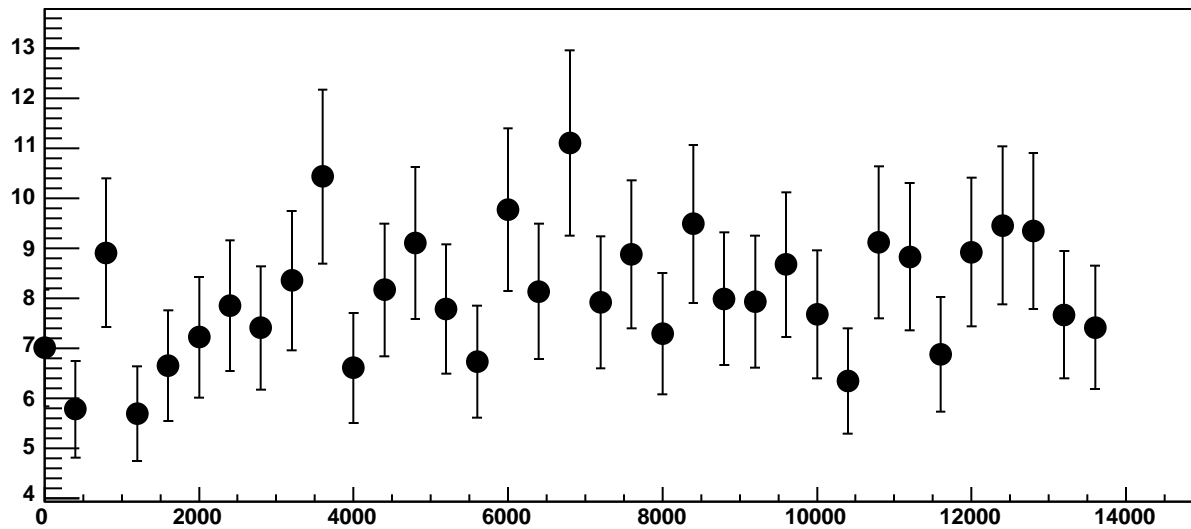
Chip 2, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC



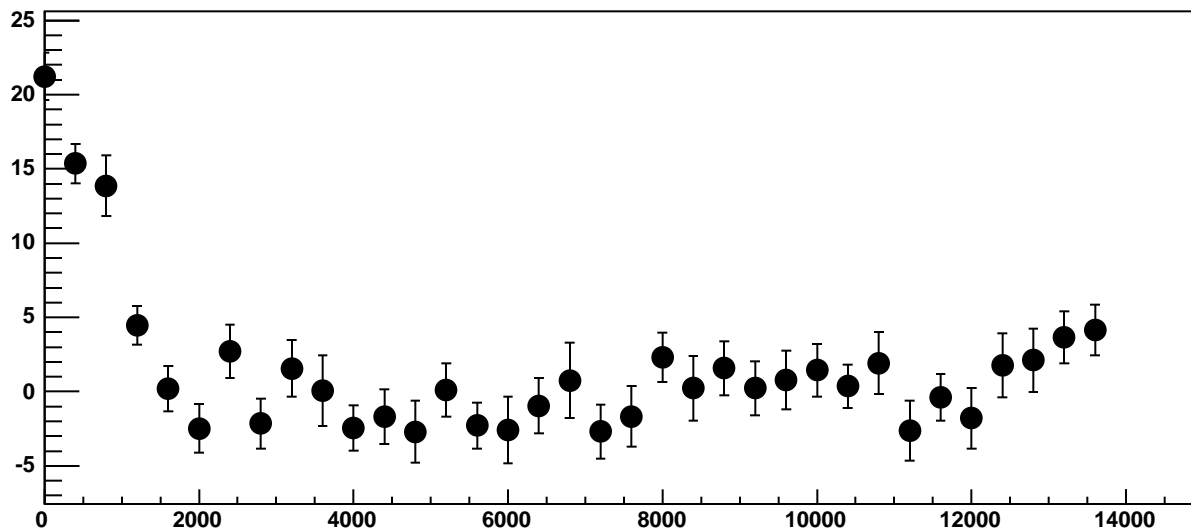
Chip 2, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



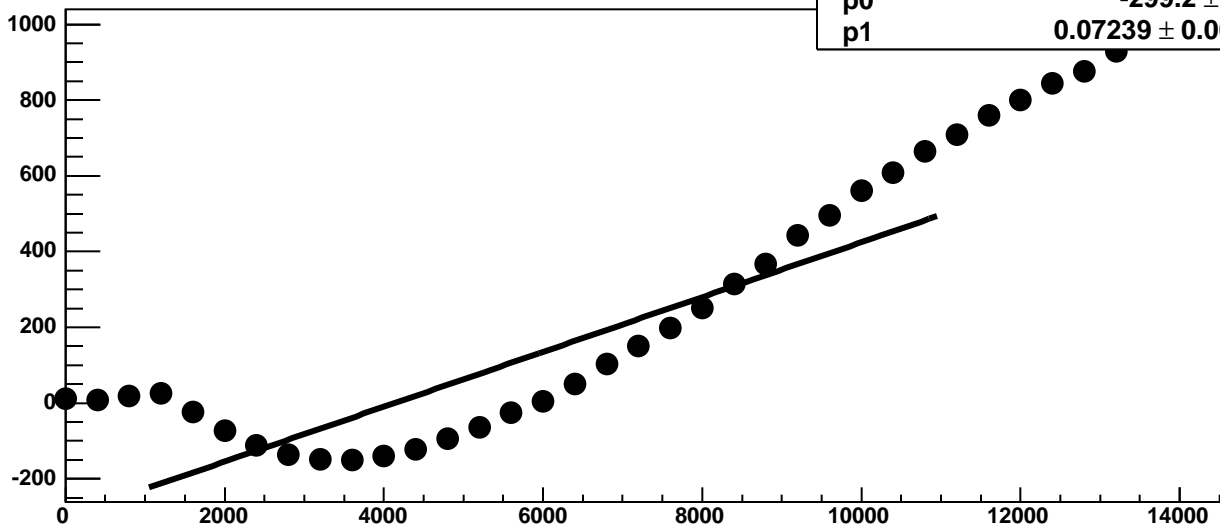
Chip 2, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



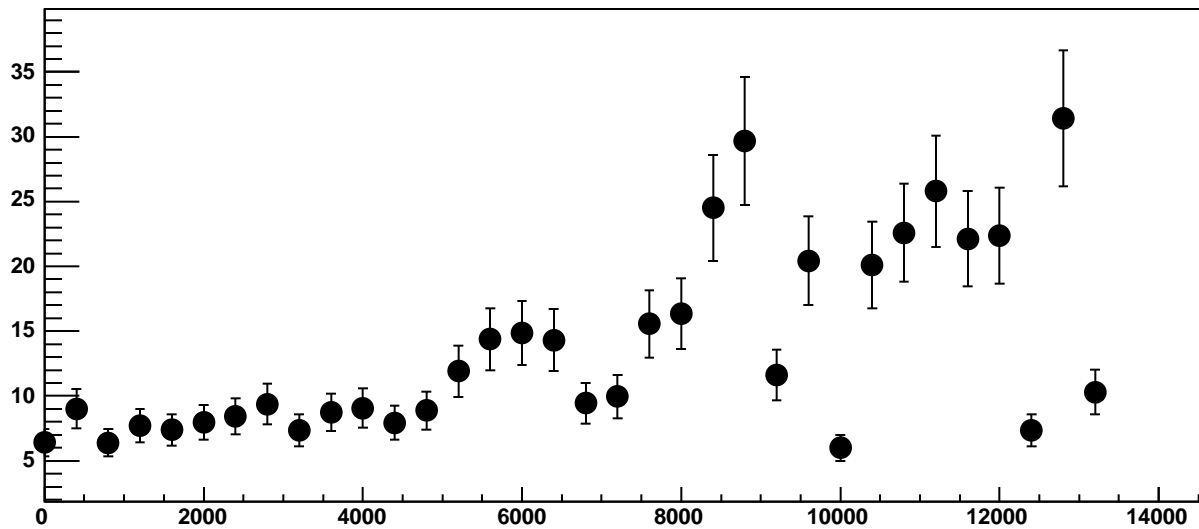
Chip 2, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



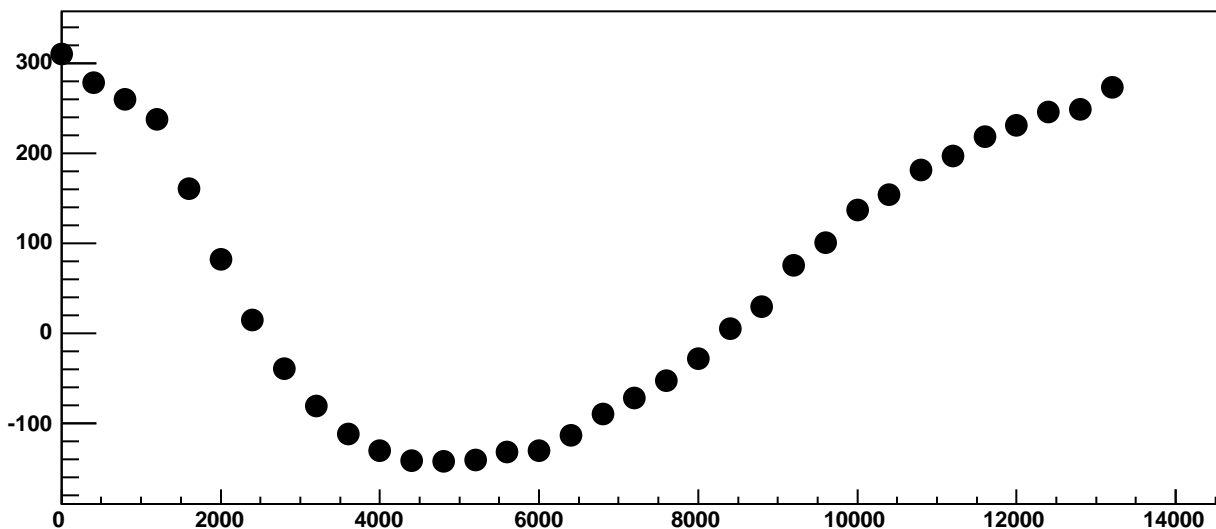
Chip 2, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



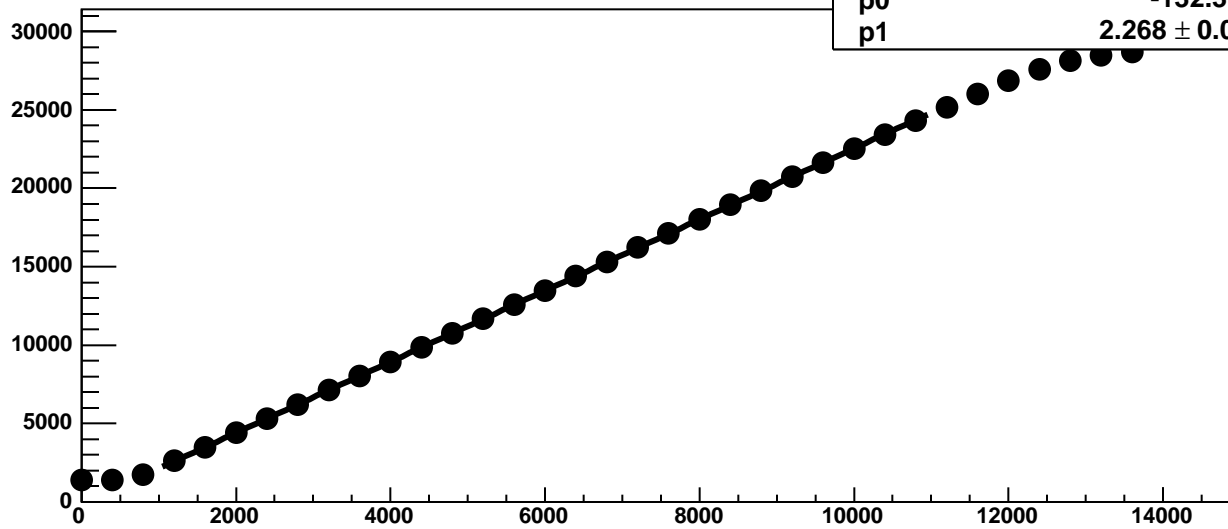
Chip 2, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



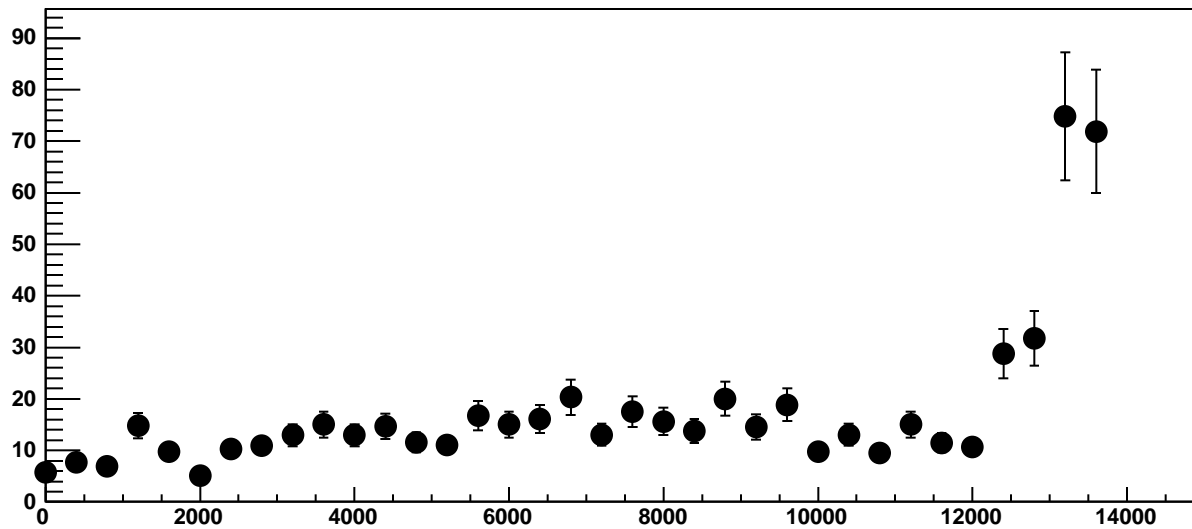
Chip 2, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



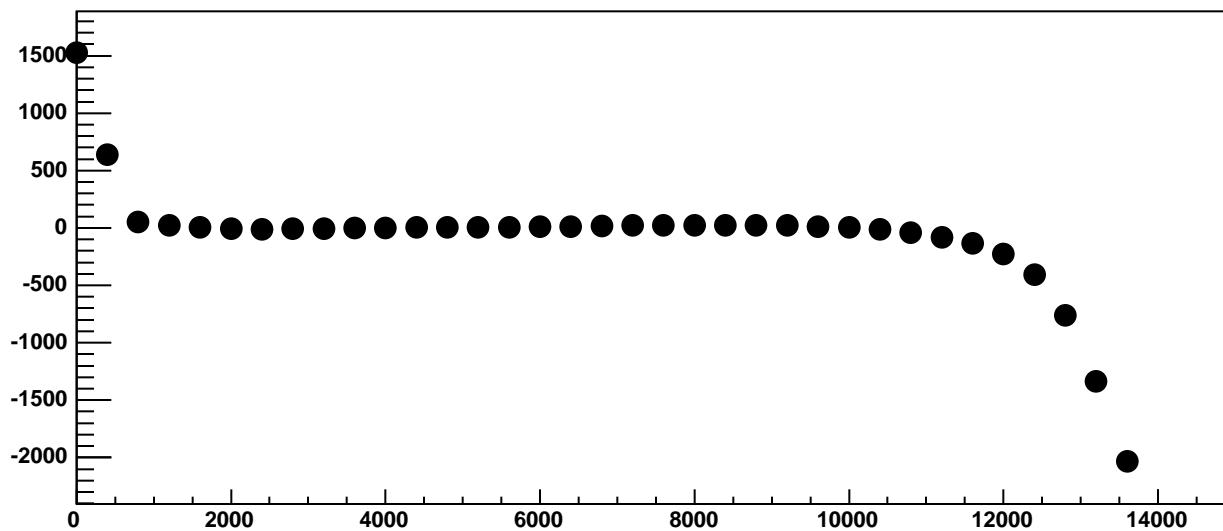
Chip 2, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC



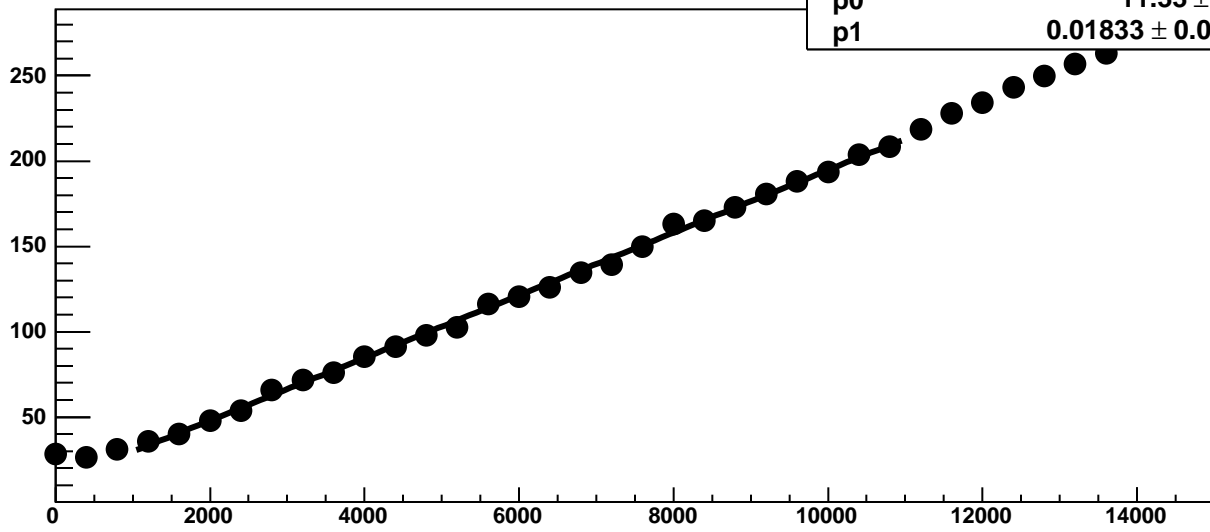
Chip 2, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

41.24 / 23

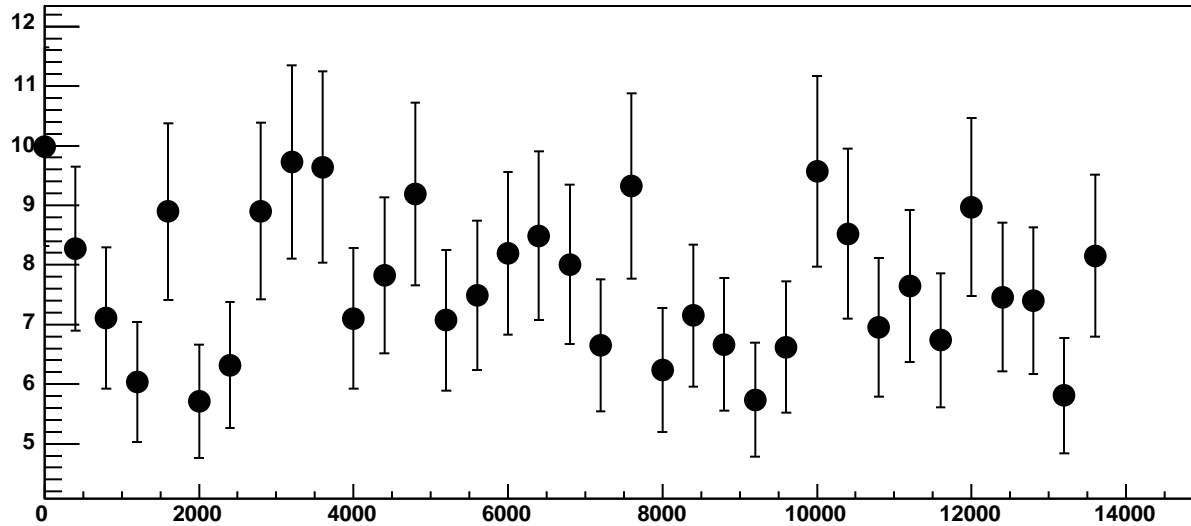
p0

11.33 ± 0.7606

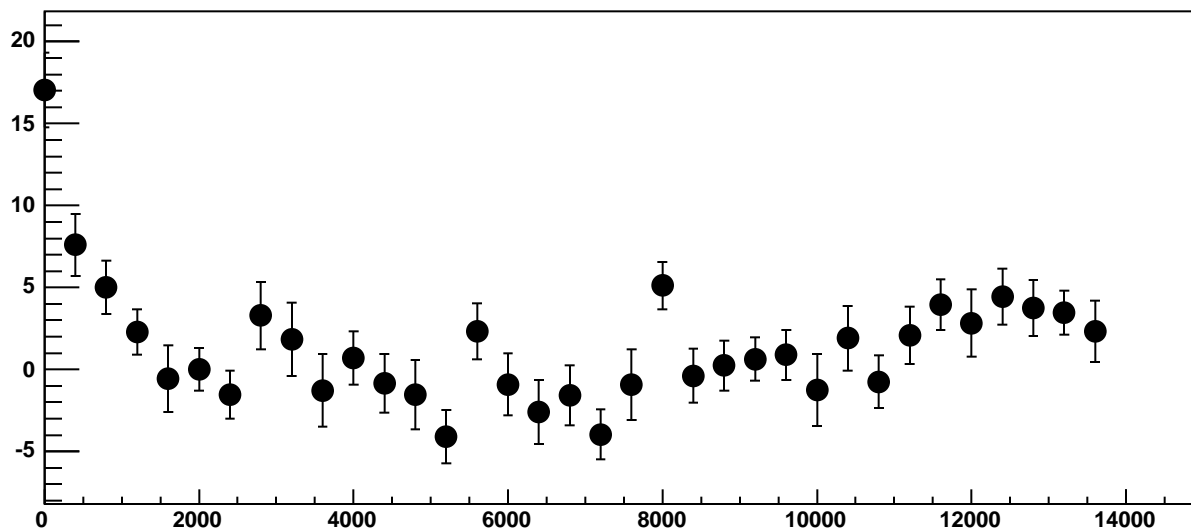
p1

0.01833 ± 0.0001132

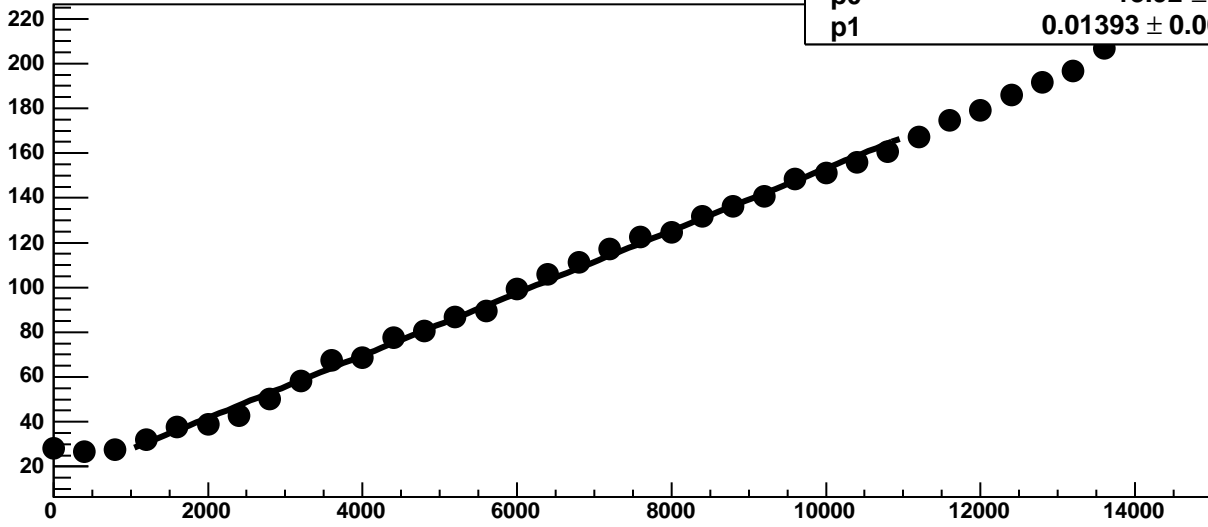
Chip 2, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

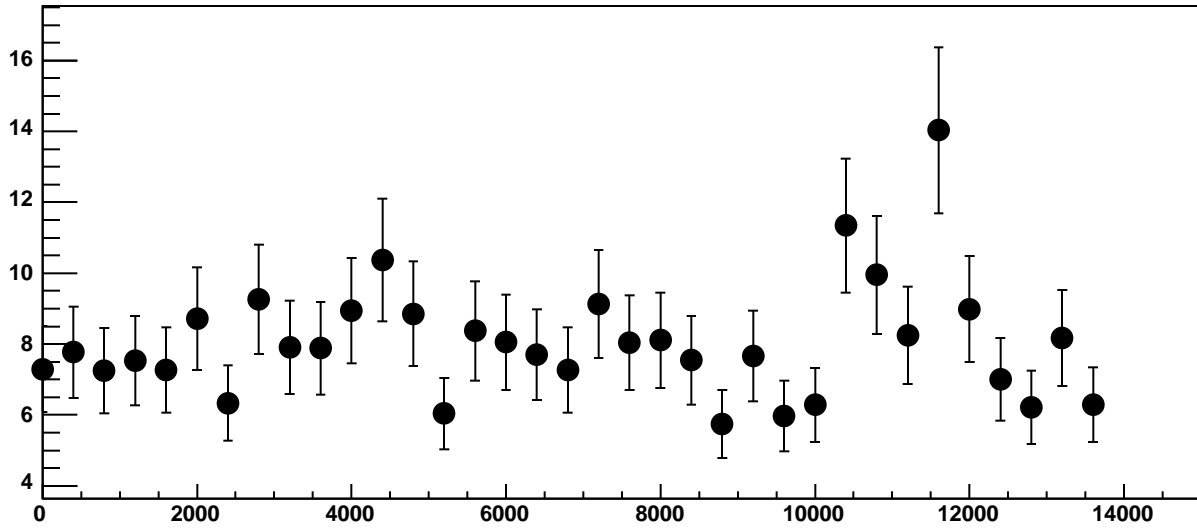


Chip 2, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

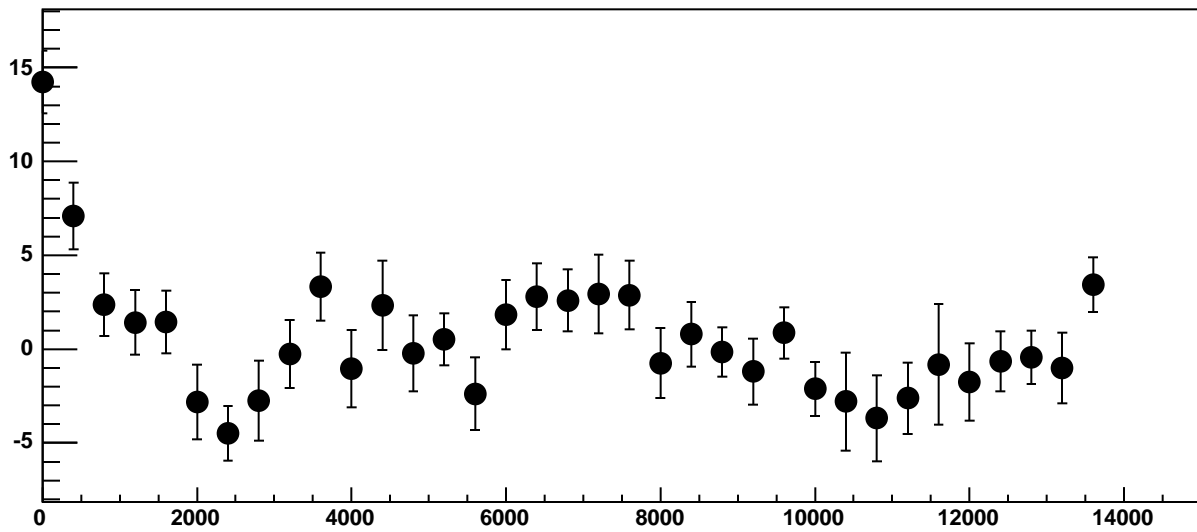


χ^2 / ndf 38.43 / 23
p0 13.92 ± 0.8226
p1 0.01393 ± 0.0001218

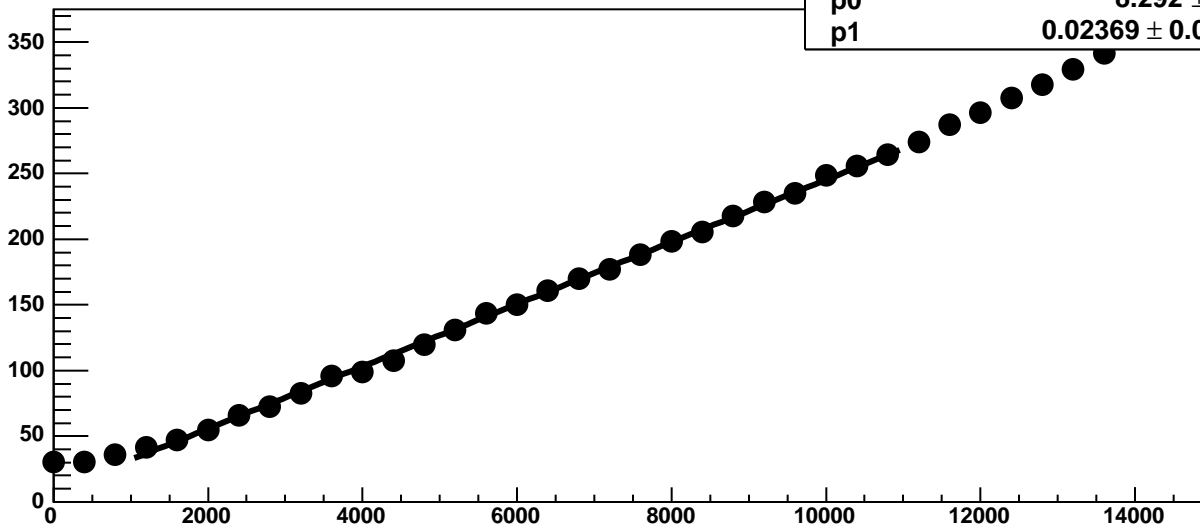
Chip 2, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

30 / 23

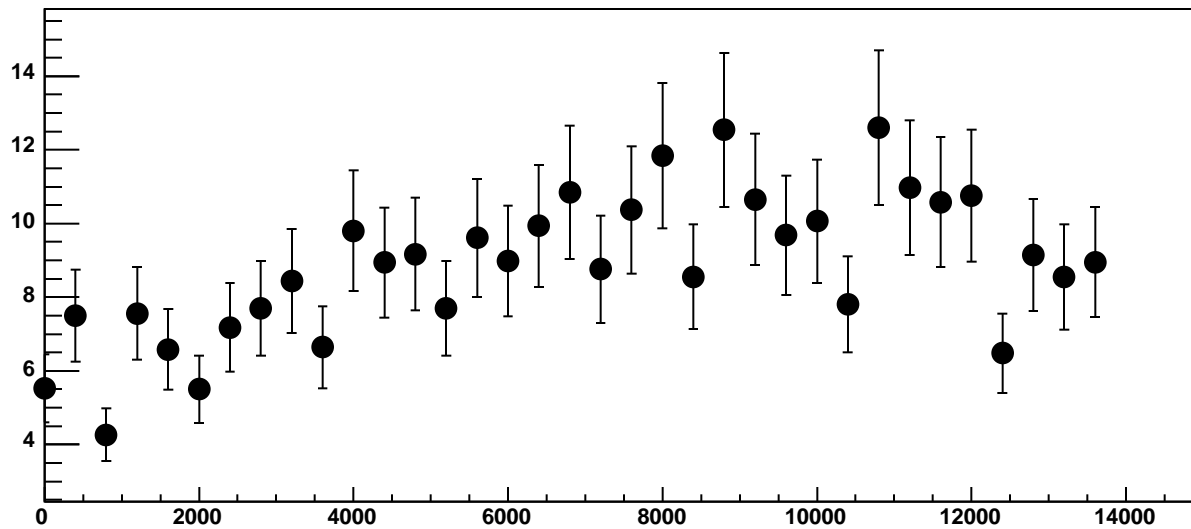
p0

8.292 ± 0.7909

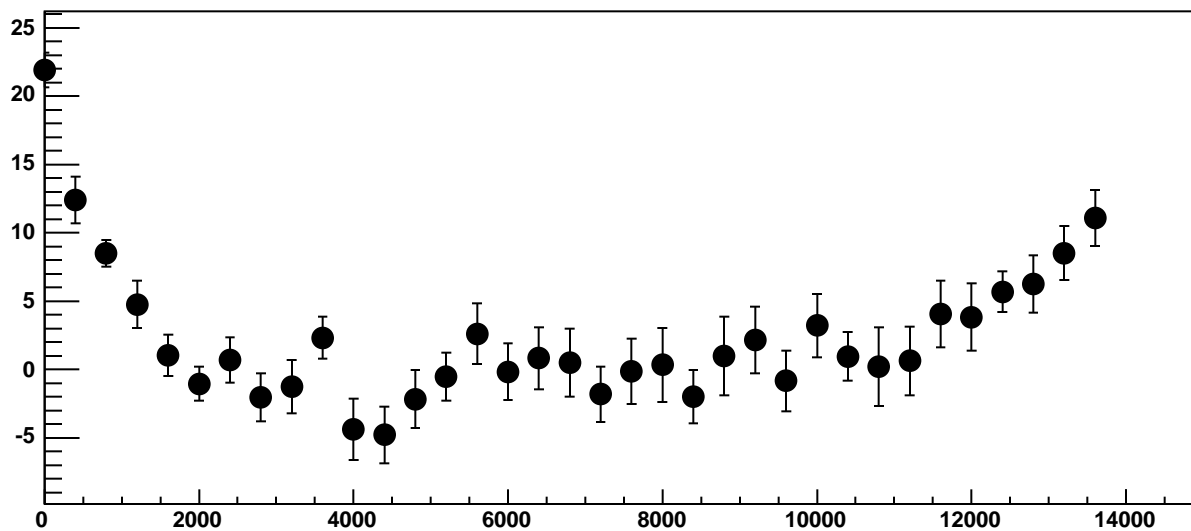
p1

0.02369 ± 0.0001336

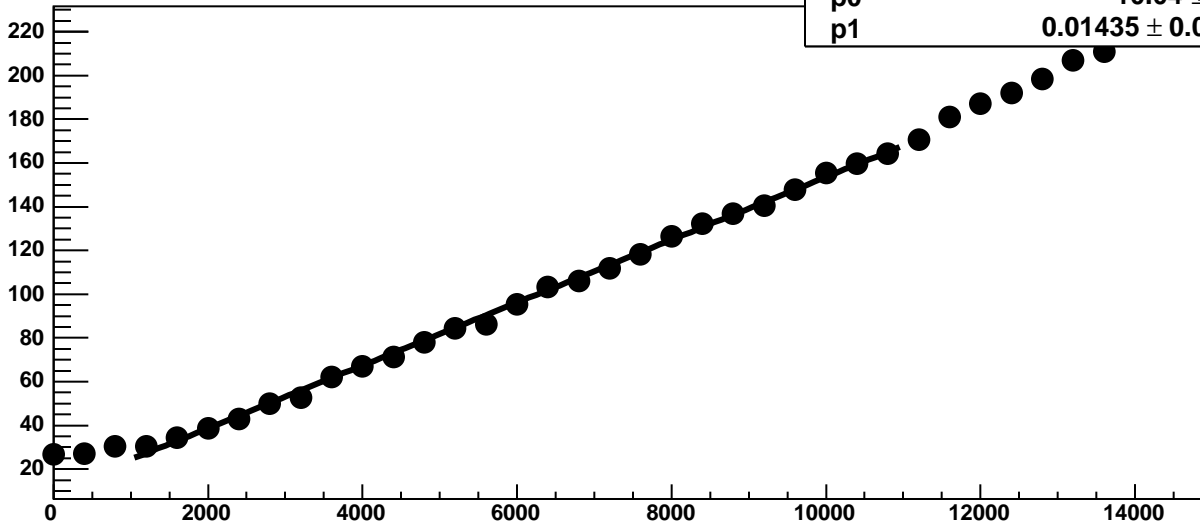
Chip 2, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

19.4 / 23

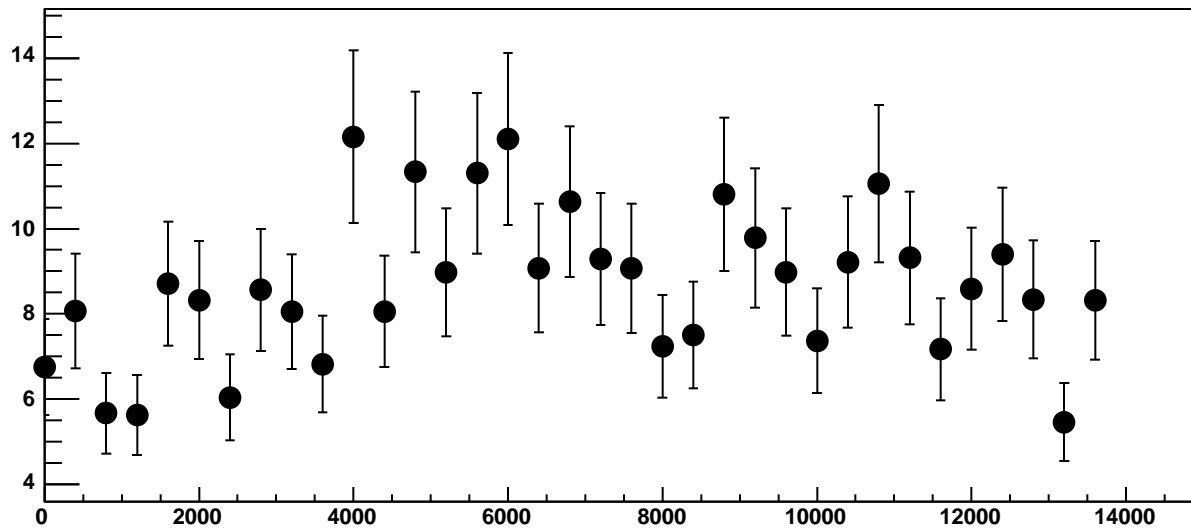
p0

10.04 ± 0.8025

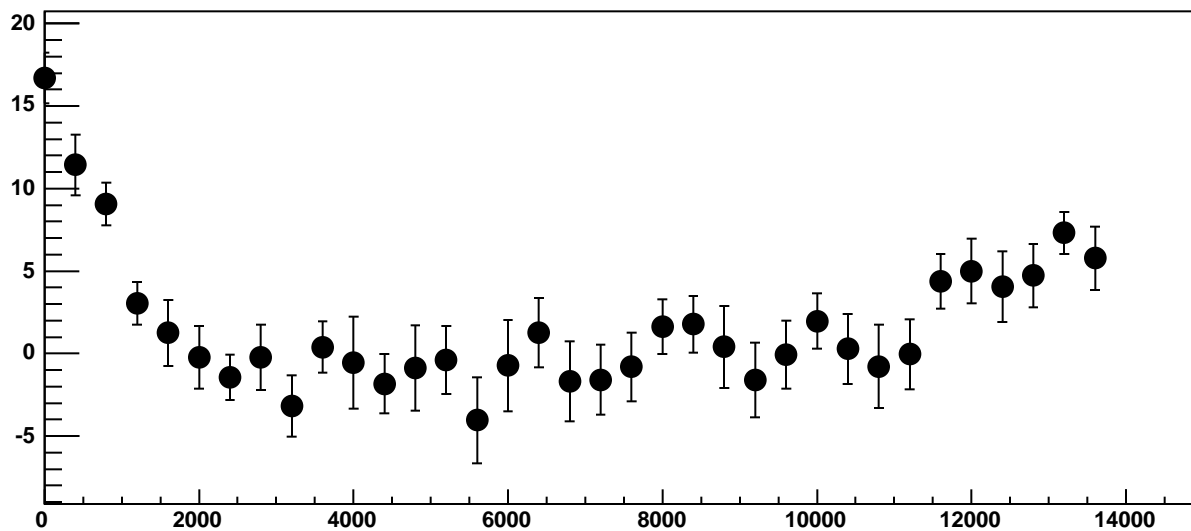
p1

0.01435 ± 0.0001275

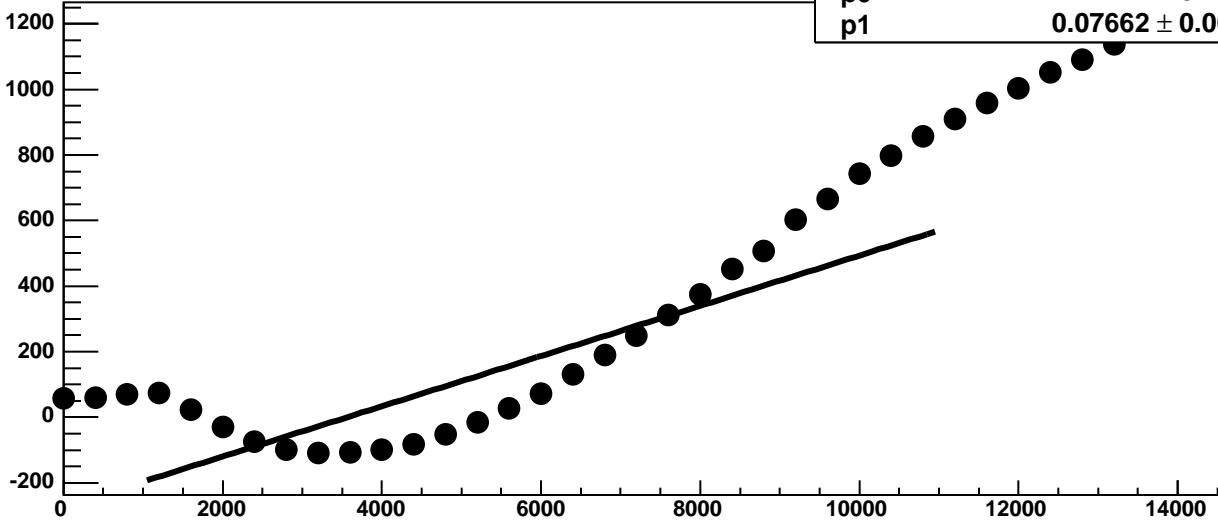
Chip 2, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



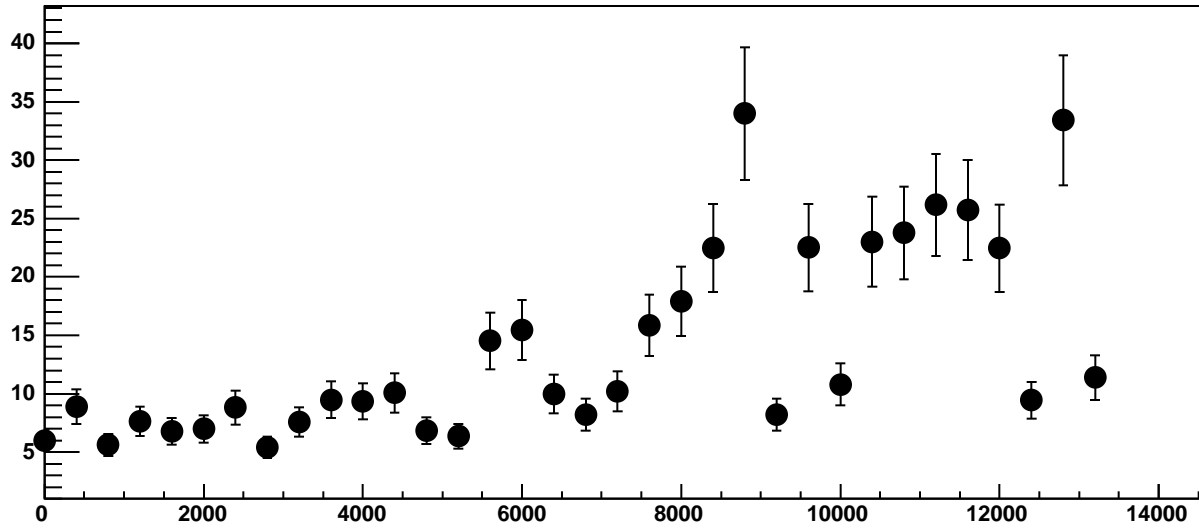
Chip 2, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



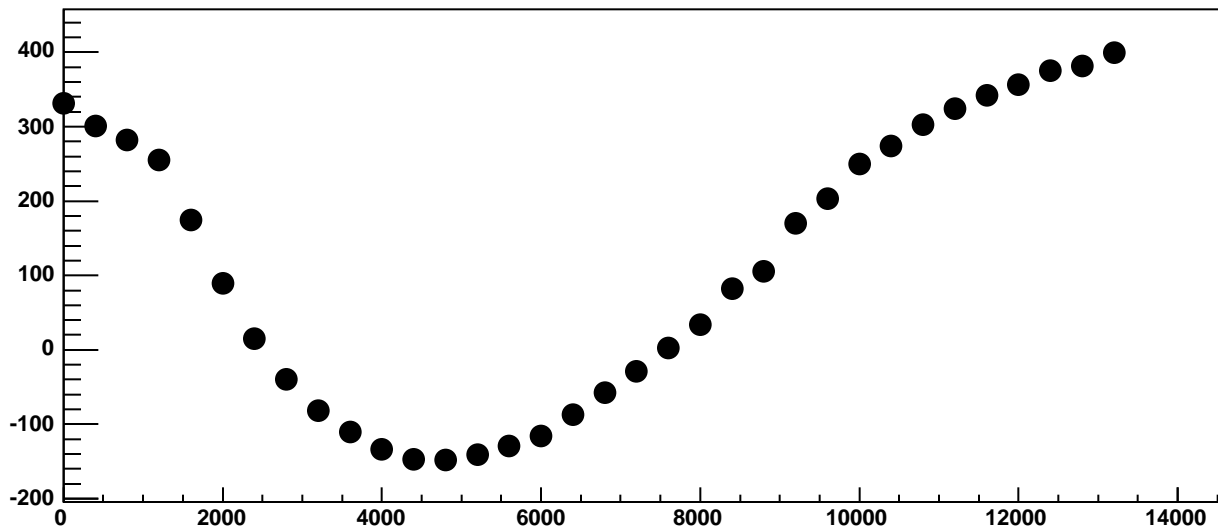
Chip 2, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



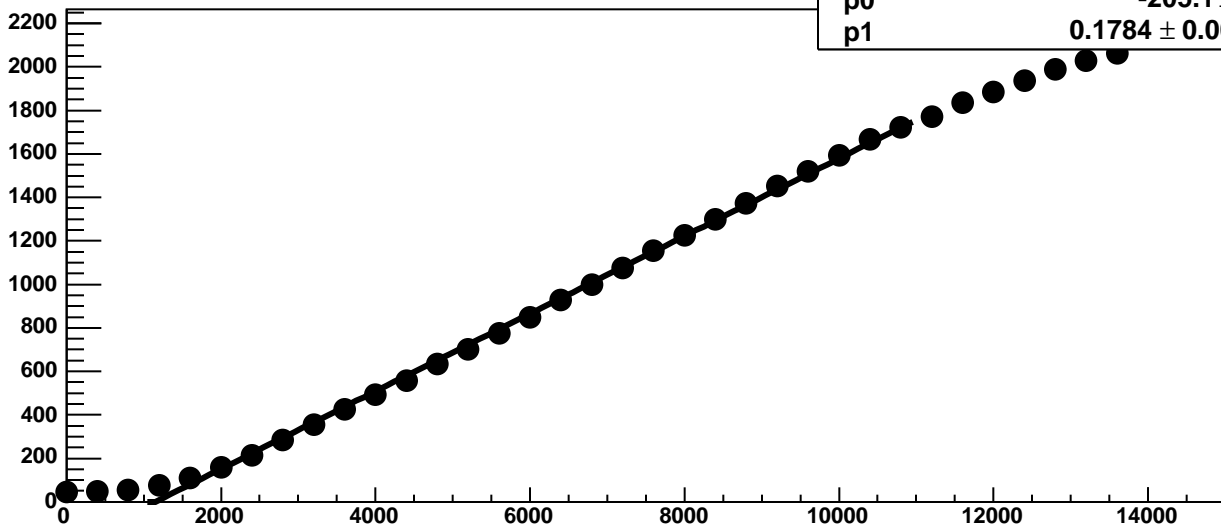
Chip 2, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

758.7 / 23

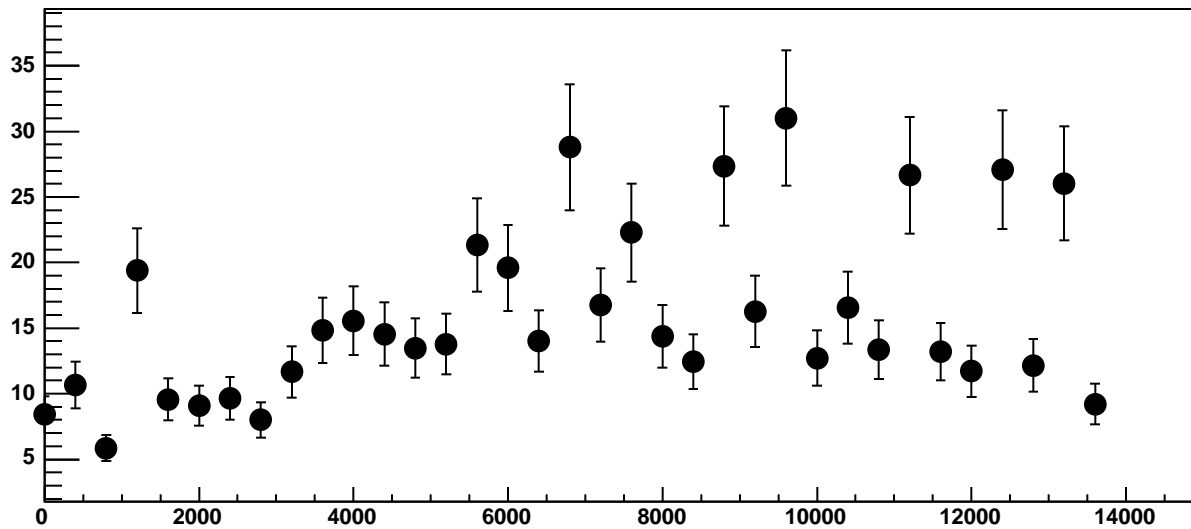
p0

-205.1 ± 1.226

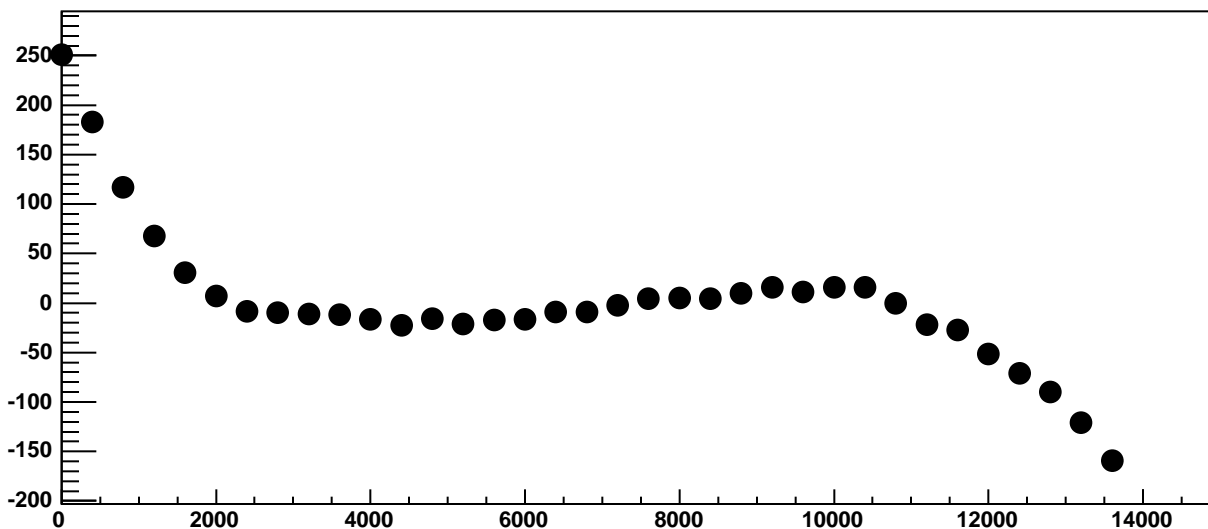
p1

0.1784 ± 0.0002108

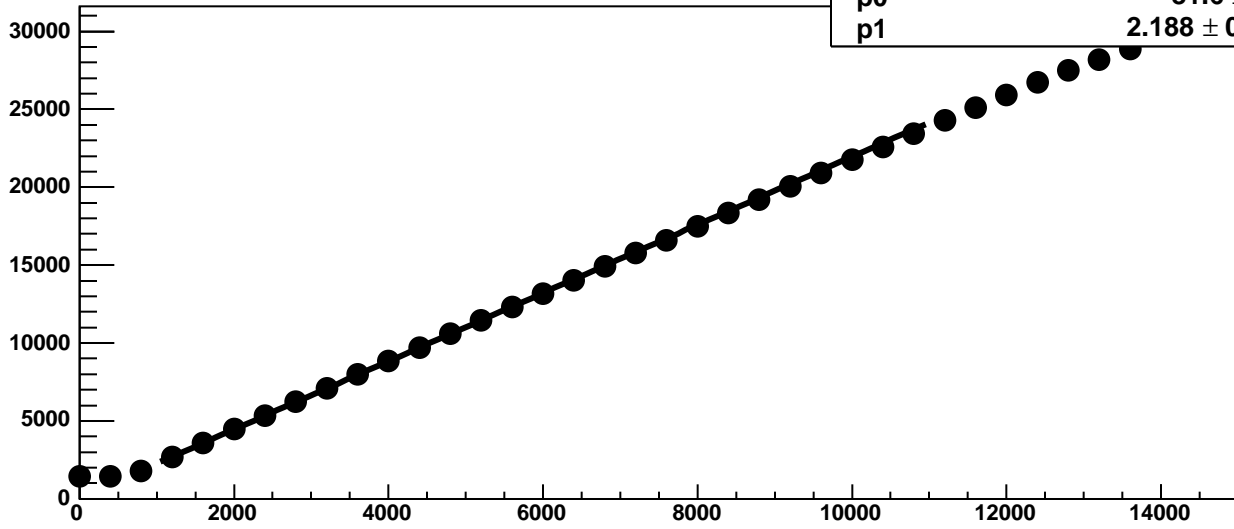
Chip 2, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



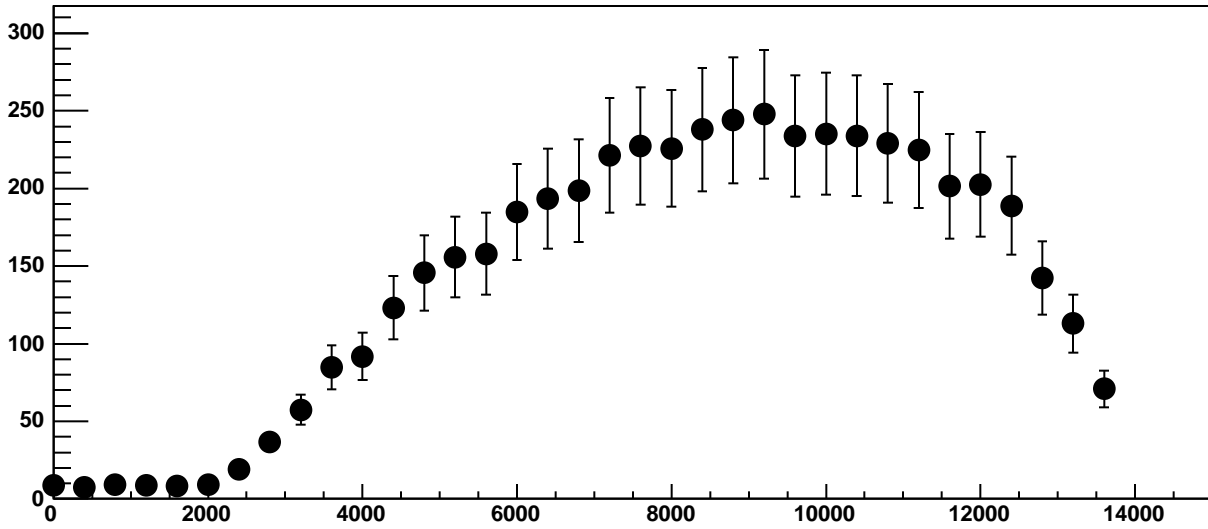
Chip 2, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



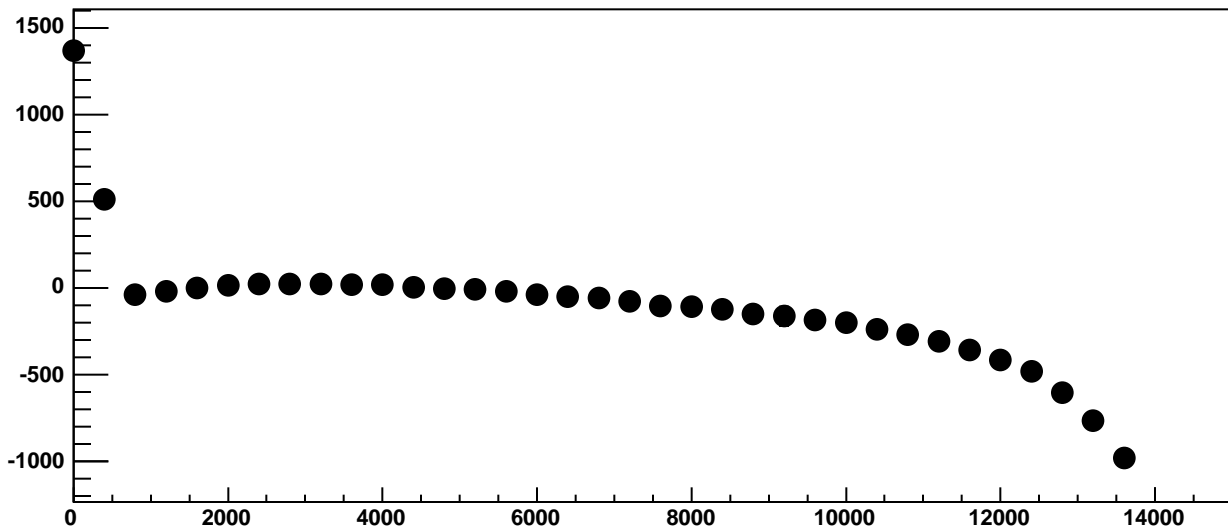
Chip 2, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC



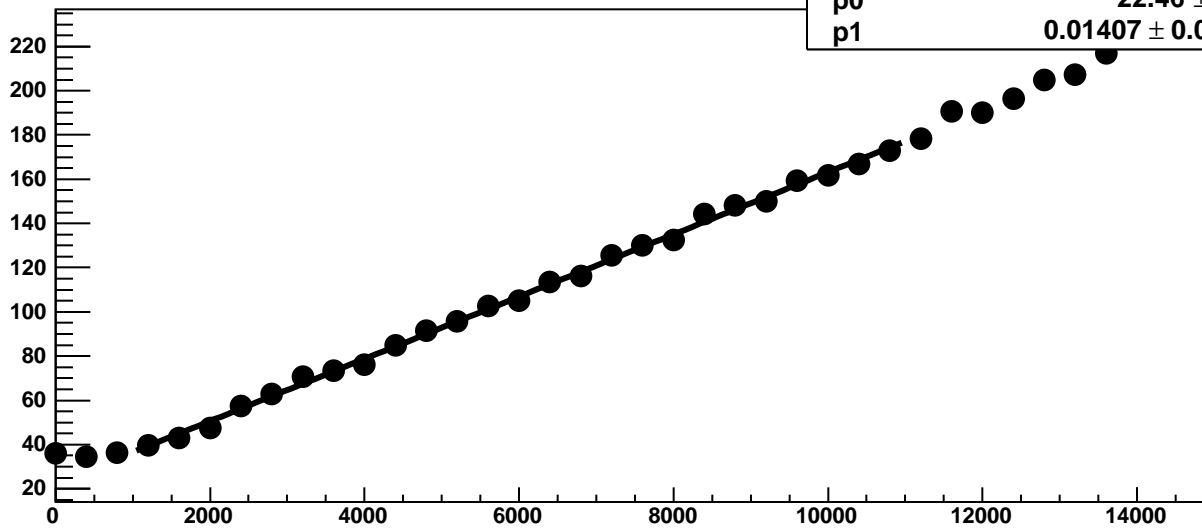
Chip 2, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

34.2 / 23

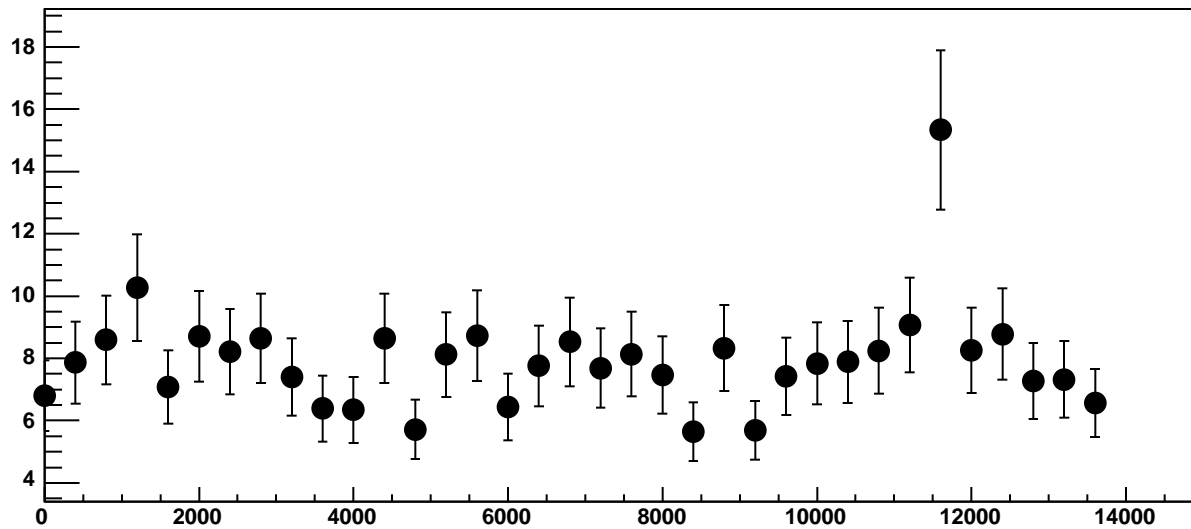
p0

22.46 ± 0.8256

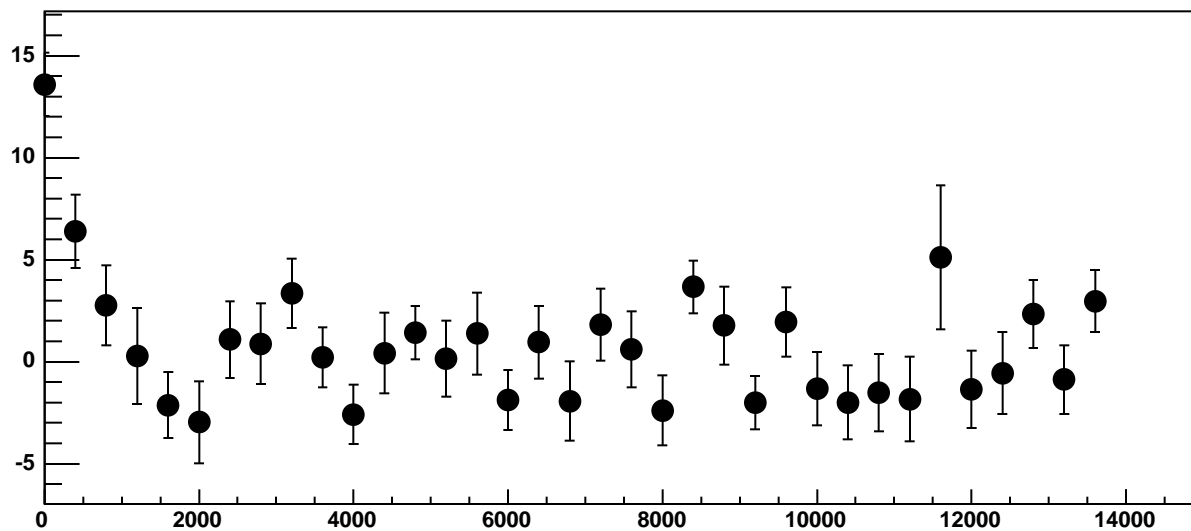
p1

0.01407 ± 0.0001224

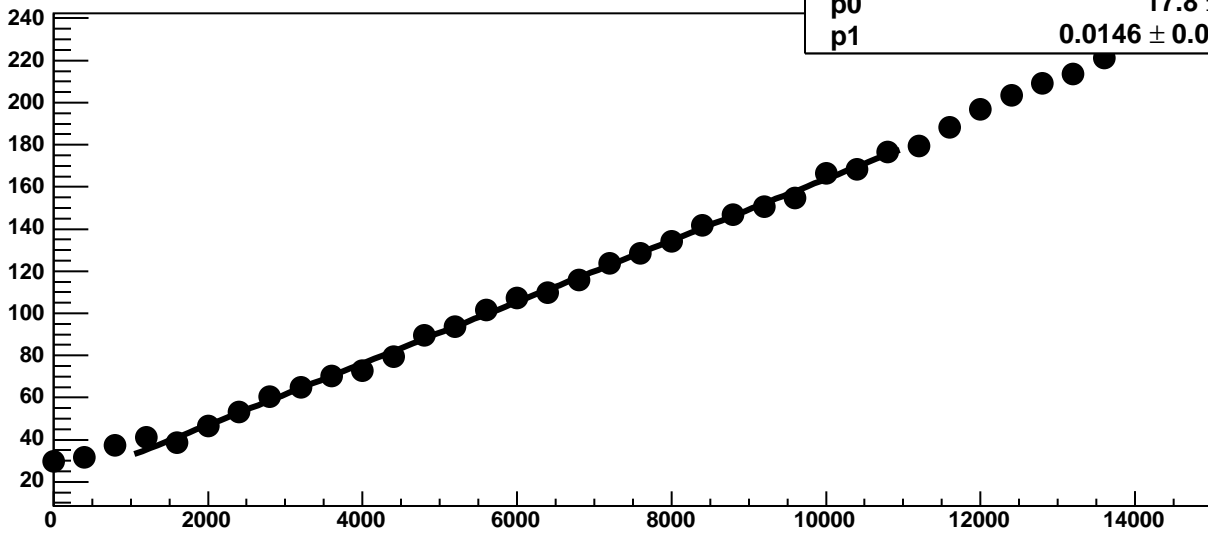
Chip 2, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

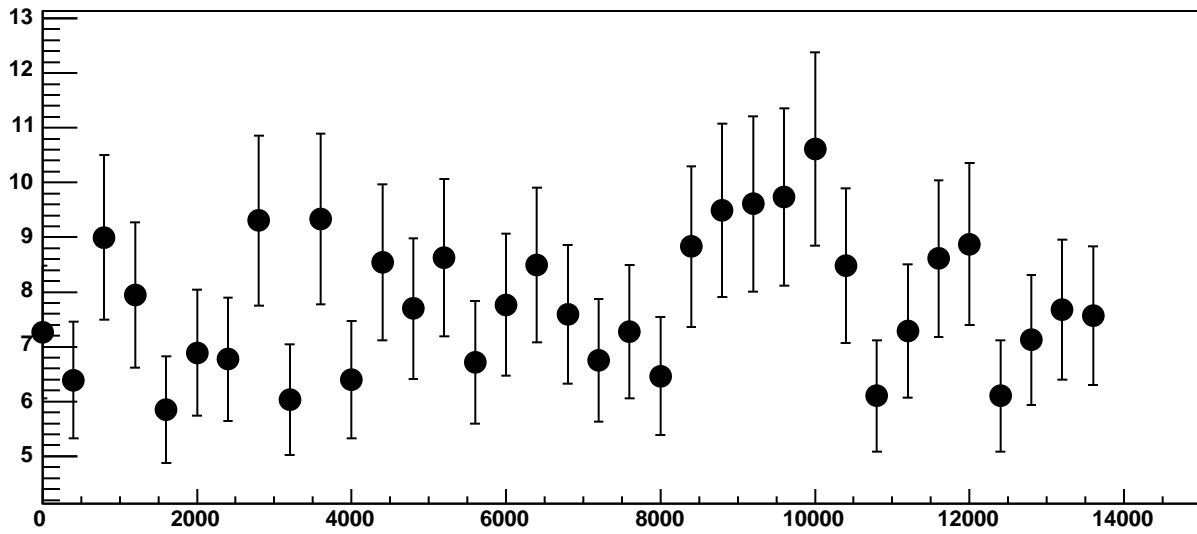


Chip 2, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

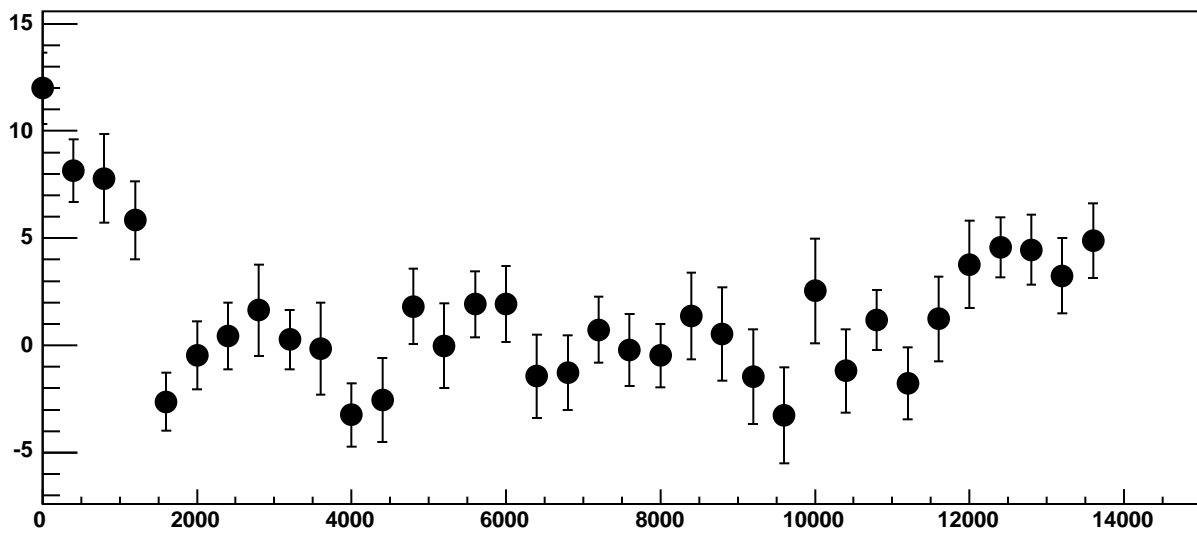


χ^2 / ndf 31.95 / 23
p0 17.8 ± 0.762
p1 0.0146 ± 0.0001192

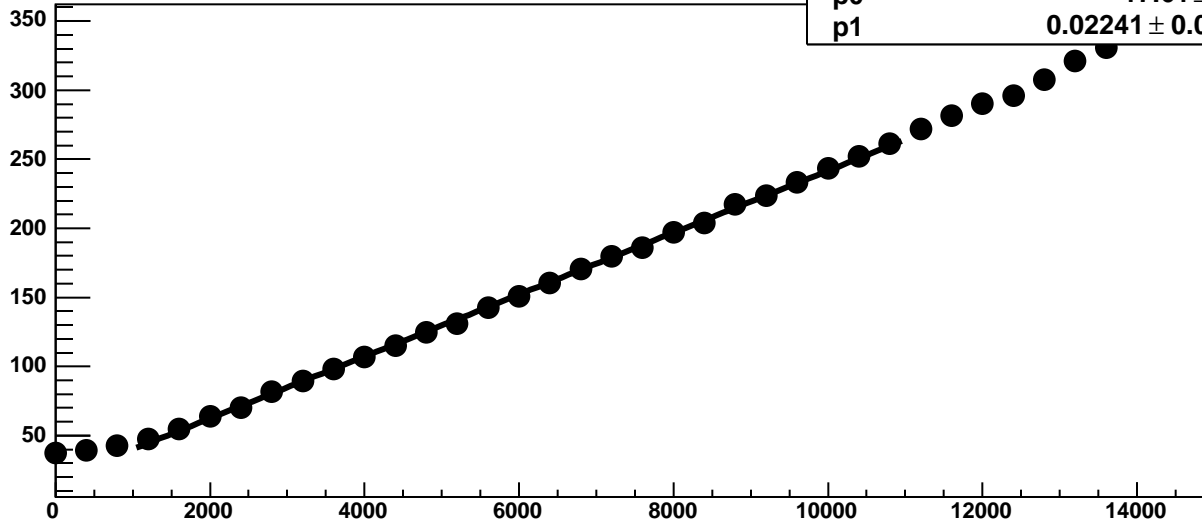
Chip 2, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

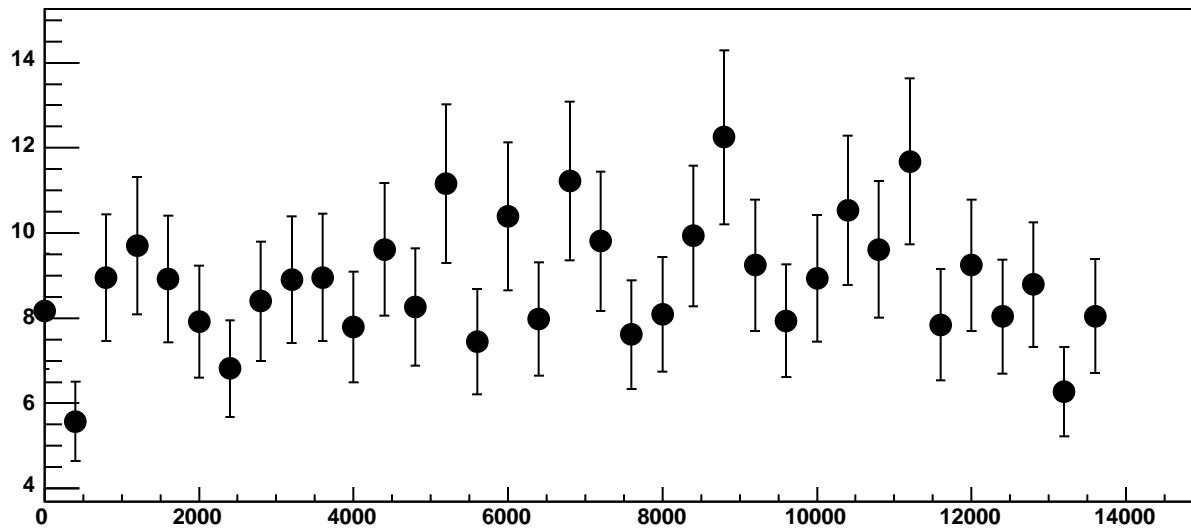


Chip 2, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

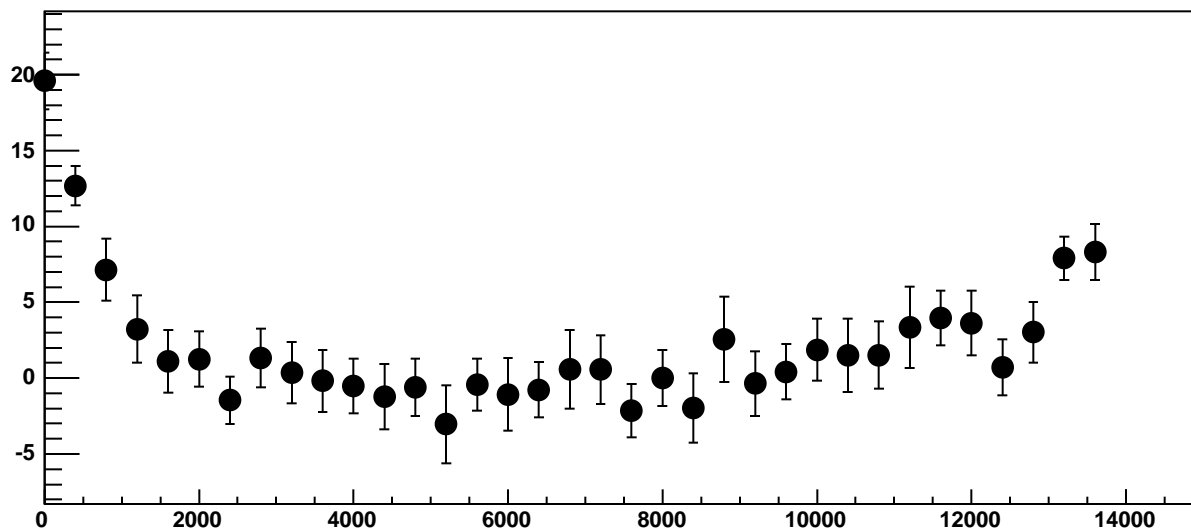


χ^2 / ndf 11.54 / 23
p0 17.61 ± 0.9094
p1 0.02241 ± 0.0001414

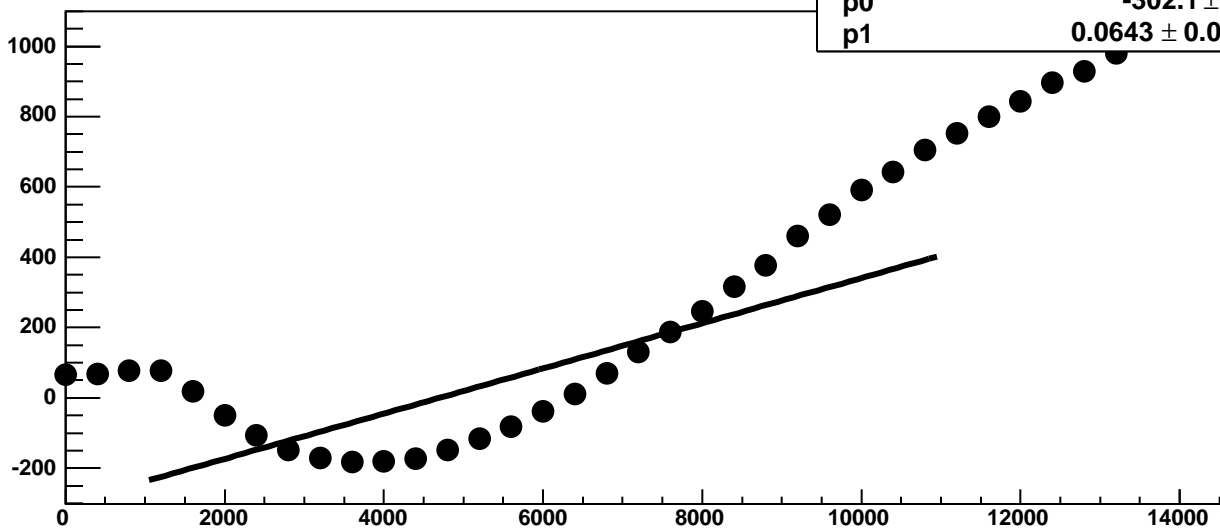
Chip 2, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC

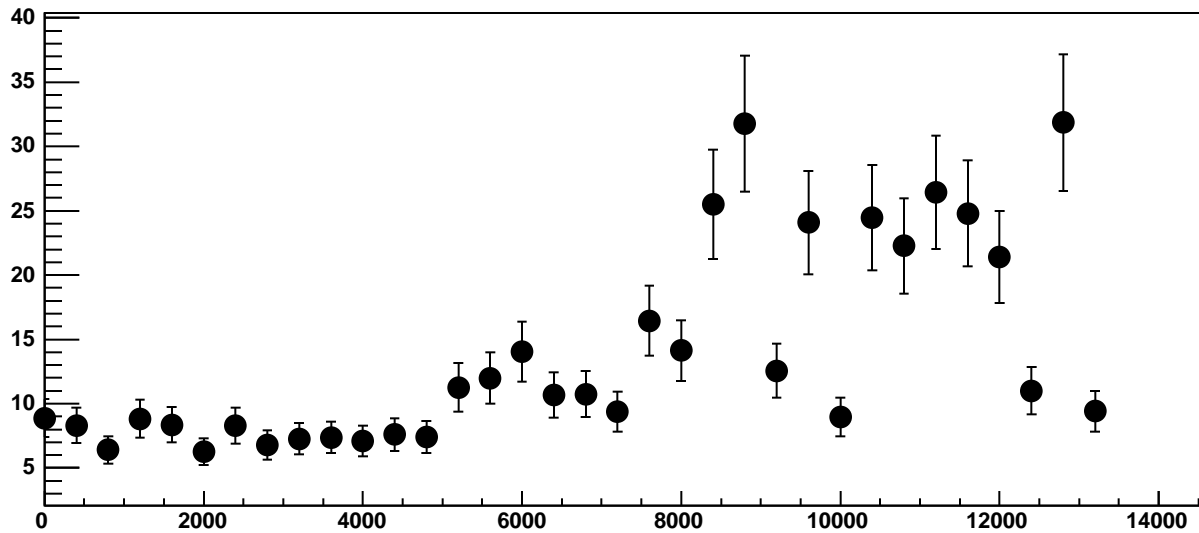


Chip 2, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

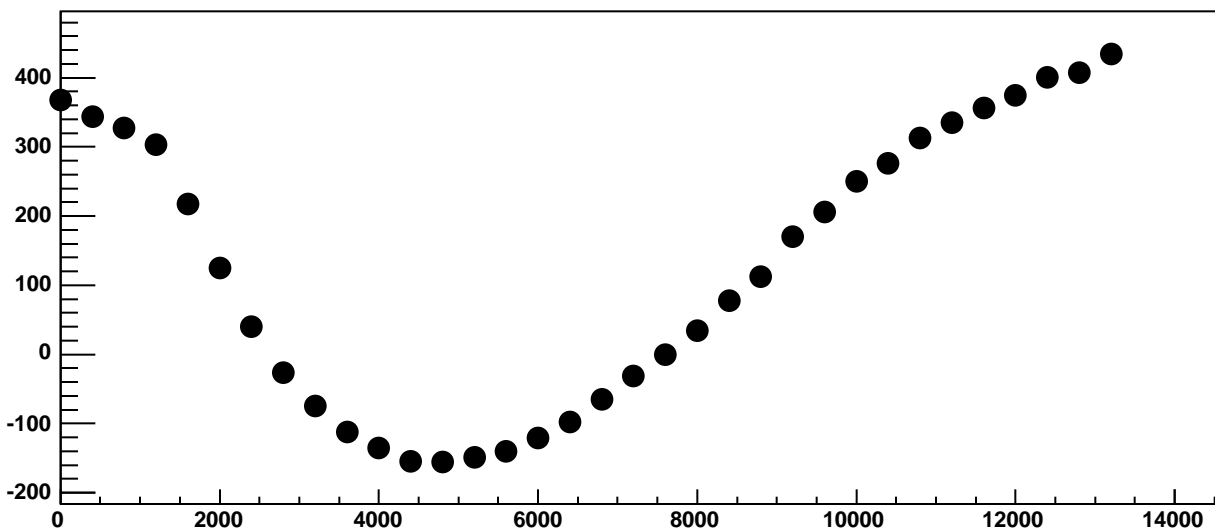


χ^2 / ndf 1.094e+05 / 23
p0 -302.1 ± 0.9218
p1 0.0643 ± 0.0001801

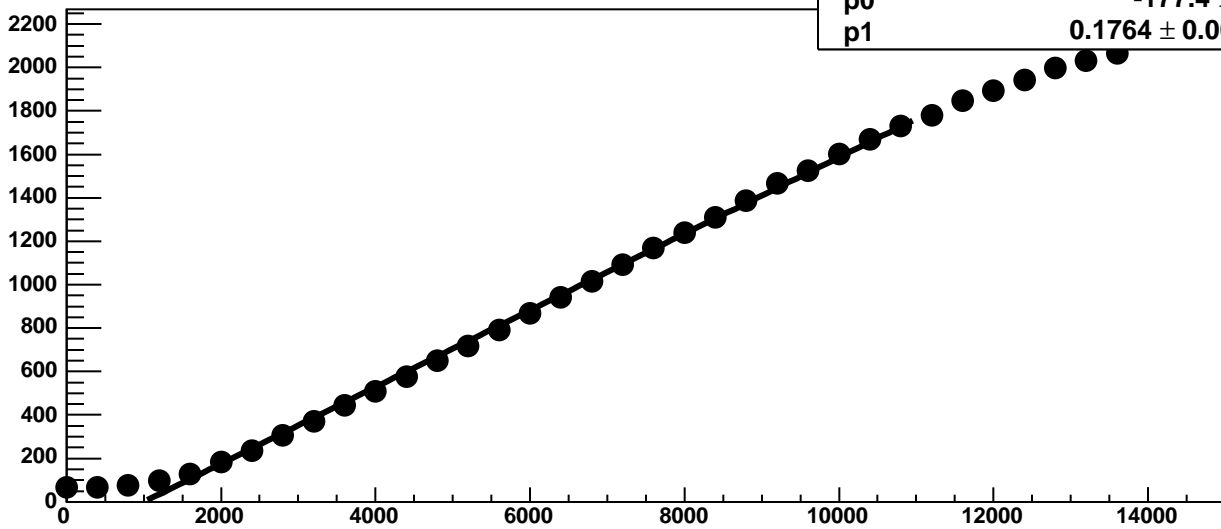
Chip 2, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



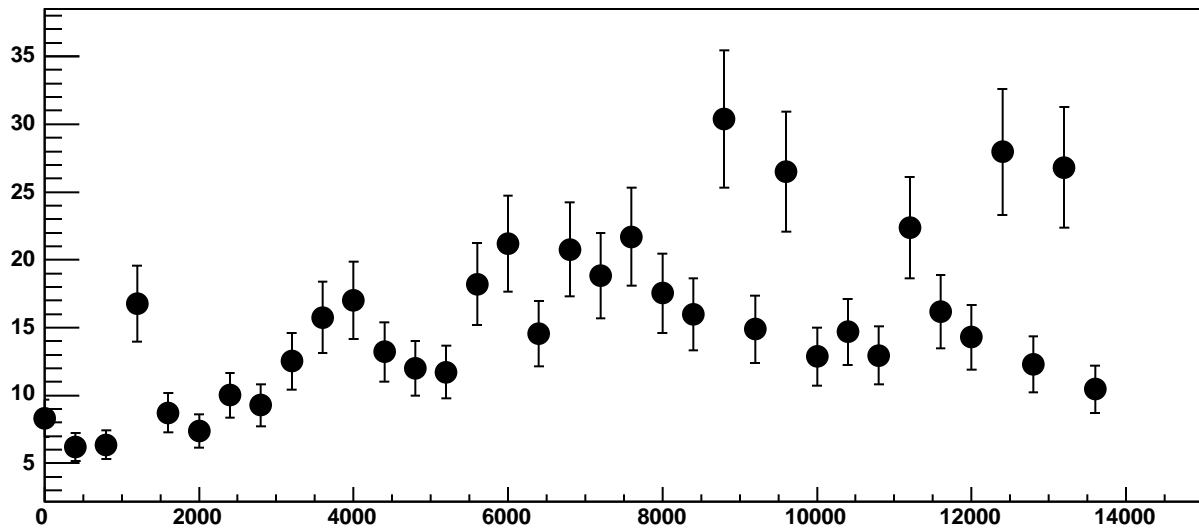
Chip 2, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



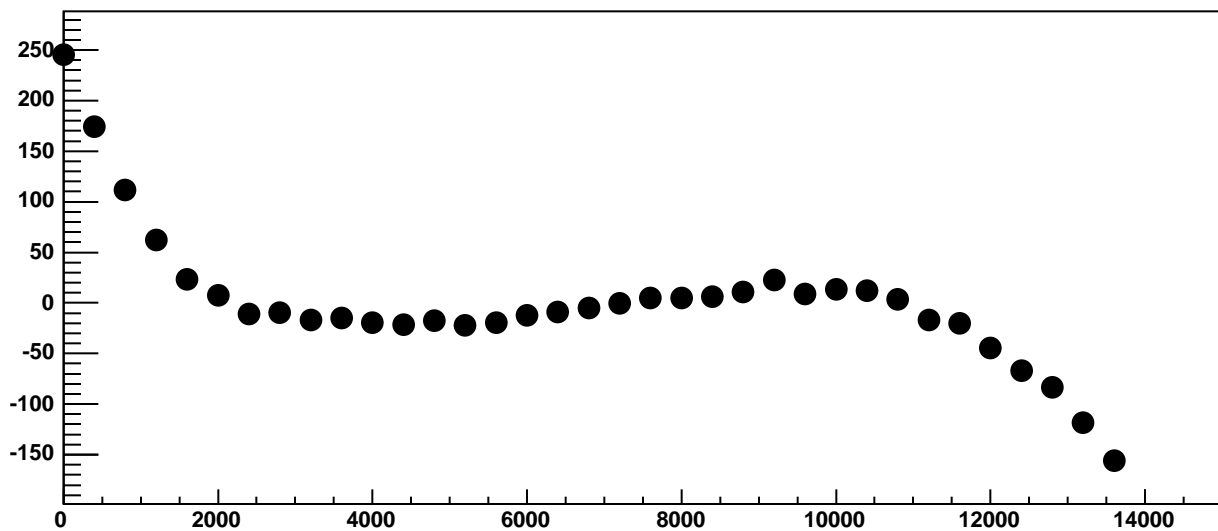
Chip 2, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



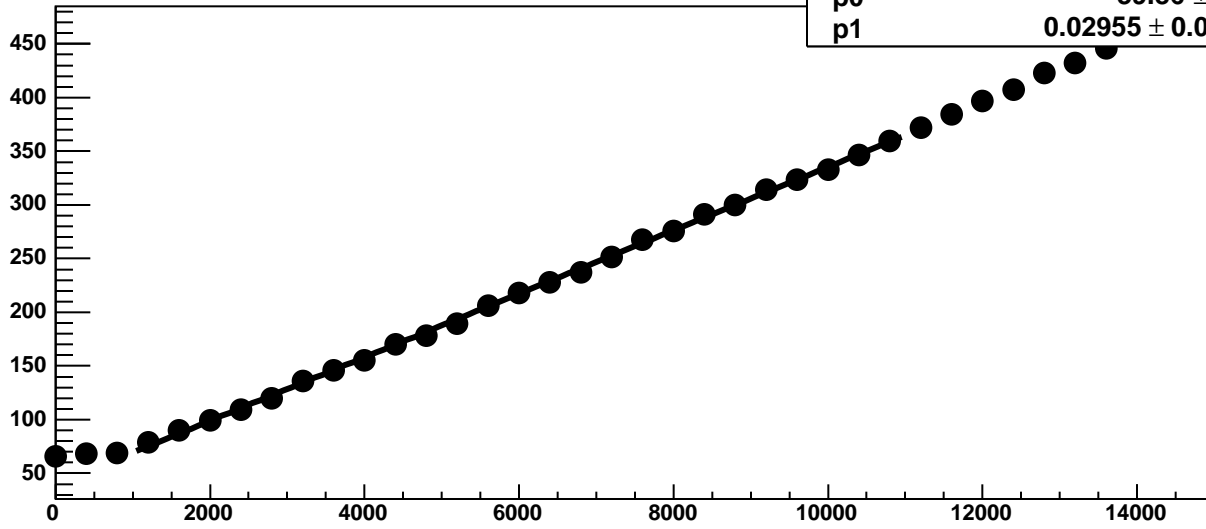
Chip 2, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

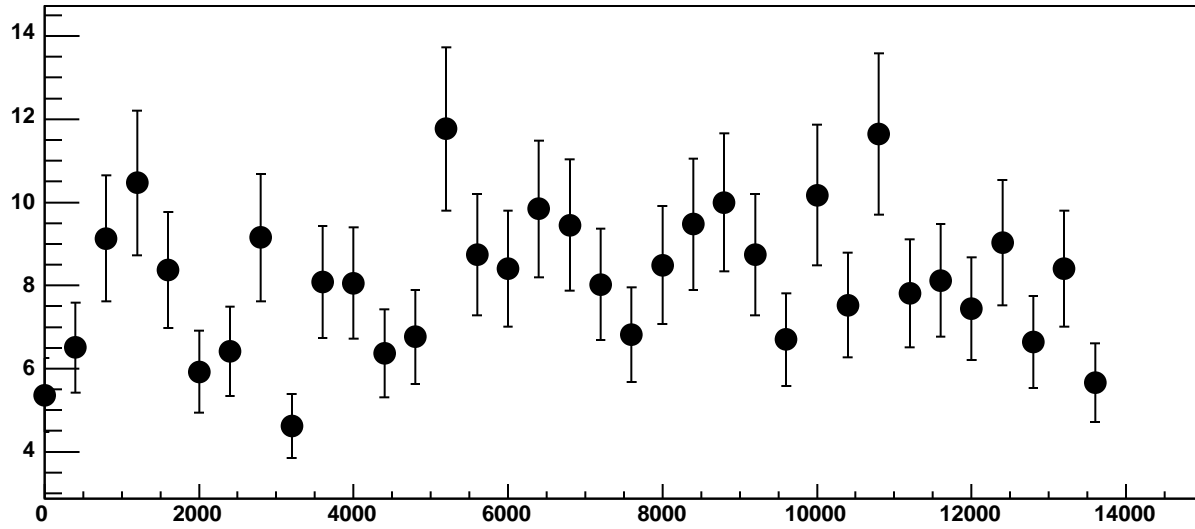


Chip 2, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

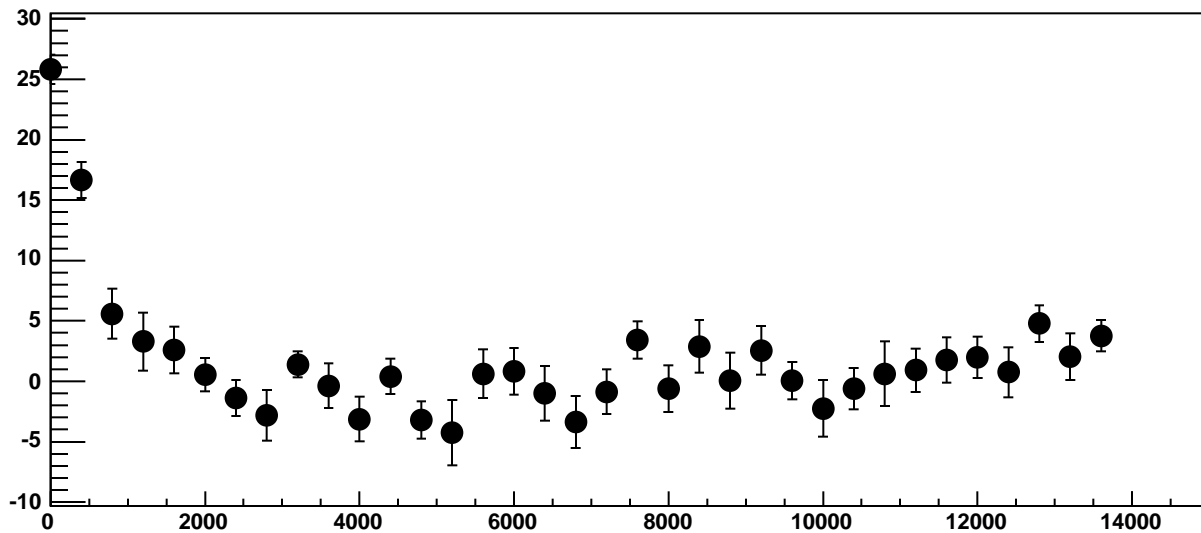


χ^2 / ndf 30.47 / 23
p0 39.86 ± 0.7854
p1 0.02955 ± 0.0001268

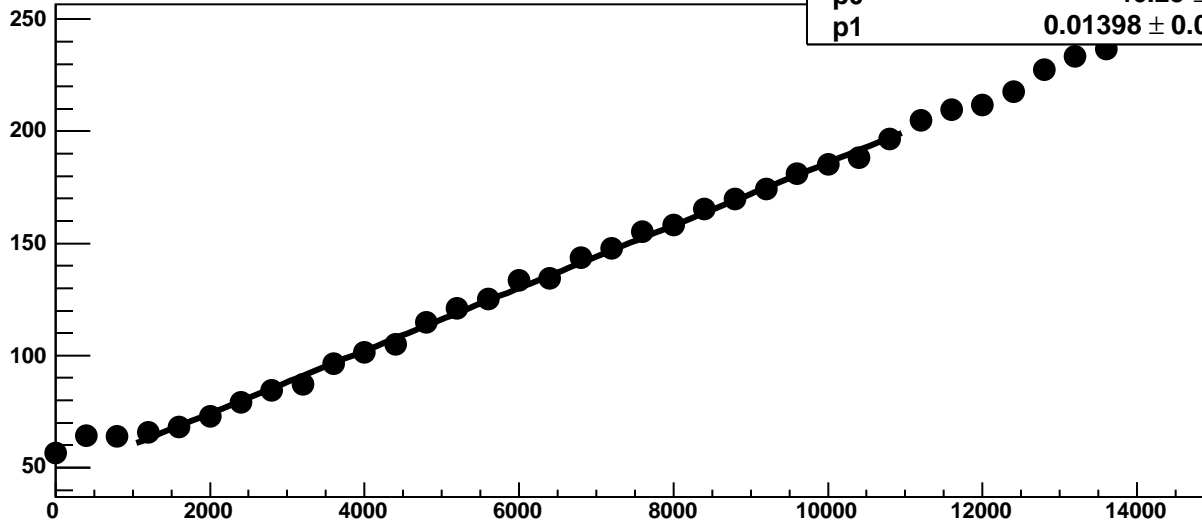
Chip 2, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC

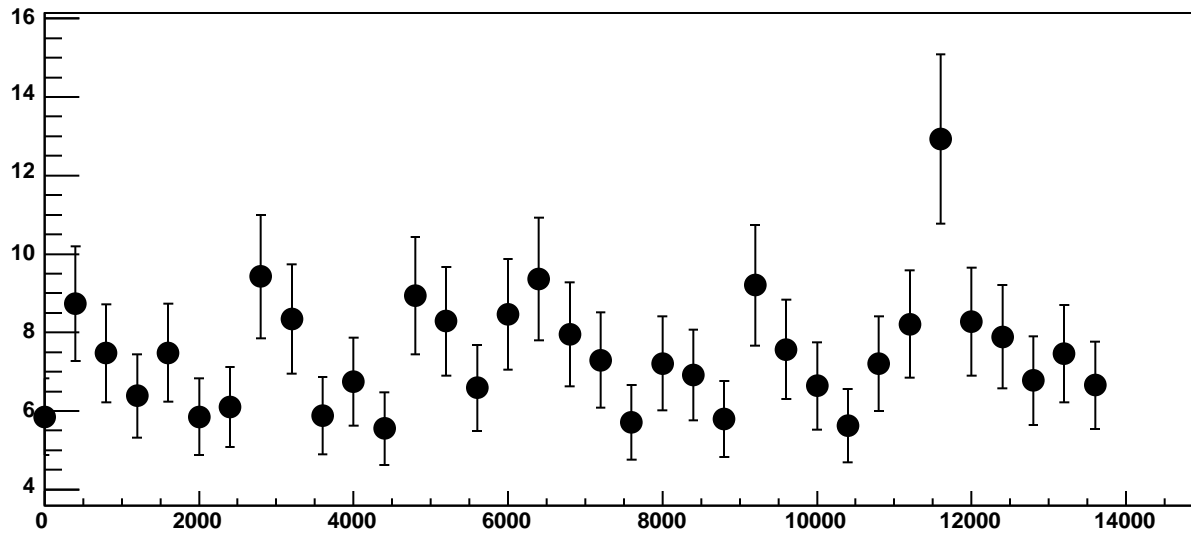


Chip 2, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

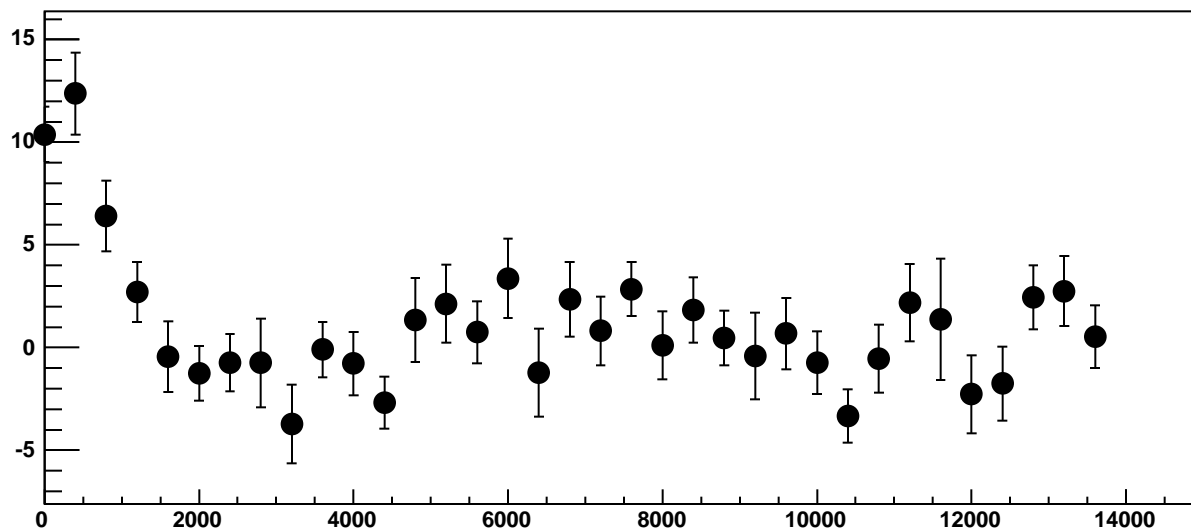


χ^2 / ndf 33.79 / 23
p0 46.25 ± 0.7135
p1 0.01398 ± 0.0001066

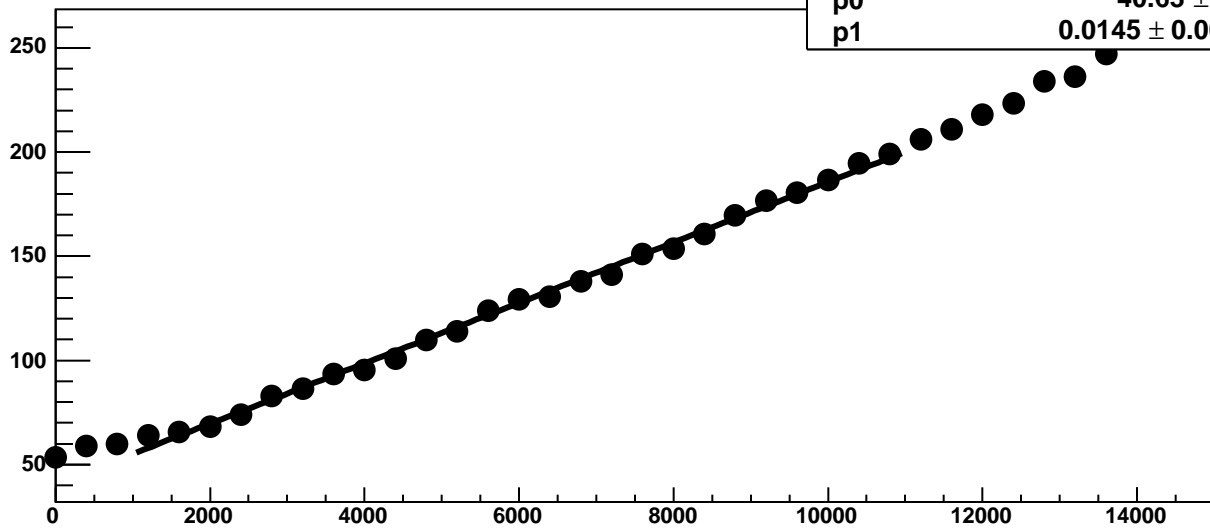
Chip 2, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

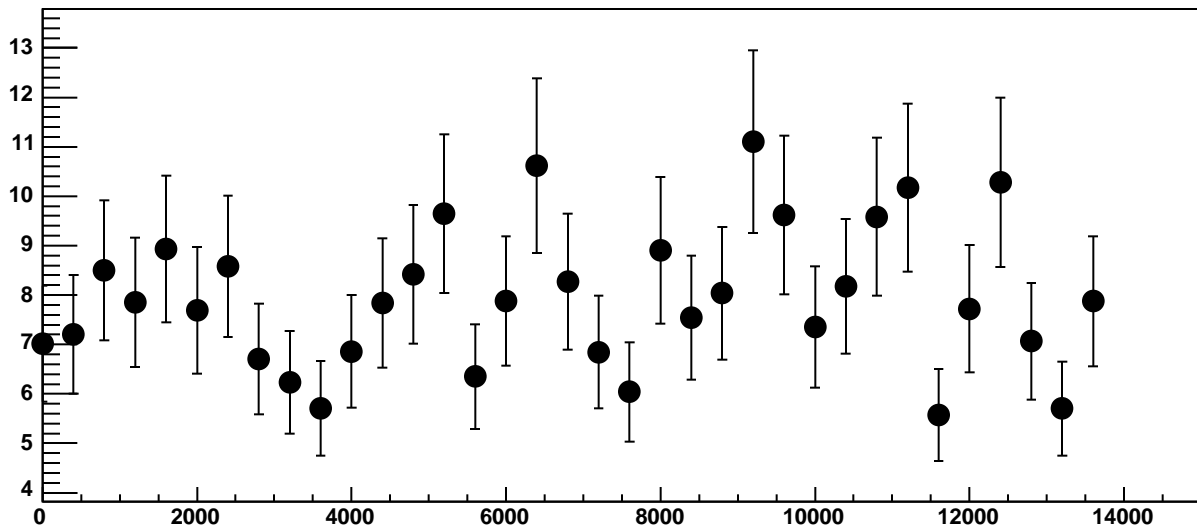


Chip 2, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

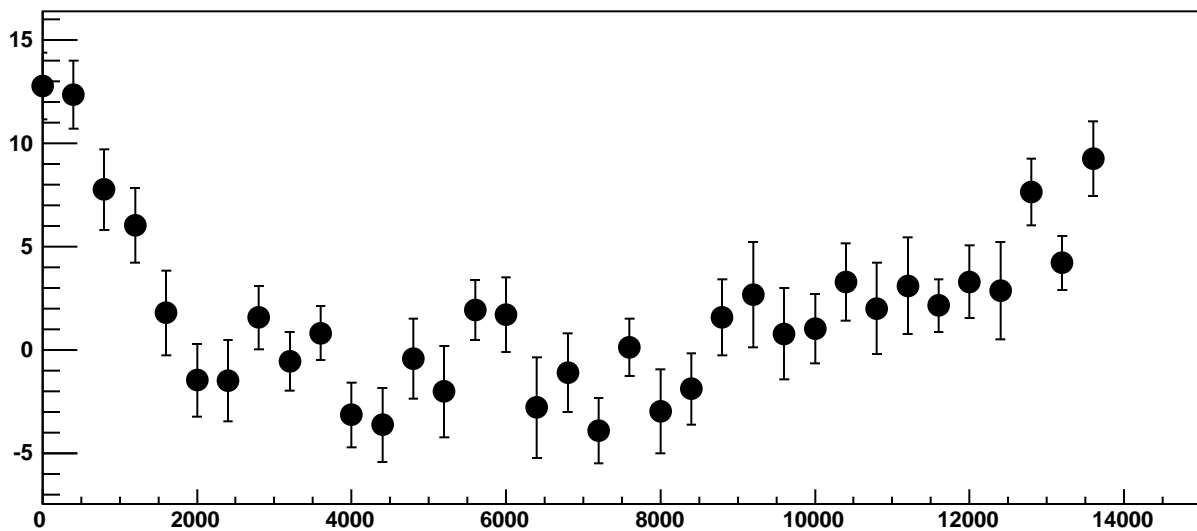


χ^2 / ndf 43.75 / 23
p0 40.63 ± 0.8102
p1 0.0145 ± 0.0001275

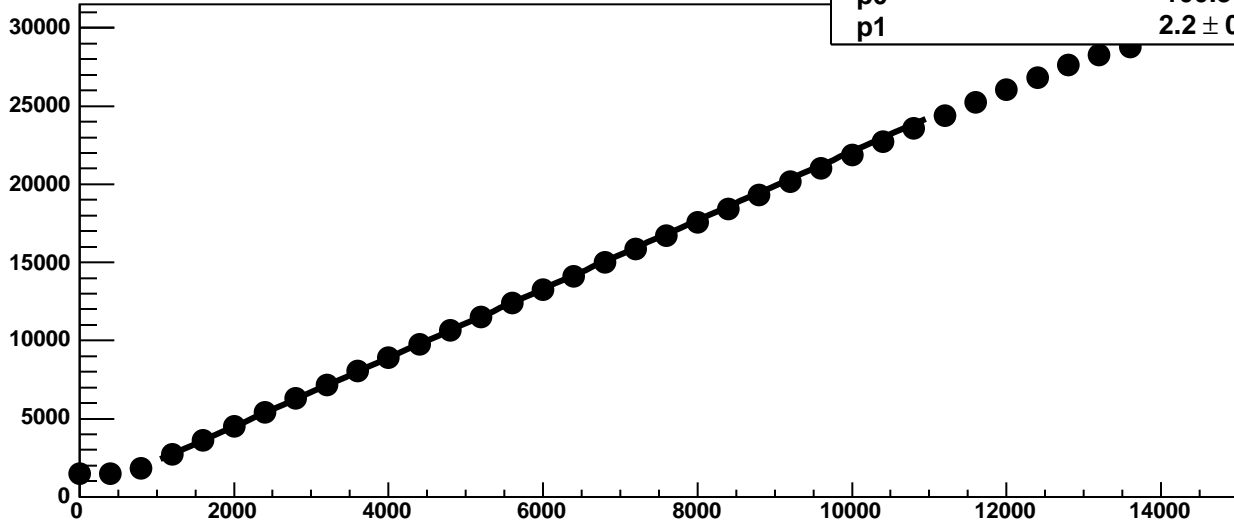
Chip 2, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

341.2 / 23

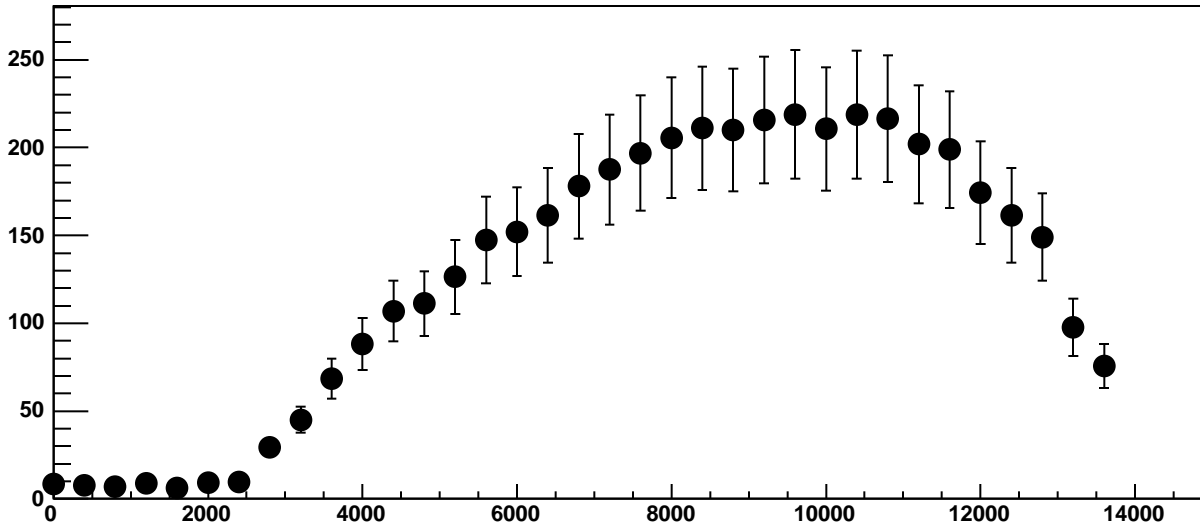
p0

100.8 ± 2.58

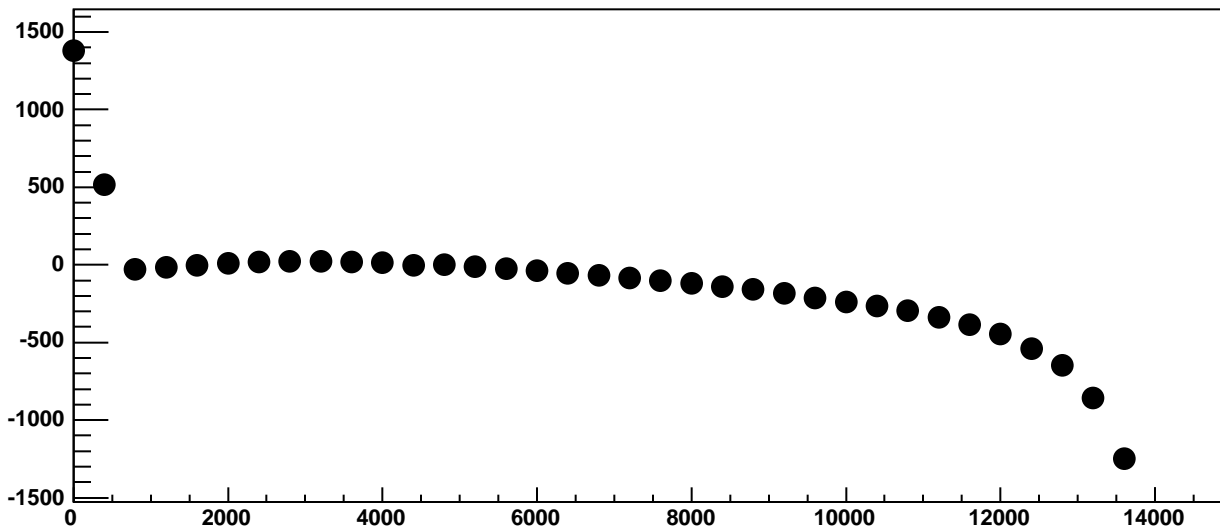
p1

2.2 ± 0.00132

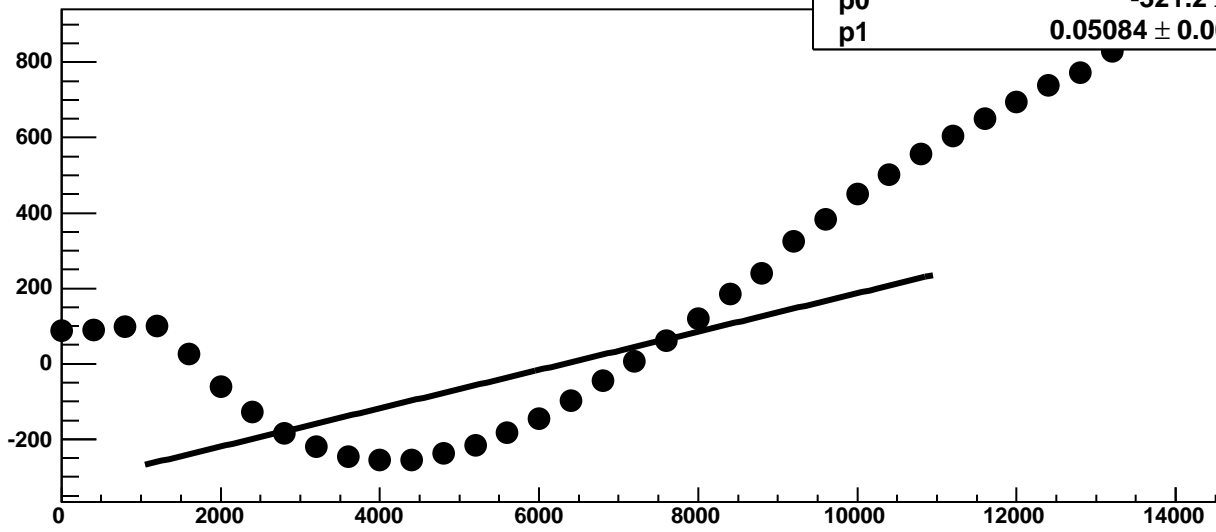
Chip 2, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



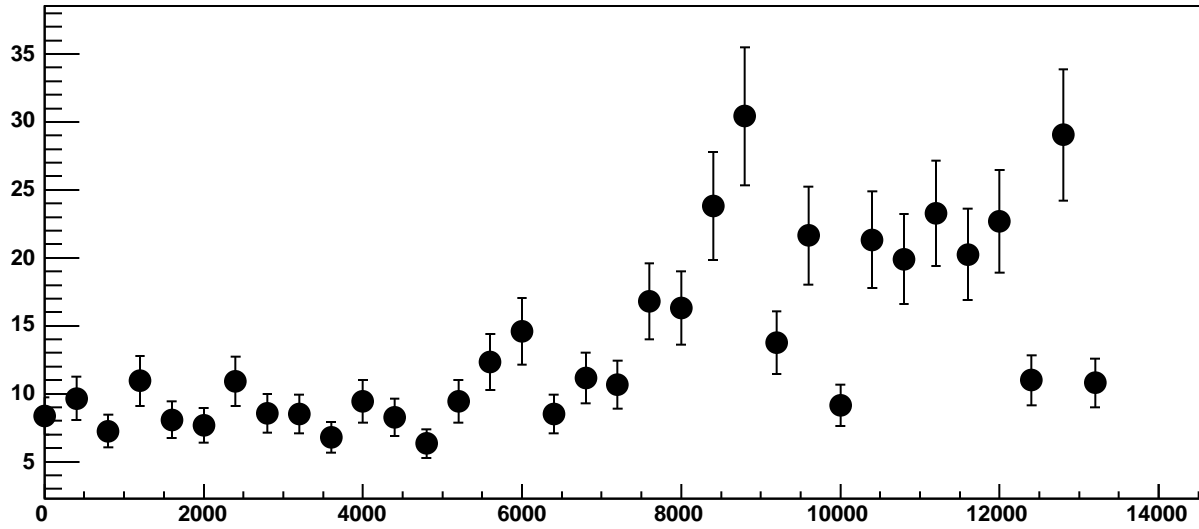
Chip 2, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC



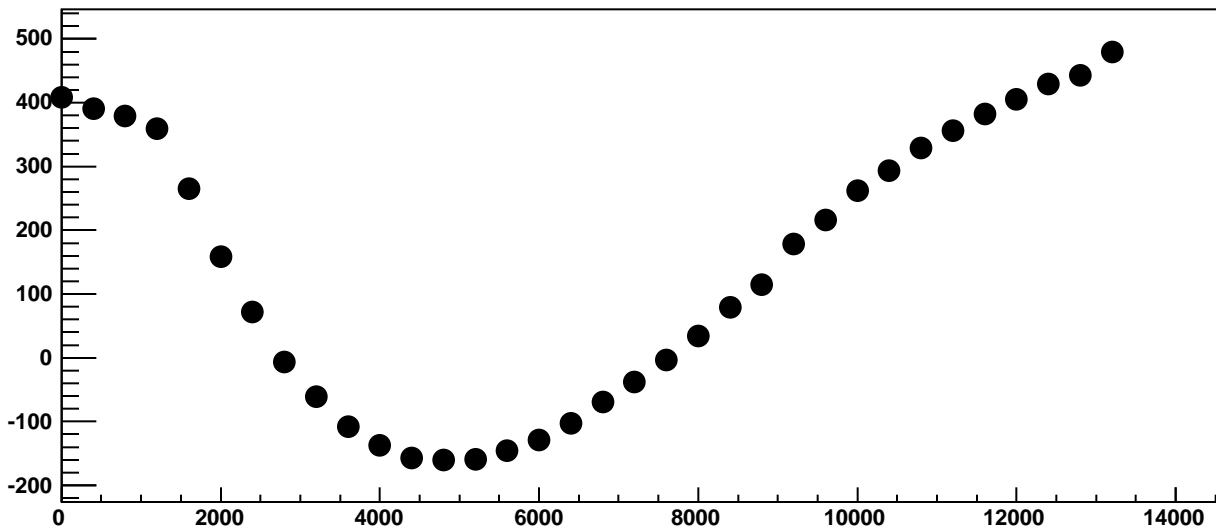
Chip 2, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



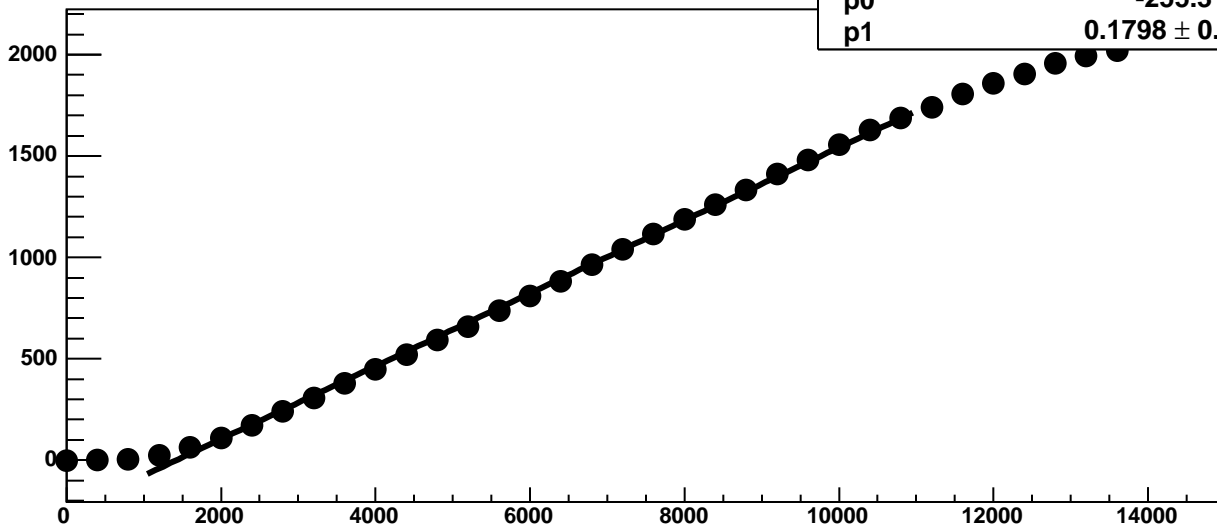
Chip 2, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



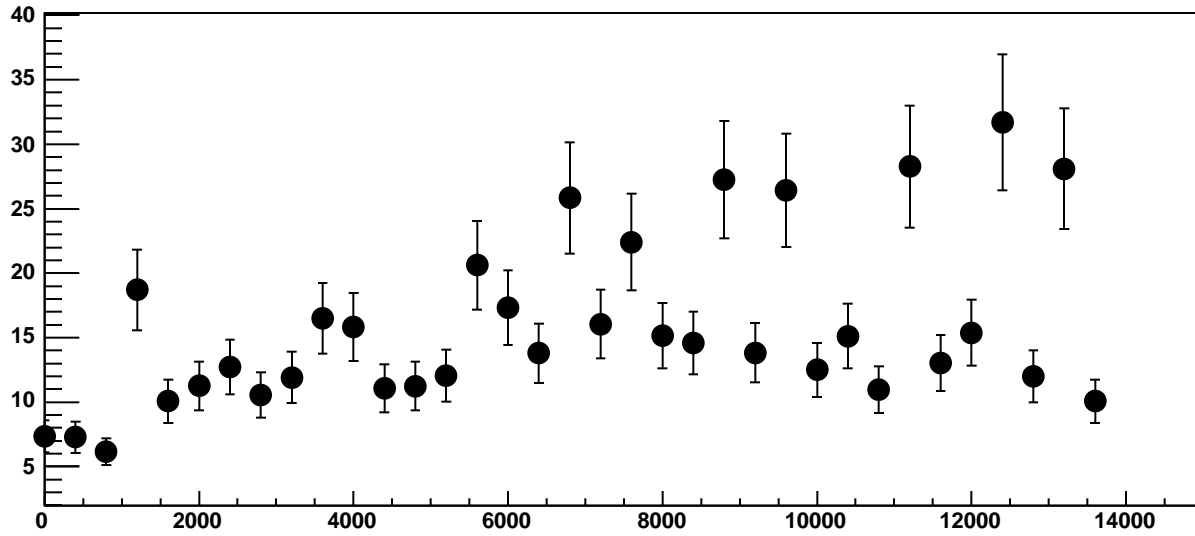
Chip 2, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



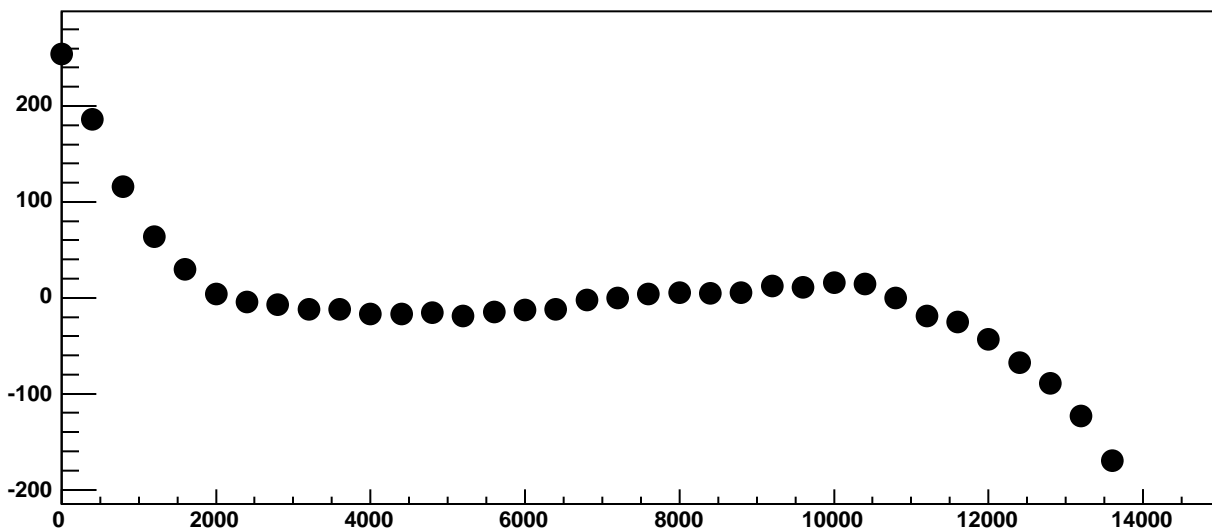
Chip 2, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



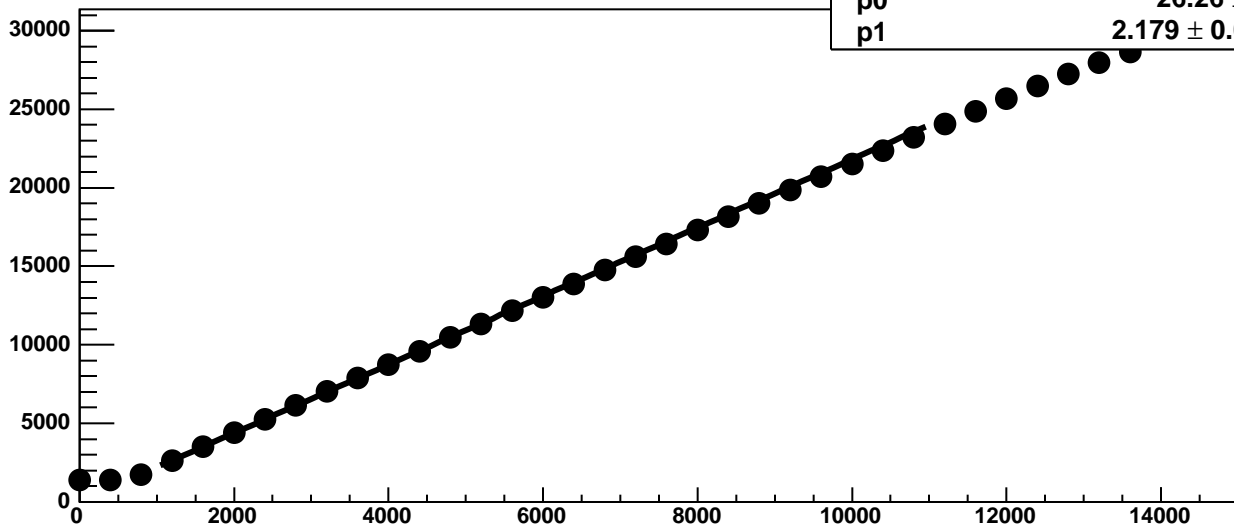
Chip 2, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



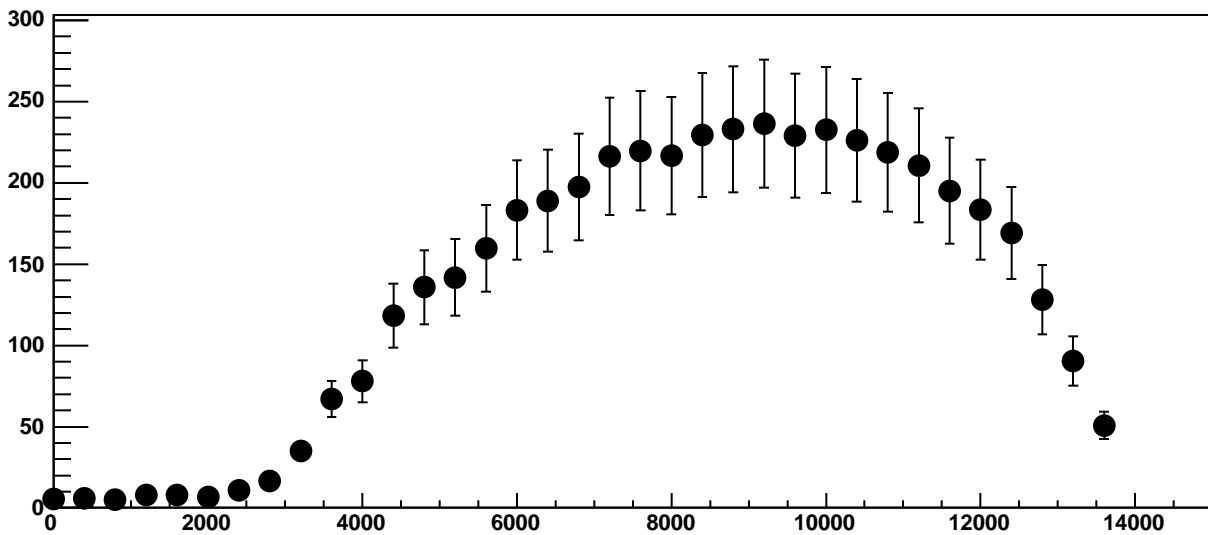
Chip 2, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



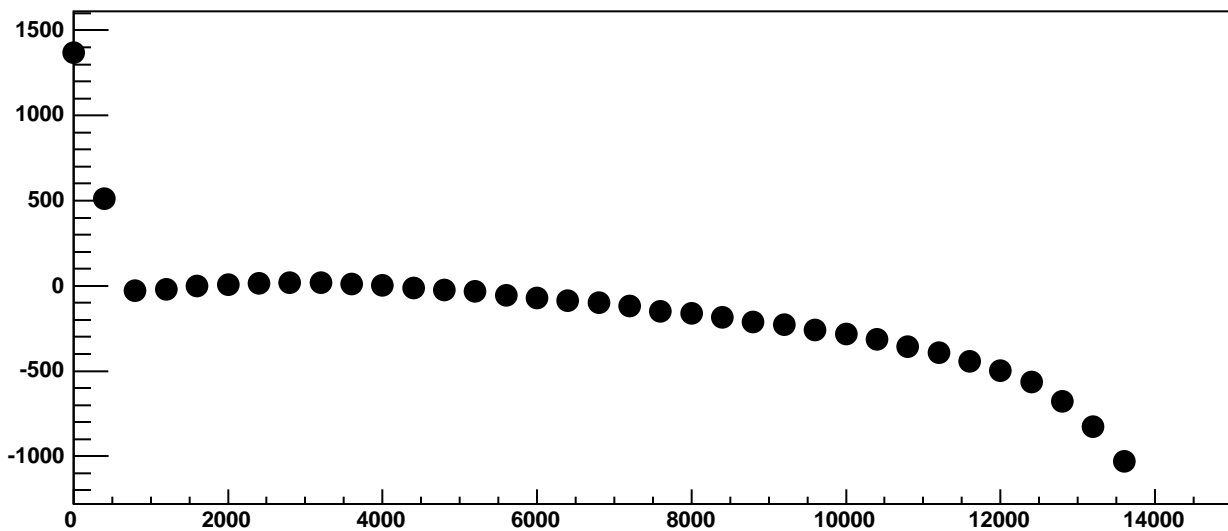
Chip 2, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC



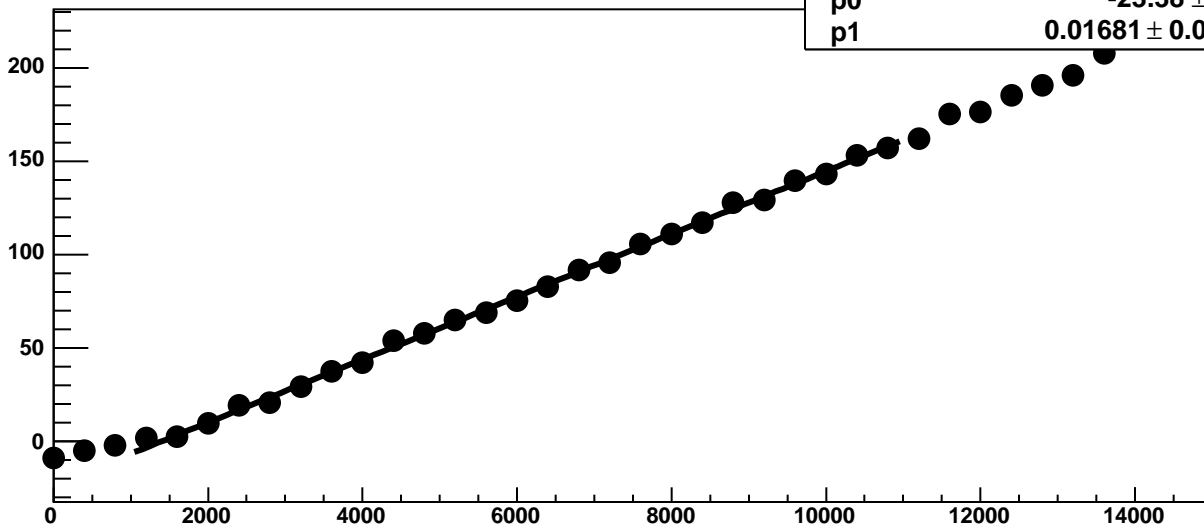
Chip 2, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC

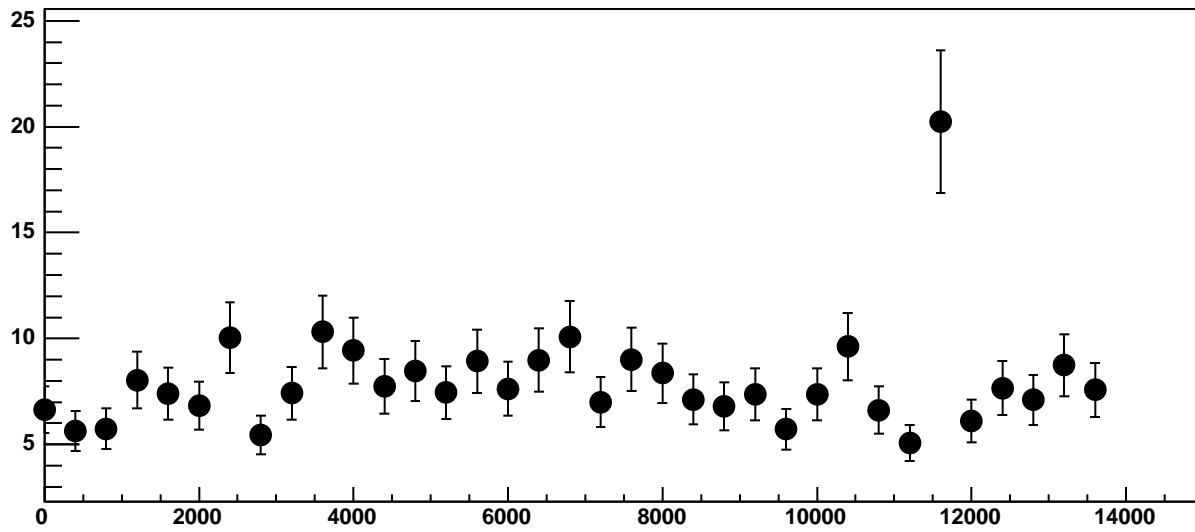


Chip 2, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

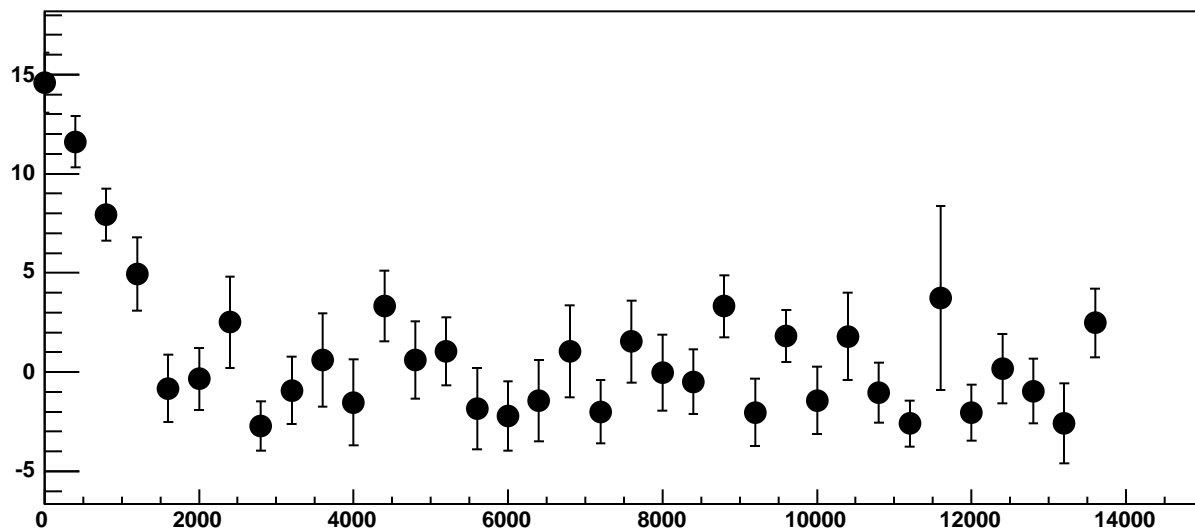


χ^2 / ndf 33.33 / 23
p0 -23.38 ± 0.7953
p1 0.01681 ± 0.0001167

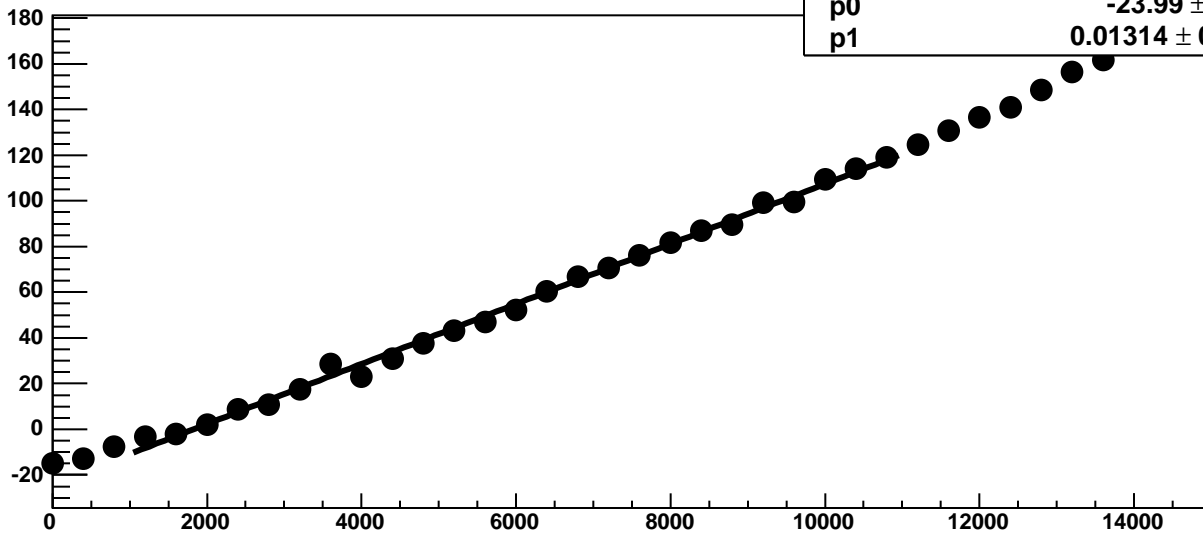
Chip 2, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



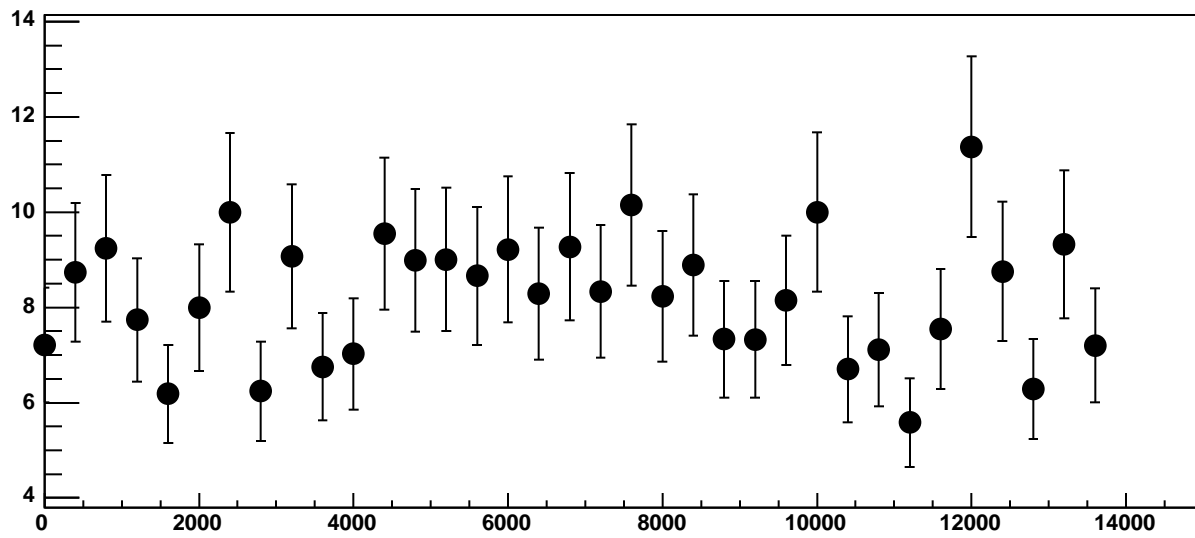
Chip 2, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



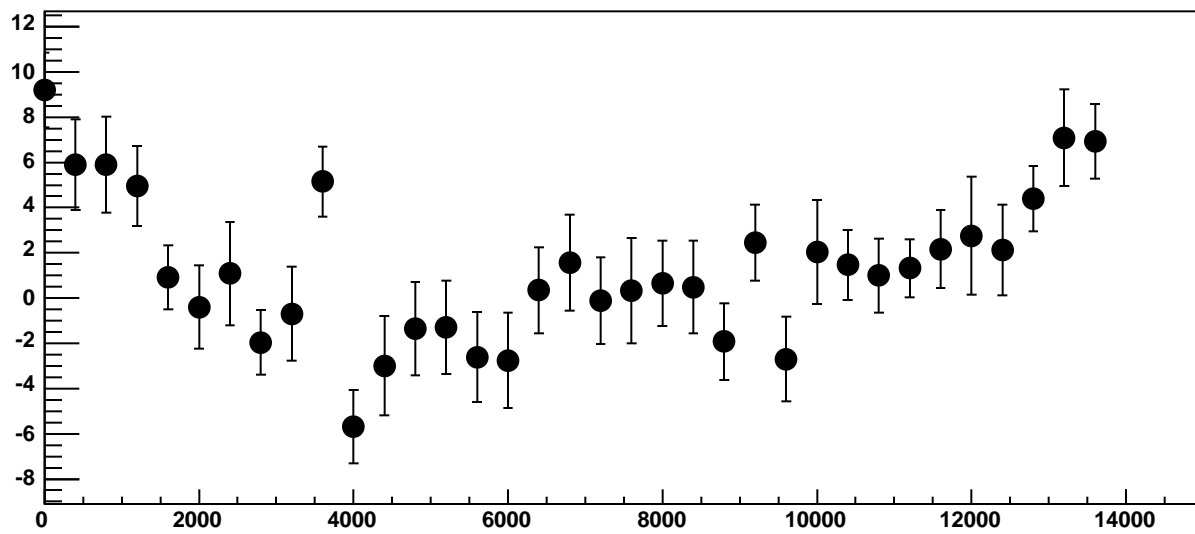
Chip 2, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



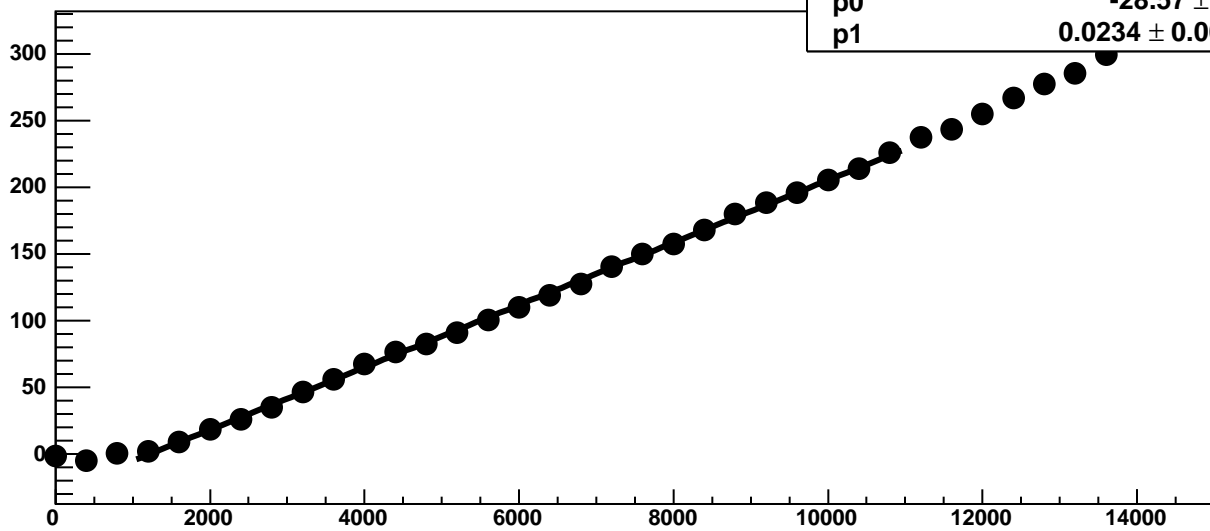
Chip 2, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

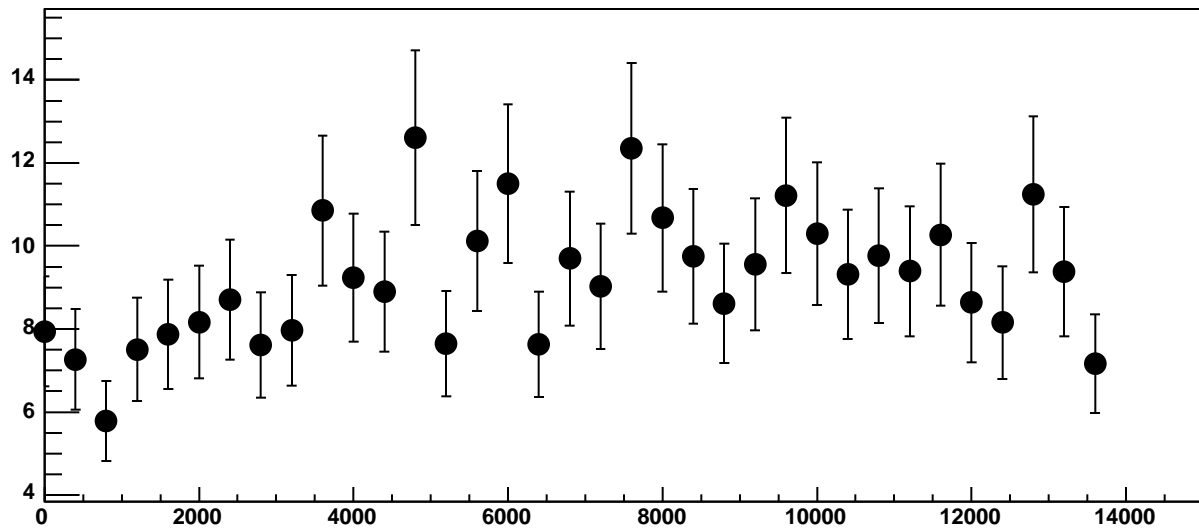


Chip 2, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

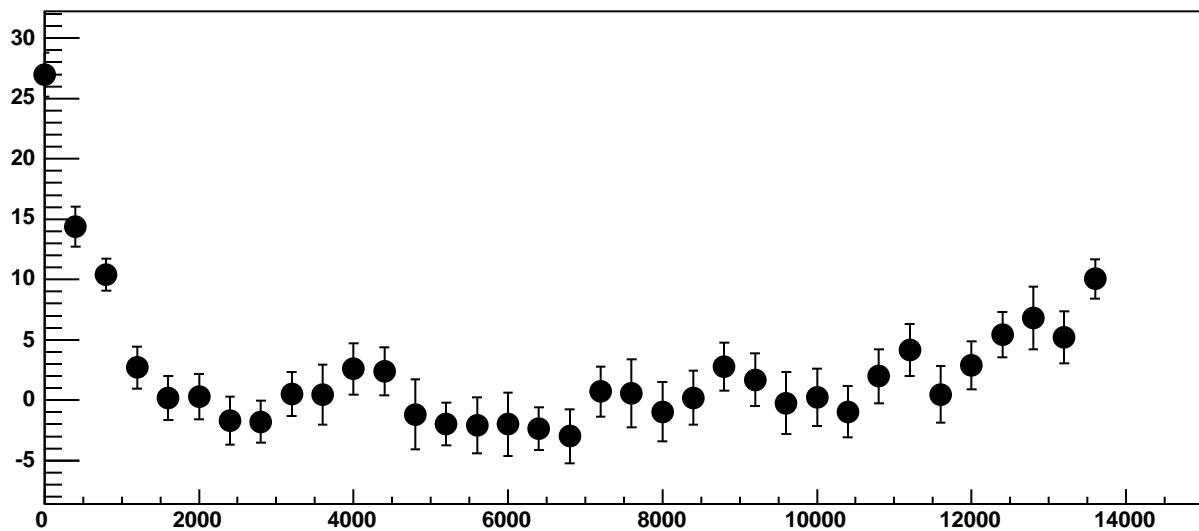


χ^2 / ndf 17.66 / 23
p0 -28.57 ± 0.8958
p1 0.0234 ± 0.0001422

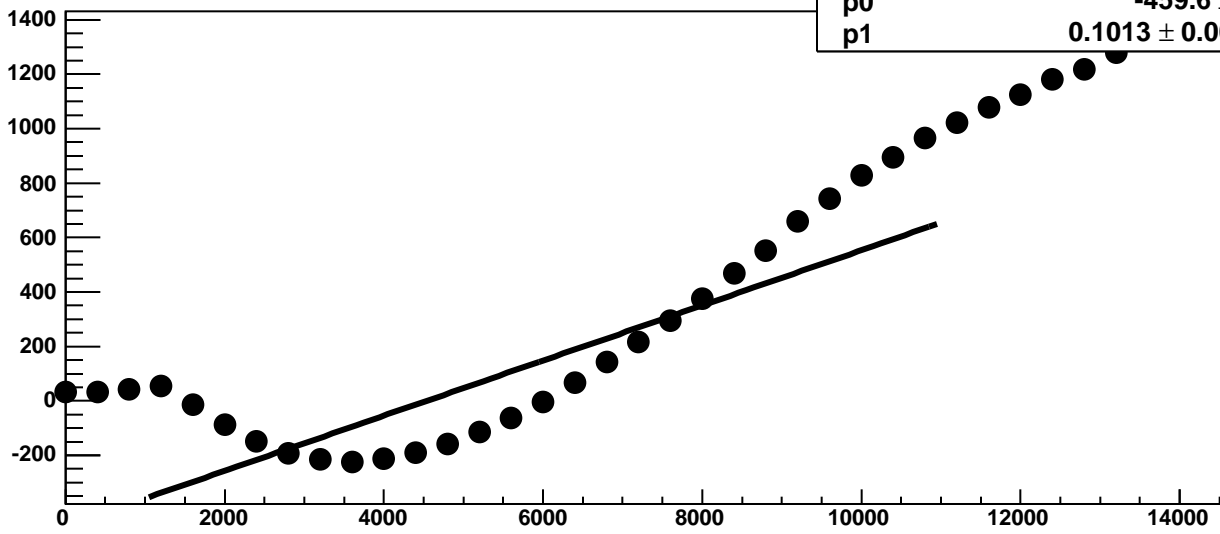
Chip 2, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC

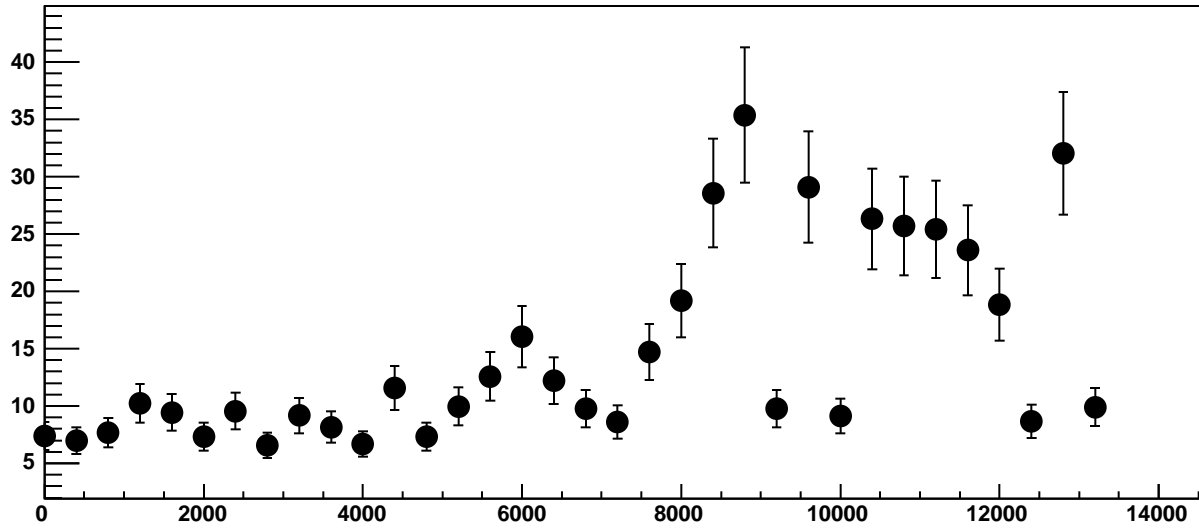


Chip 2, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

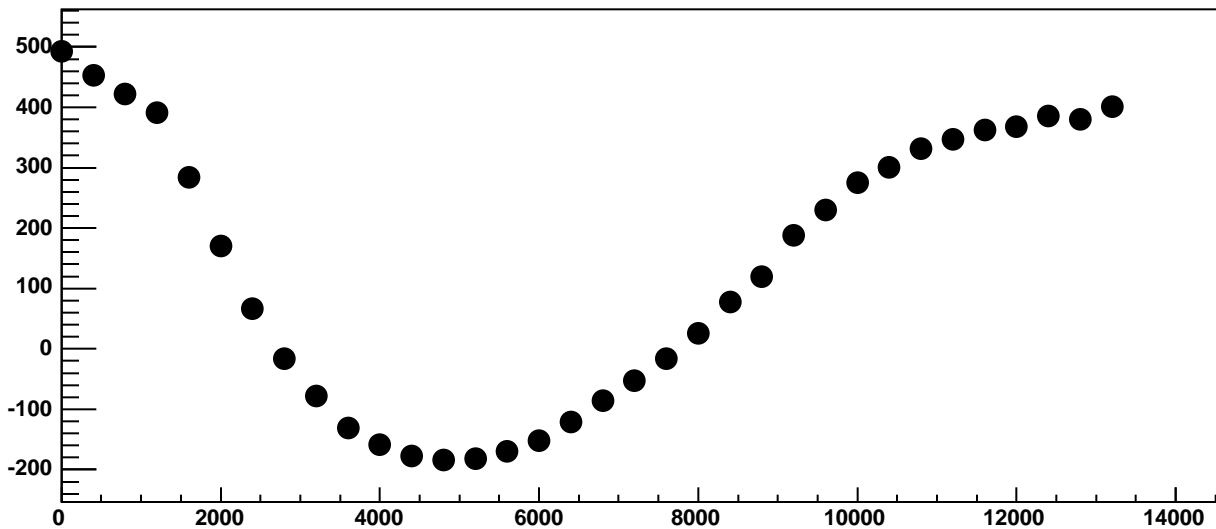


χ^2 / ndf 1.369e+05 / 23
p0 -459.6 ± 1.005
p1 0.1013 ± 0.0001872

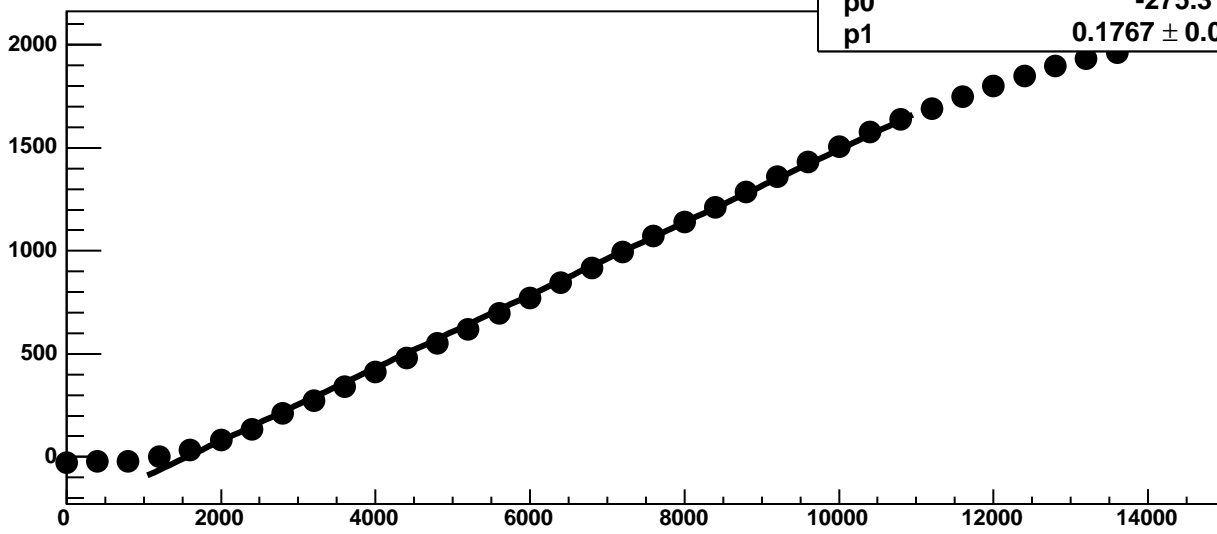
Chip 2, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



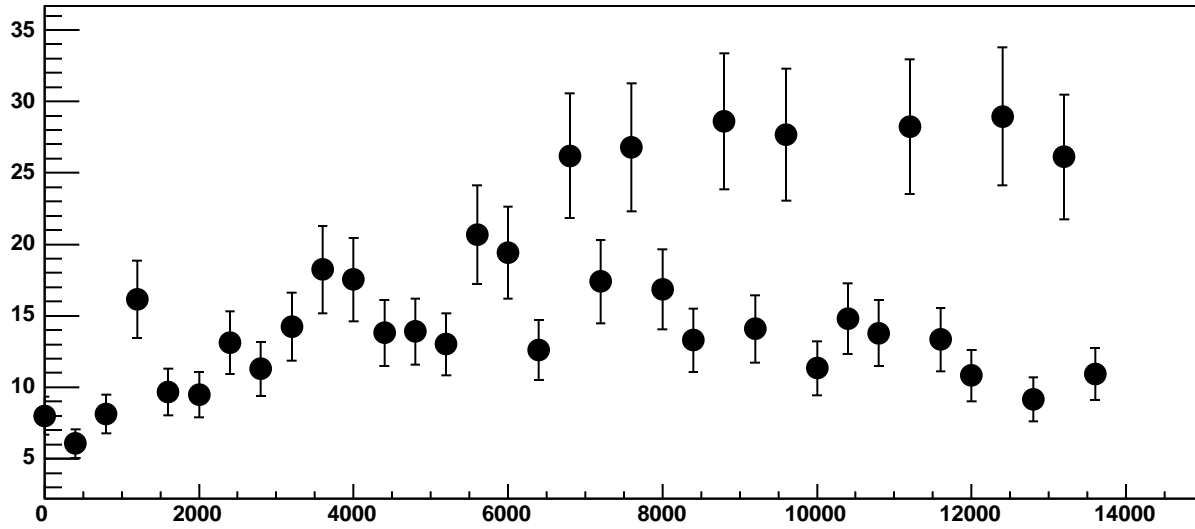
Chip 2, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



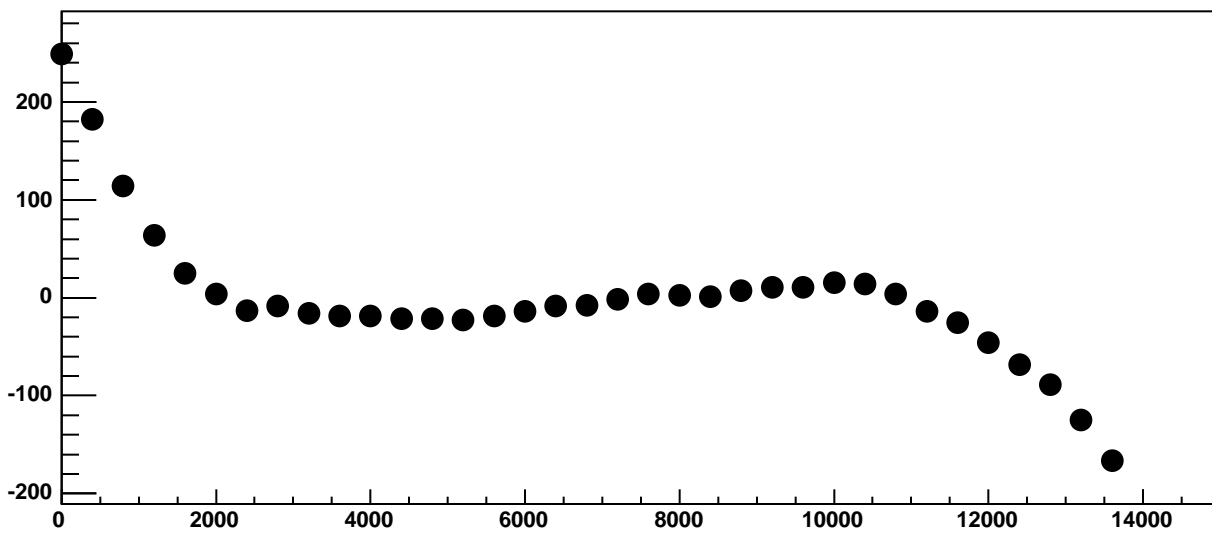
Chip 2, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



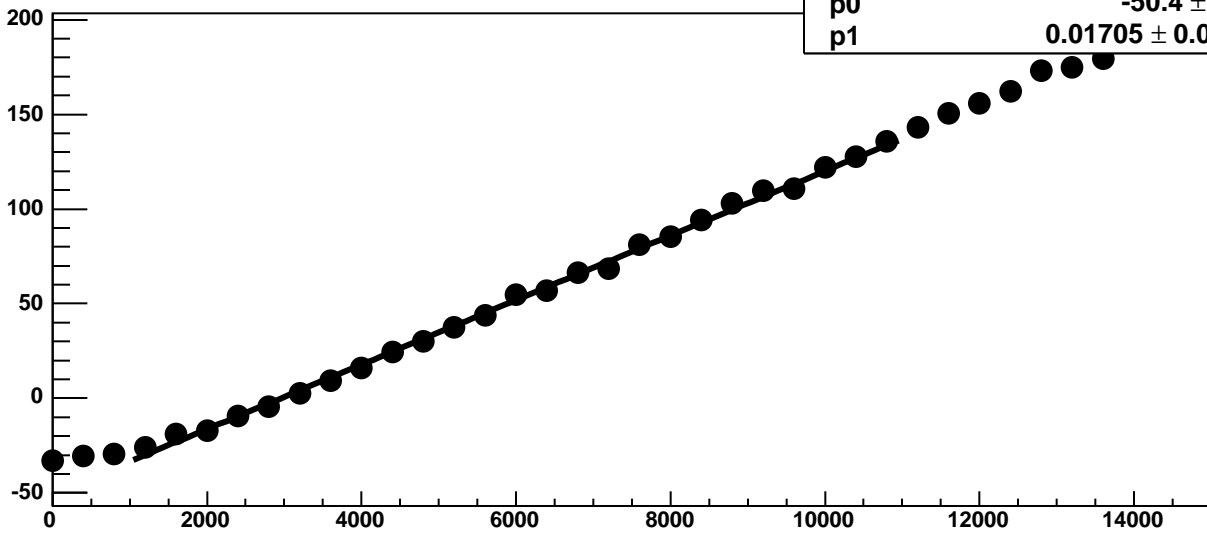
Chip 2, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

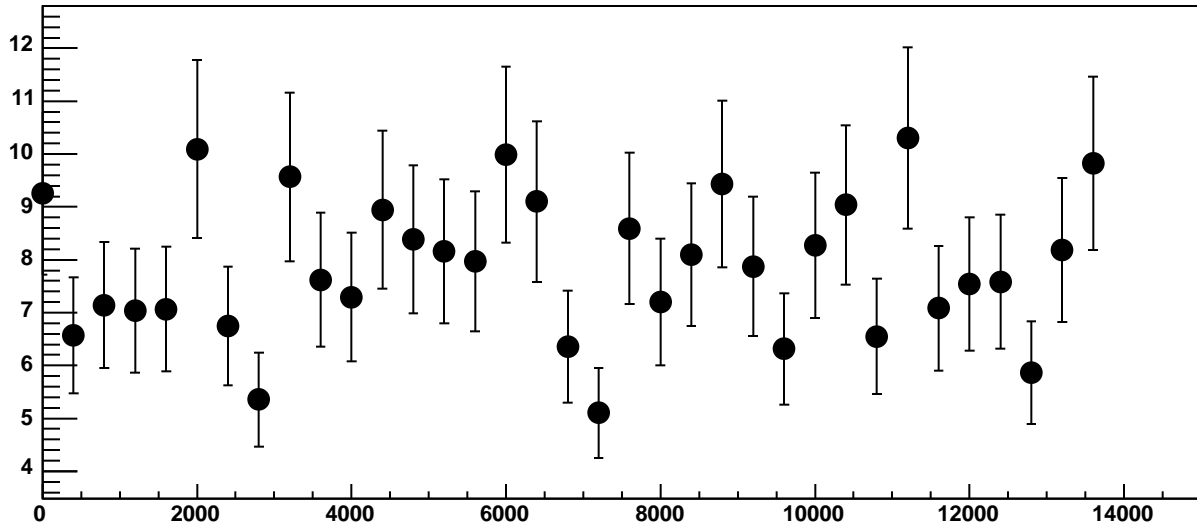


Chip 2, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

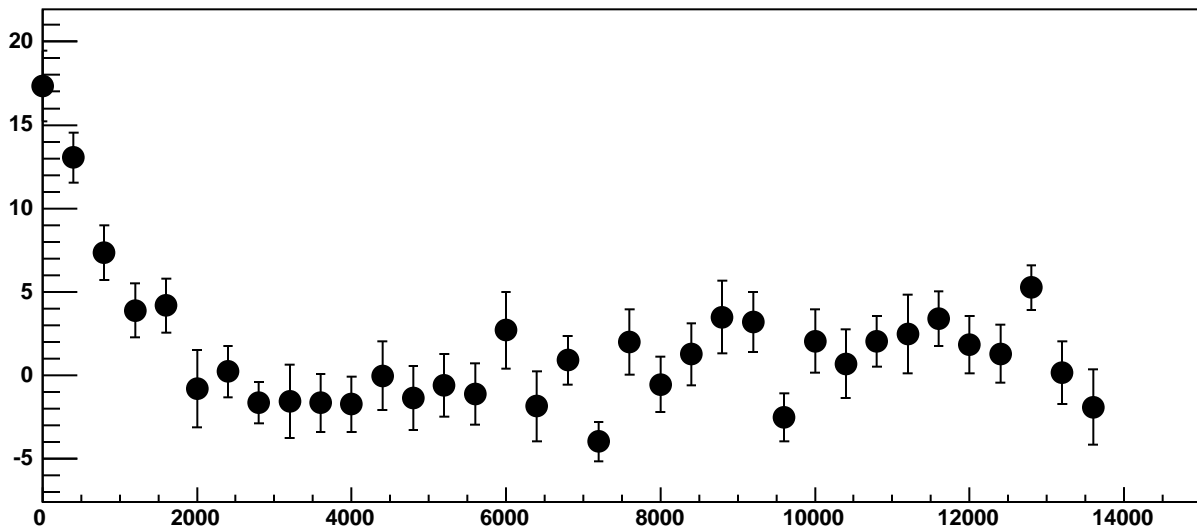


χ^2 / ndf 45.37 / 23
p0 -50.4 ± 0.7787
p1 0.01705 ± 0.0001171

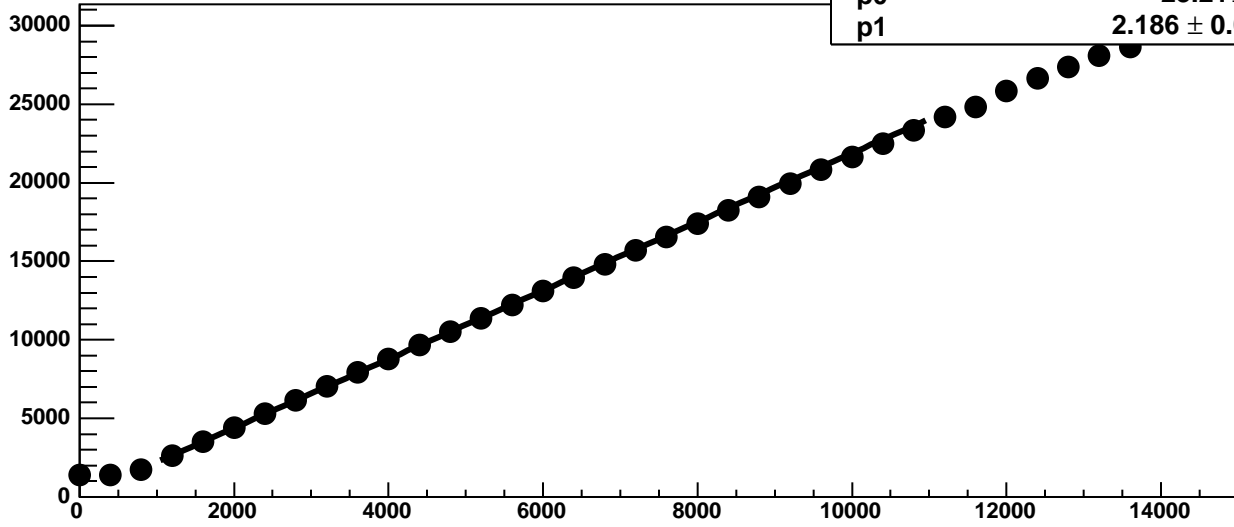
Chip 2, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

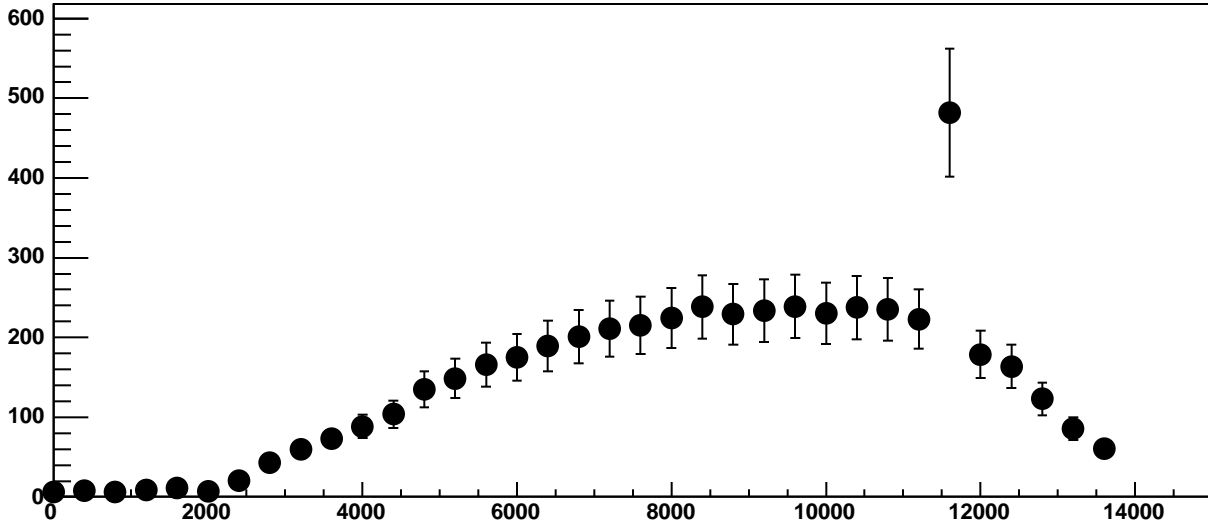


Chip 2, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC

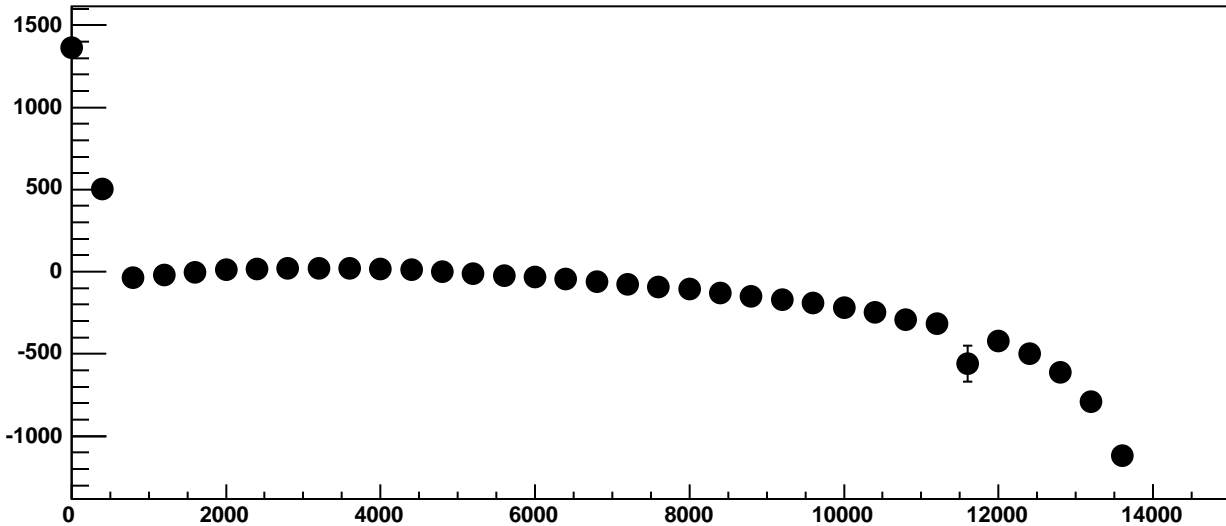


χ^2 / ndf 286 / 23
p0 23.21 ± 2.923
p1 2.186 ± 0.001515

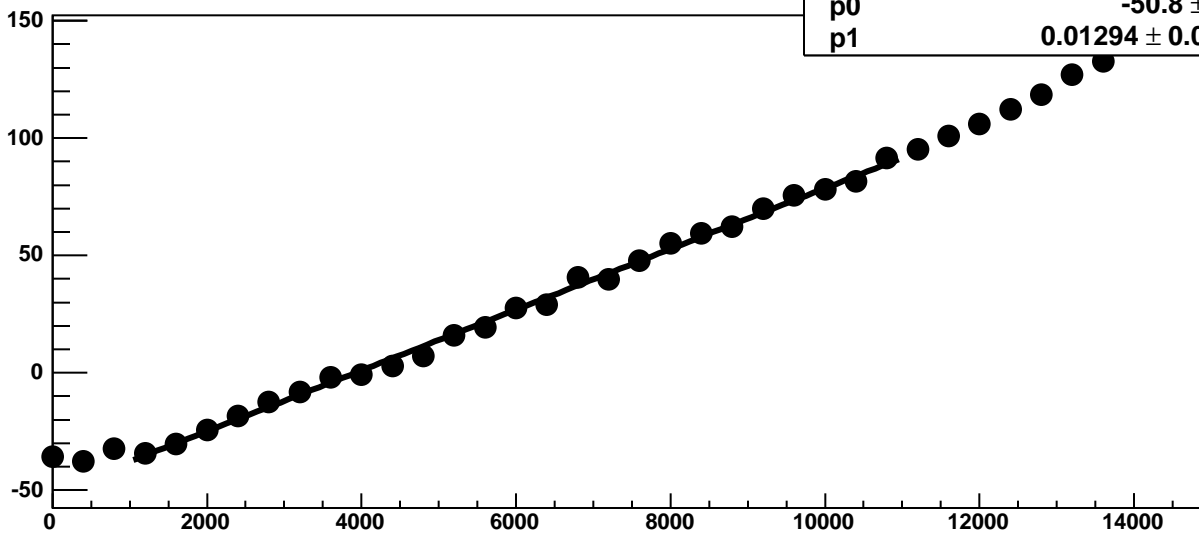
Chip 2, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC

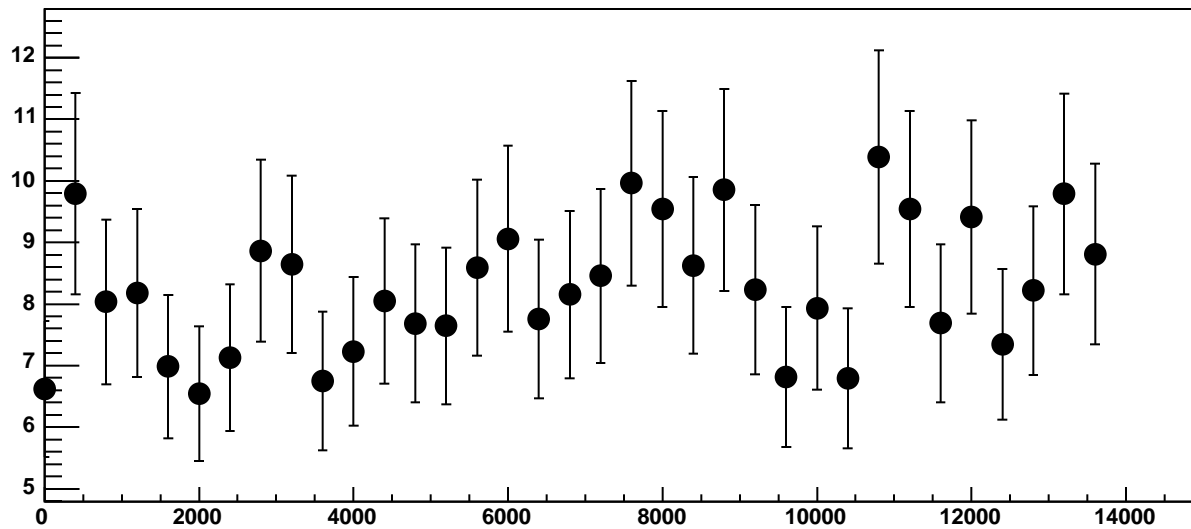


Chip 2, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

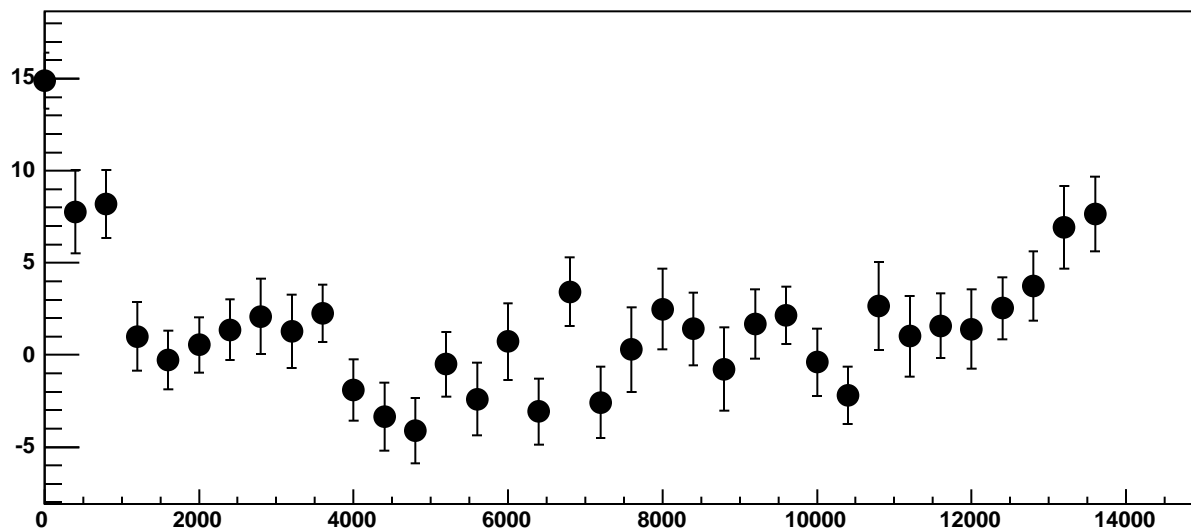


χ^2 / ndf 32.55 / 23
p0 -50.8 ± 0.8002
p1 0.01294 ± 0.0001237

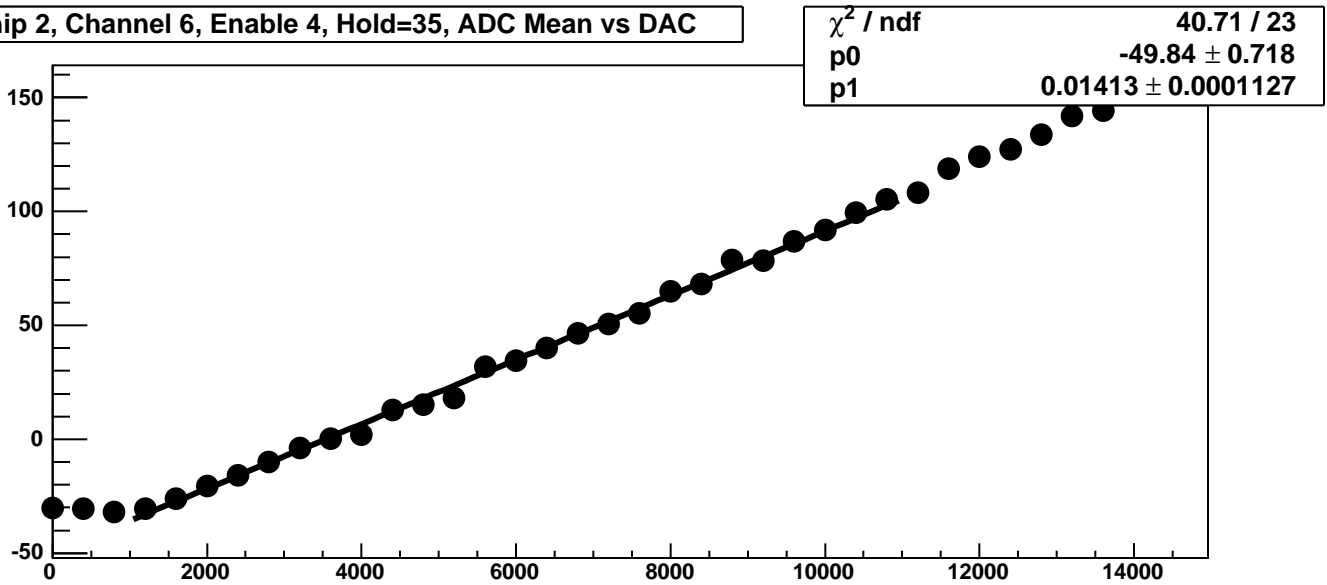
Chip 2, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



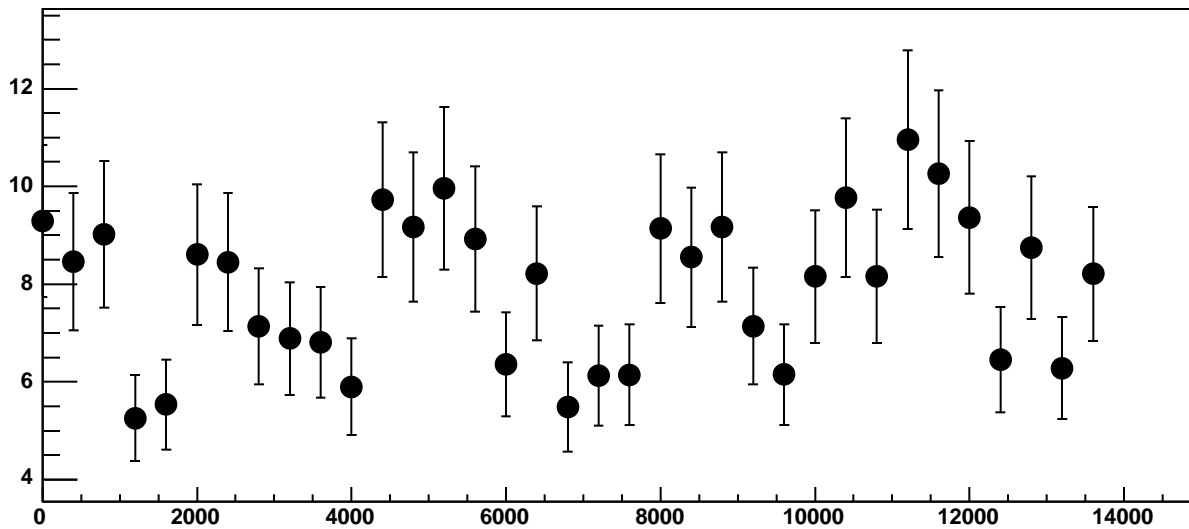
Chip 2, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



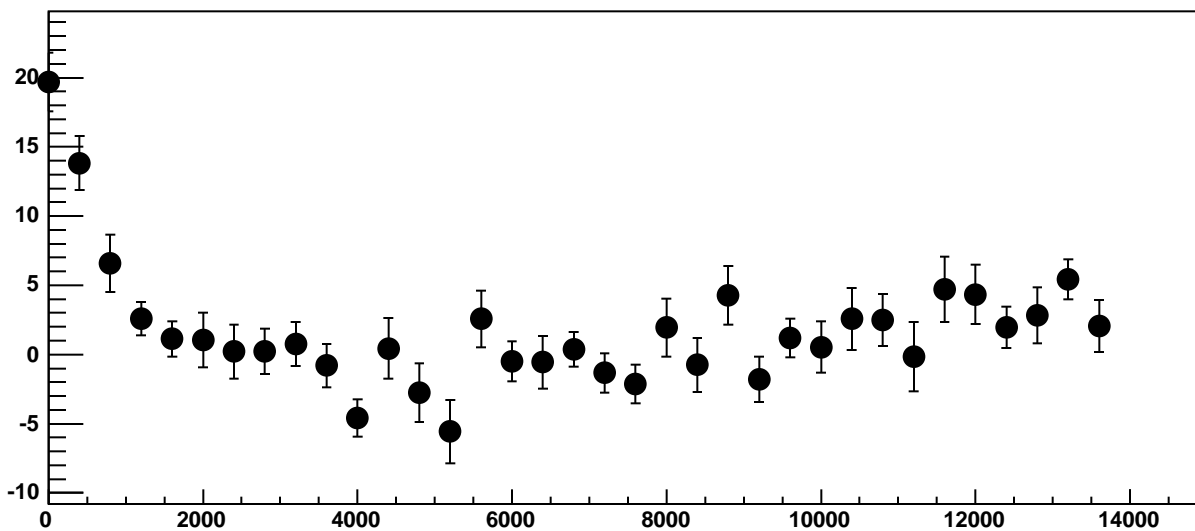
Chip 2, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



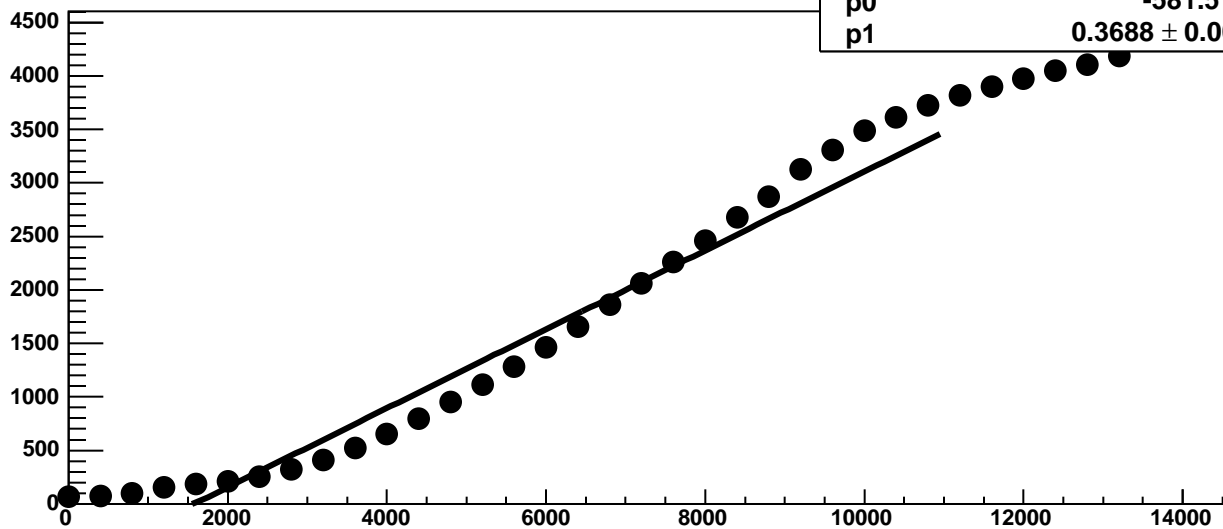
Chip 2, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

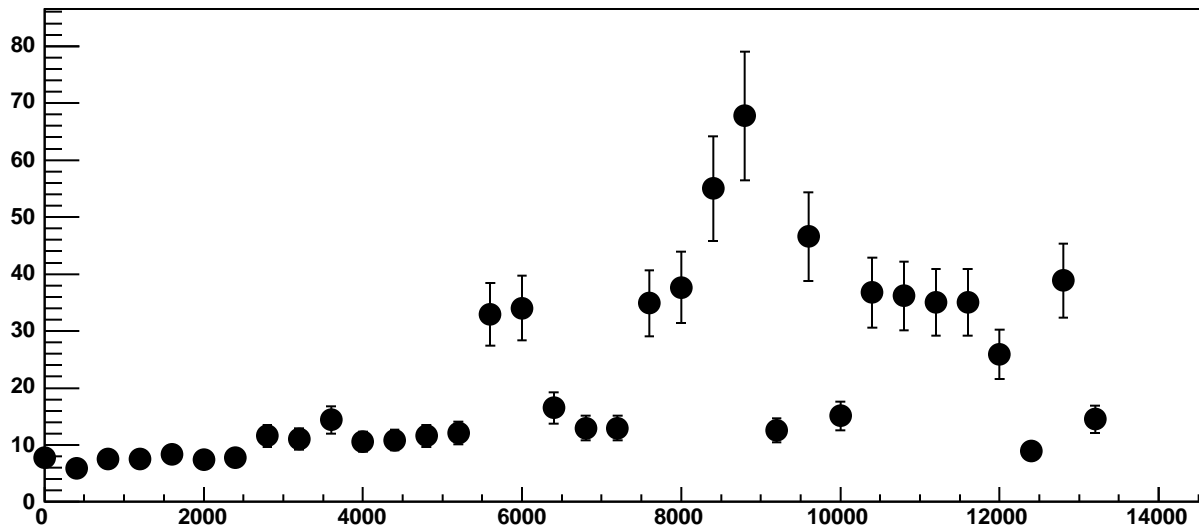


Chip 2, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC

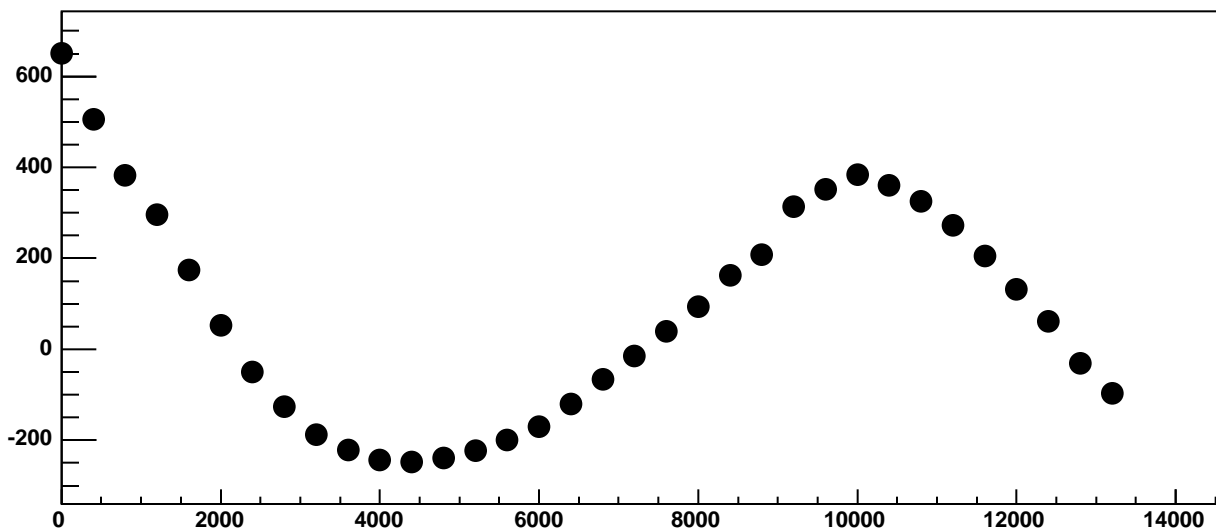


χ^2 / ndf 1.177e+05 / 23
p0 -581.5 ± 1.091
p1 0.3688 ± 0.0002364

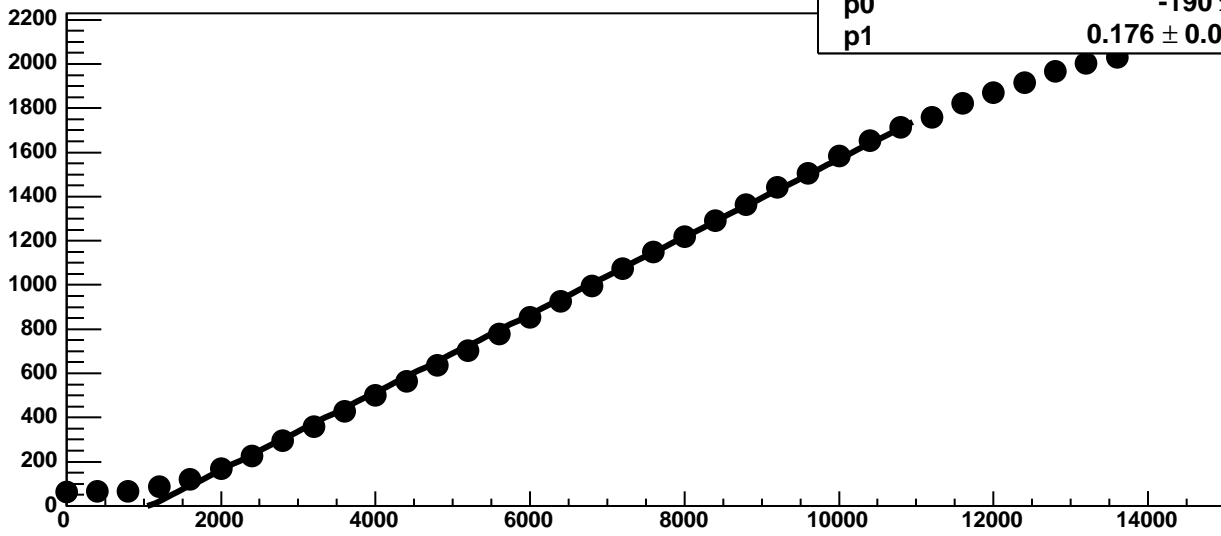
Chip 2, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC

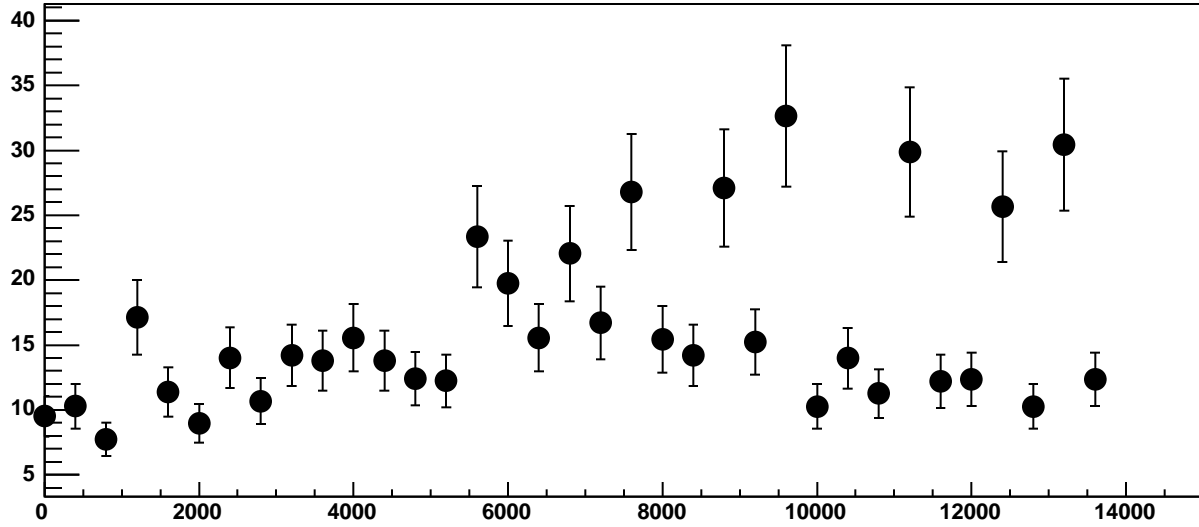


Chip 2, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

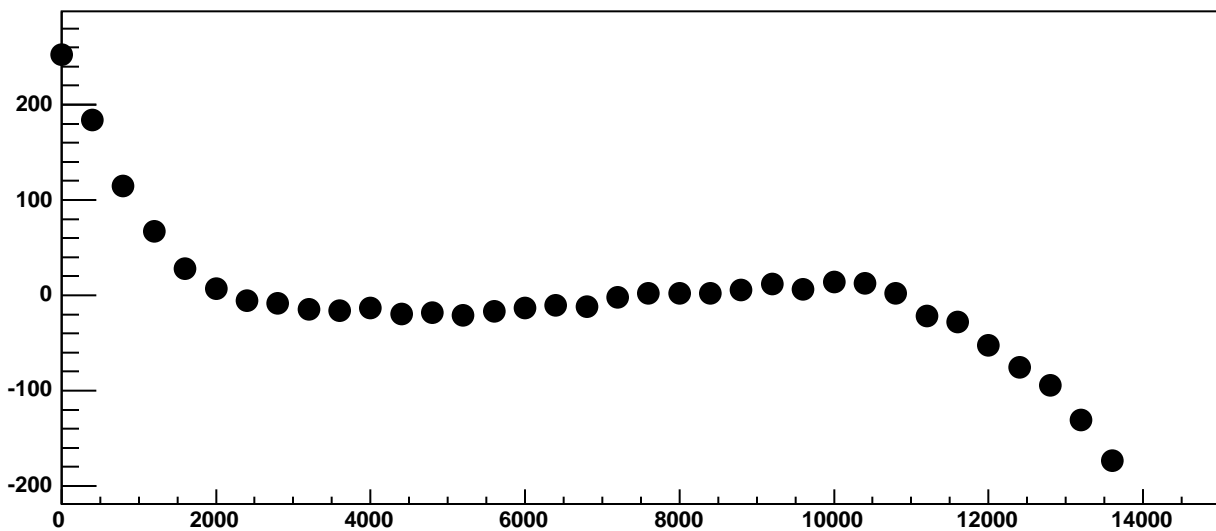


χ^2 / ndf 727.9 / 23
p0 -190 ± 1.324
p1 0.176 ± 0.0002071

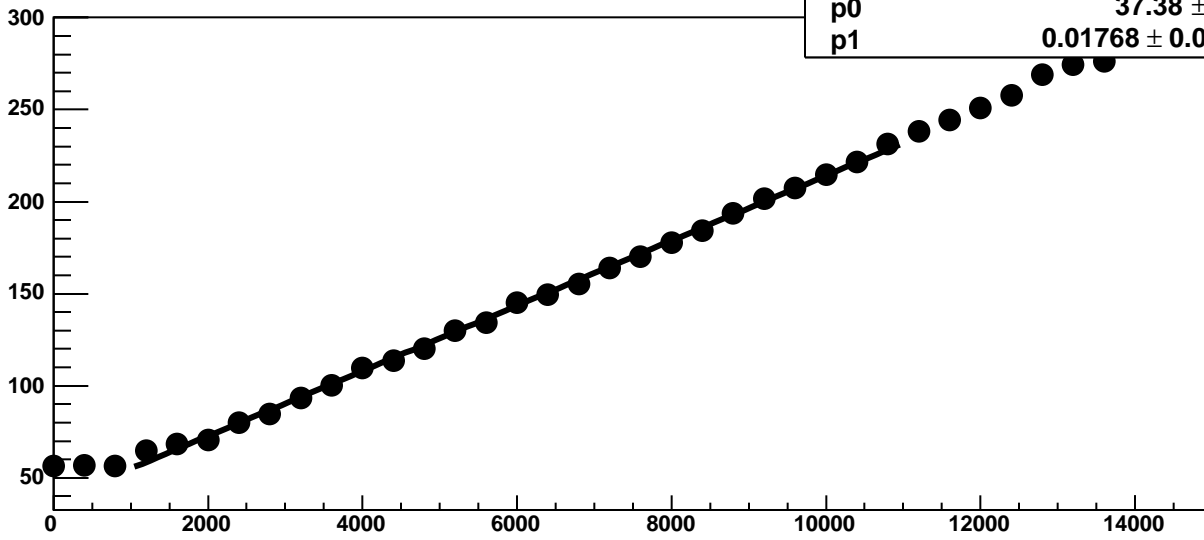
Chip 2, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

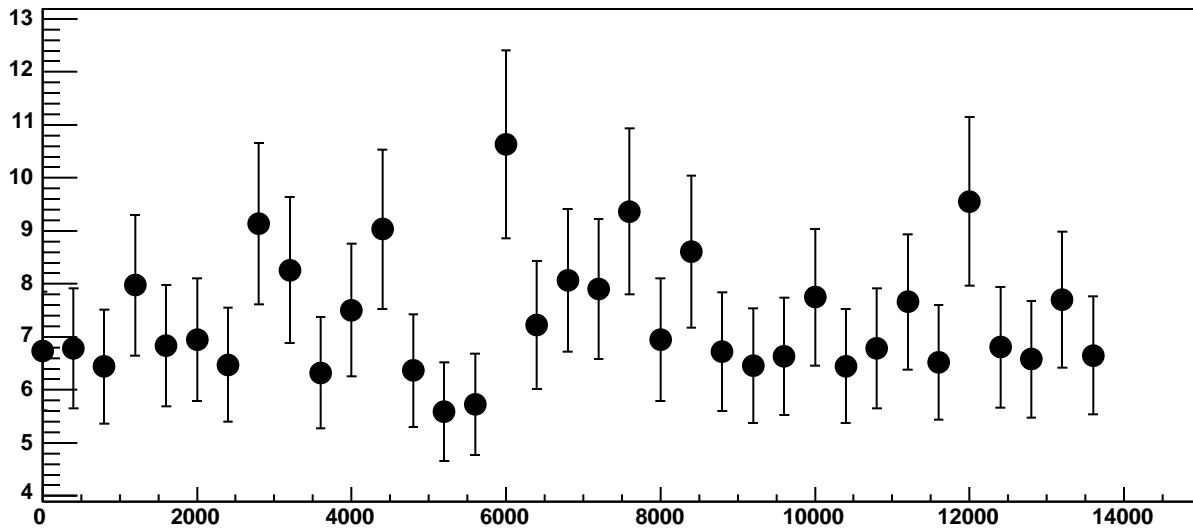


Chip 2, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

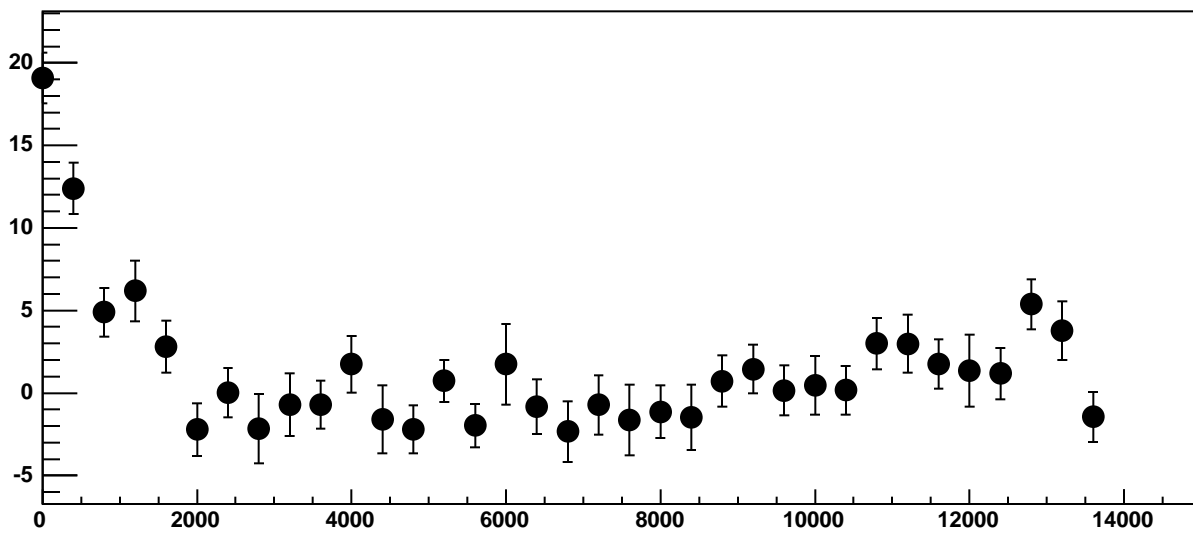


χ^2 / ndf 33.58 / 23
p0 37.38 ± 0.7586
p1 0.01768 ± 0.0001128

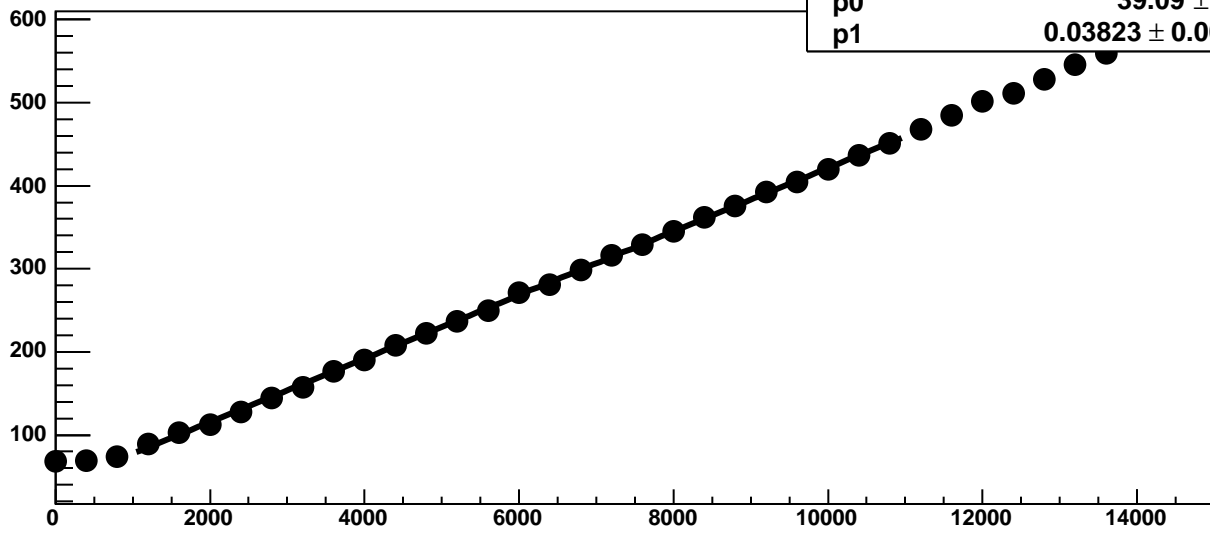
Chip 2, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

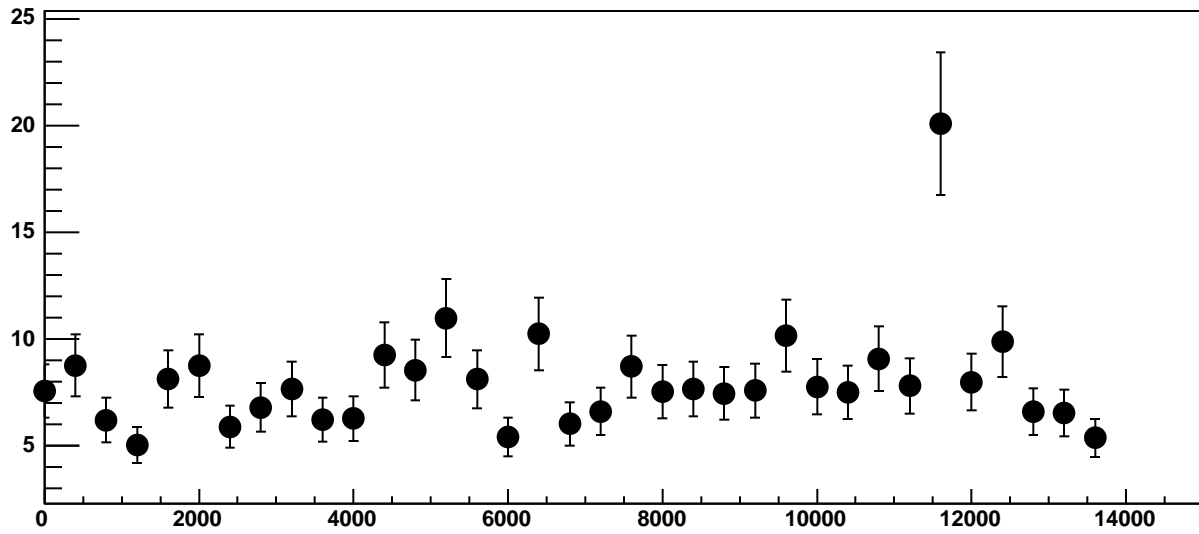


Chip 2, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC

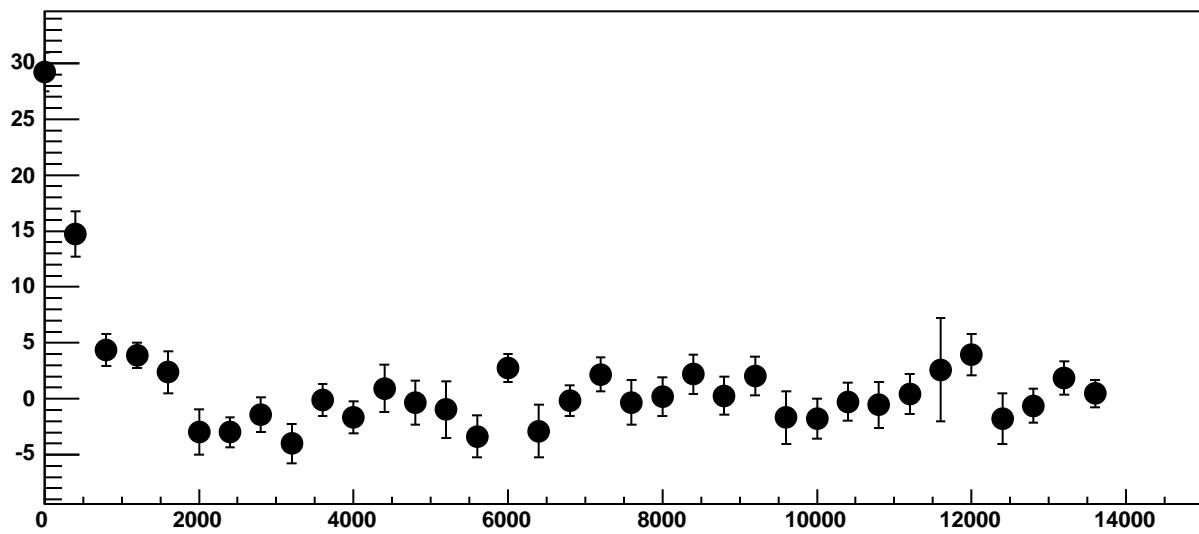


χ^2 / ndf 44.1 / 23
p0 39.09 ± 0.7262
p1 0.03823 ± 0.0001154

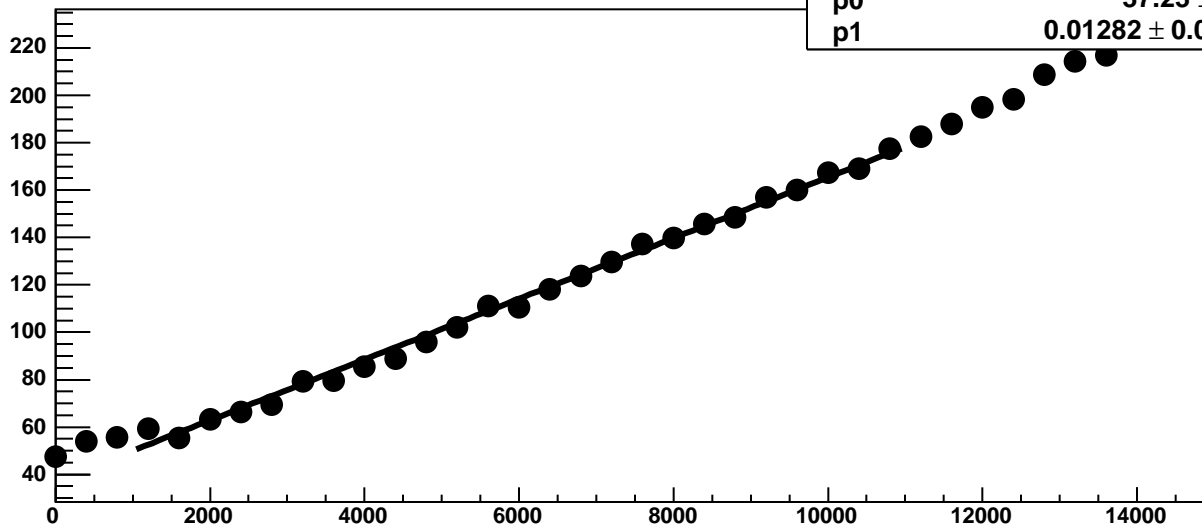
Chip 2, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

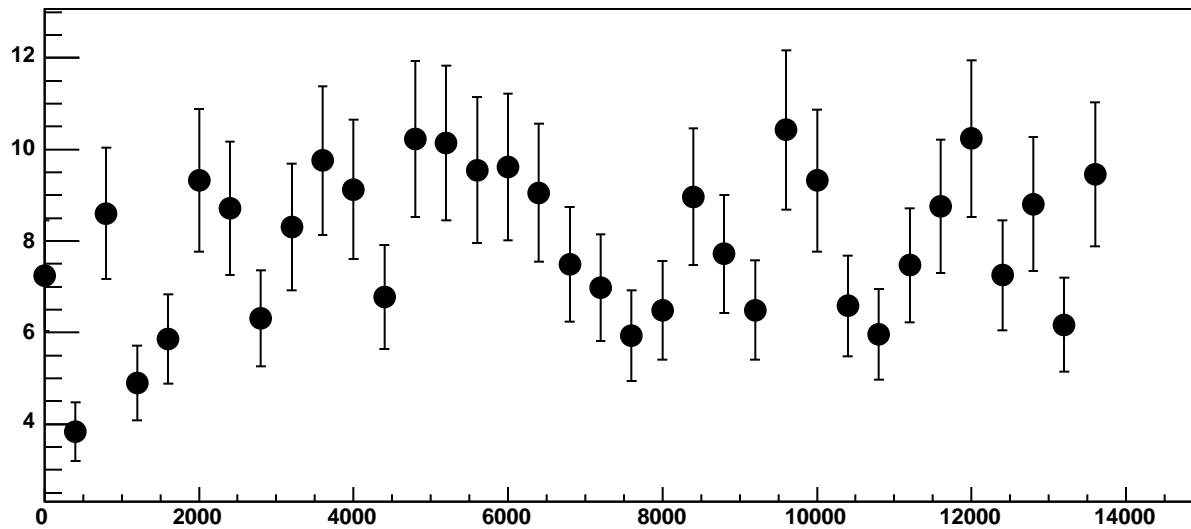


Chip 2, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

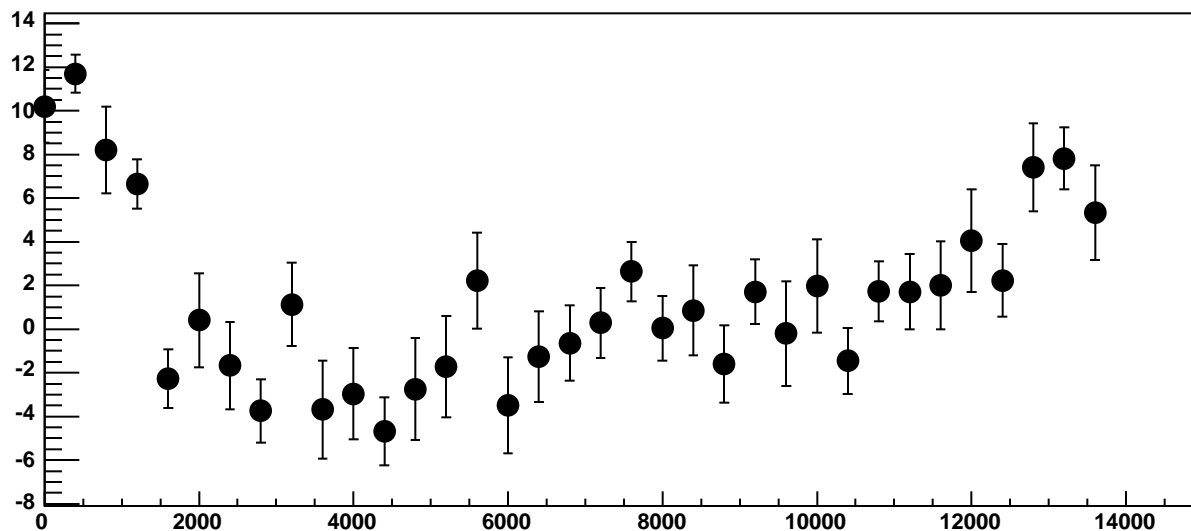


χ^2 / ndf 74.83 / 23
p0 37.23 ± 0.7251
p1 0.01282 ± 0.0001083

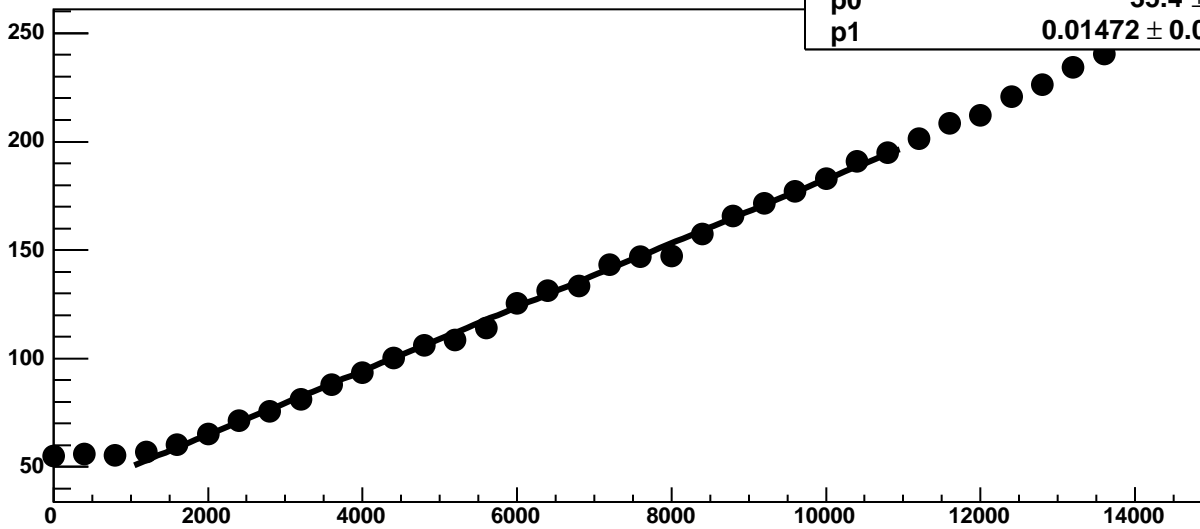
Chip 2, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



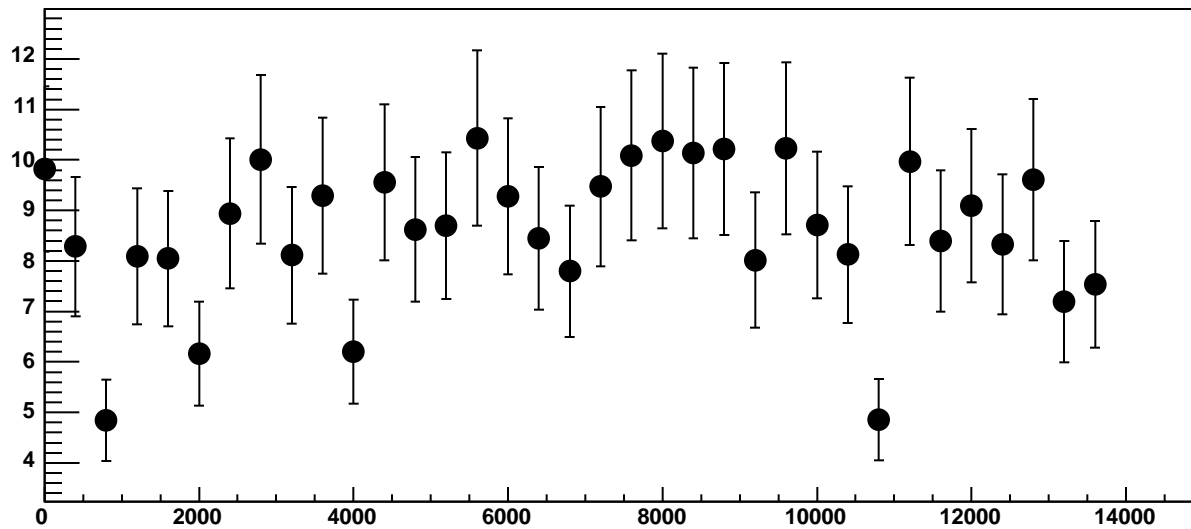
Chip 2, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



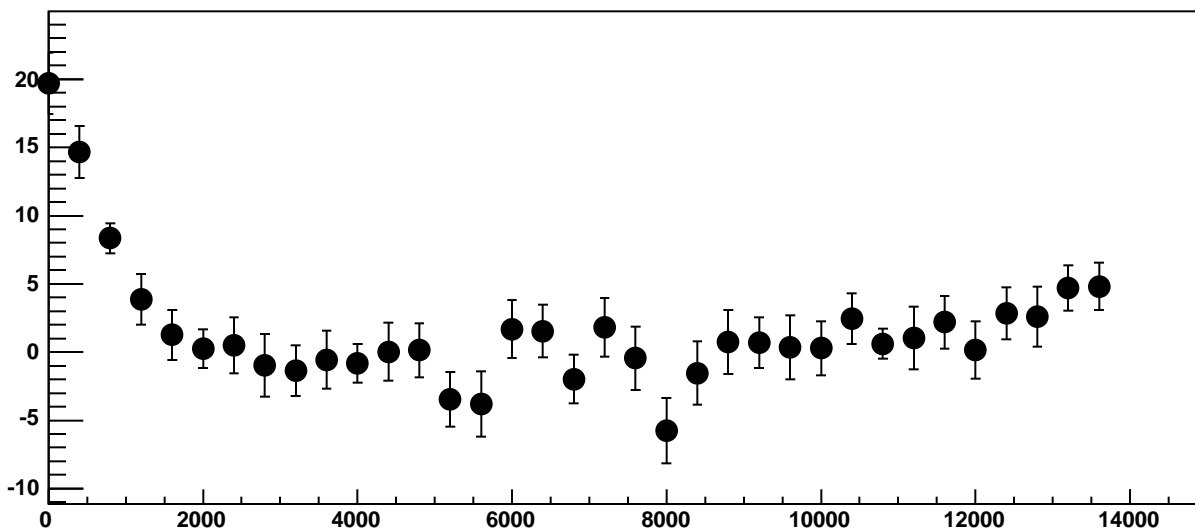
Chip 2, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



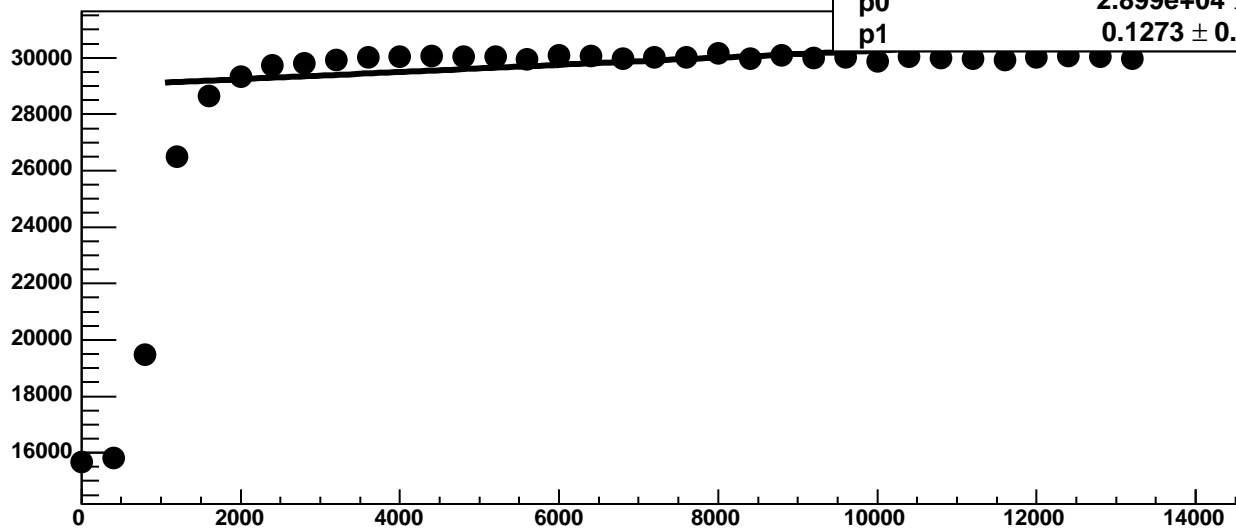
Chip 2, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



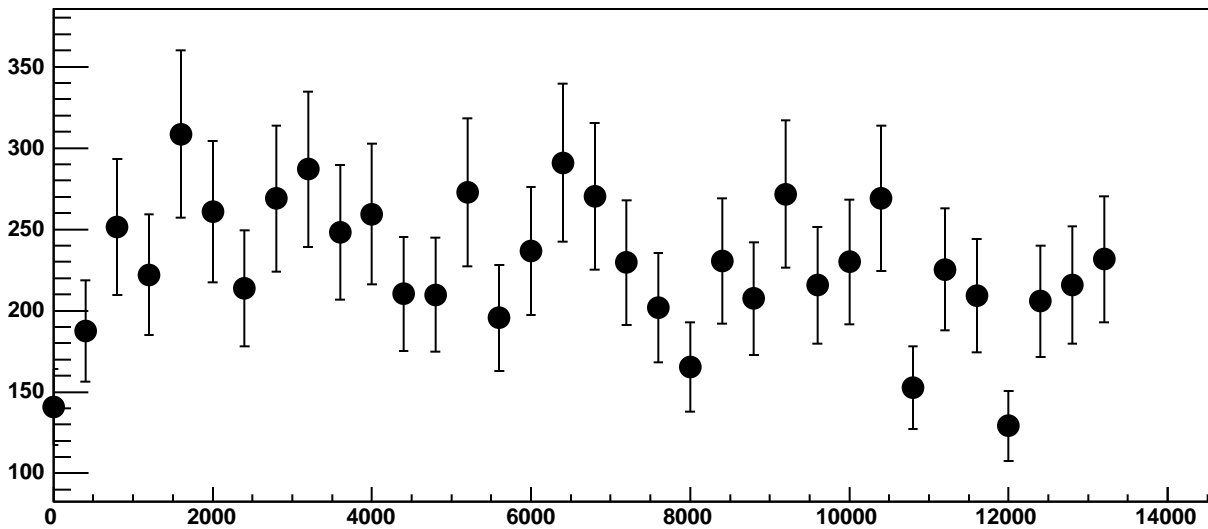
Chip 2, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



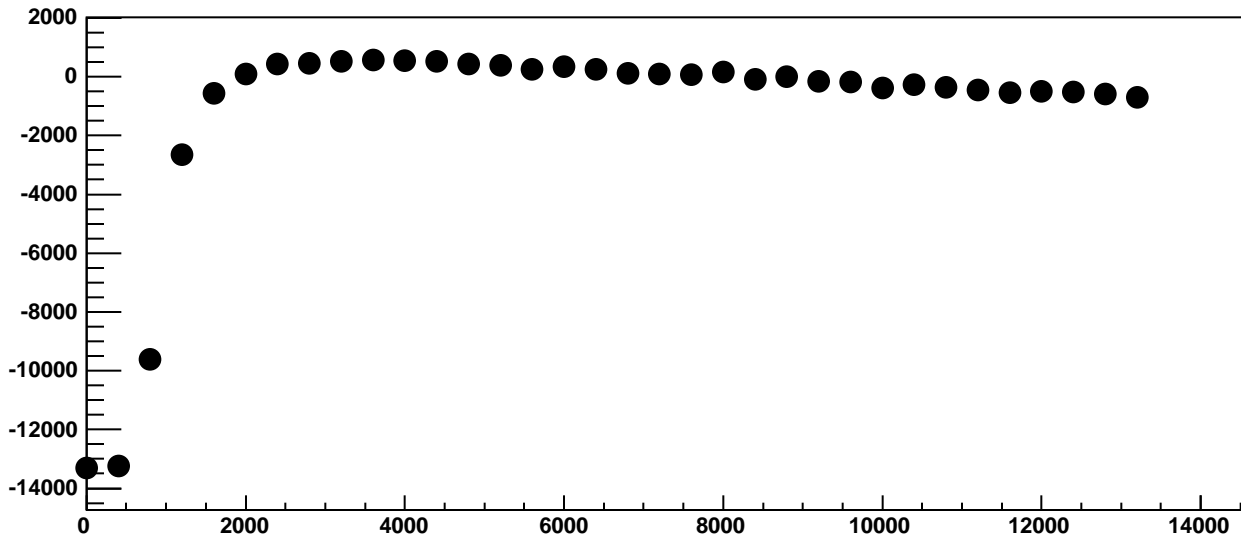
Chip 2, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC



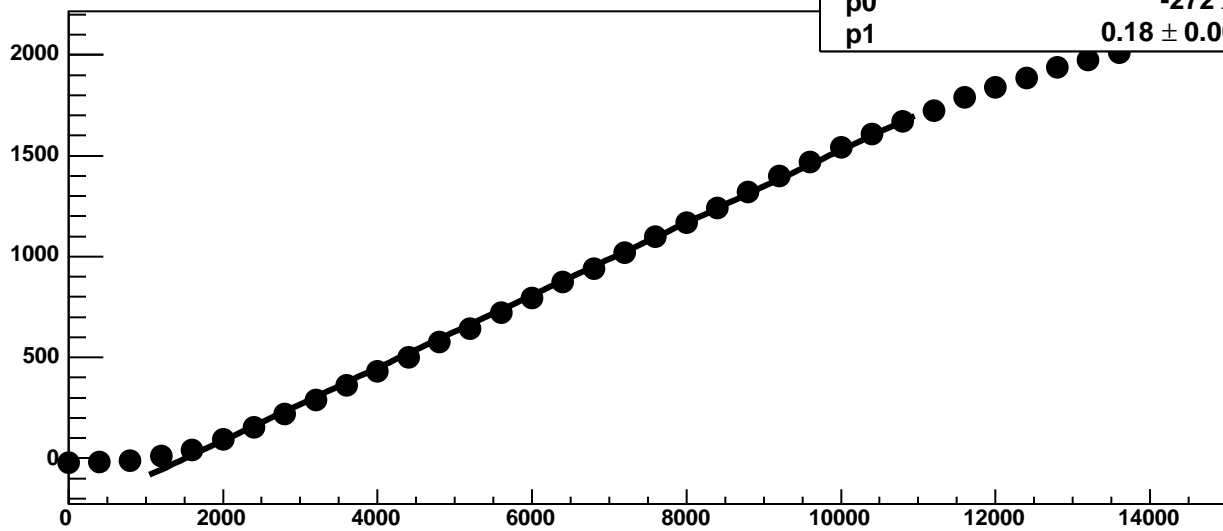
Chip 2, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



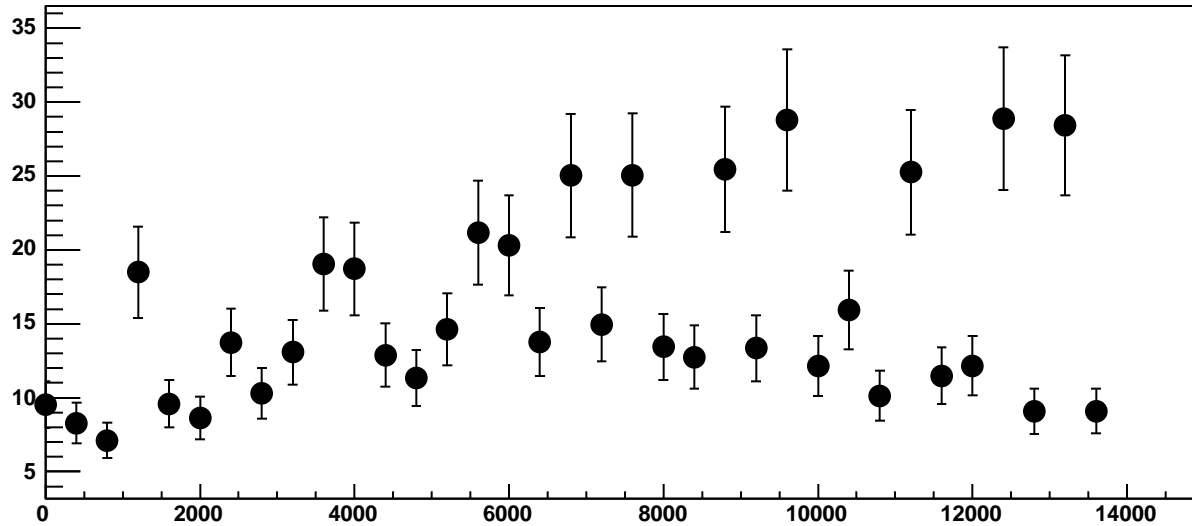
Chip 2, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC



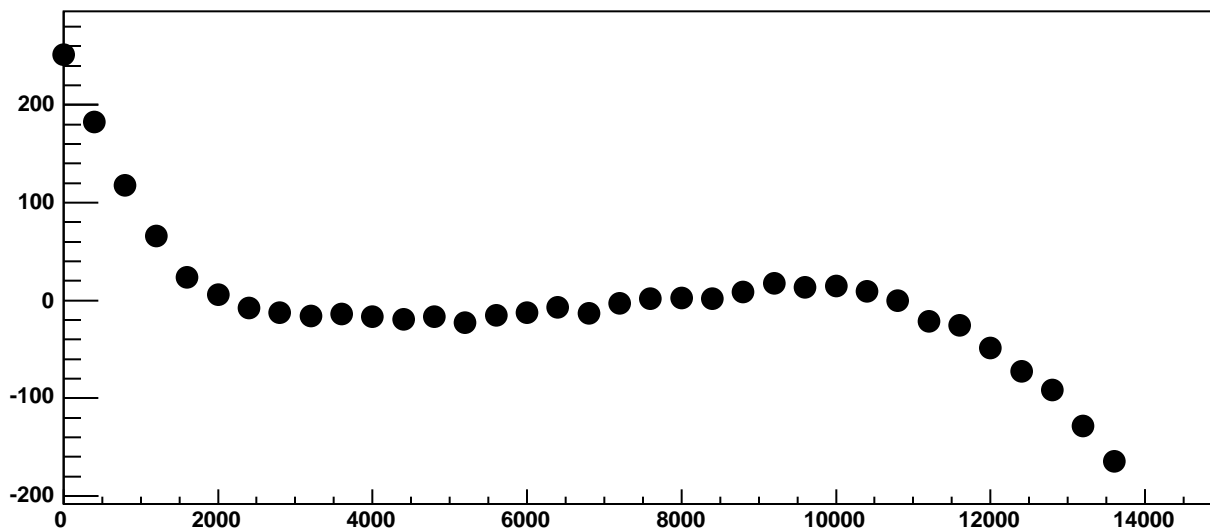
Chip 2, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



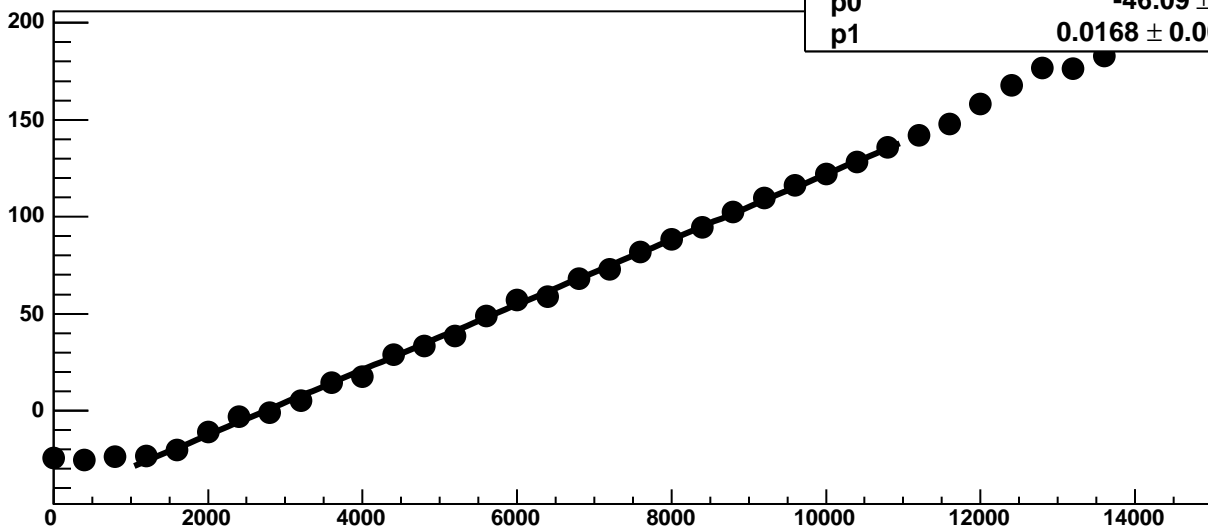
Chip 2, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

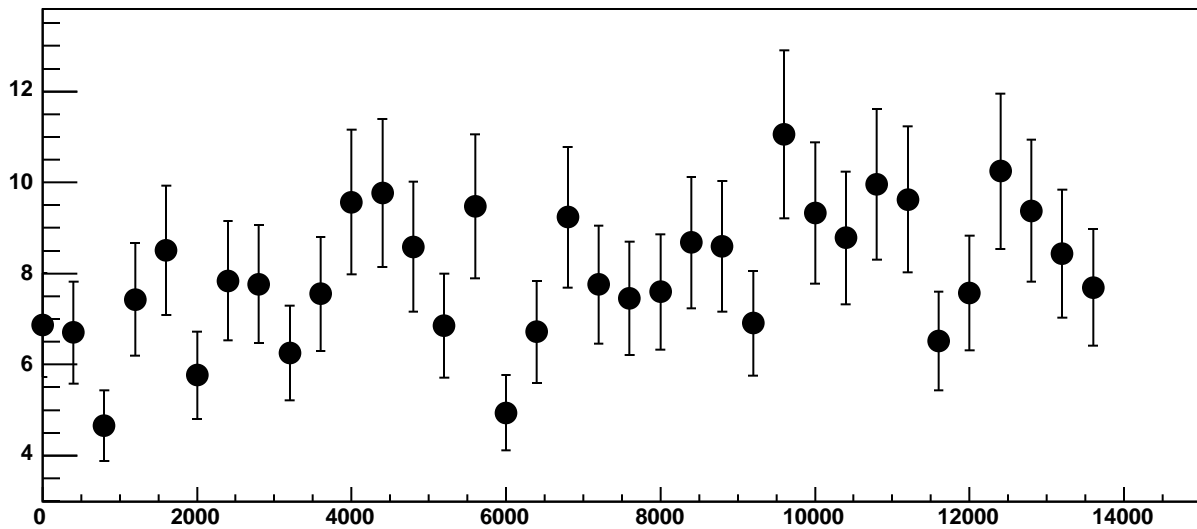


Chip 2, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

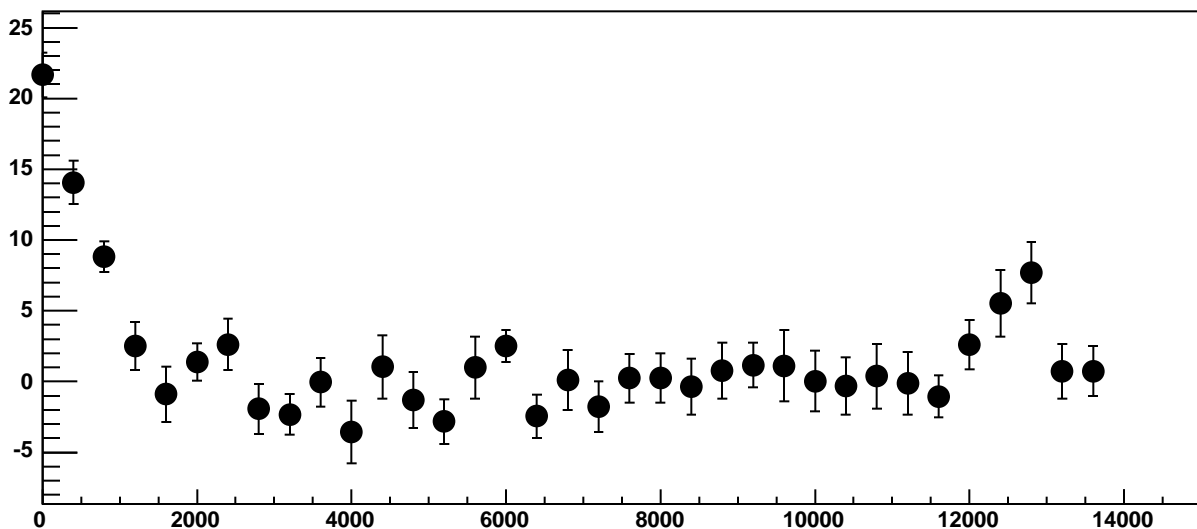


χ^2 / ndf 25.71 / 23
p0 -46.09 ± 0.8031
p1 0.0168 ± 0.0001278

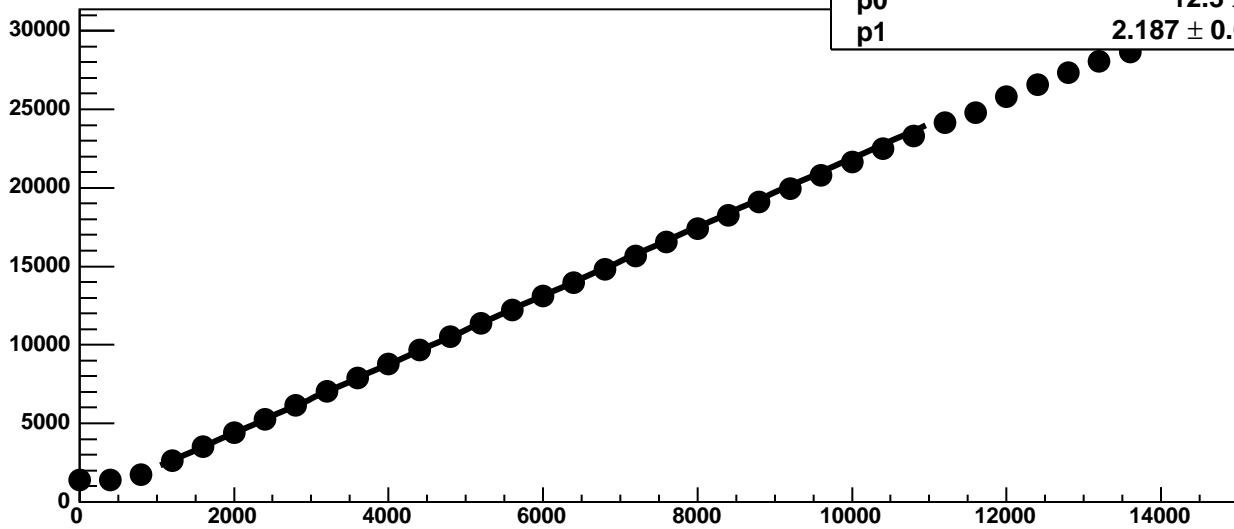
Chip 2, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

349.3 / 23

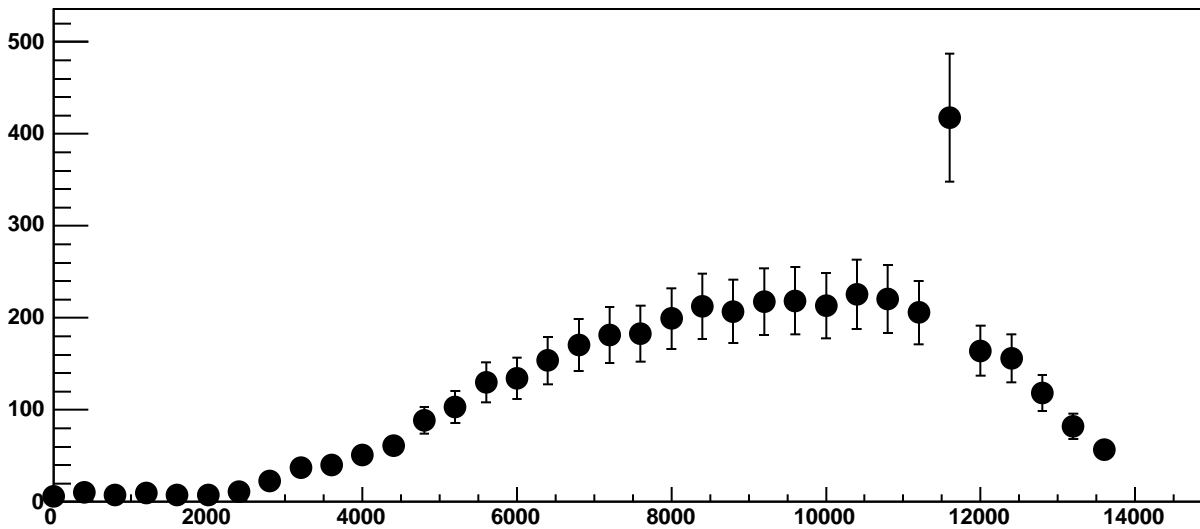
p0

12.3 ± 2.588

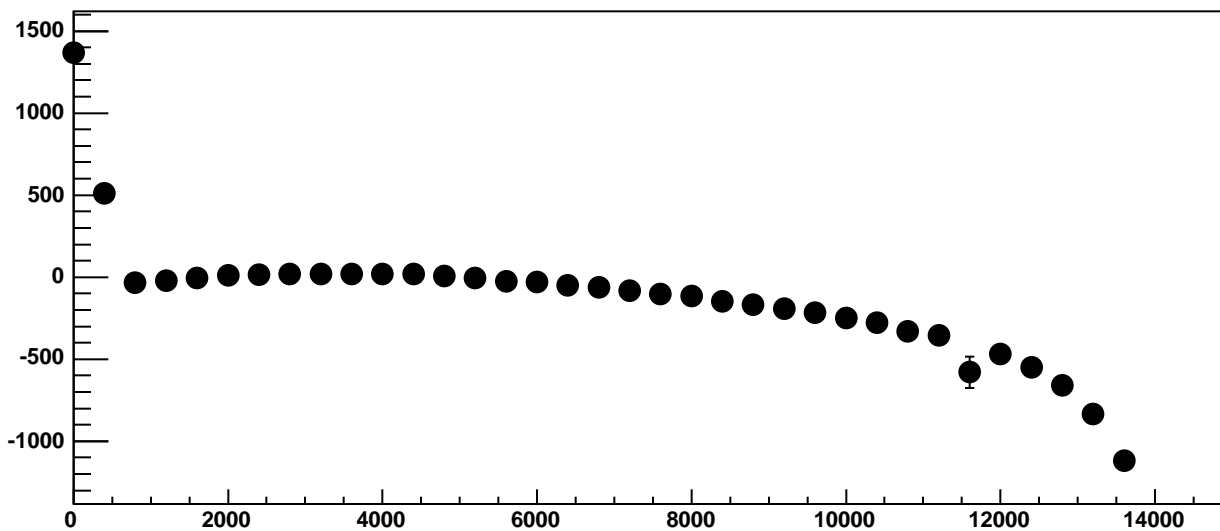
p1

2.187 ± 0.001249

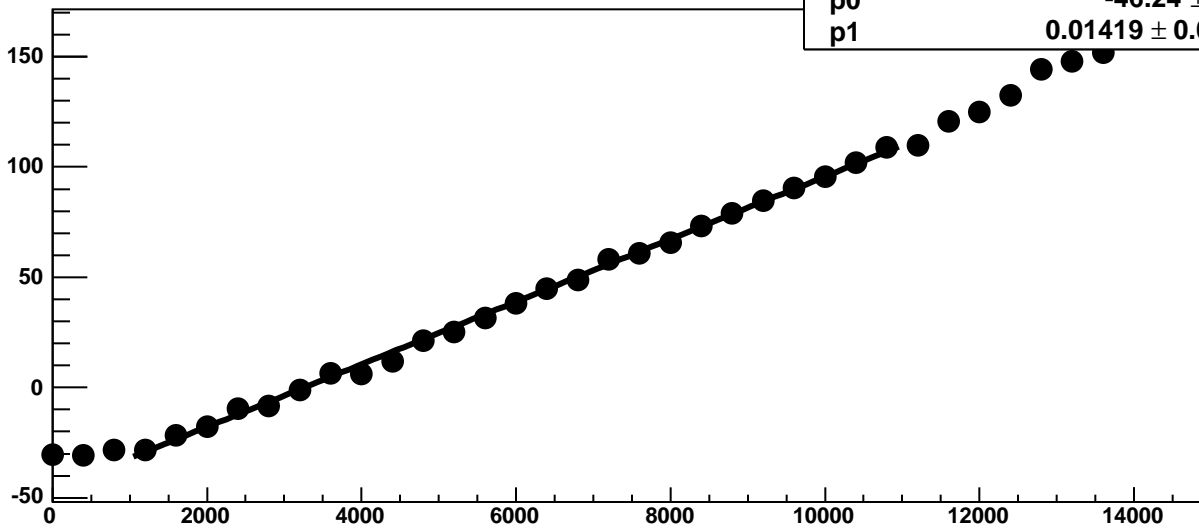
Chip 2, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC

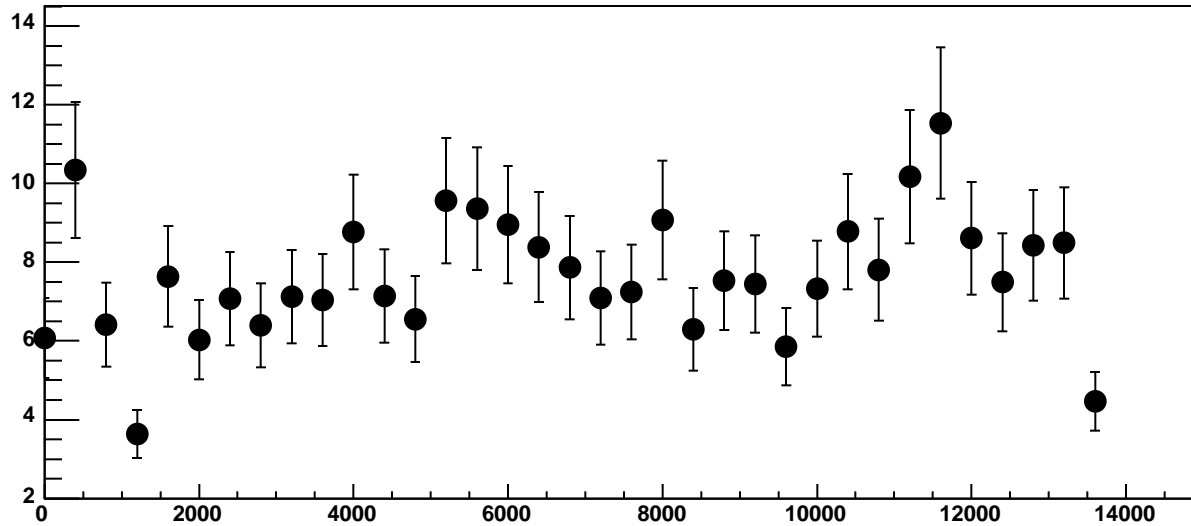


Chip 2, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

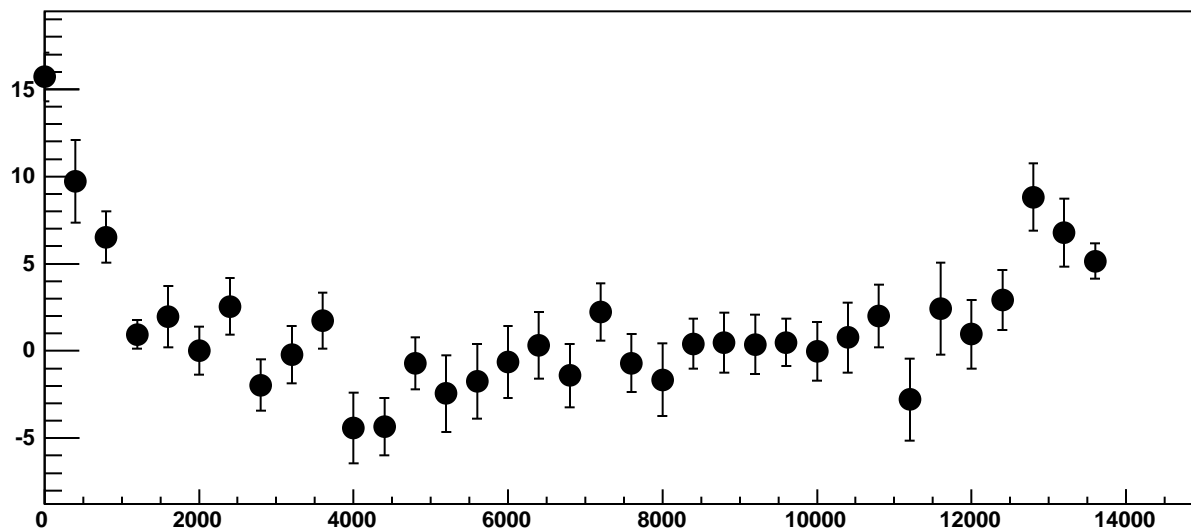


χ^2 / ndf 27.13 / 23
p0 -46.24 ± 0.6304
p1 0.01419 ± 0.0001011

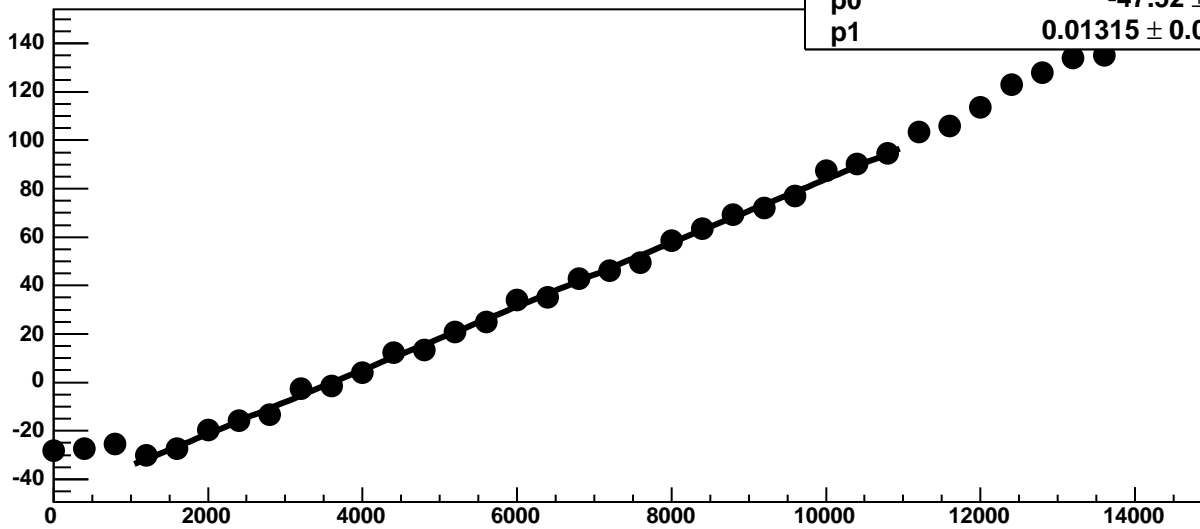
Chip 2, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



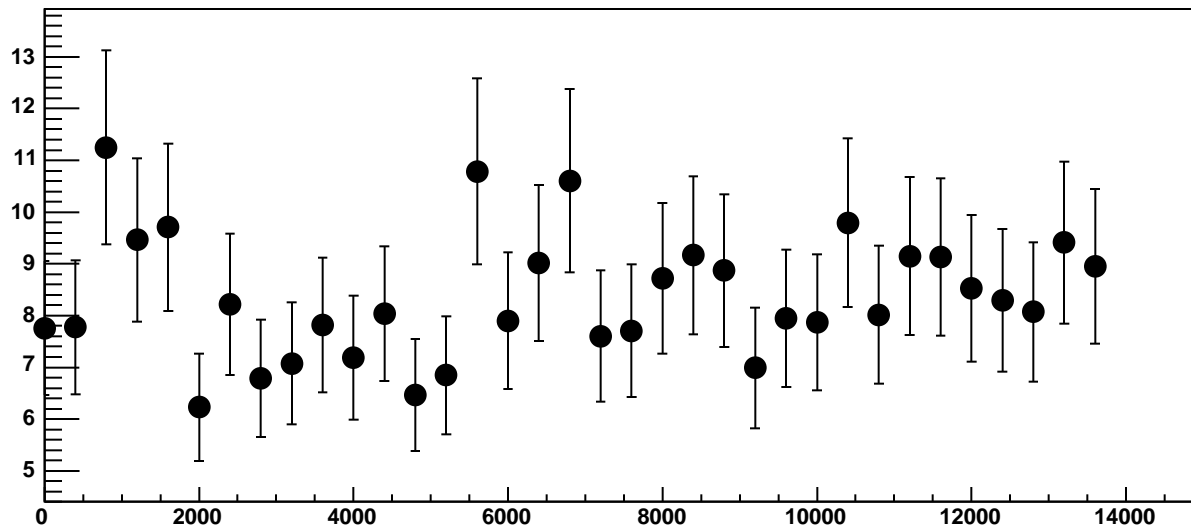
Chip 2, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



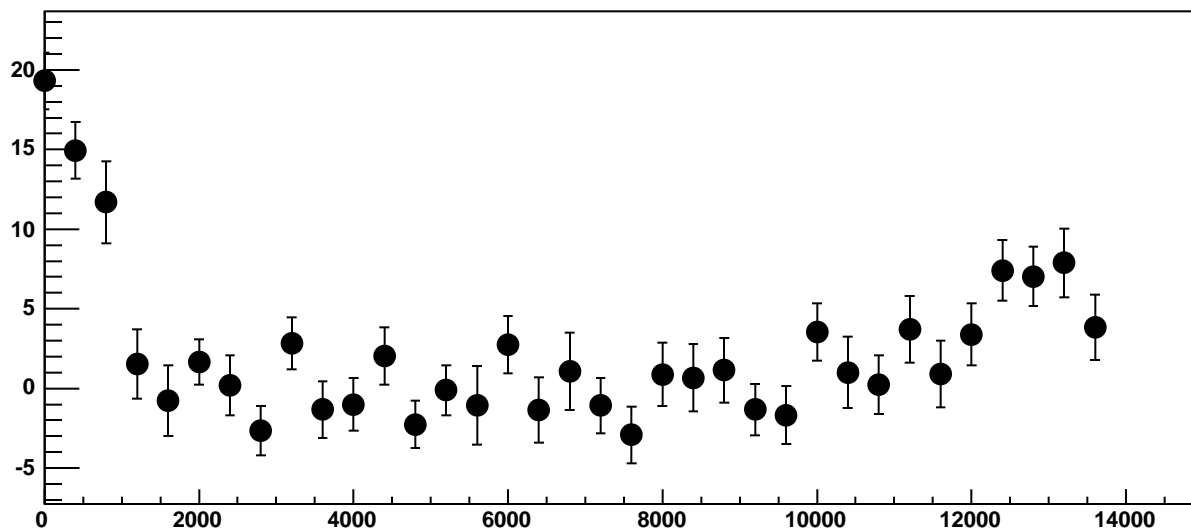
Chip 2, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



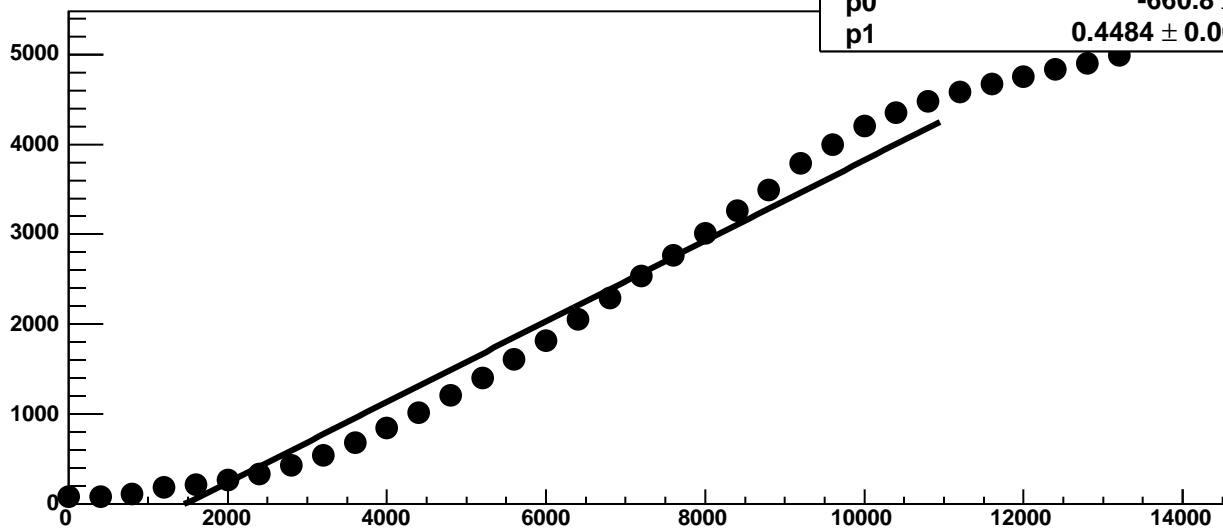
Chip 2, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



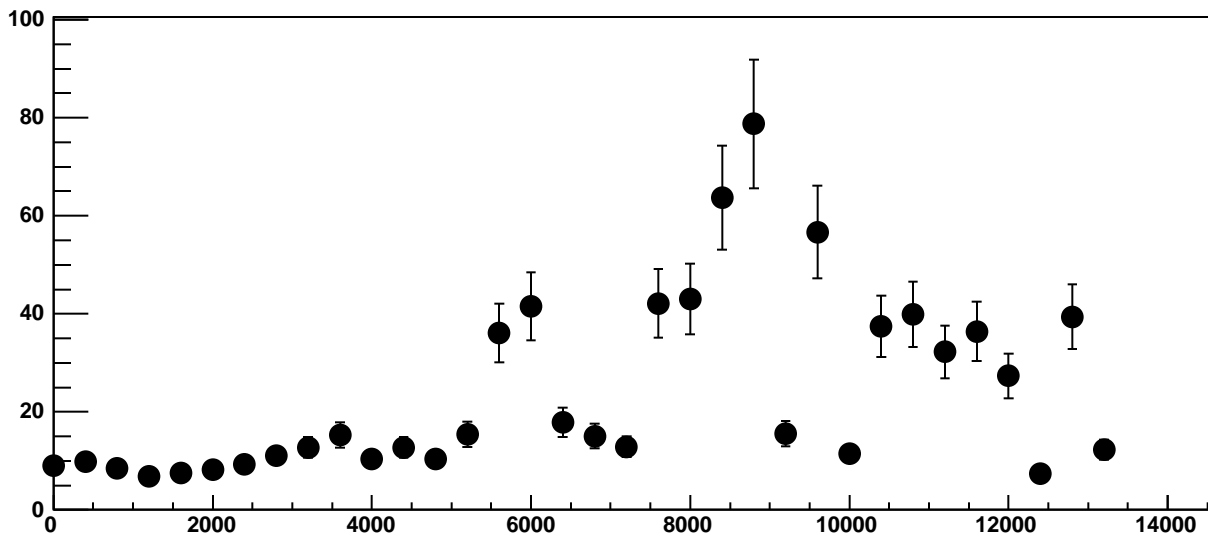
Chip 2, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



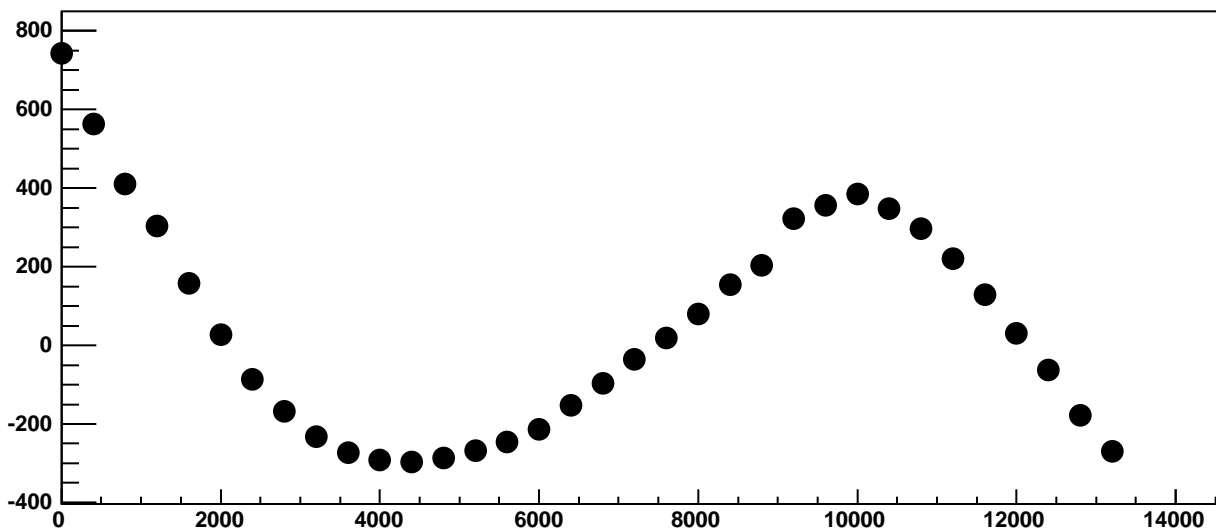
Chip 2, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC



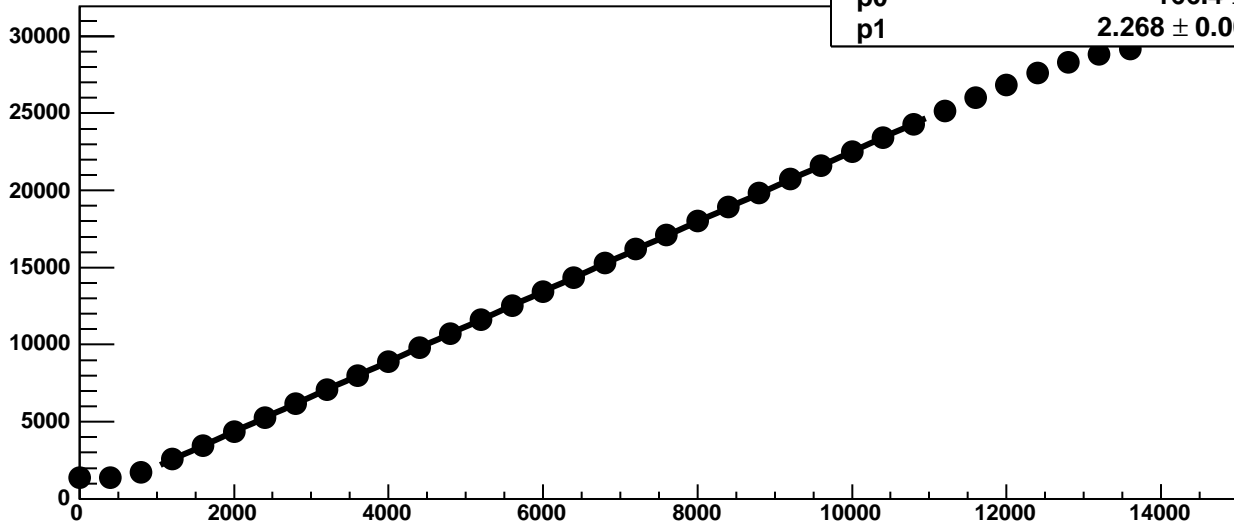
Chip 2, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



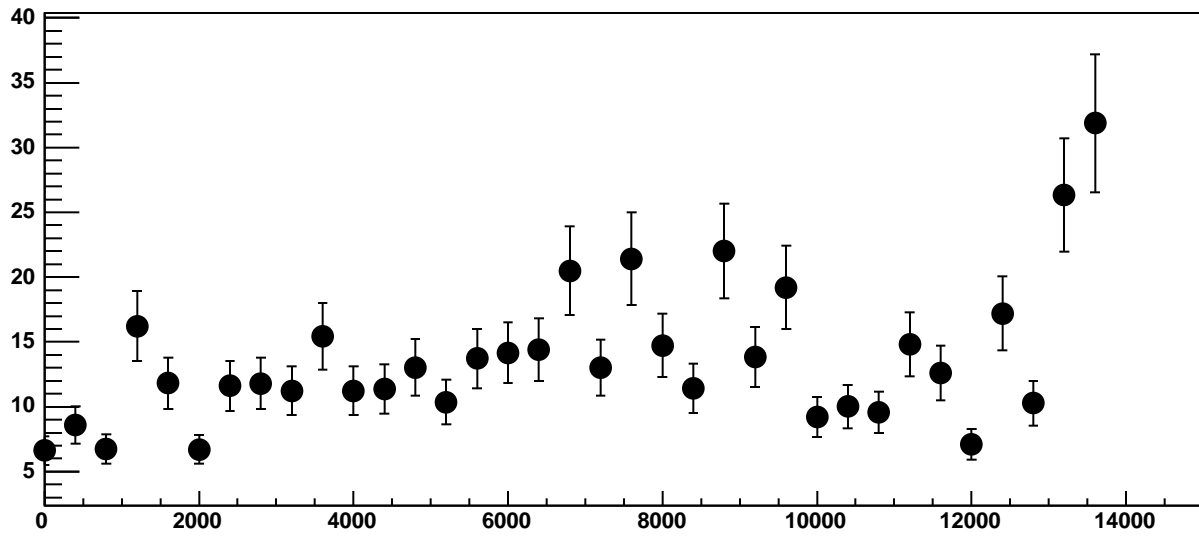
Chip 2, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



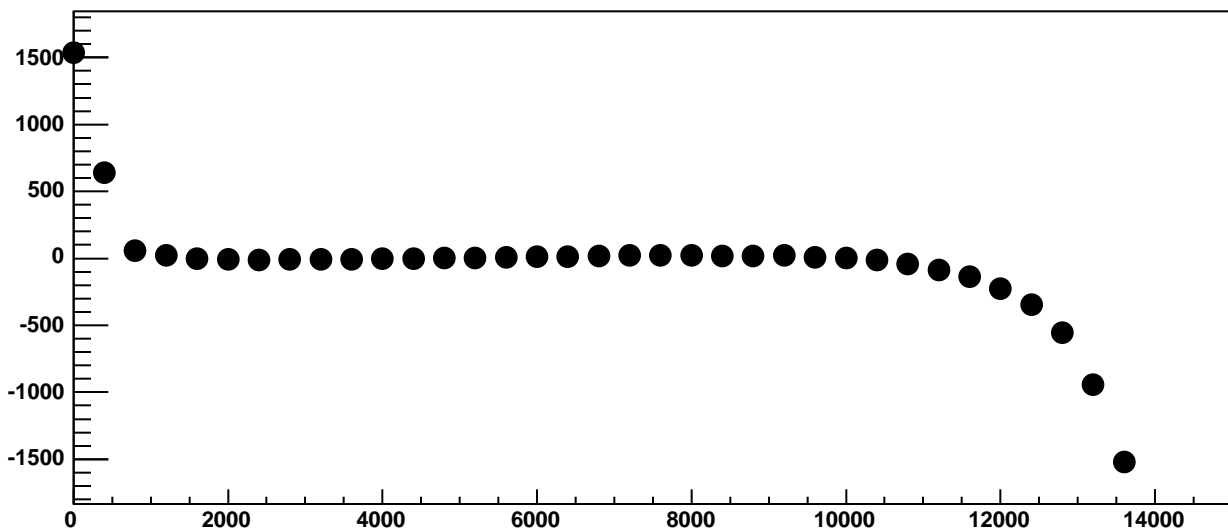
Chip 2, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



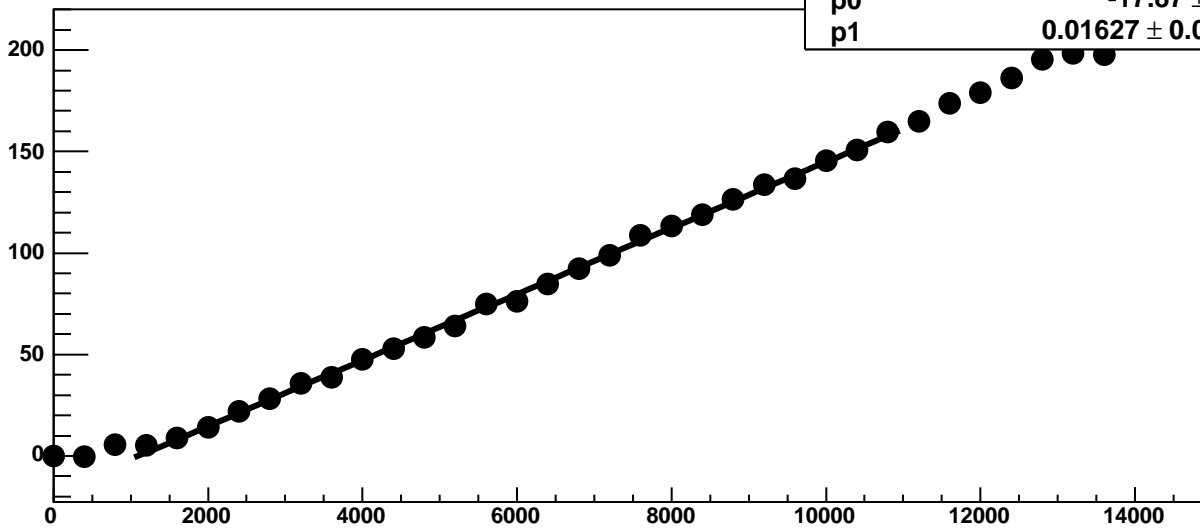
Chip 2, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



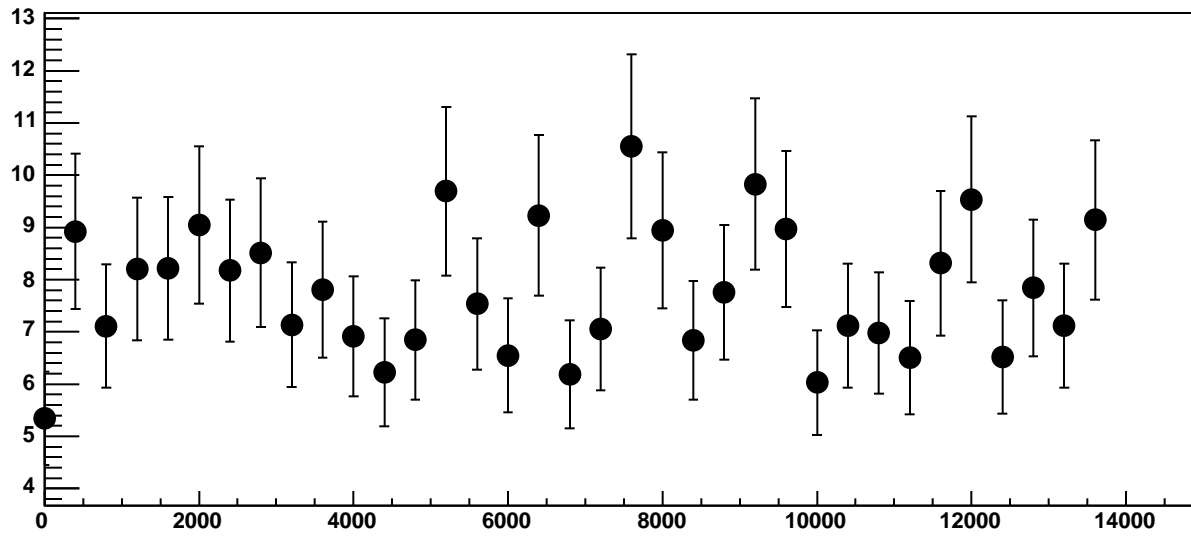
Chip 2, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC



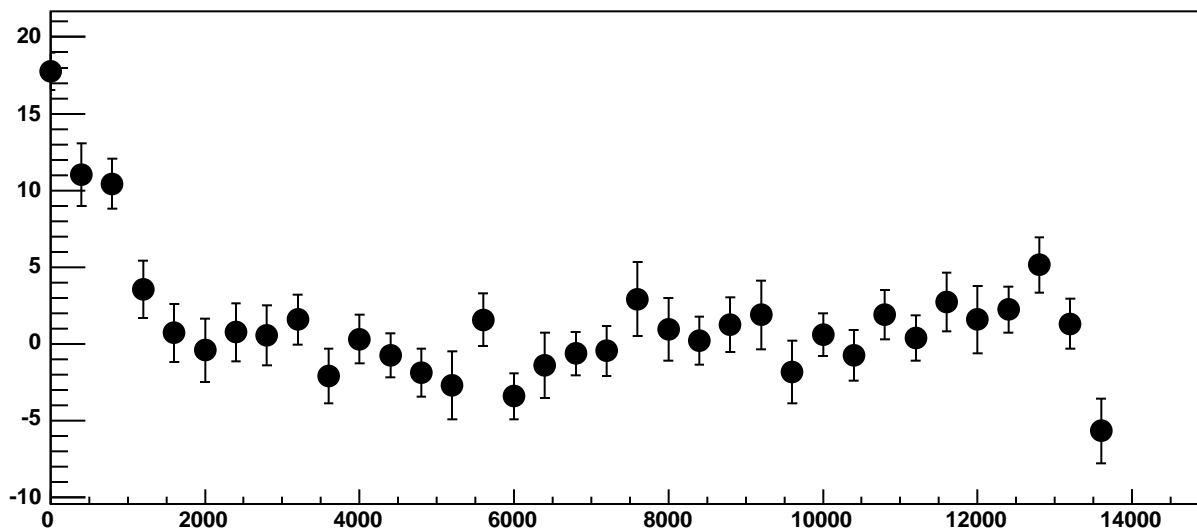
Chip 2, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



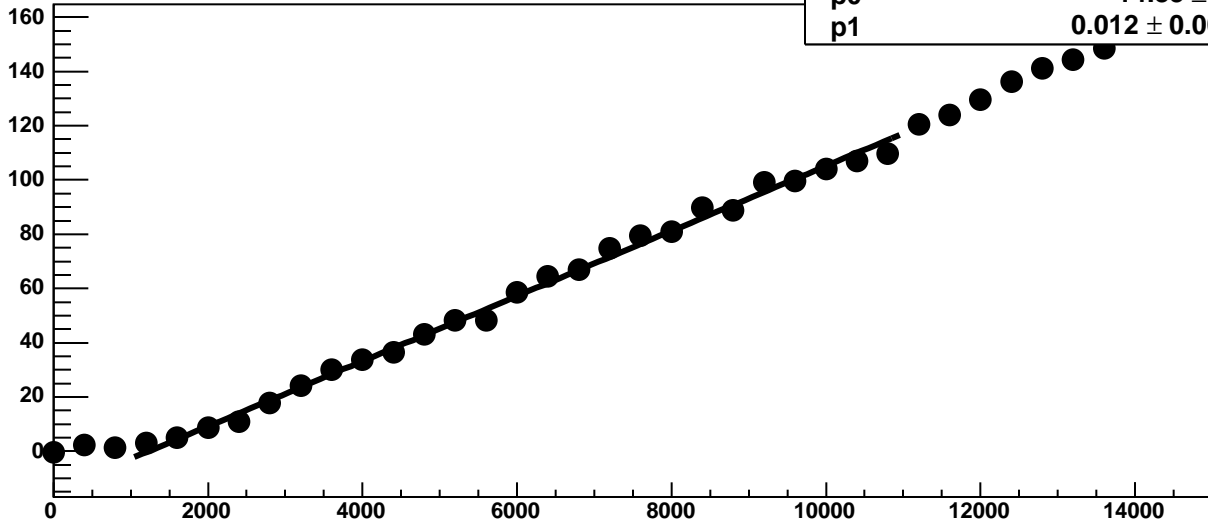
Chip 2, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



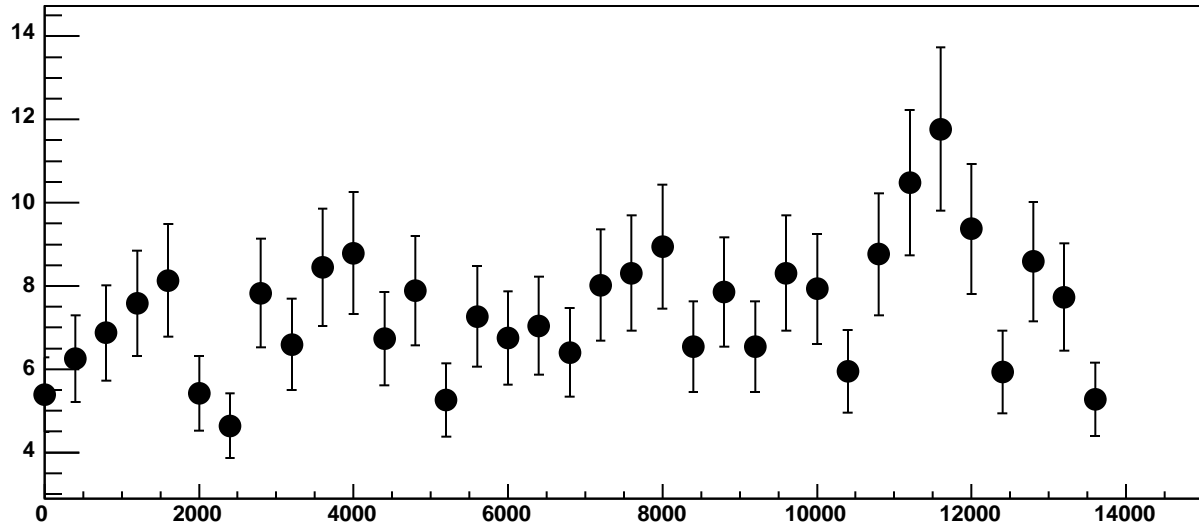
Chip 2, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



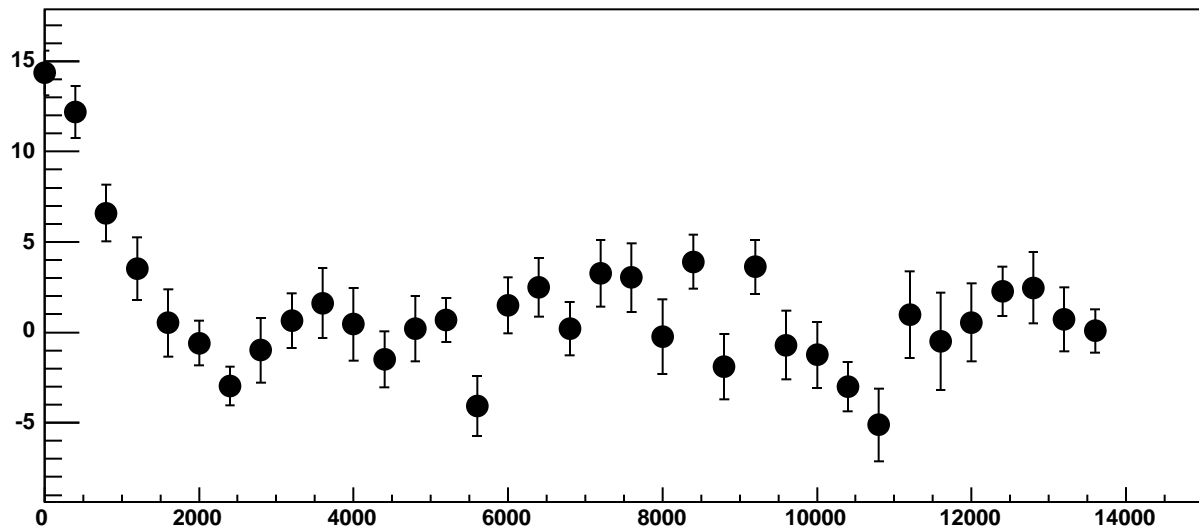
Chip 2, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



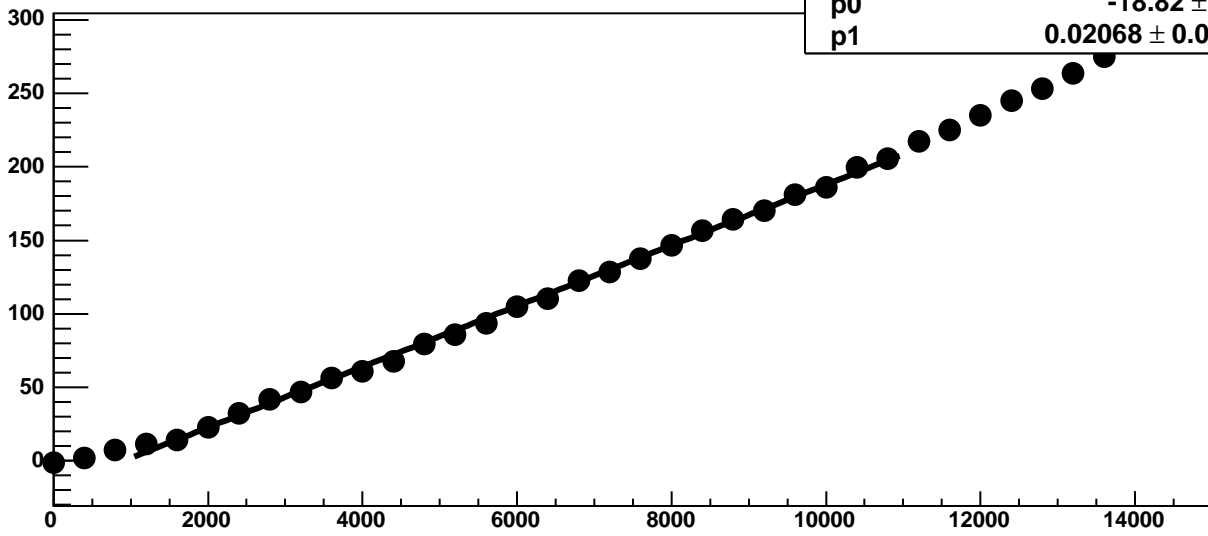
Chip 2, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

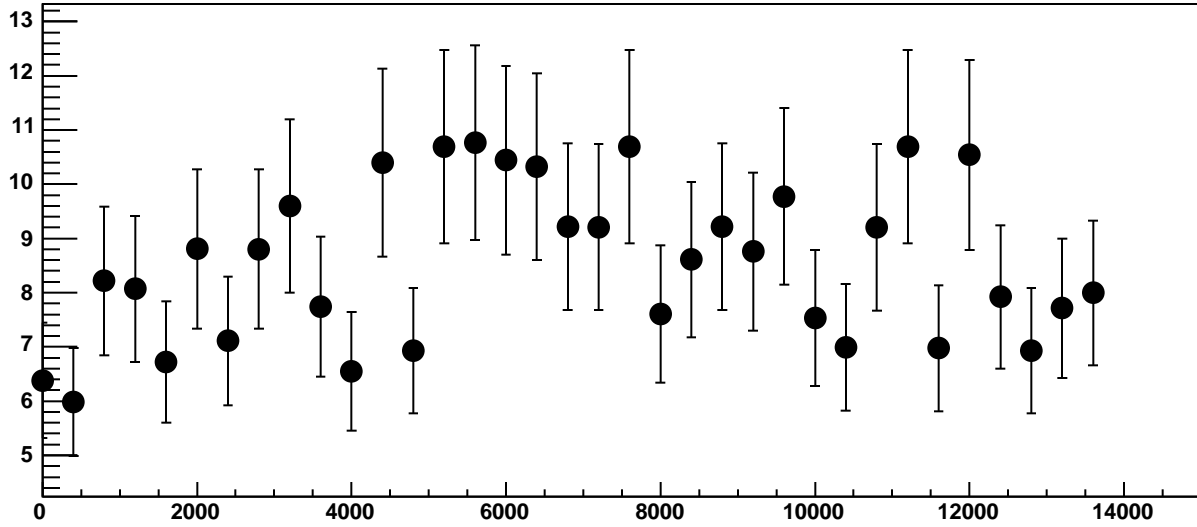


Chip 2, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

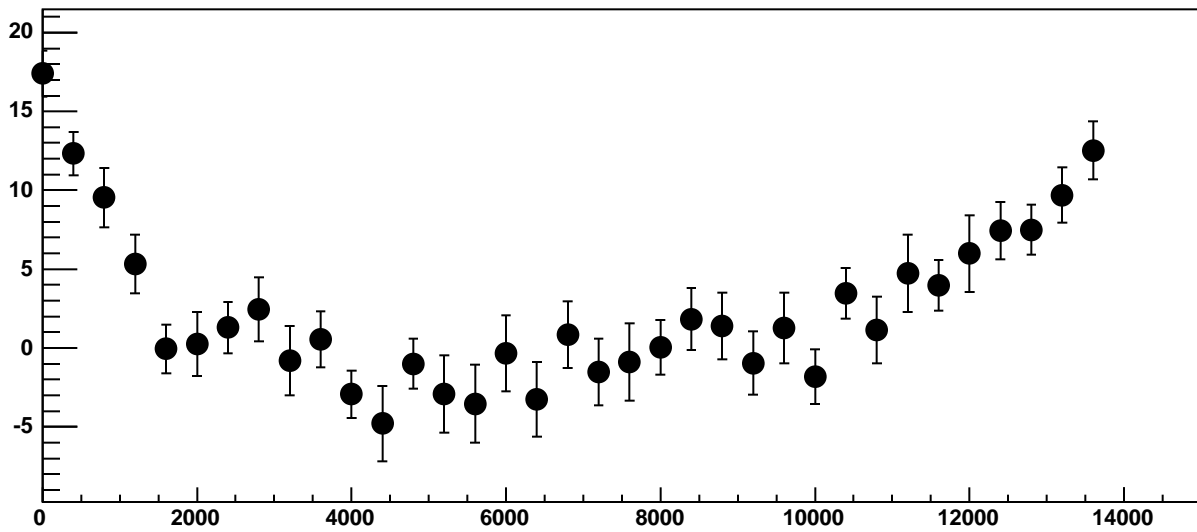


χ^2 / ndf 33.01 / 23
p0 -18.82 ± 0.8402
p1 0.02068 ± 0.0001281

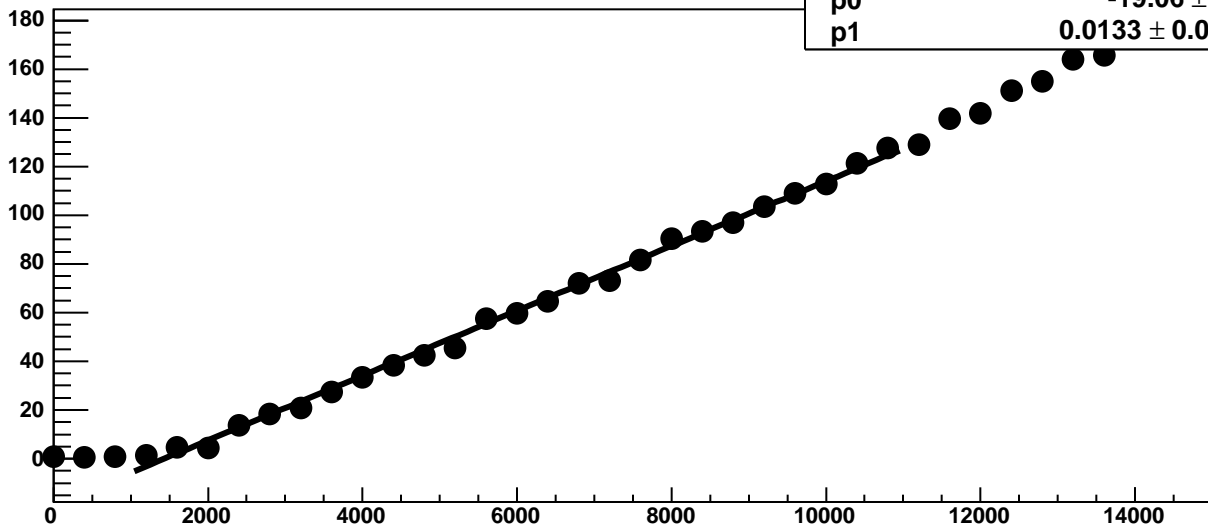
Chip 2, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

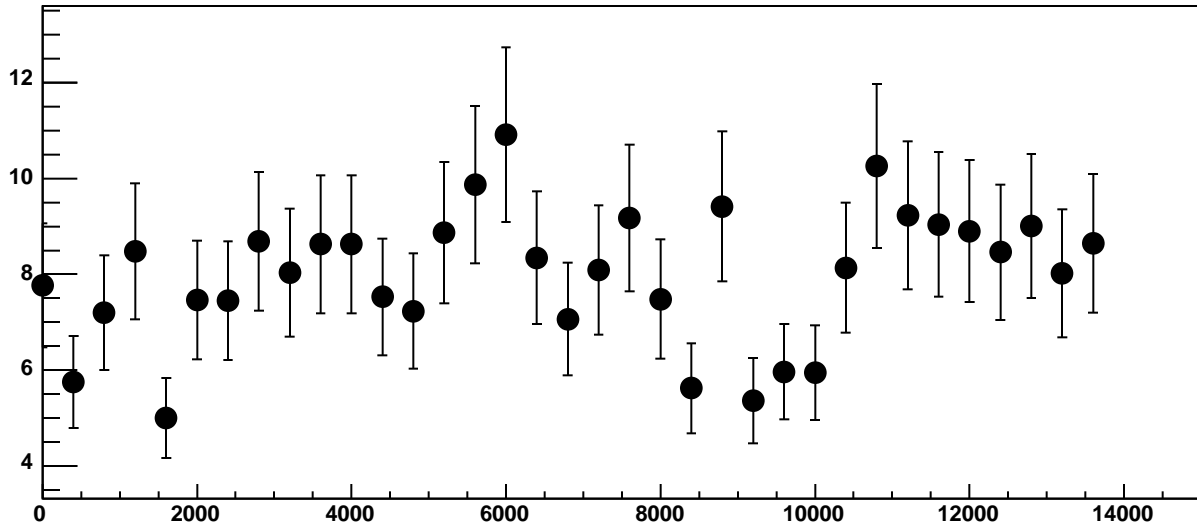


Chip 2, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

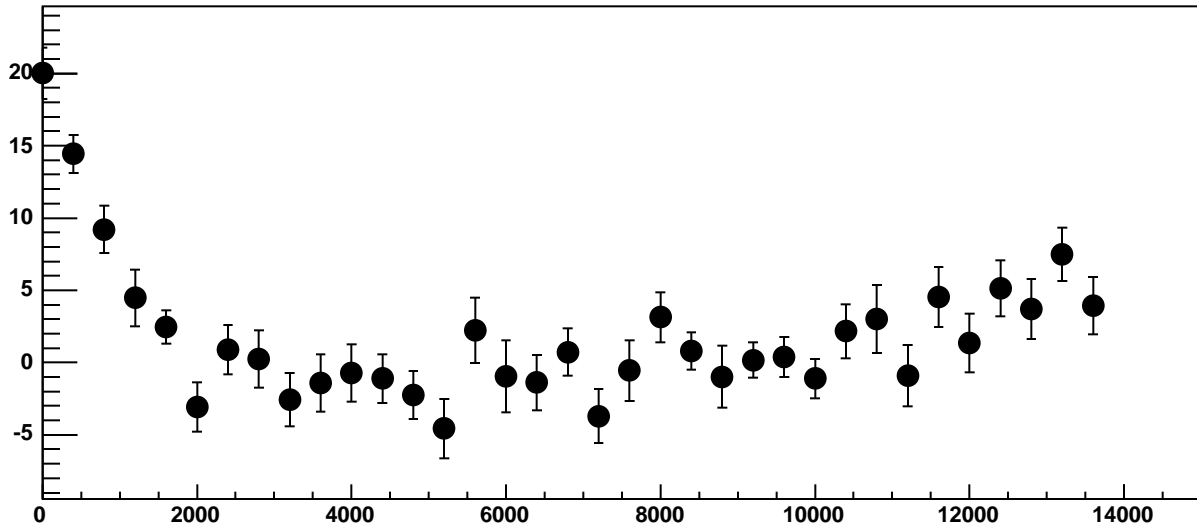


χ^2 / ndf 36.77 / 23
p0 -19.06 ± 0.7657
p1 0.0133 ± 0.0001122

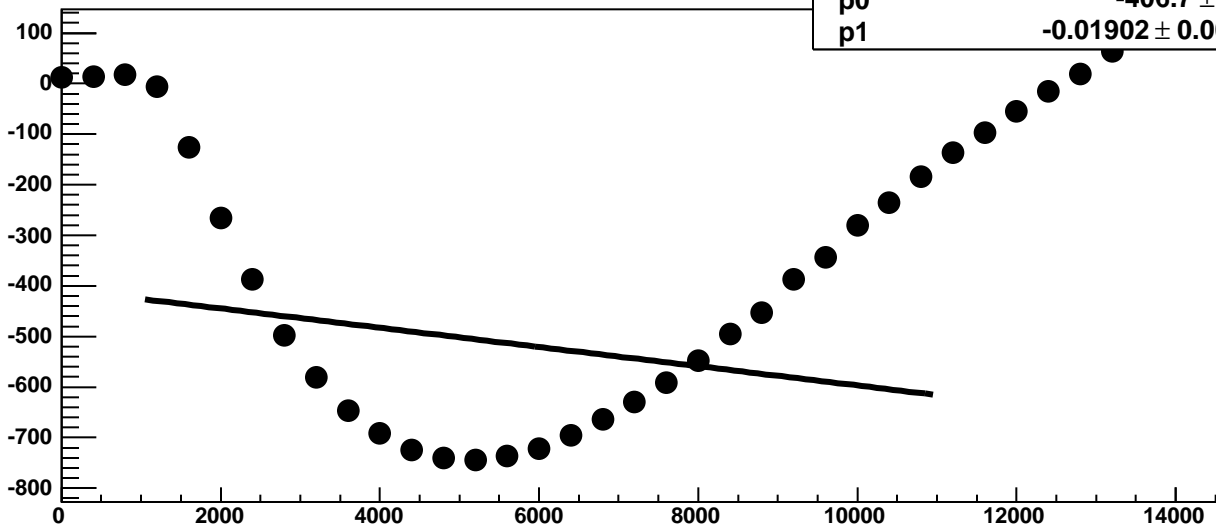
Chip 2, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

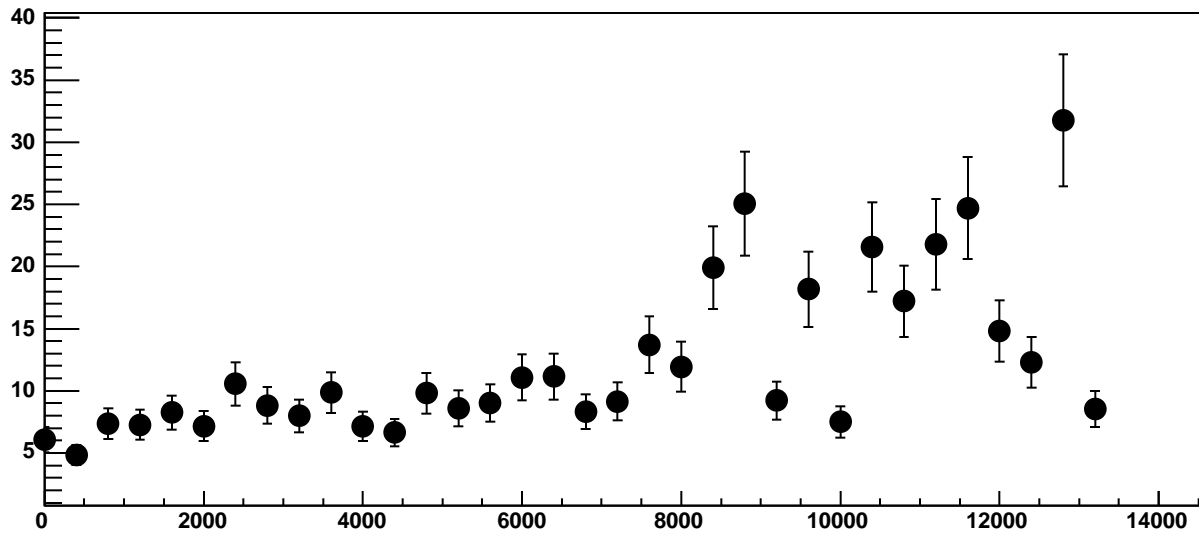


Chip 2, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

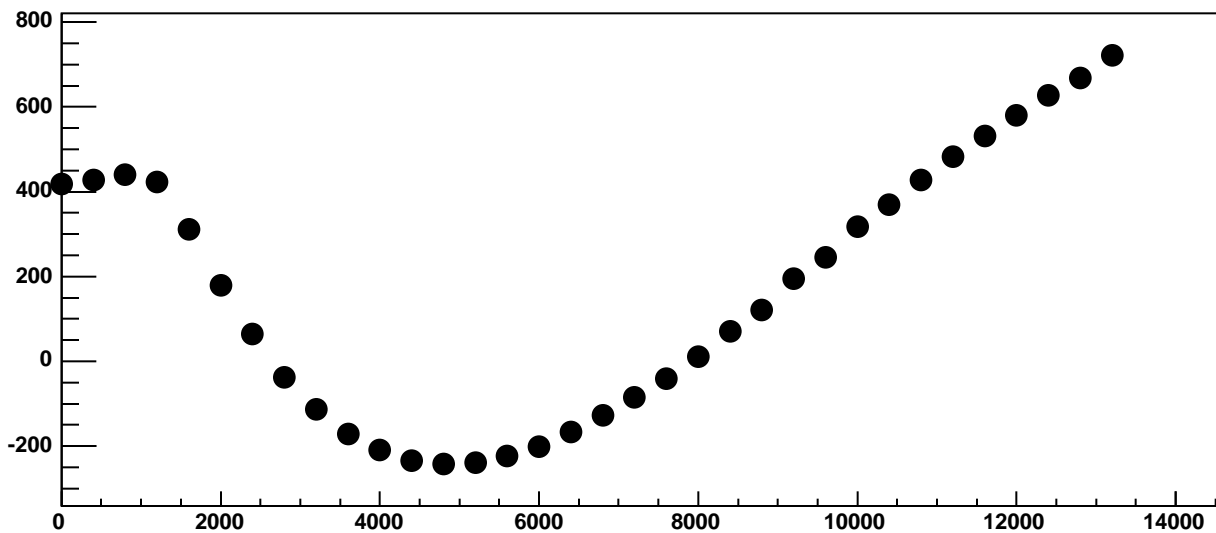


χ^2 / ndf 2.729e+05 / 23
p0 -406.7 ± 0.9202
p1 -0.01902 ± 0.0001615

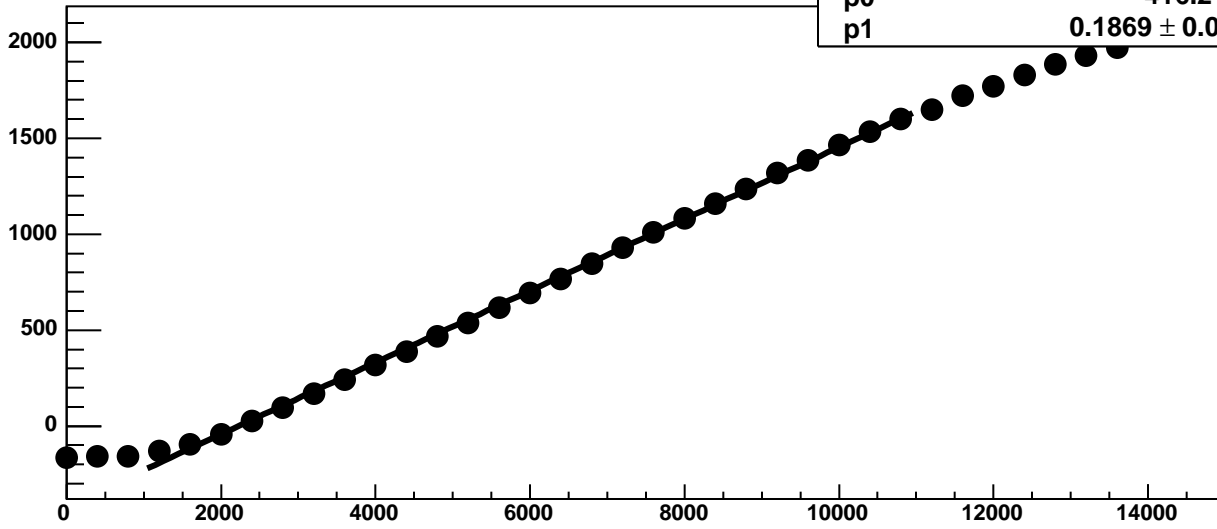
Chip 2, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

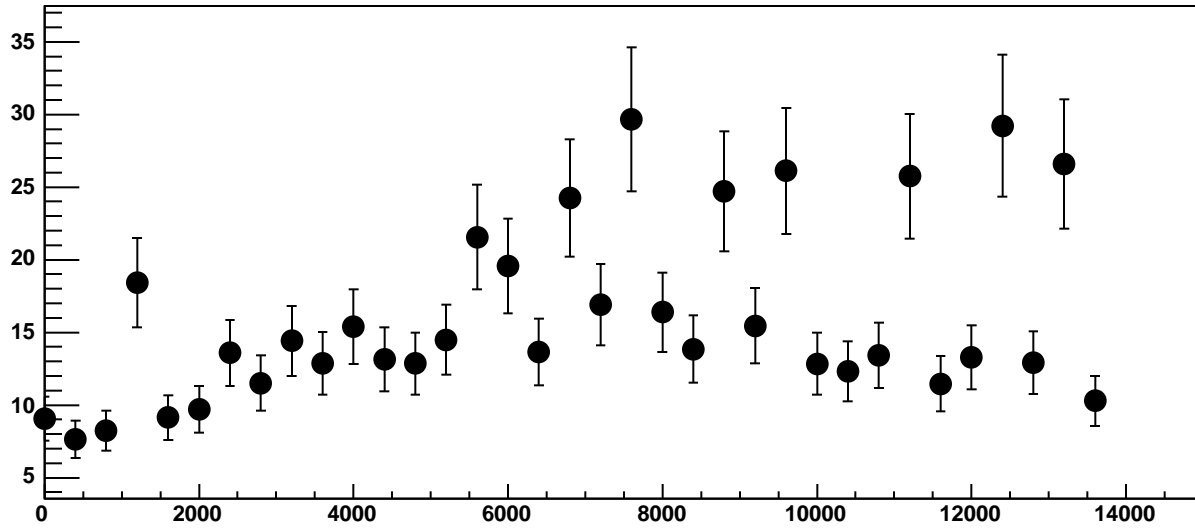


Chip 2, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC

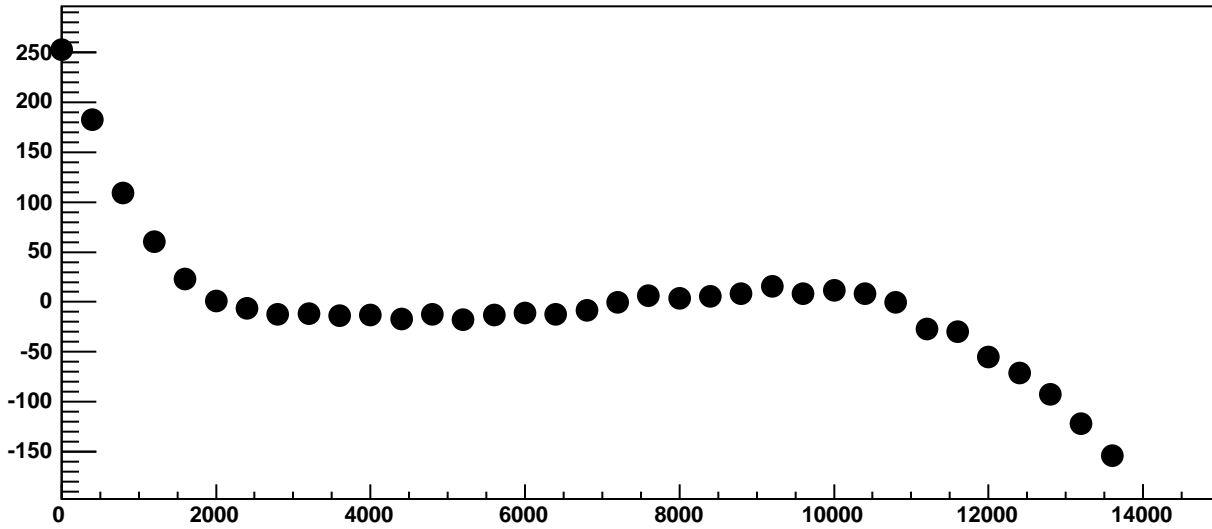


χ^2 / ndf 557.8 / 23
p0 -416.2 ± 1.316
p1 0.1869 ± 0.0002125

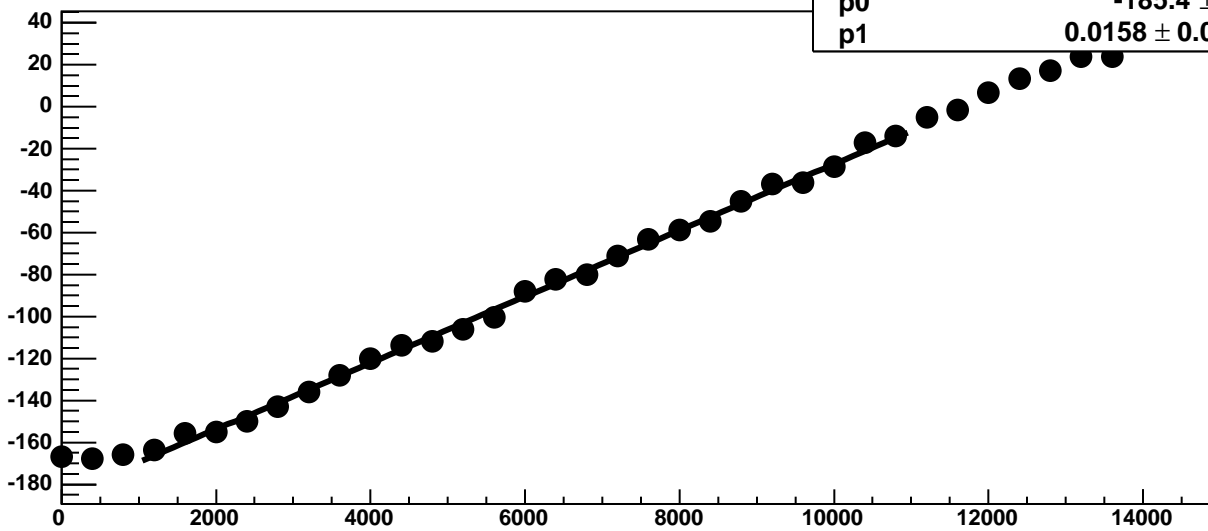
Chip 2, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



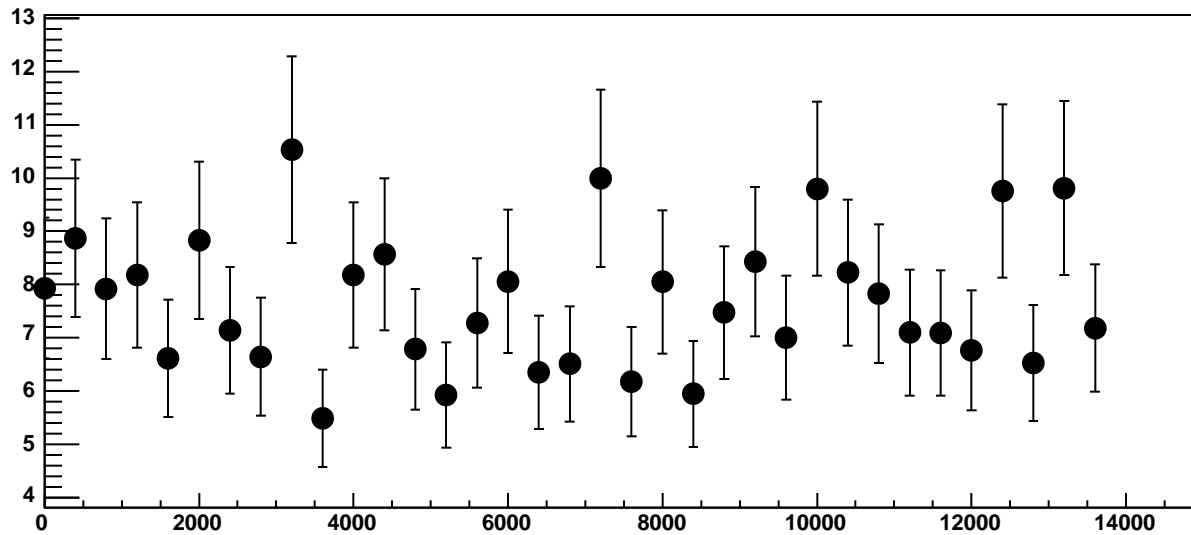
Chip 2, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC



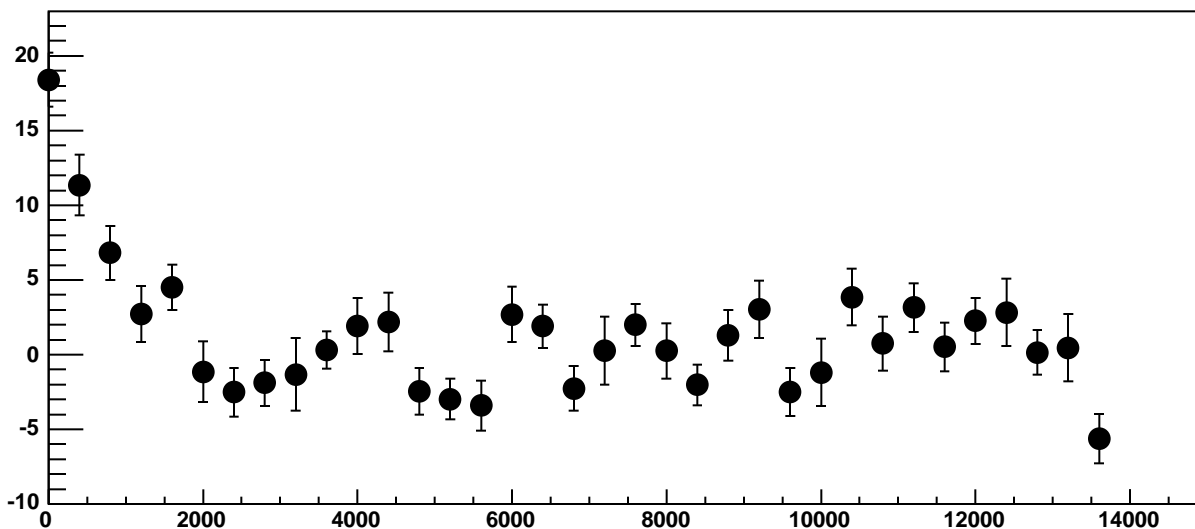
Chip 2, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



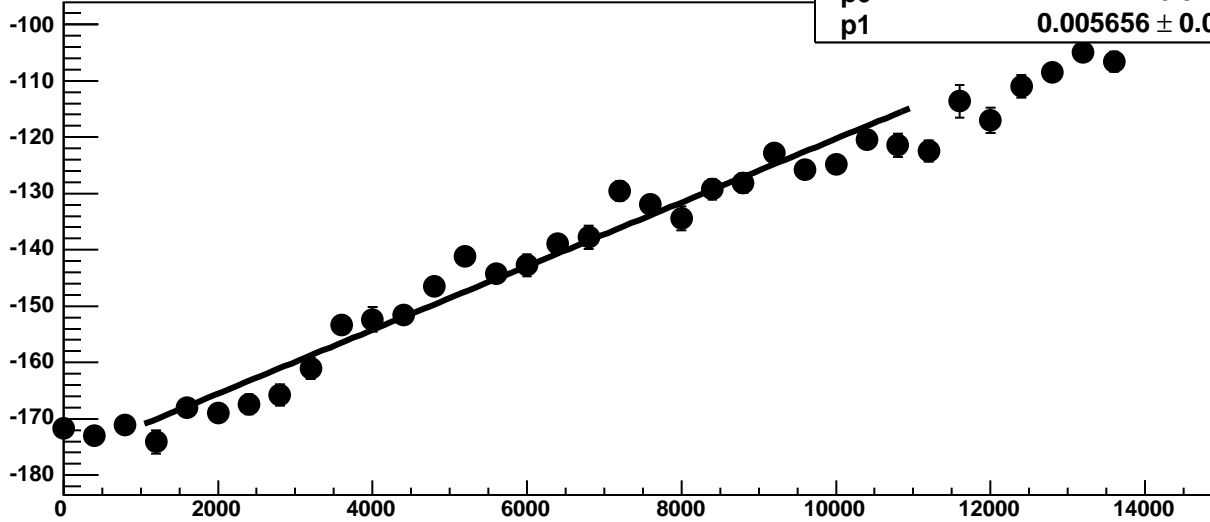
Chip 2, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



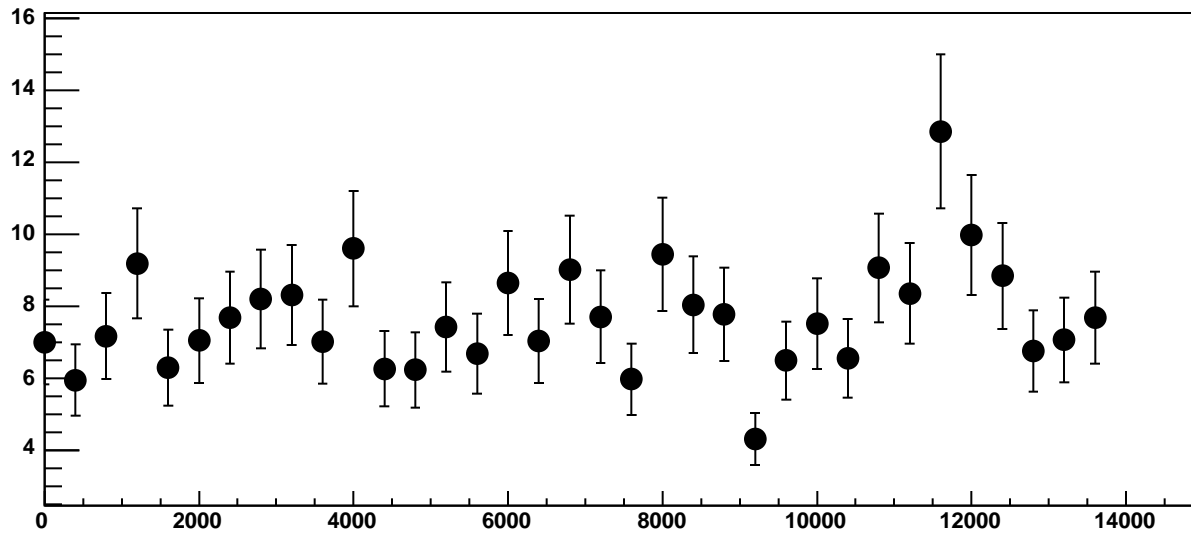
Chip 2, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



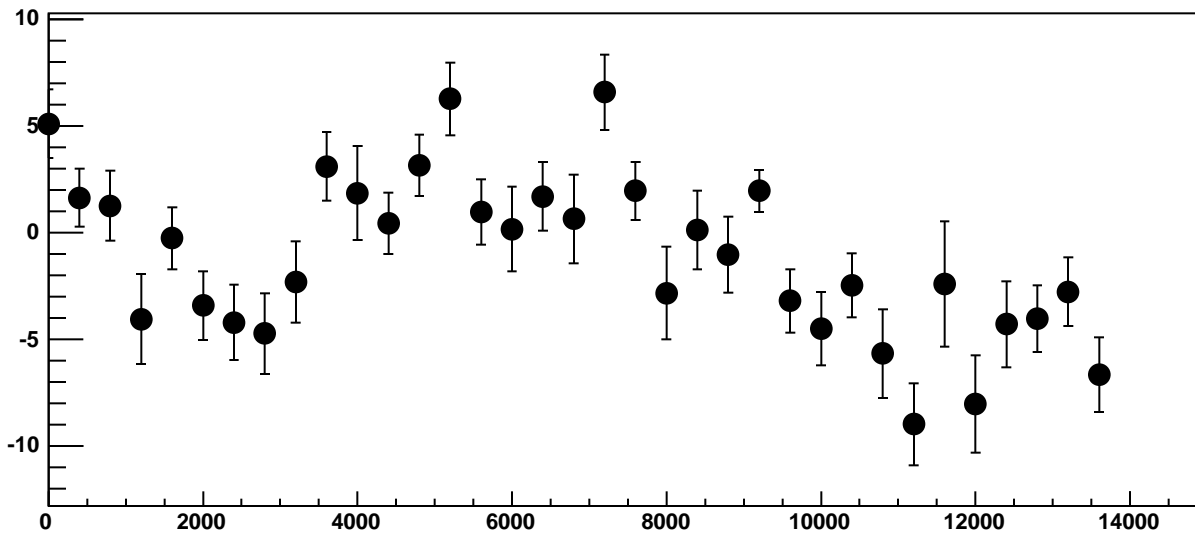
Chip 2, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



Chip 2, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

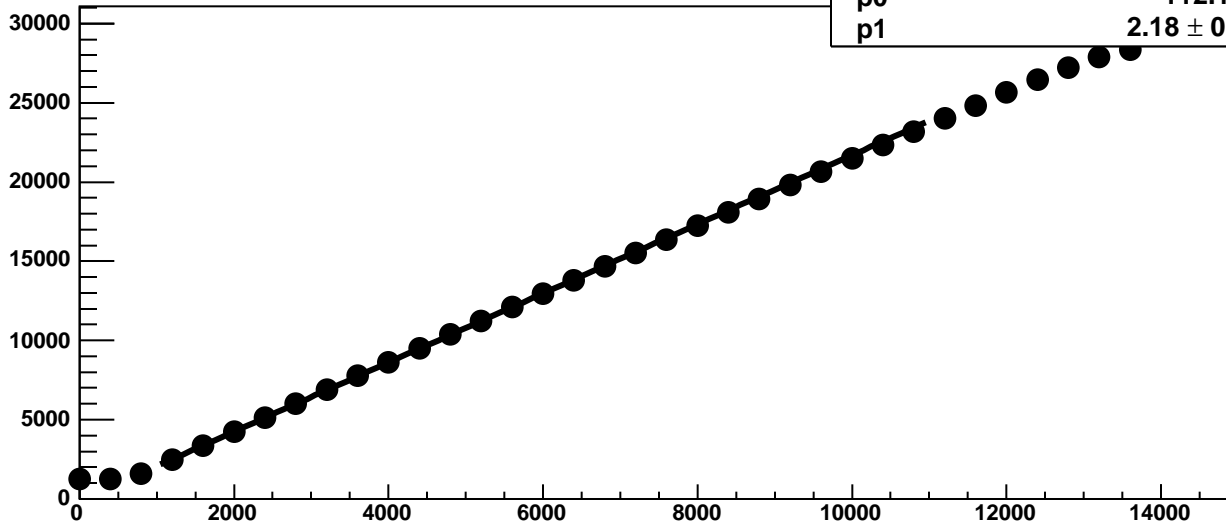


Chip 2, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

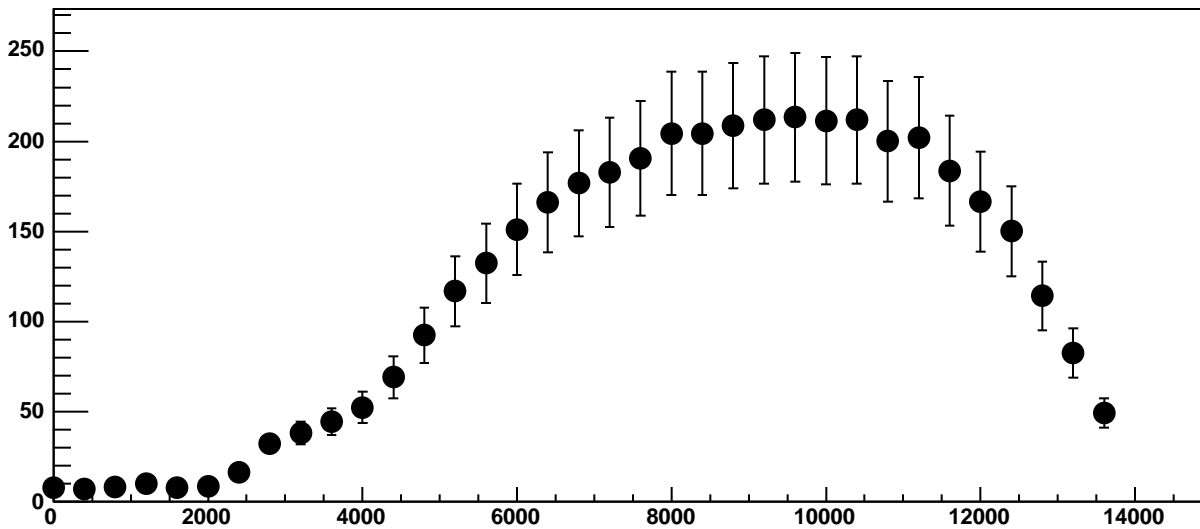


Chip 2, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC

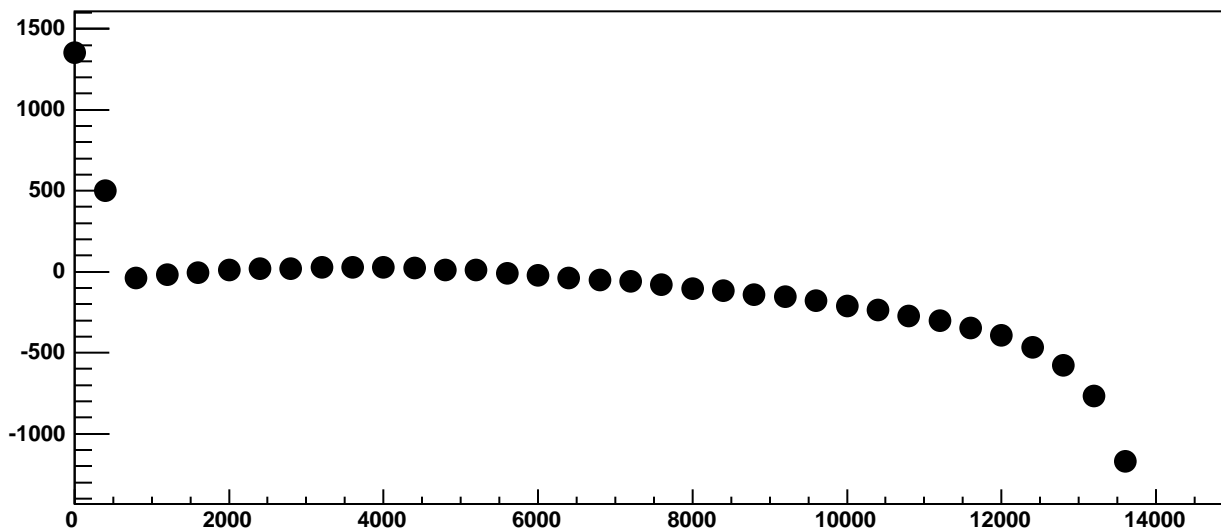
χ^2 / ndf 287.6 / 23
p0 -112.1 ± 2.687
p1 2.18 ± 0.001316



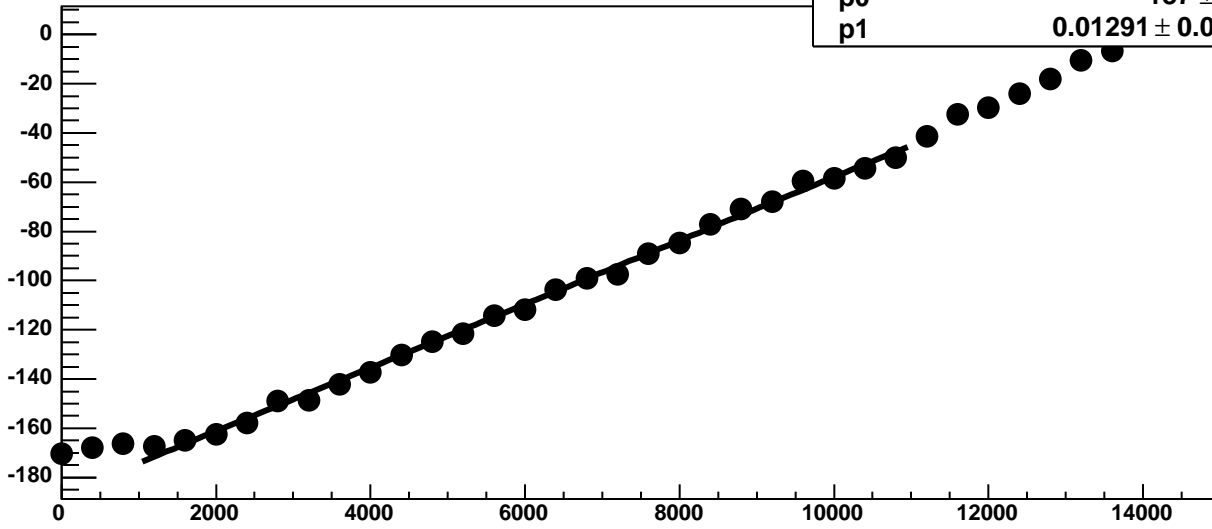
Chip 2, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

31.17 / 23

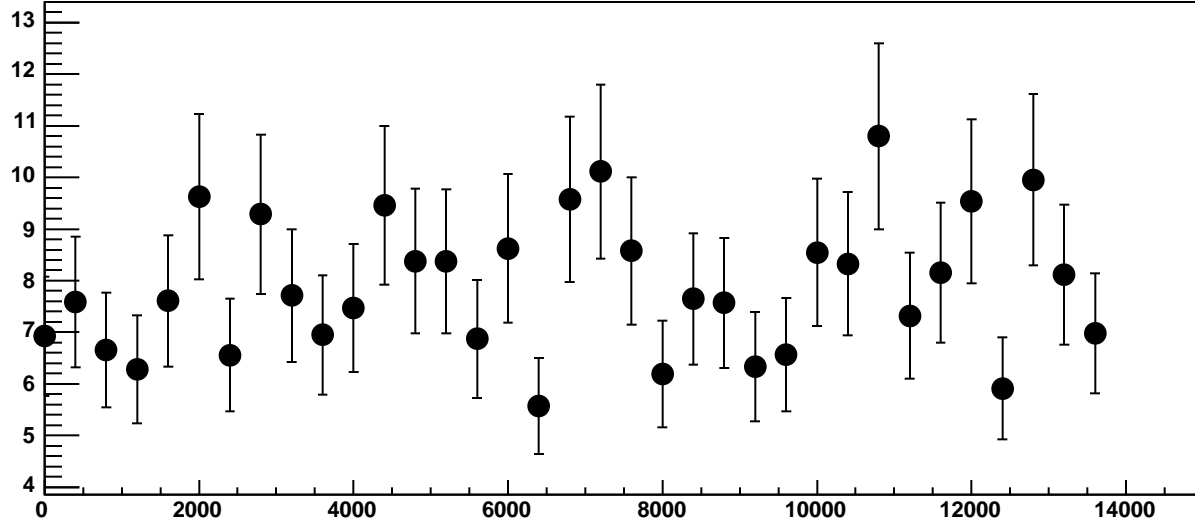
p0

-187 ± 0.8025

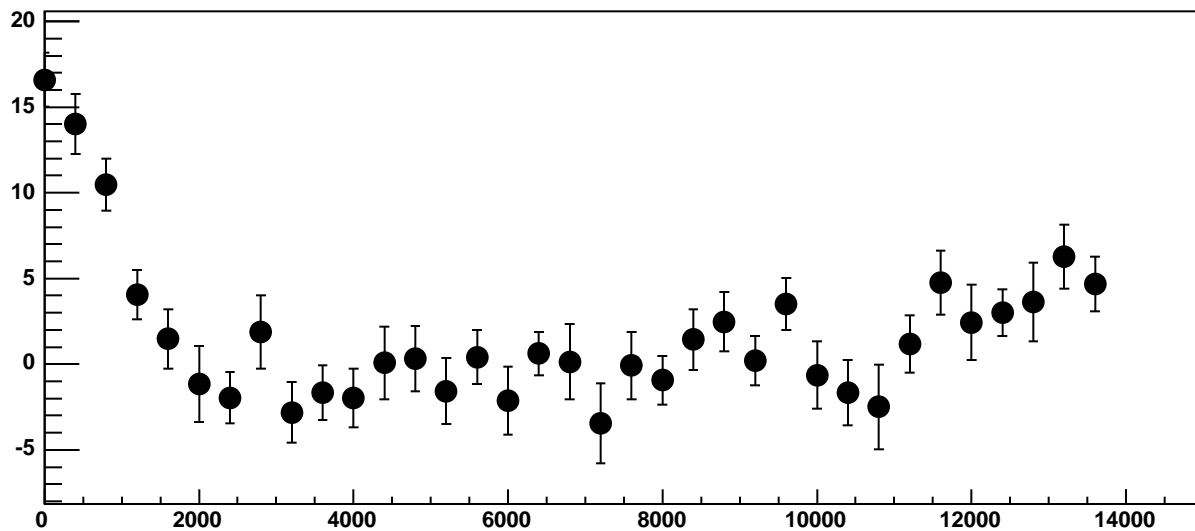
p1

0.01291 ± 0.0001218

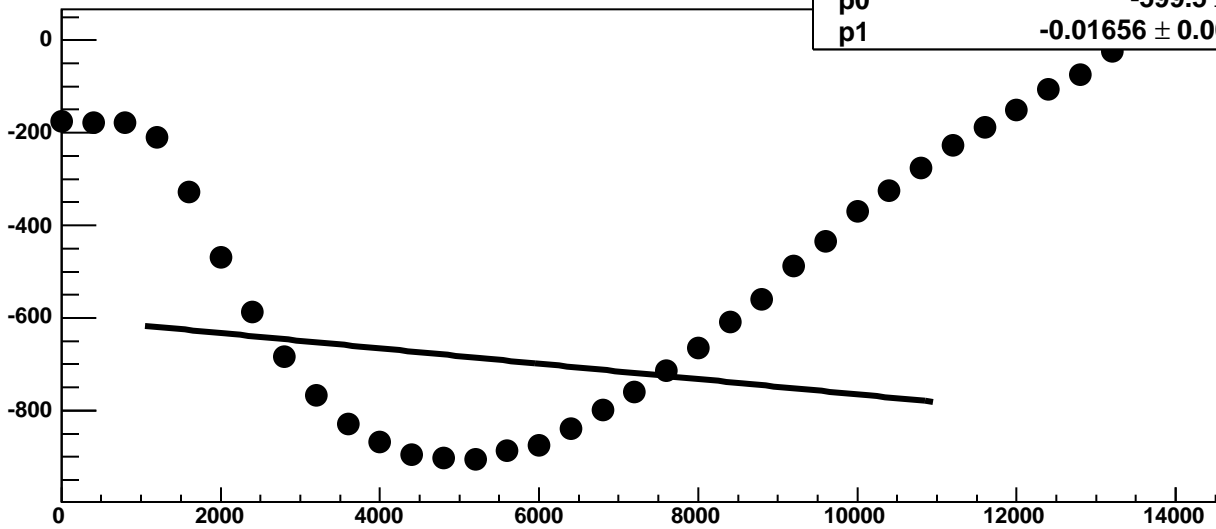
Chip 2, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

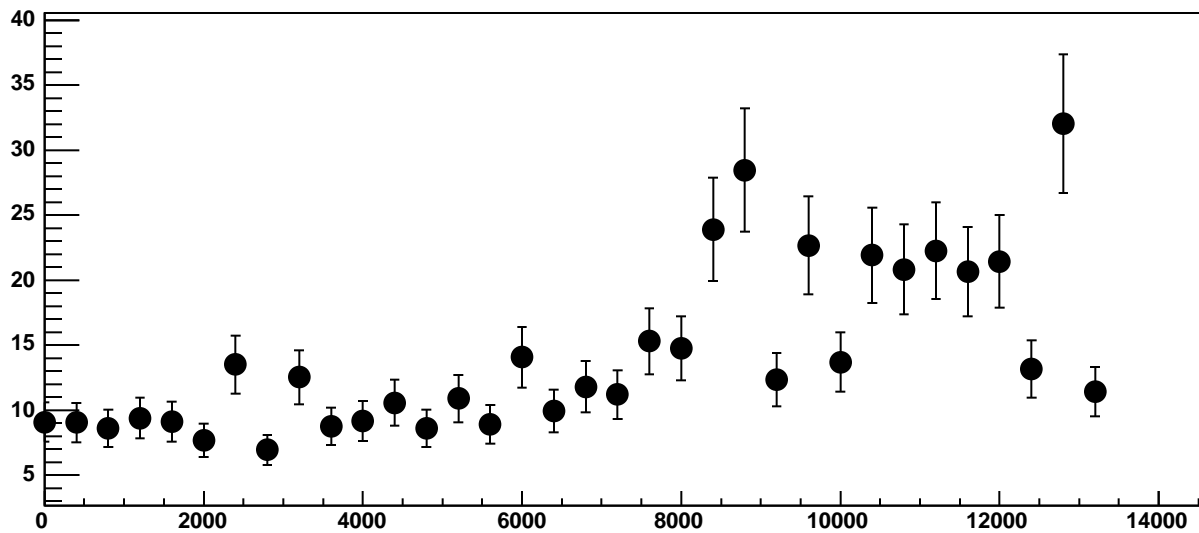


Chip 2, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

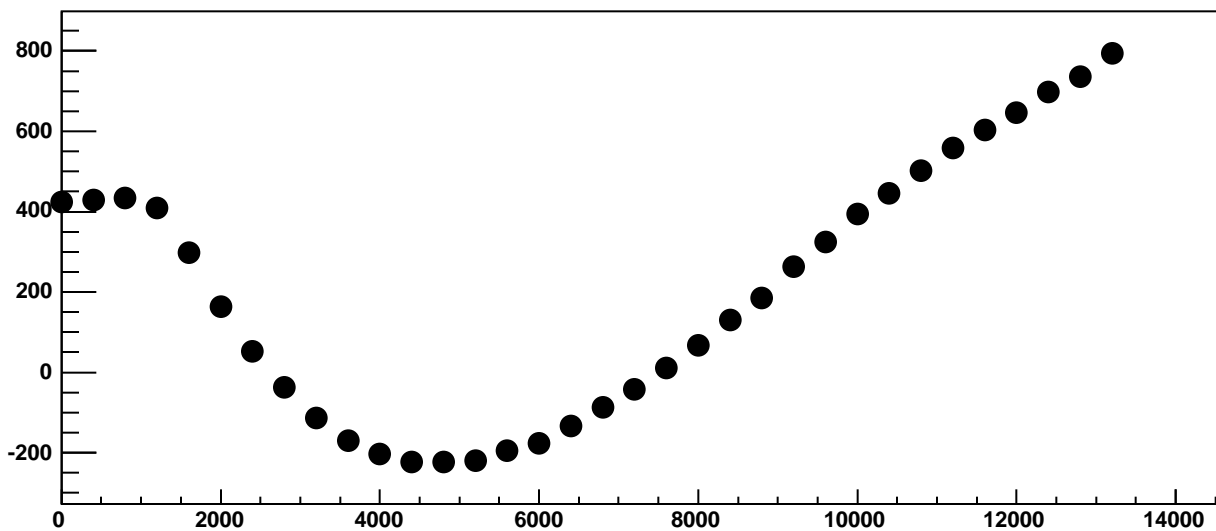


χ^2 / ndf 1.79e+05 / 23
p0 -599.5 ± 1.084
p1 -0.01656 ± 0.0002039

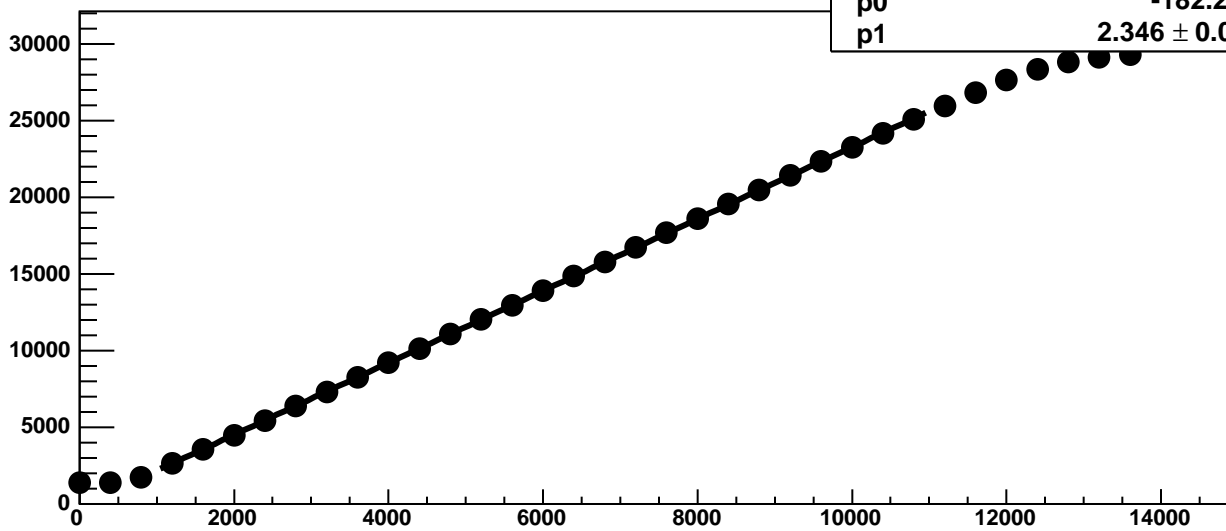
Chip 2, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



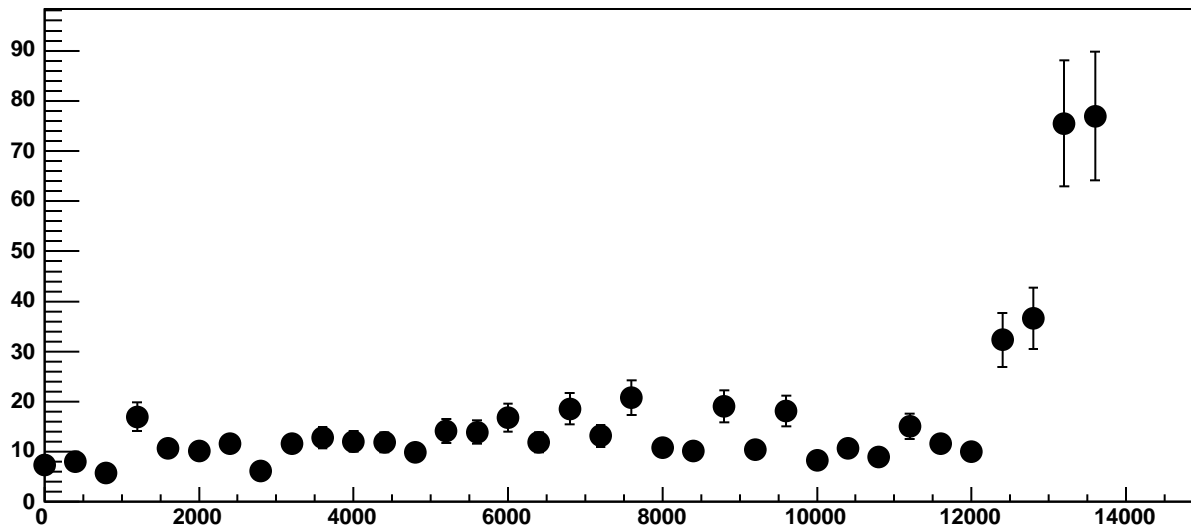
Chip 2, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



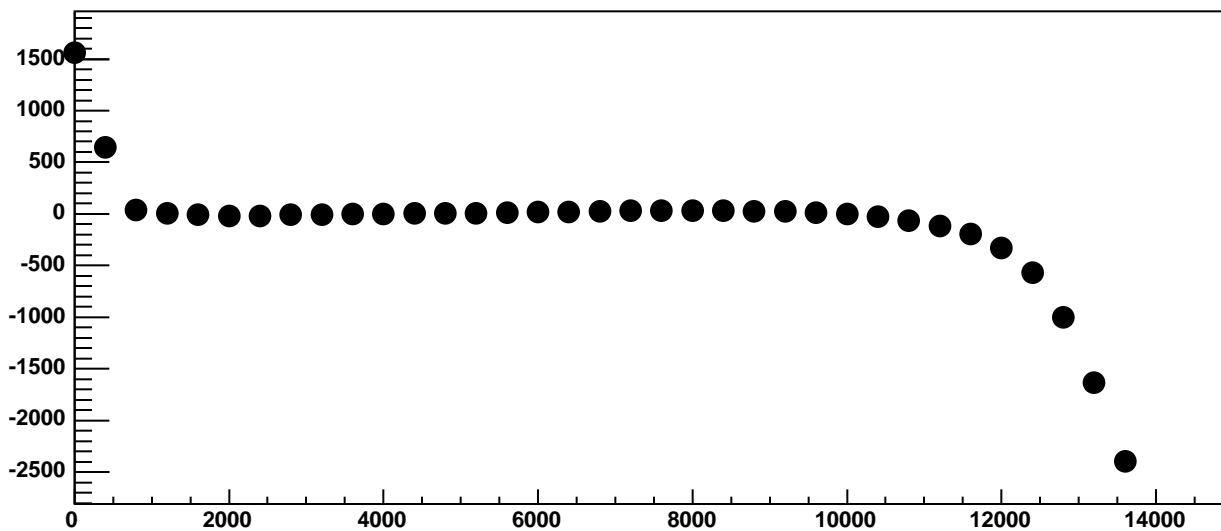
Chip 2, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC



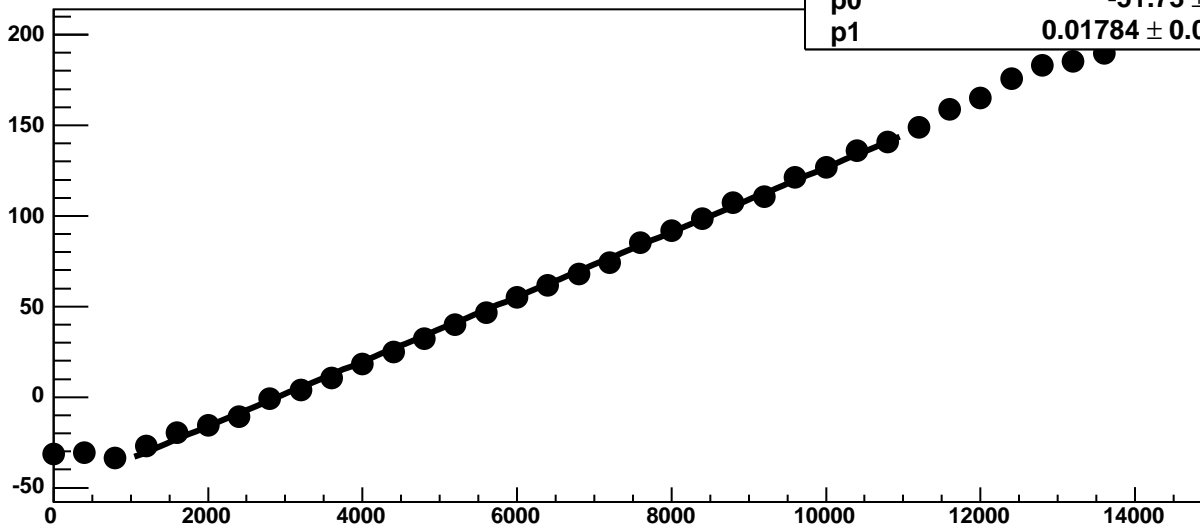
Chip 2, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



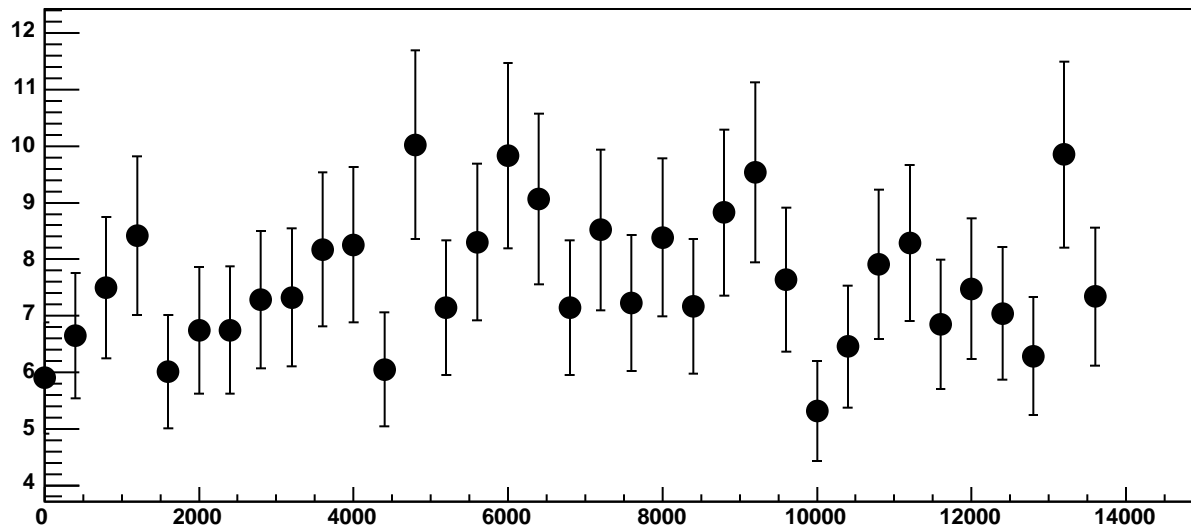
Chip 2, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC



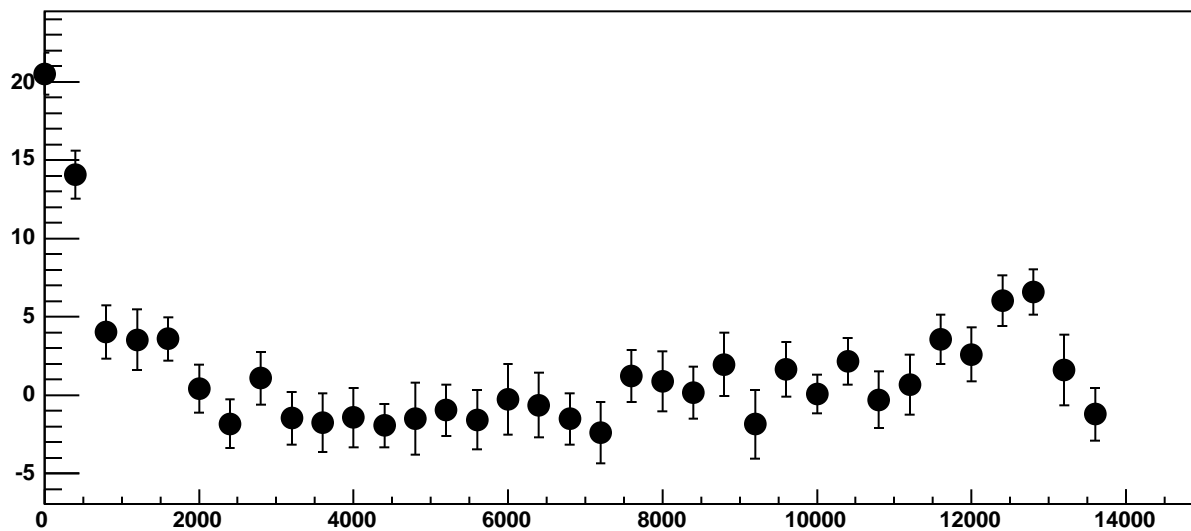
Chip 2, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



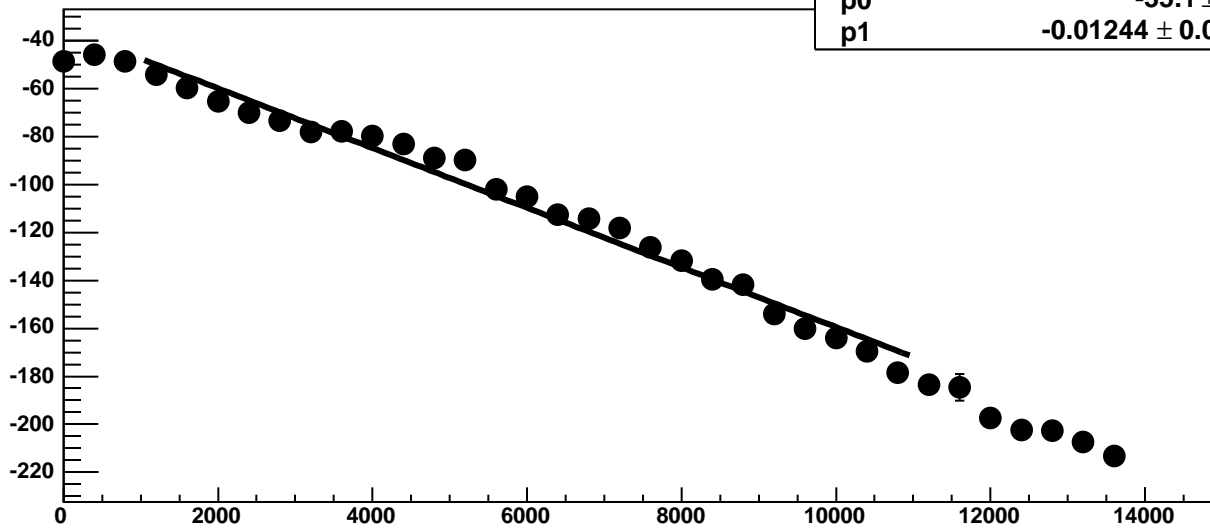
Chip 2, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

197.9 / 23

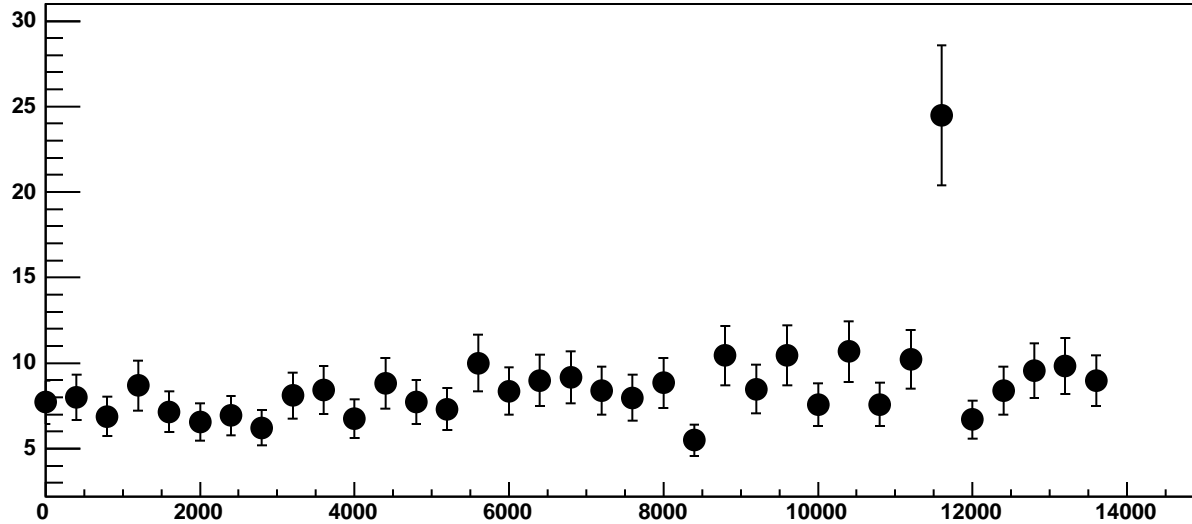
p0

-35.1 ± 0.7948

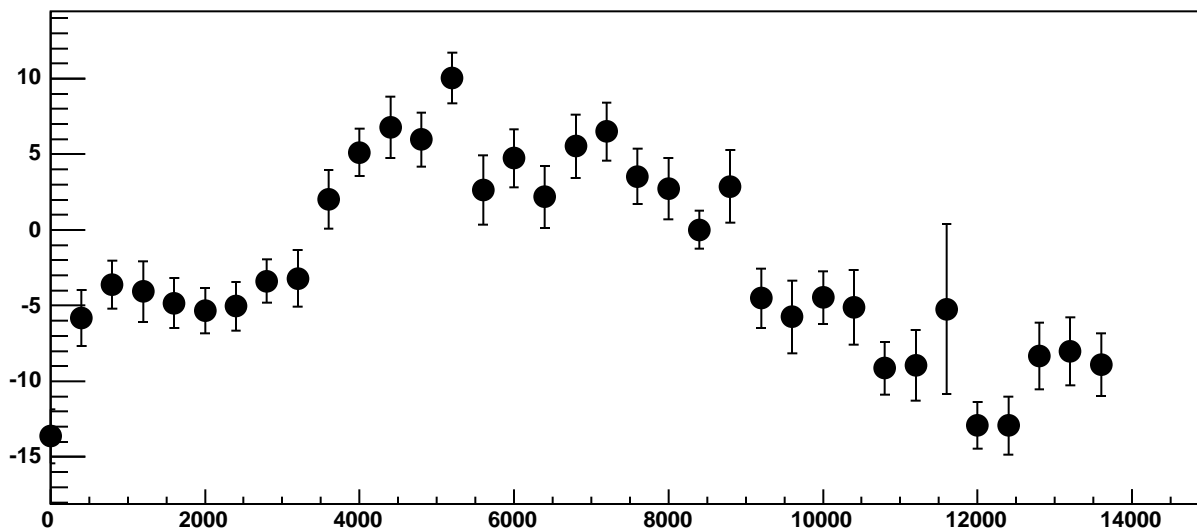
p1

-0.01244 ± 0.0001243

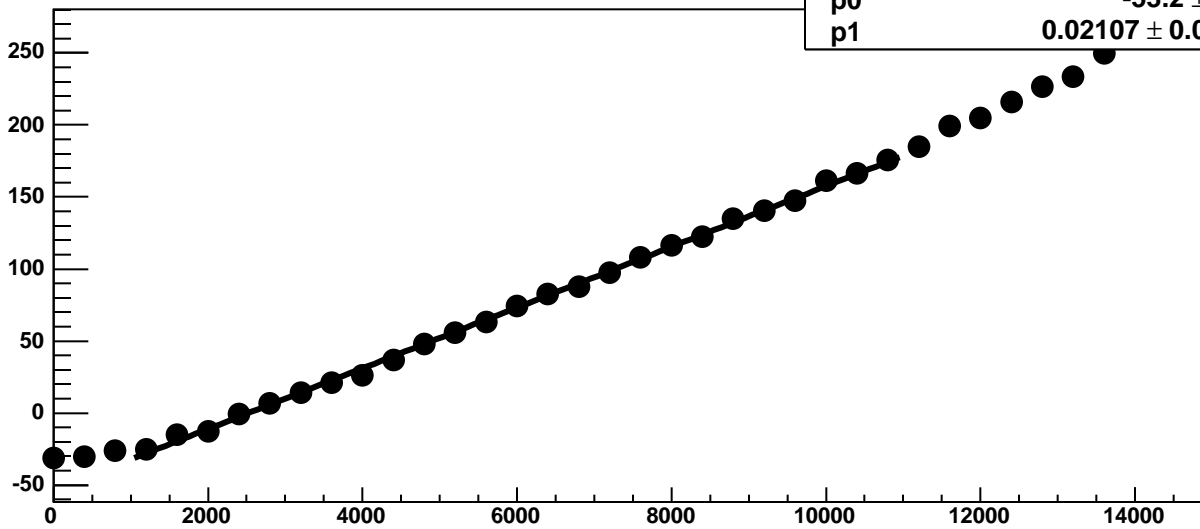
Chip 2, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

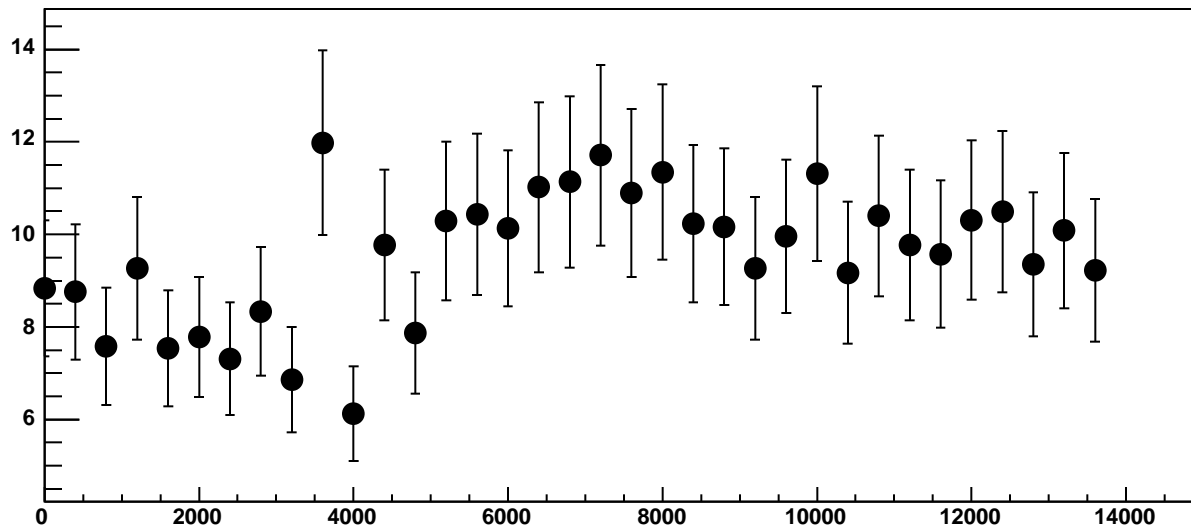


Chip 2, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

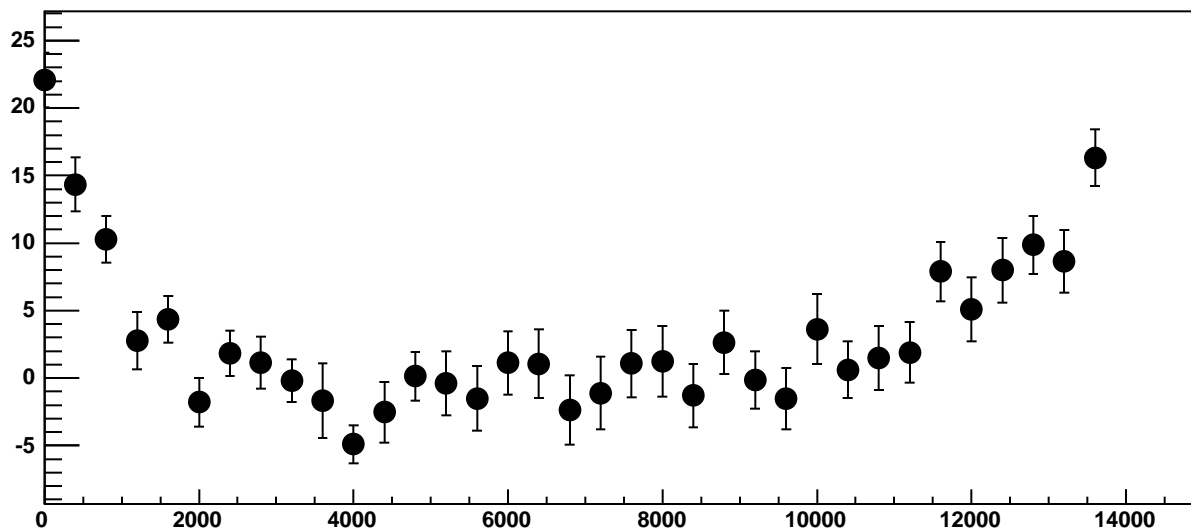


χ^2 / ndf 31.13 / 23
p0 -53.2 ± 0.8796
p1 0.02107 ± 0.0001442

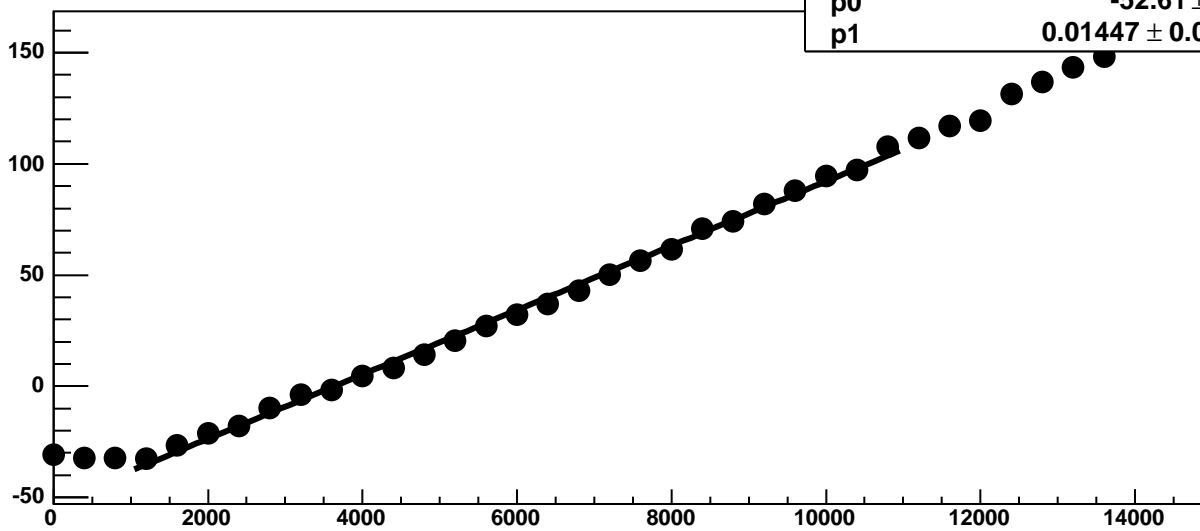
Chip 2, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

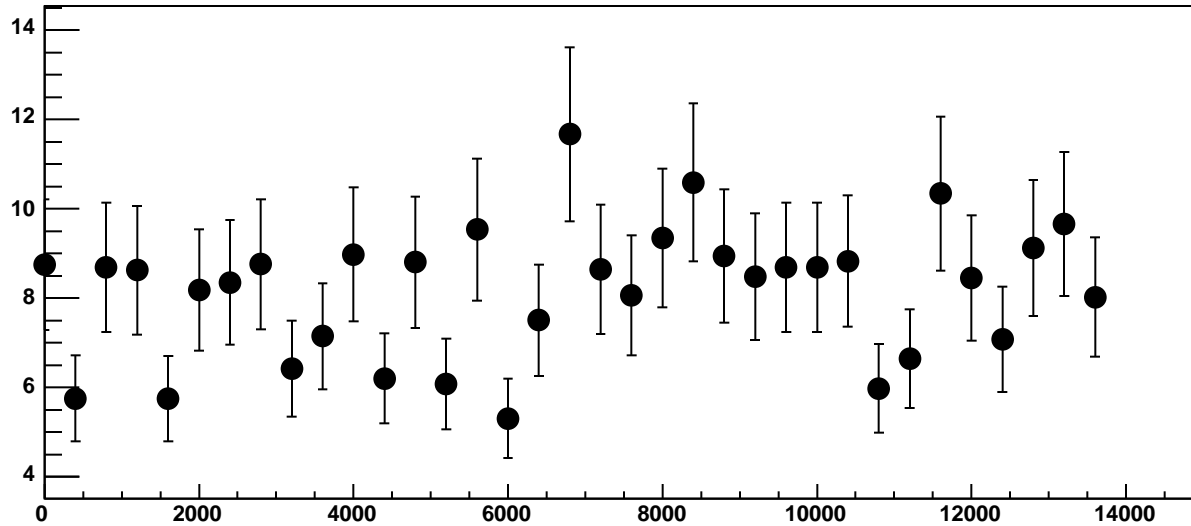


Chip 2, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

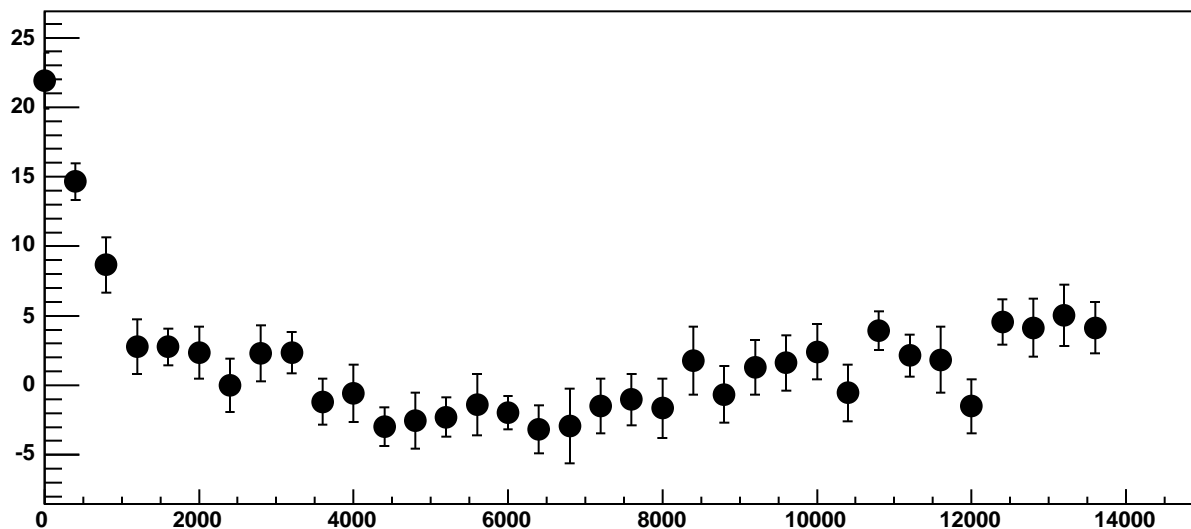


χ^2 / ndf 41.72 / 23
p0 -52.61 ± 0.7825
p1 0.01447 ± 0.0001215

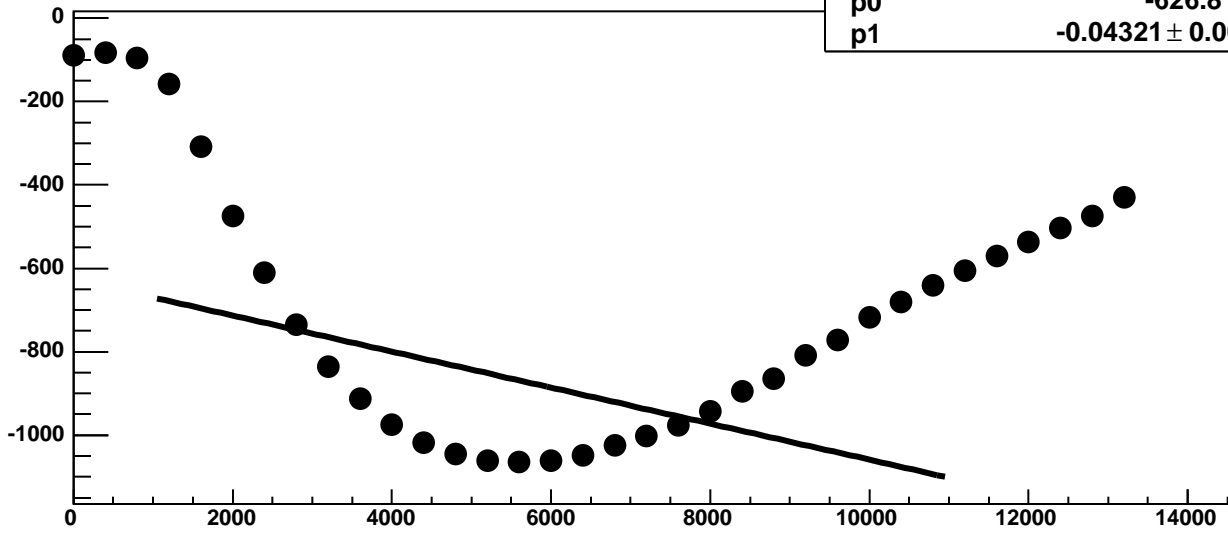
Chip 2, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

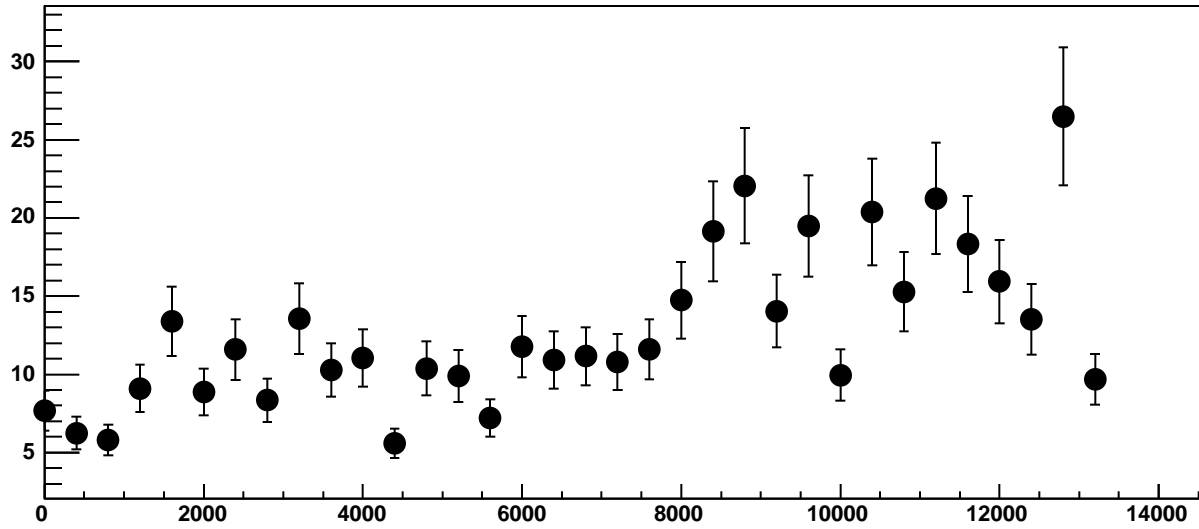


Chip 2, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

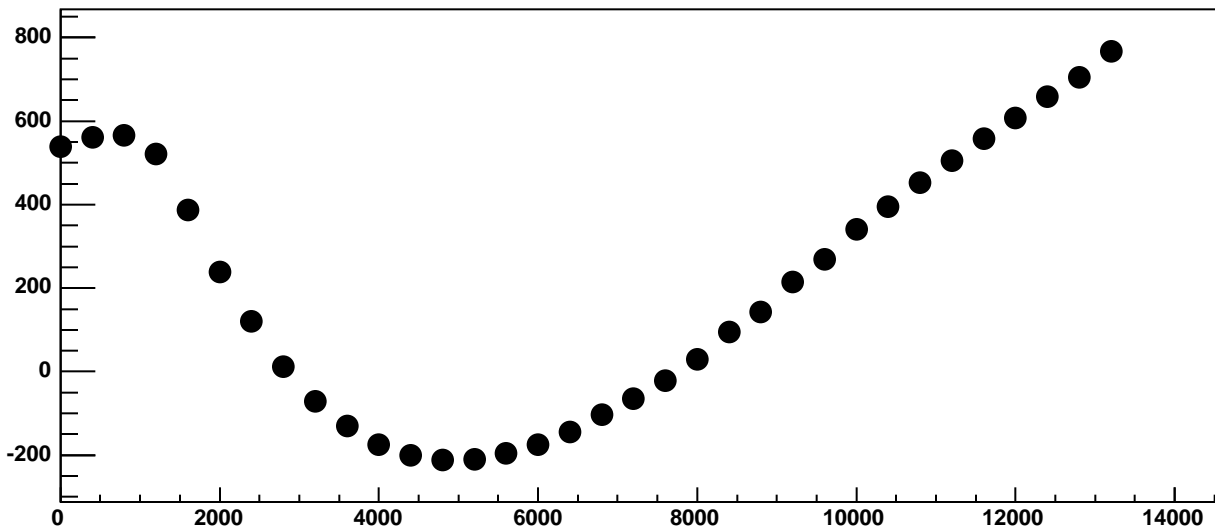


χ^2 / ndf 2.233e+05 / 23
p0 -626.8 ± 1.115
p1 -0.04321 ± 0.0001947

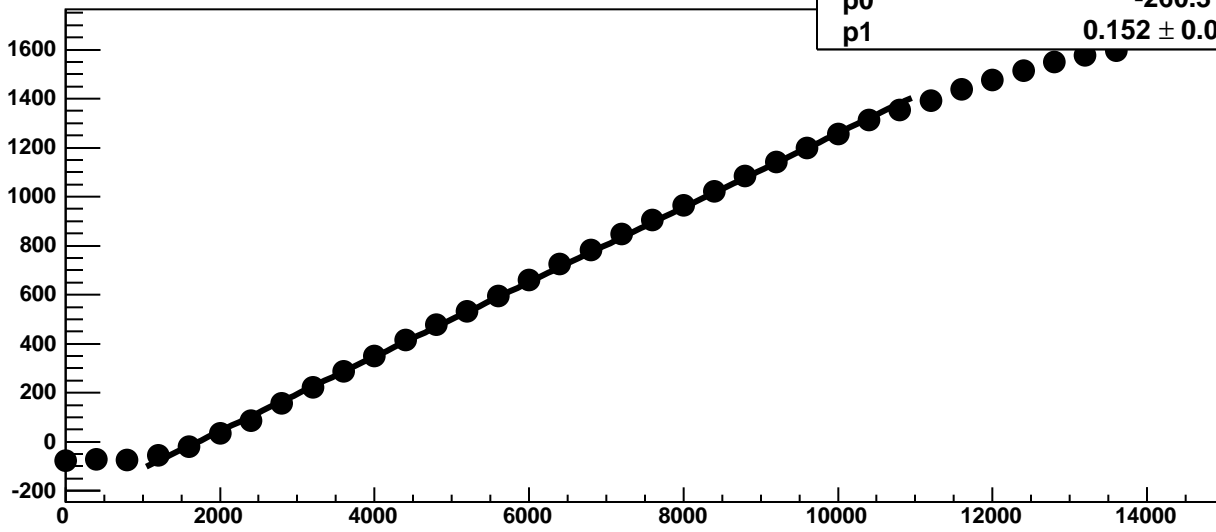
Chip 2, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



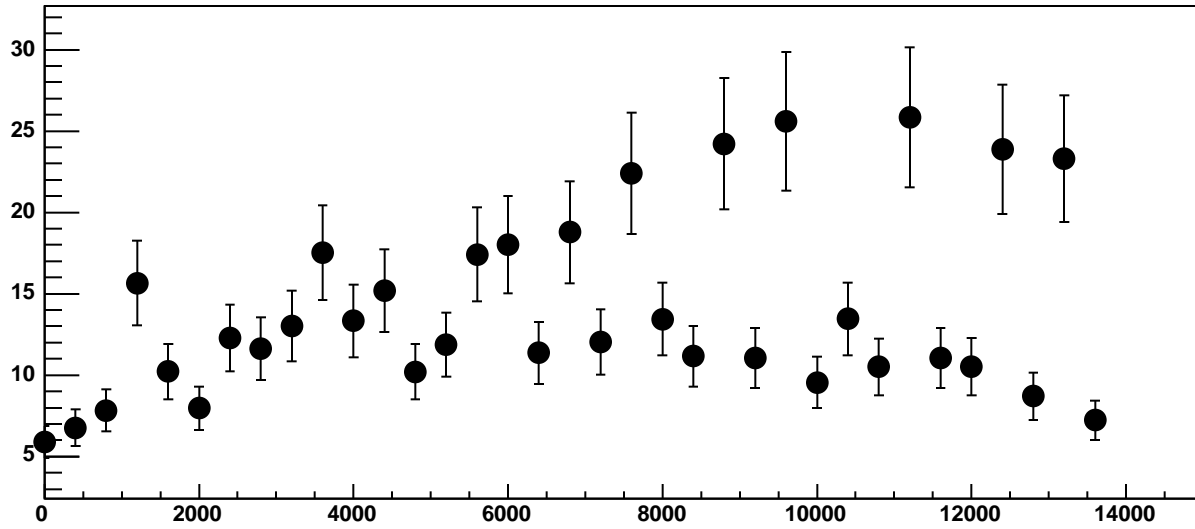
Chip 2, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



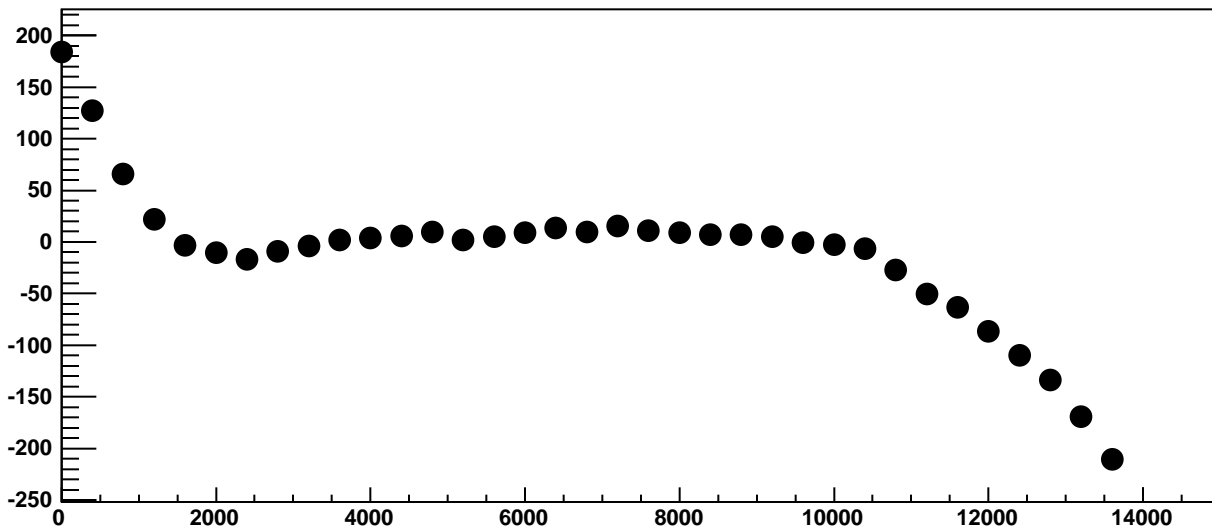
Chip 2, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



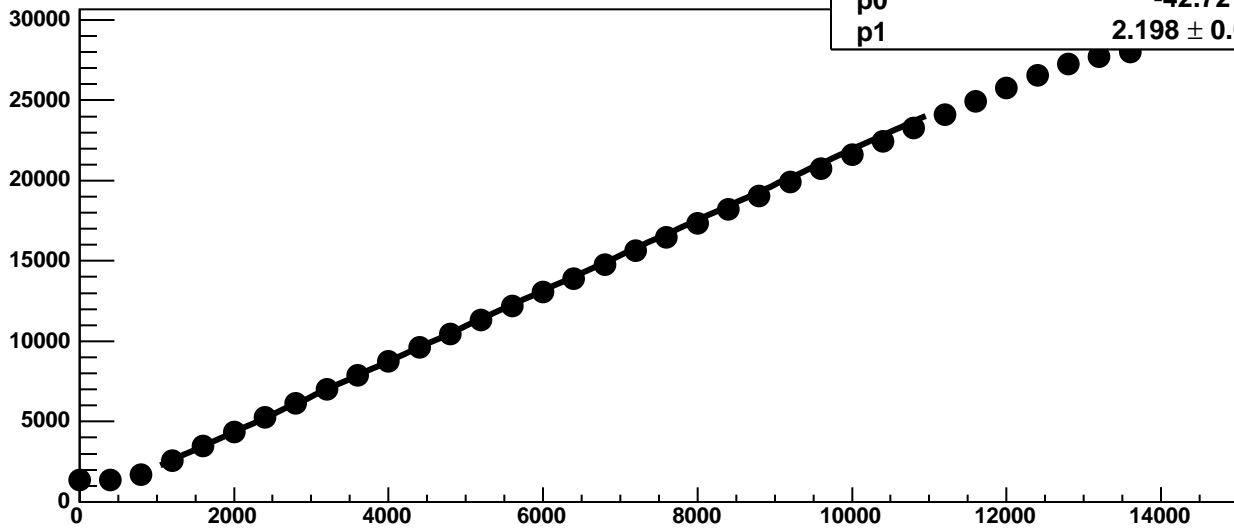
Chip 2, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



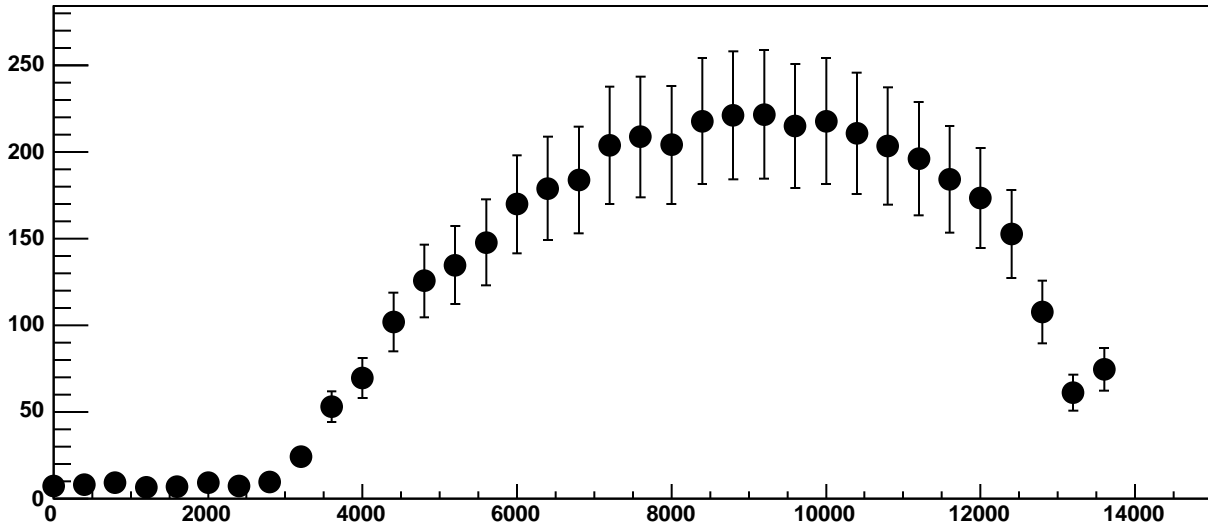
Chip 2, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



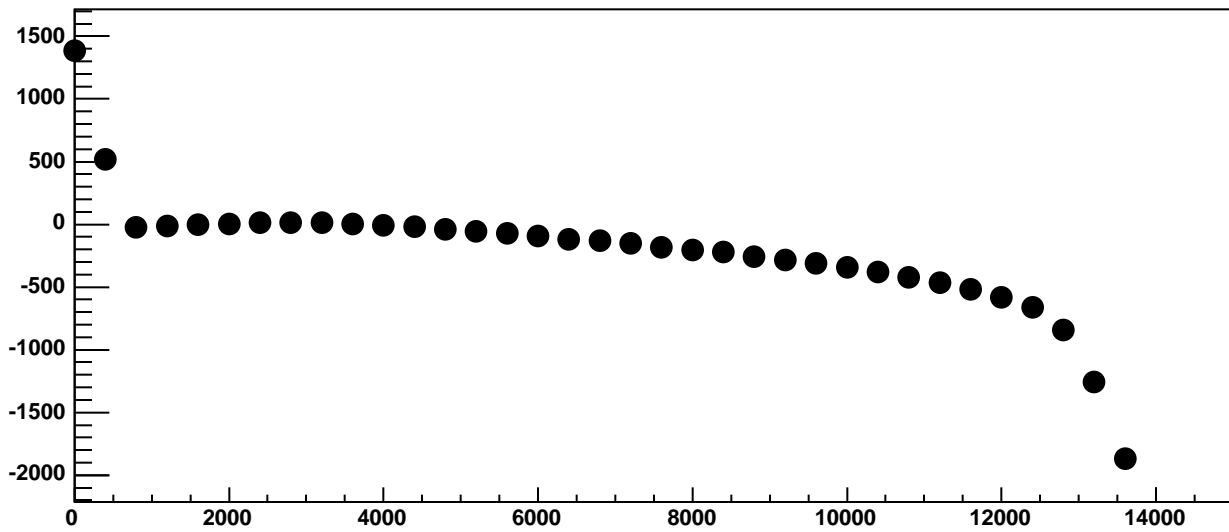
Chip 2, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC



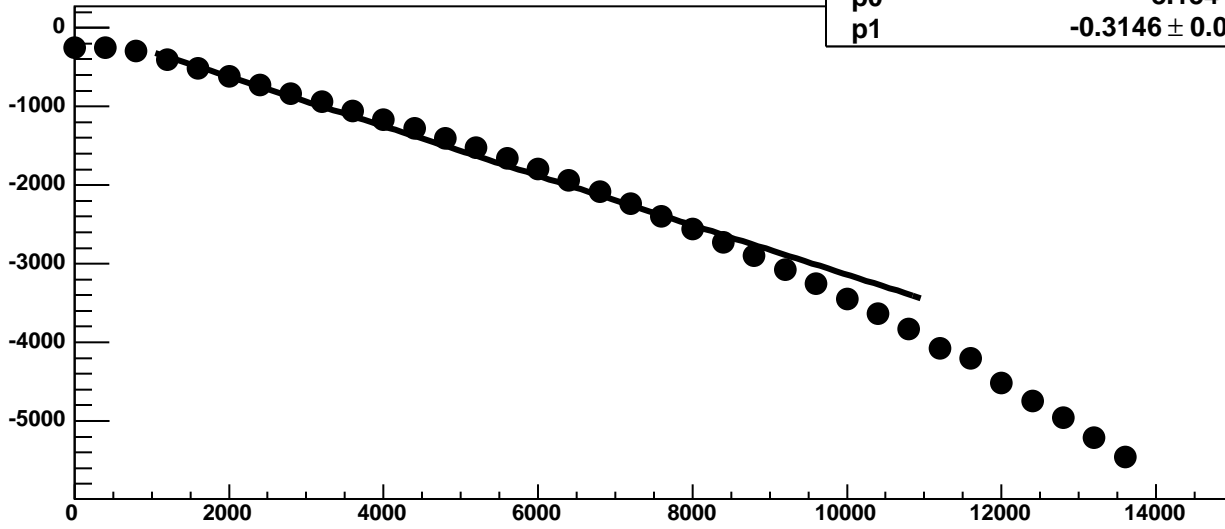
Chip 2, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC

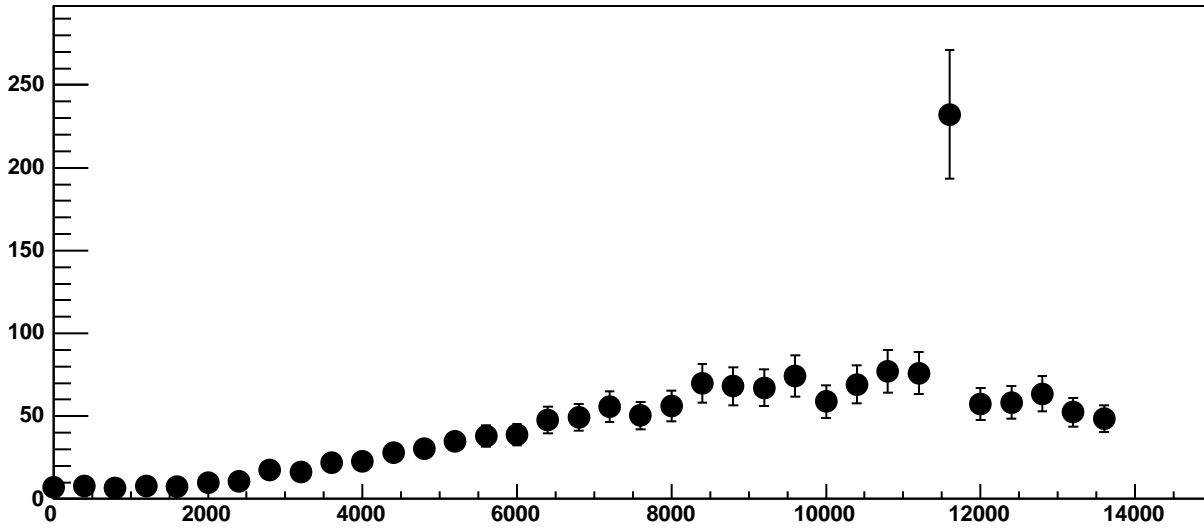


Chip 2, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

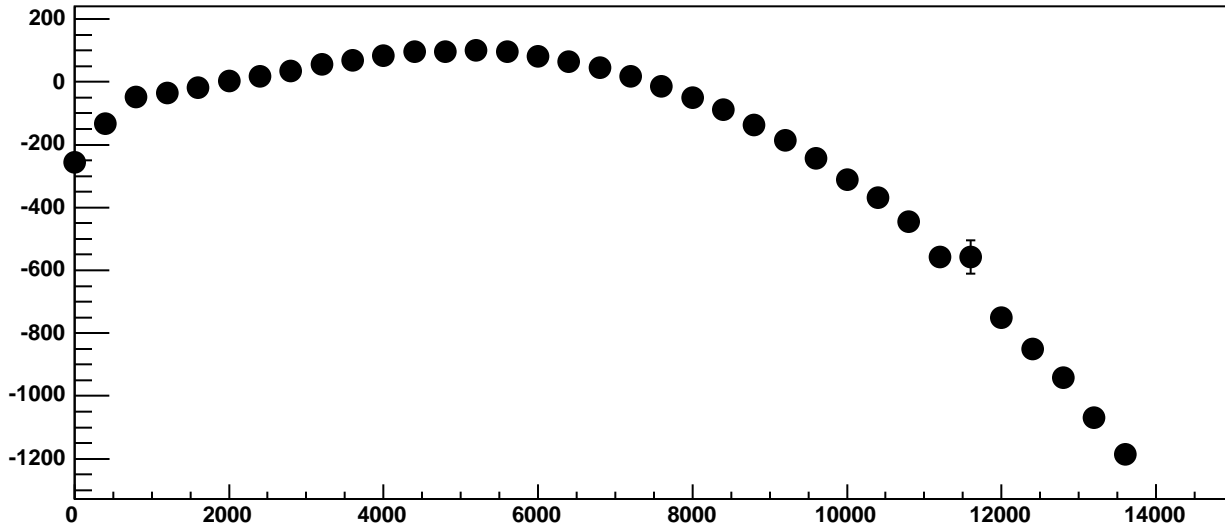


χ^2 / ndf 4310 / 23
p0 8.154 ± 1.522
p1 -0.3146 ± 0.0005118

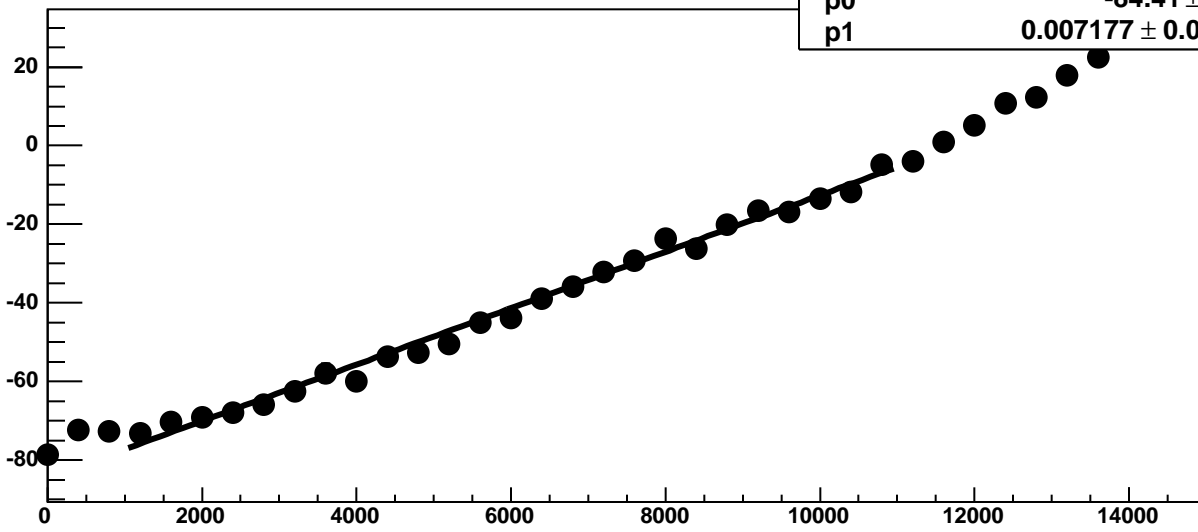
Chip 2, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



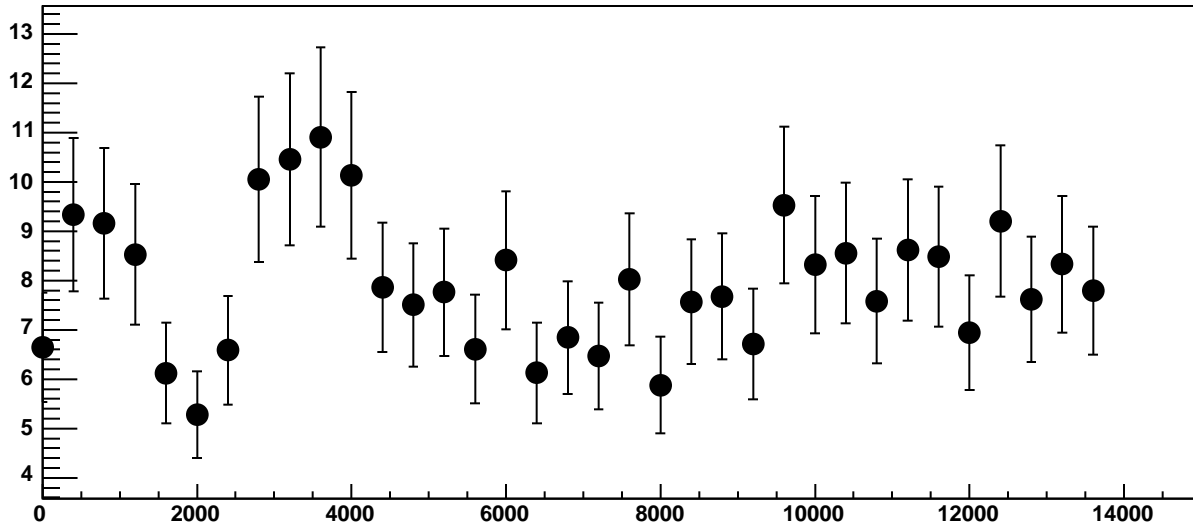
Chip 2, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



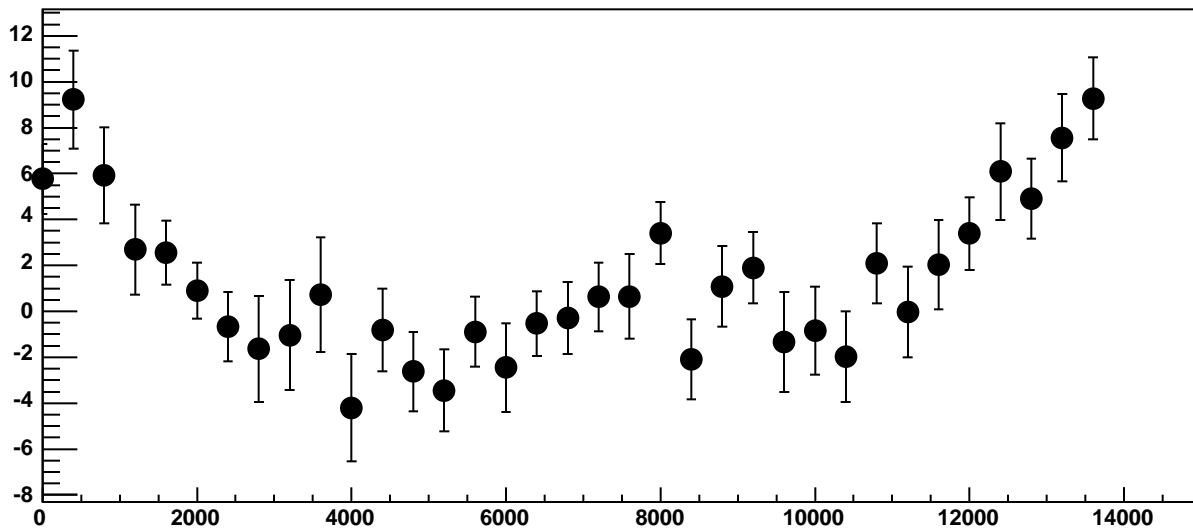
Chip 2, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



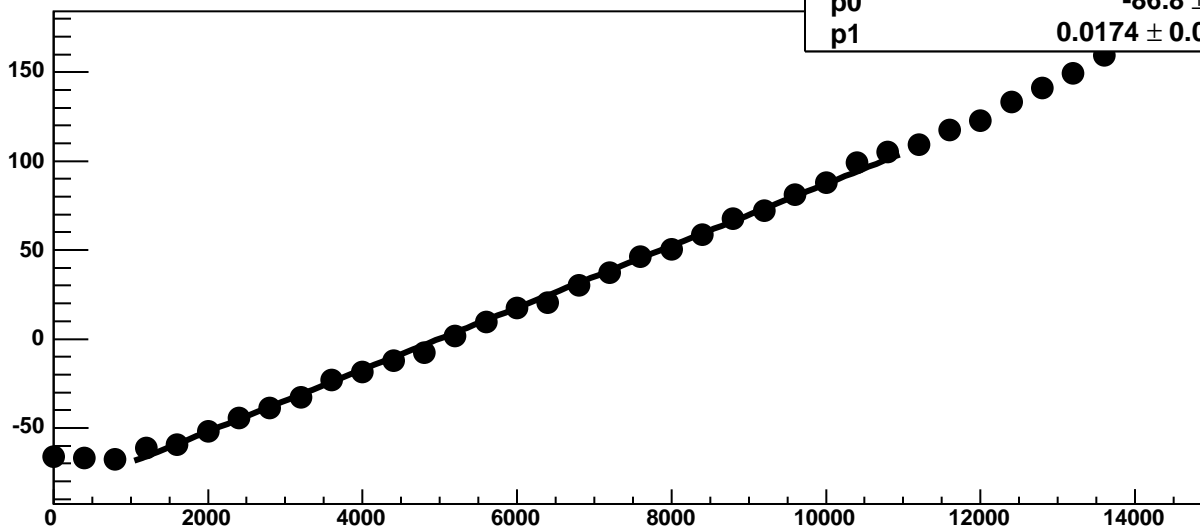
Chip 2, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



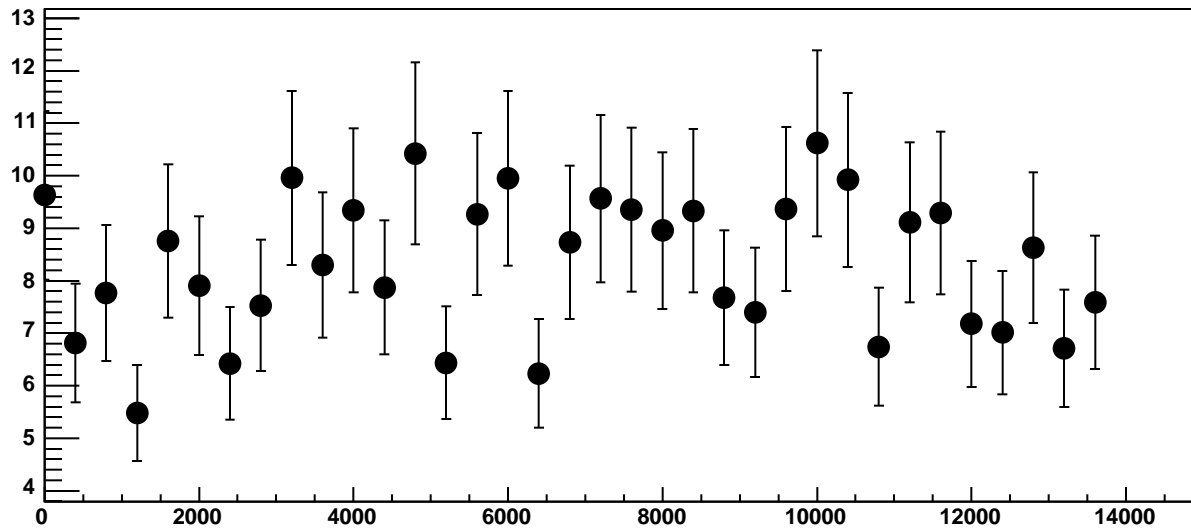
Chip 2, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



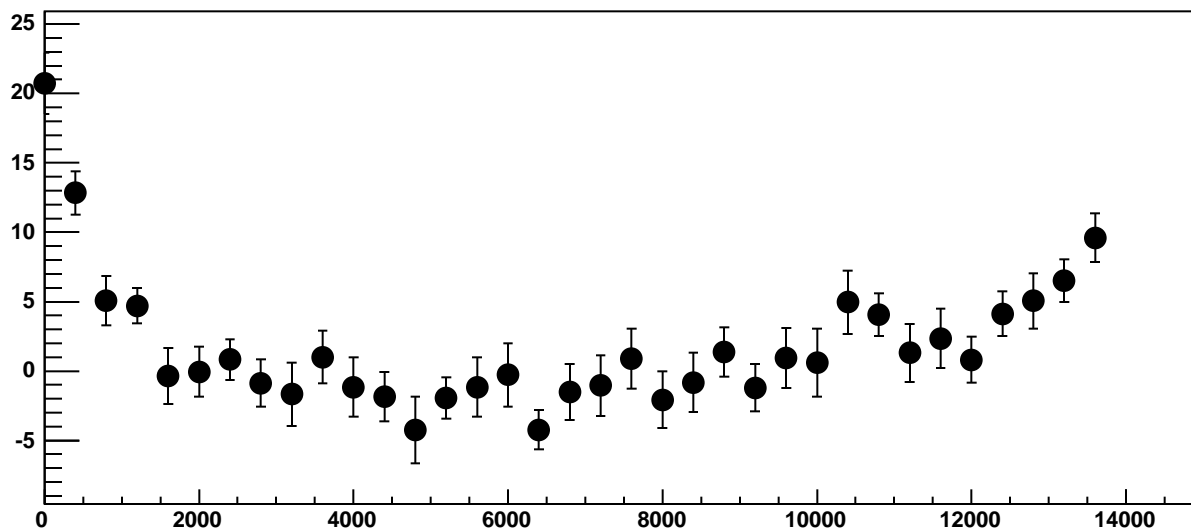
Chip 2, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC



Chip 2, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC

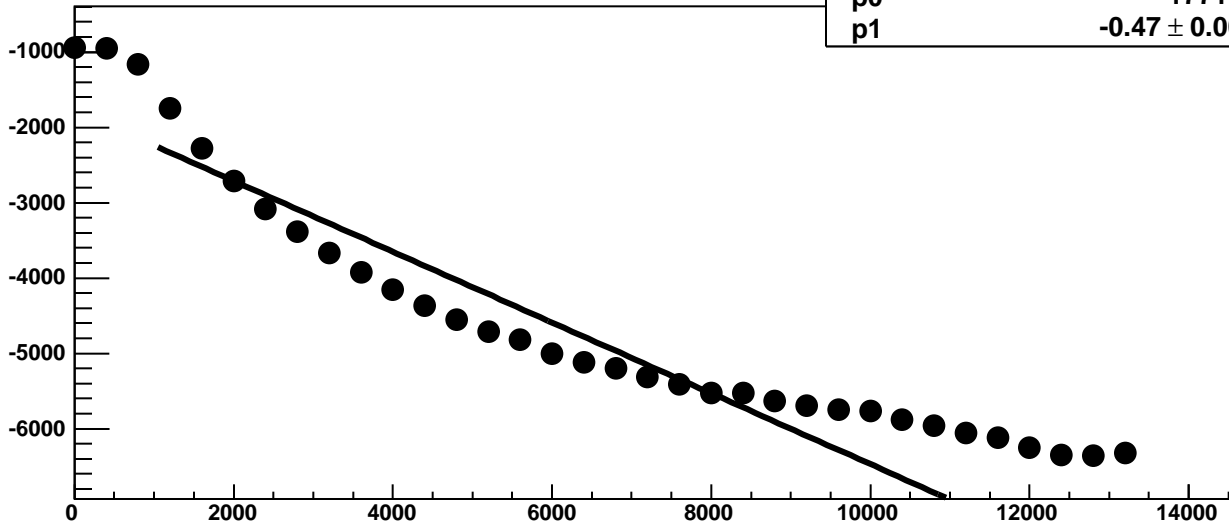


Chip 2, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC

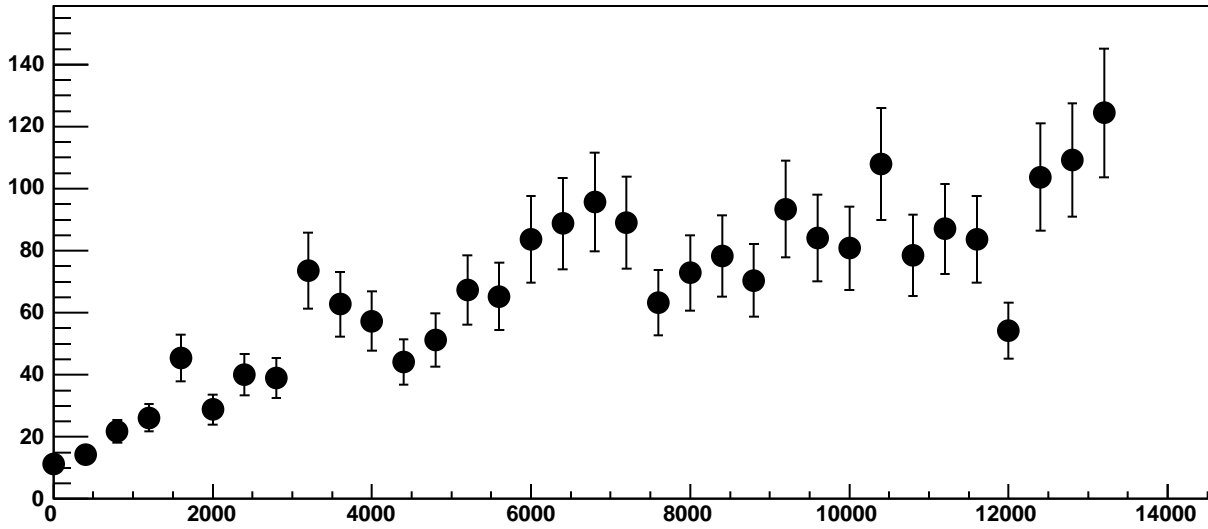


Chip 2, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

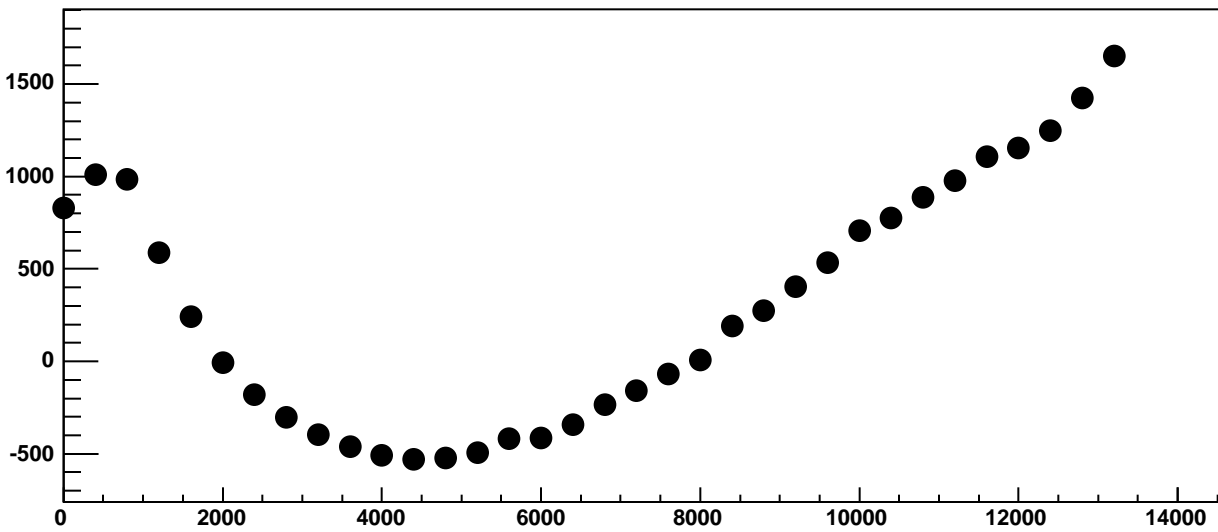
χ^2 / ndf 2.859e+04 / 23
p0 -1771 ± 4.391
p1 -0.47 ± 0.0009029



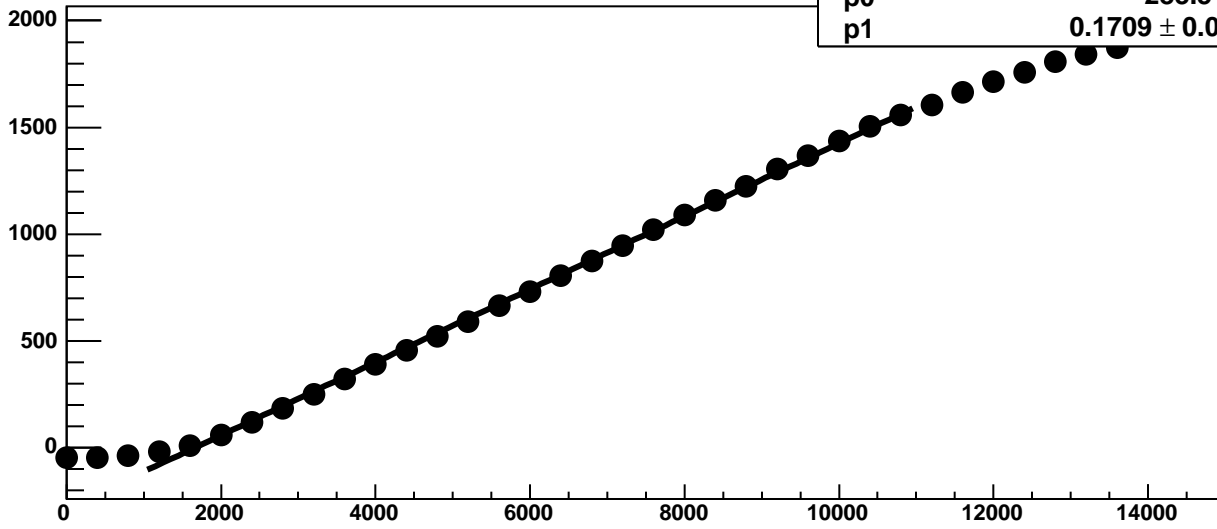
Chip 2, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

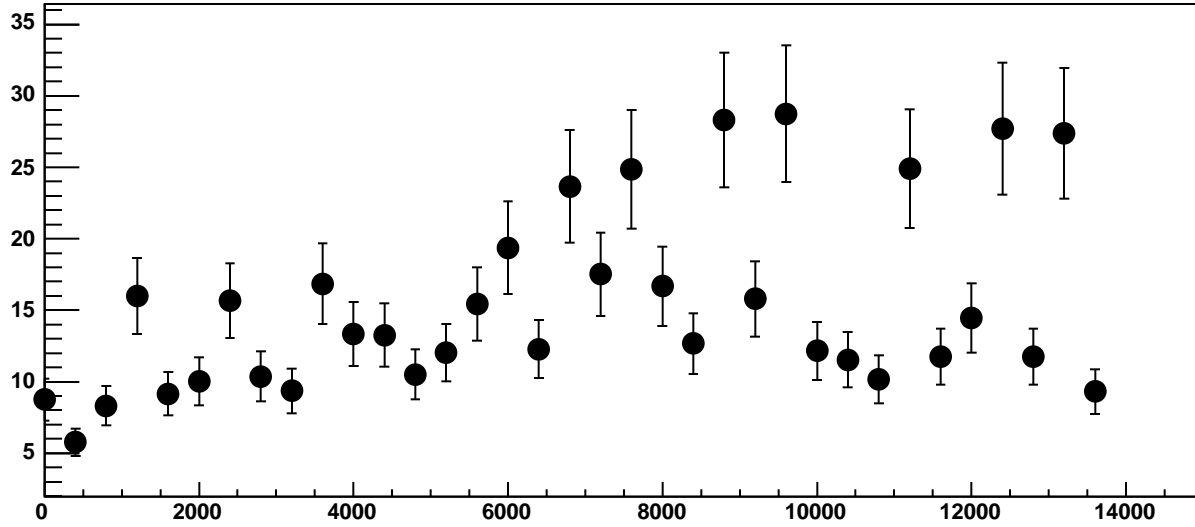


Chip 2, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

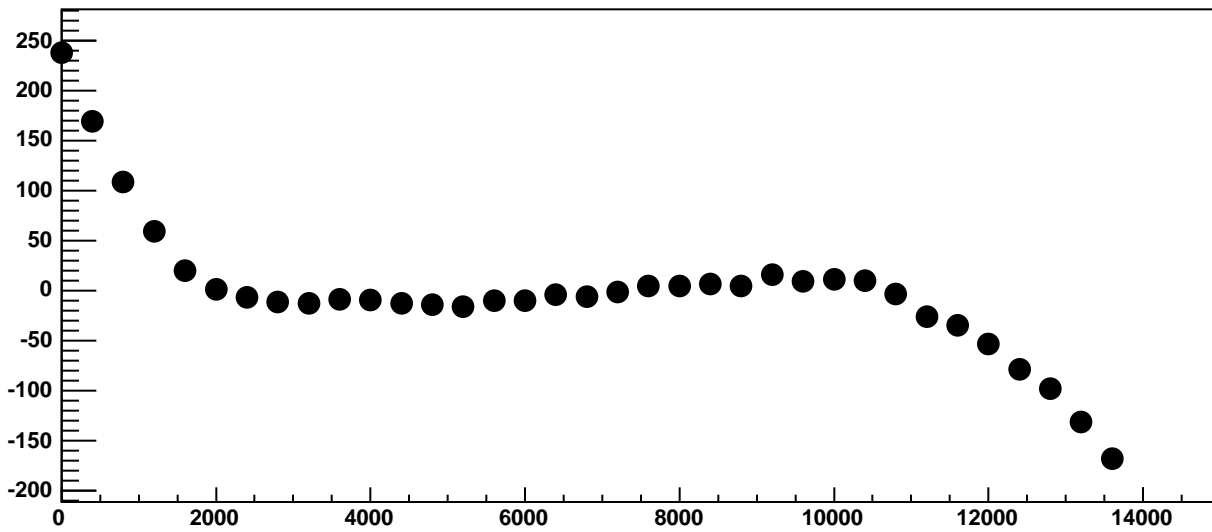


χ^2 / ndf 585.4 / 23
p0 -283.5 ± 1.238
p1 0.1709 ± 0.0001967

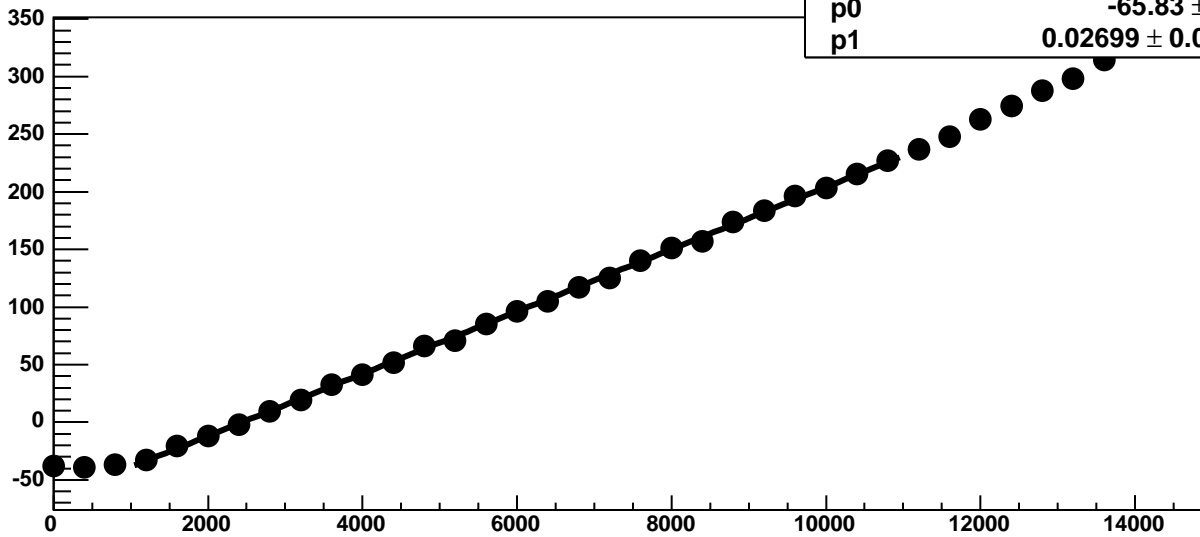
Chip 2, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

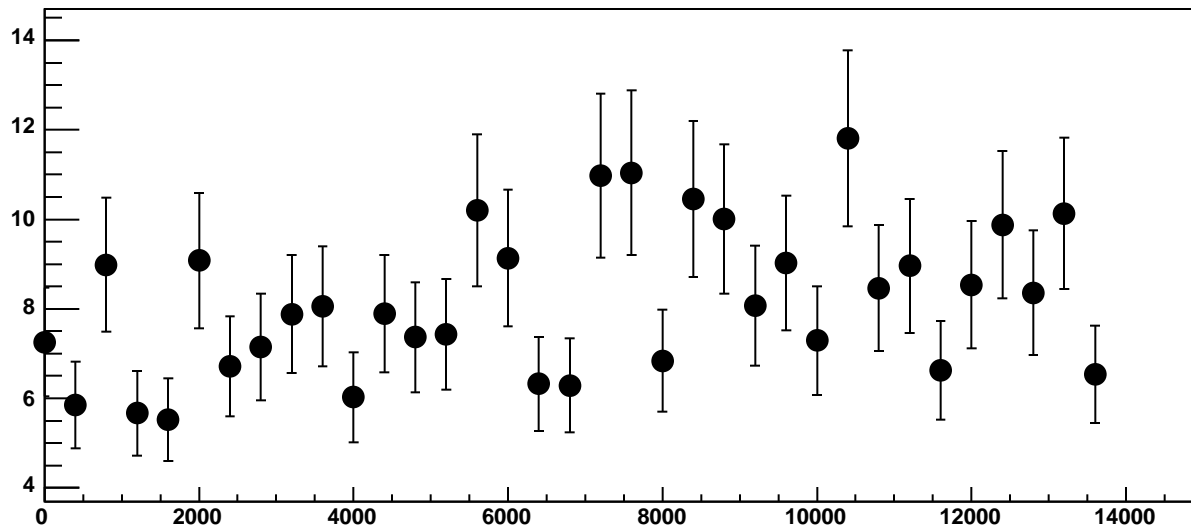


Chip 2, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC

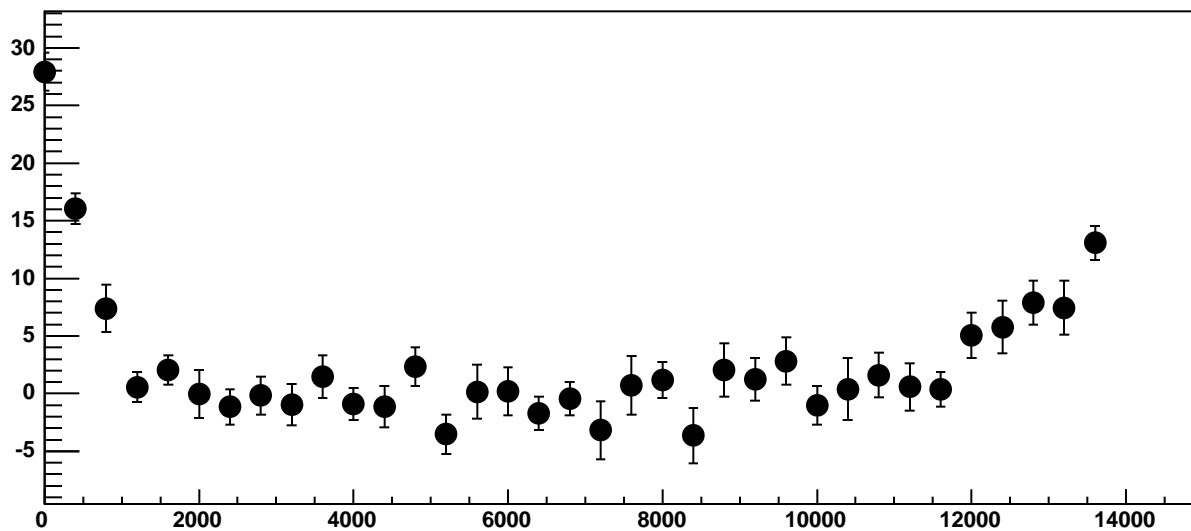


χ^2 / ndf 21.5 / 23
p0 -65.83 ± 0.7359
p1 0.02699 ± 0.0001207

Chip 2, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC

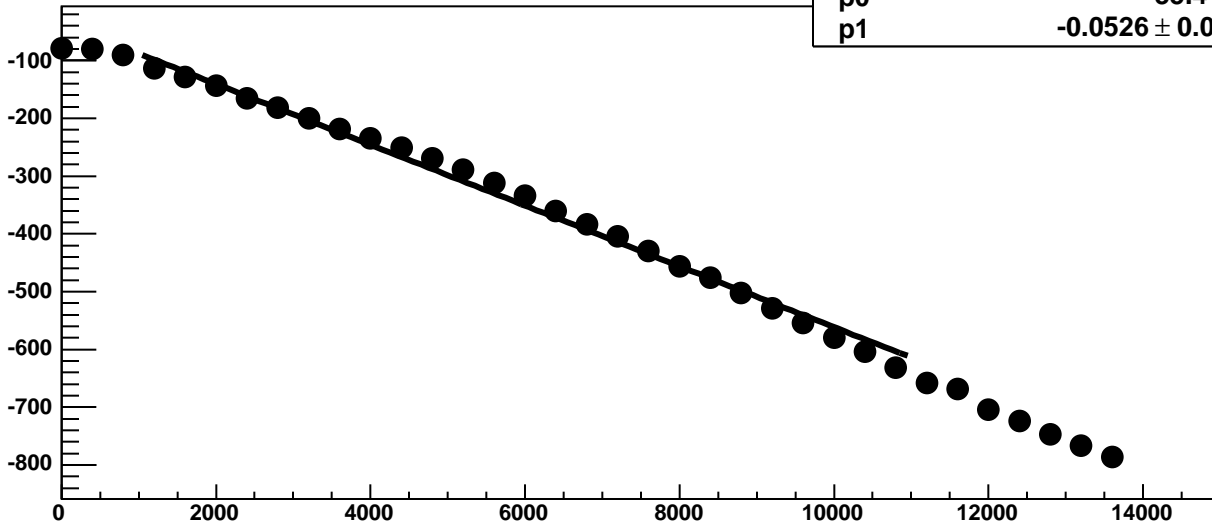


Chip 2, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC

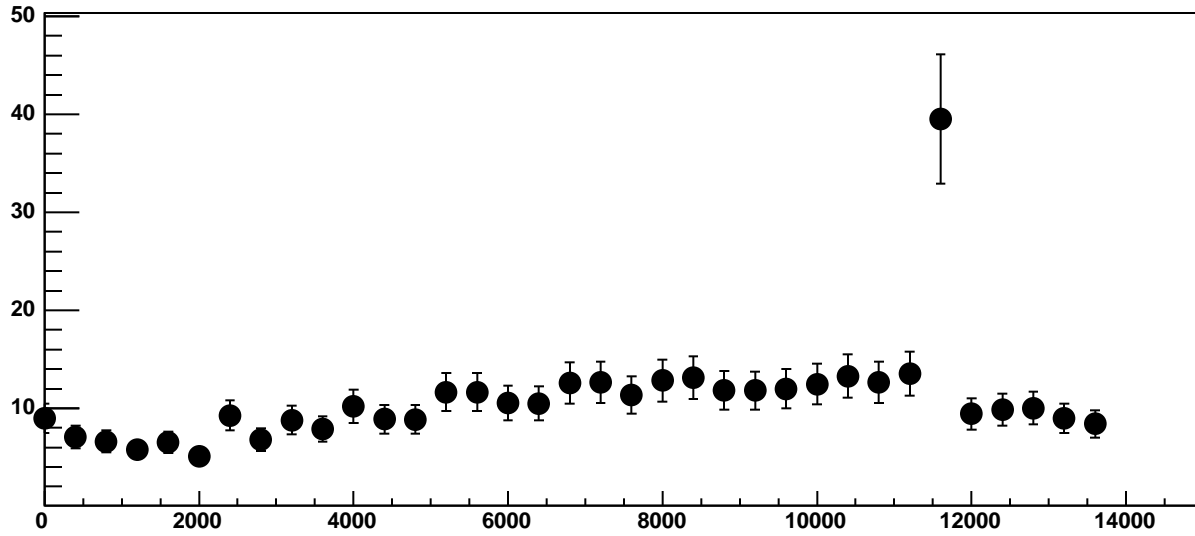


Chip 2, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

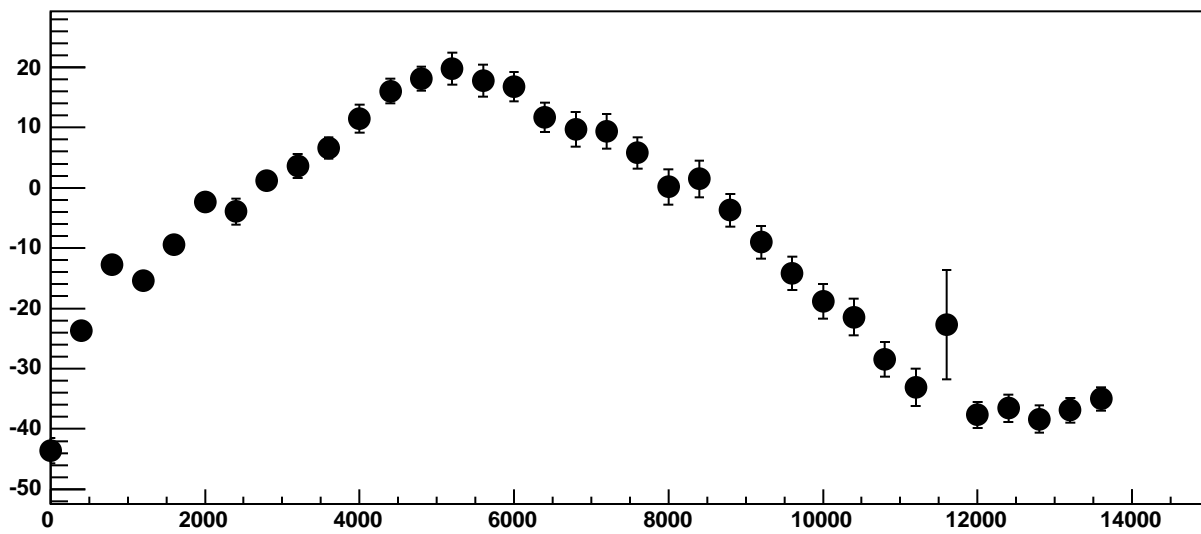
χ^2 / ndf 790 / 23
p0 -35.4 ± 0.784
p1 -0.0526 ± 0.0001476



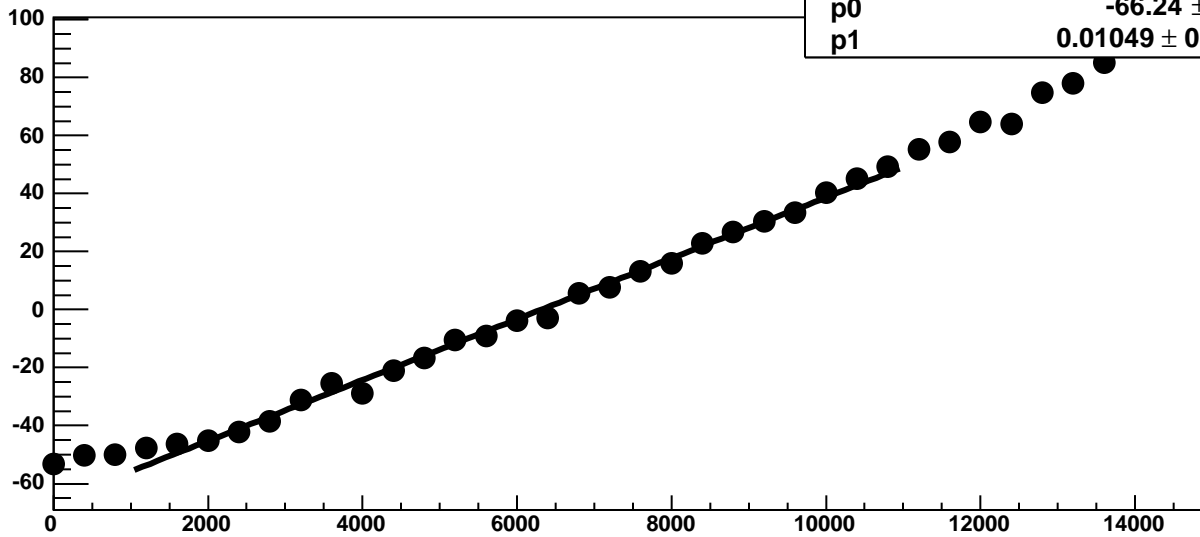
Chip 2, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

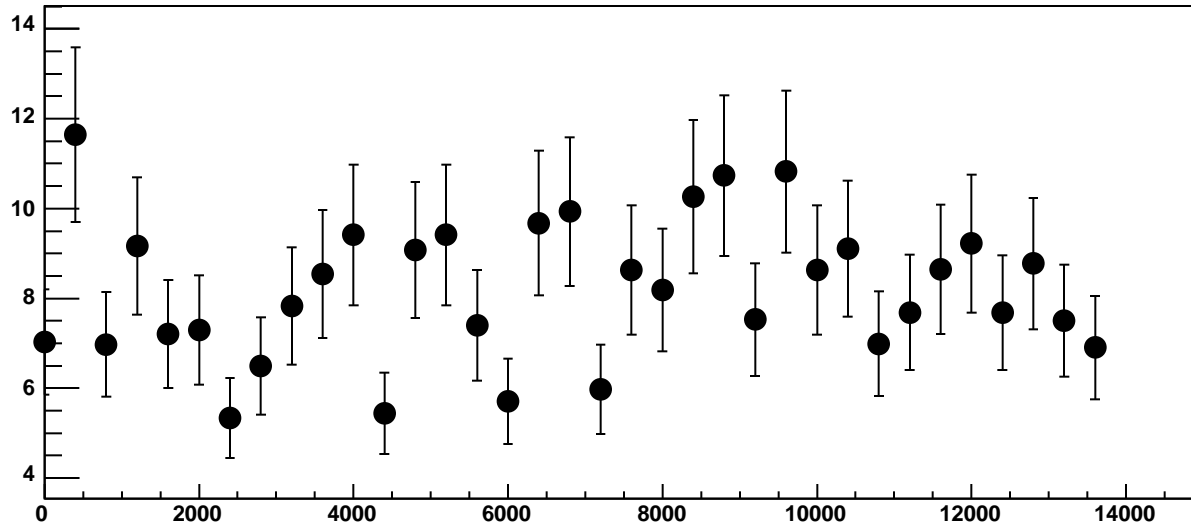


Chip 2, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

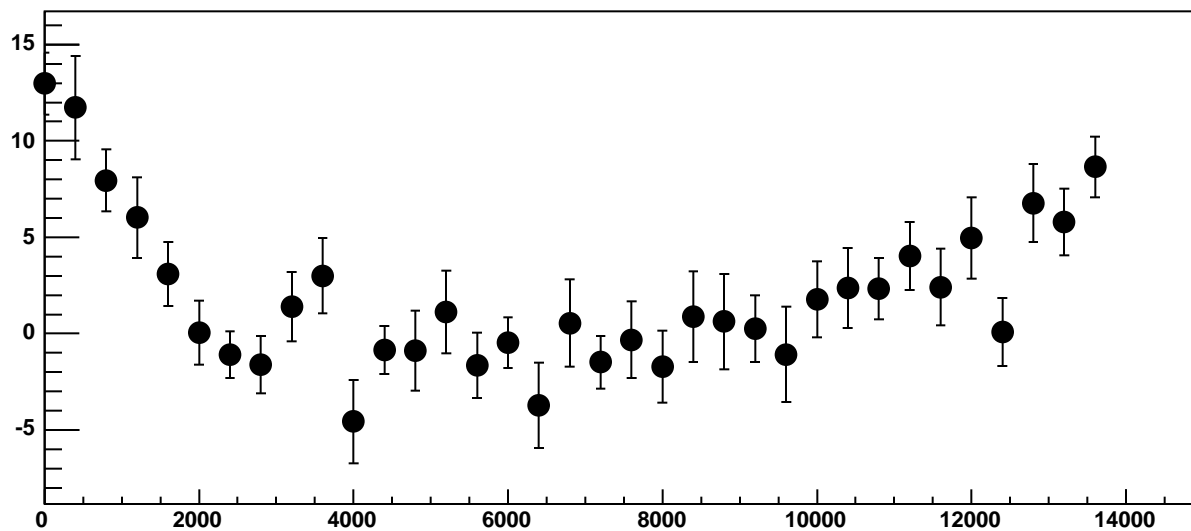


χ^2 / ndf 32.61 / 23
p0 -66.24 ± 0.7793
p1 0.01049 ± 0.000124

Chip 2, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

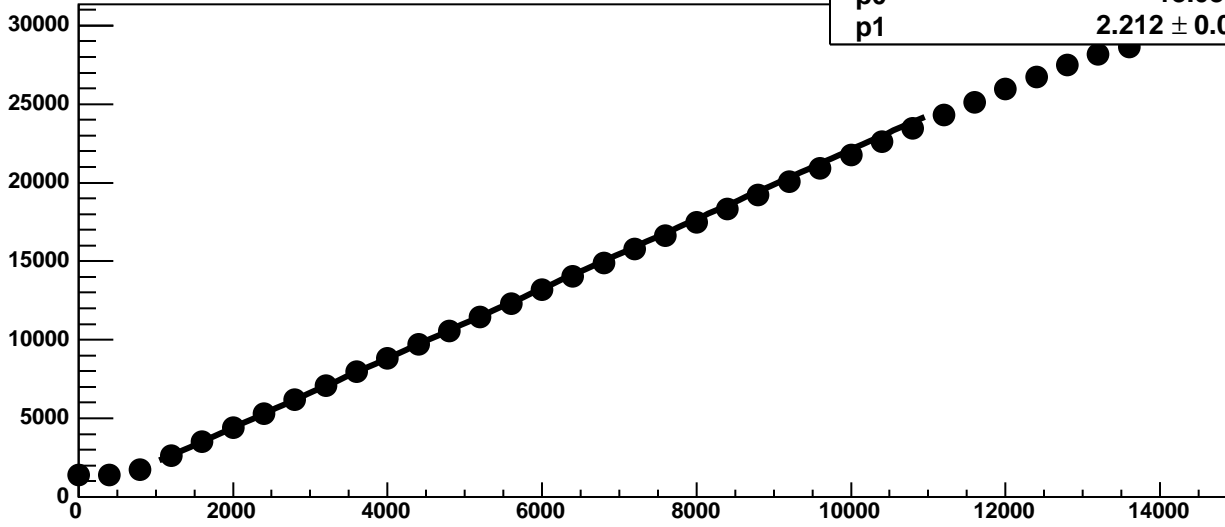


Chip 2, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

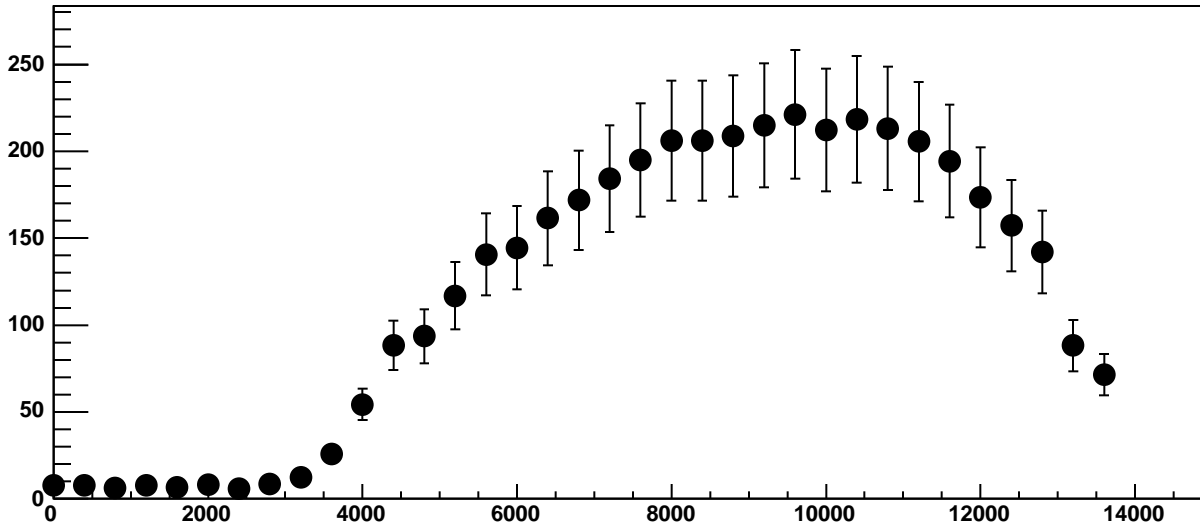


Chip 2, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

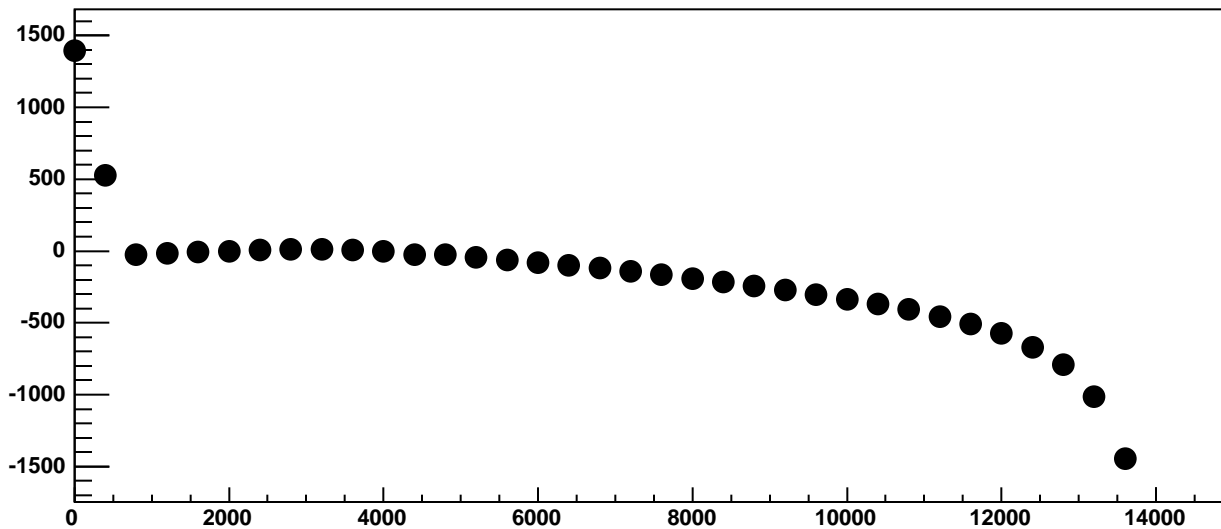
χ^2 / ndf 505.9 / 23
p0 -18.08 ± 2.189
p1 2.212 ± 0.0009694



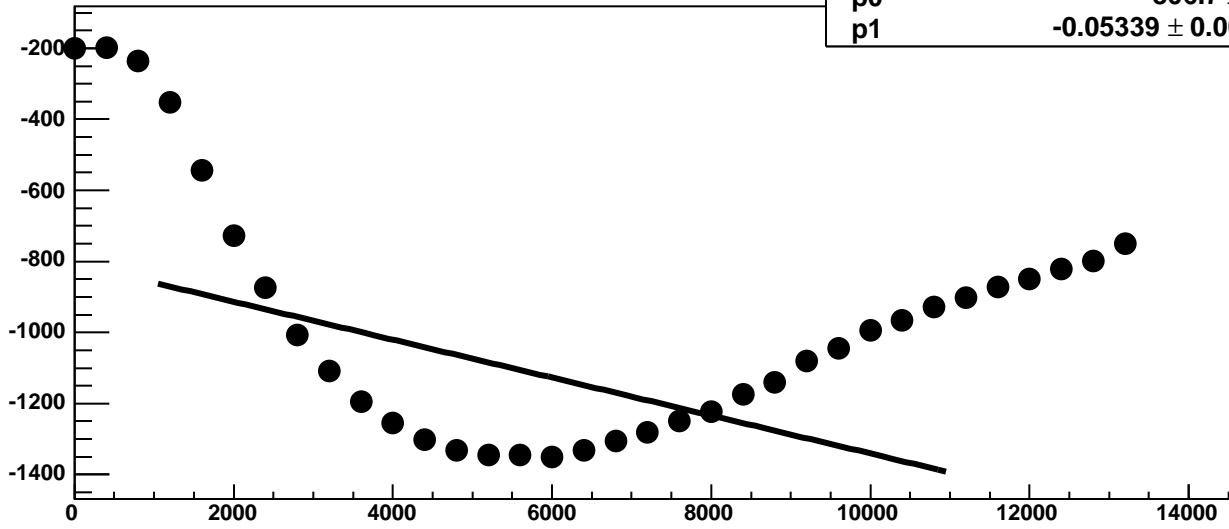
Chip 2, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC

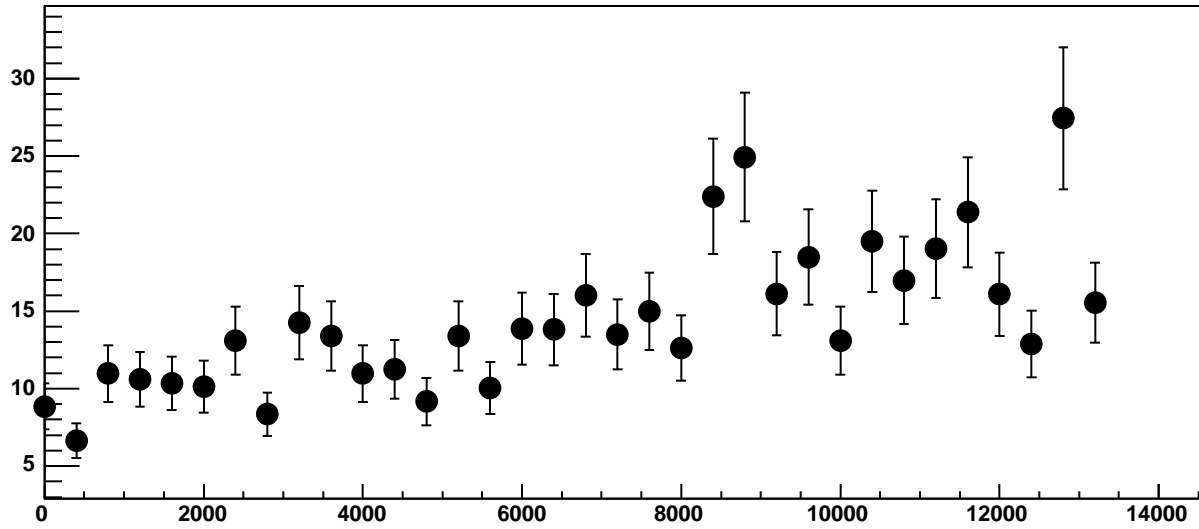


Chip 2, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

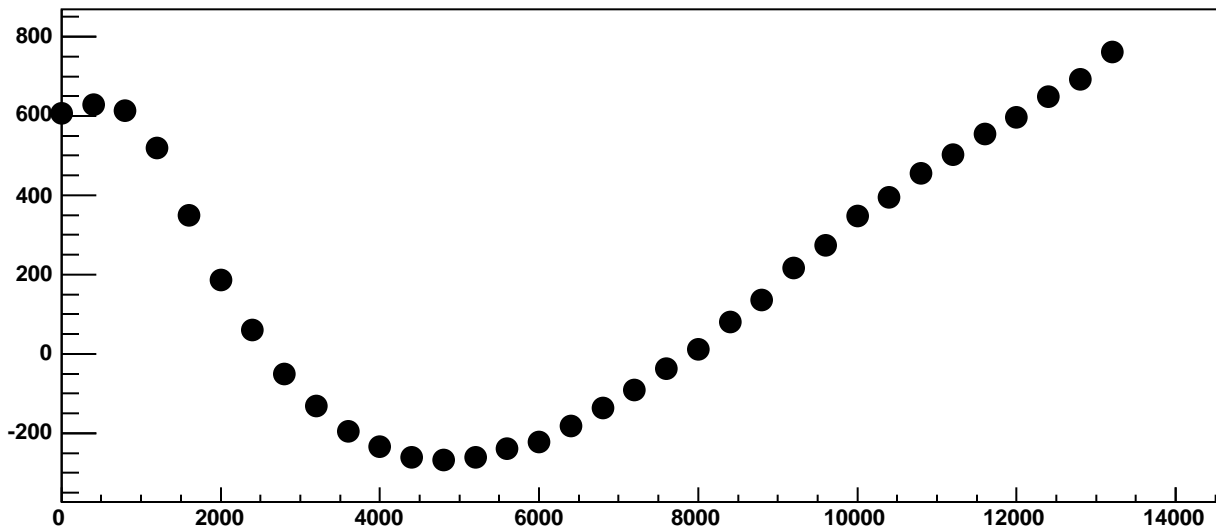


χ^2 / ndf 1.875e+05 / 23
p0 -806.7 ± 1.227
p1 -0.05339 ± 0.0002156

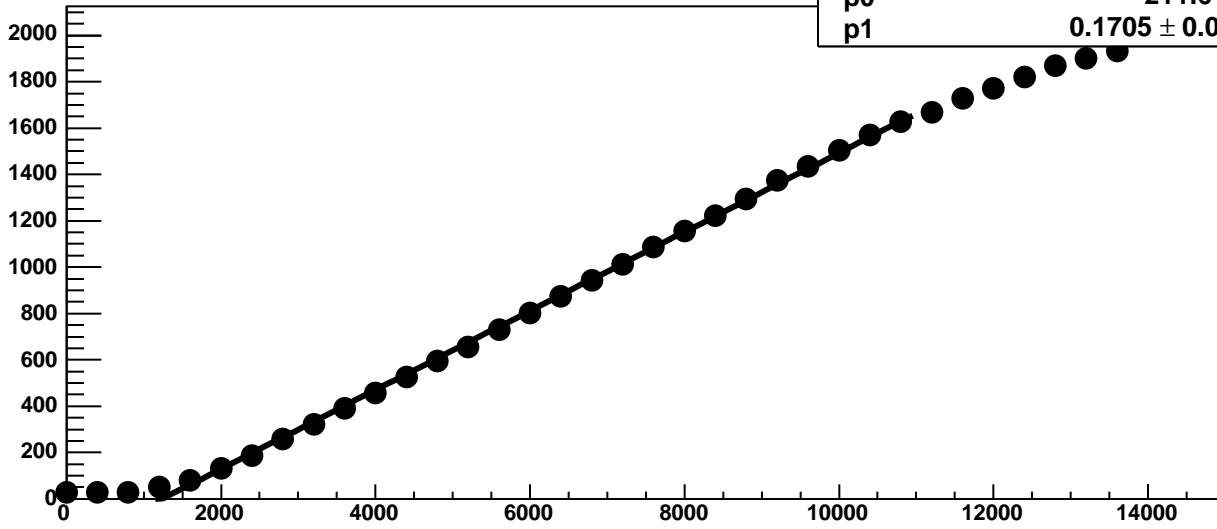
Chip 2, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

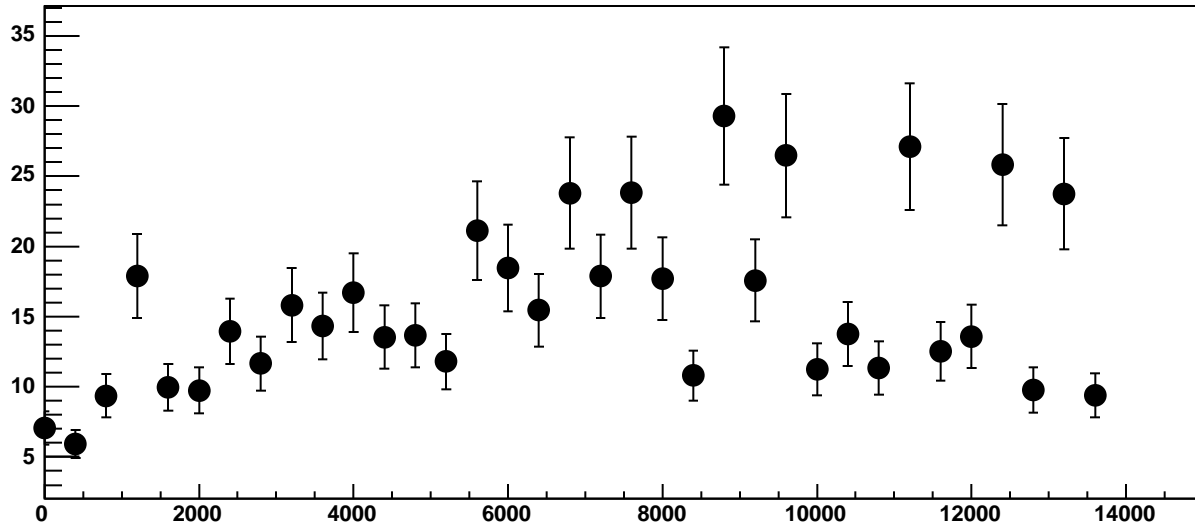


Chip 2, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

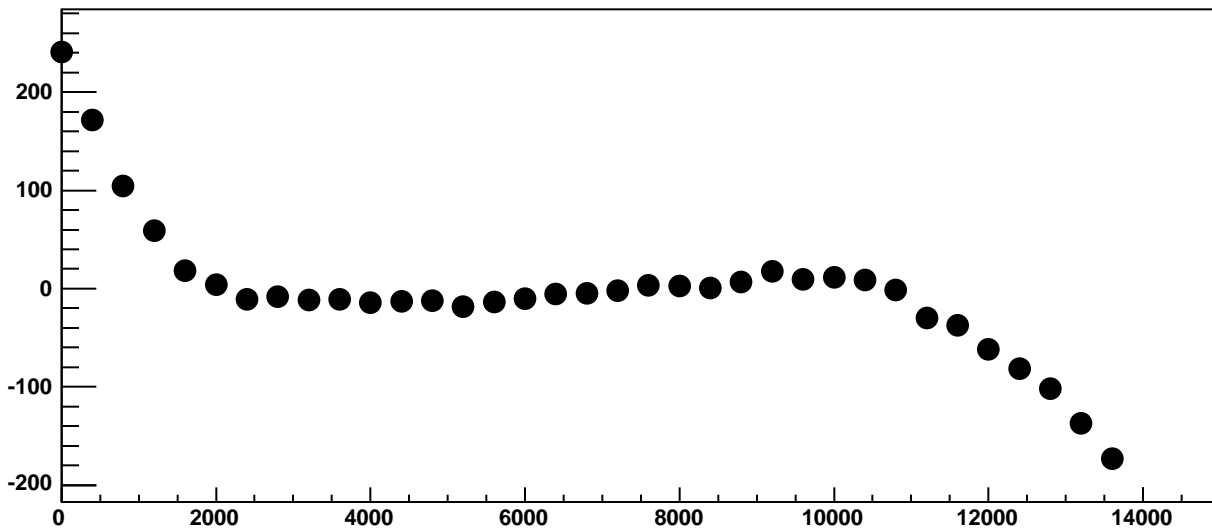


χ^2 / ndf 477.5 / 23
p0 -211.6 ± 1.346
p1 0.1705 ± 0.0002094

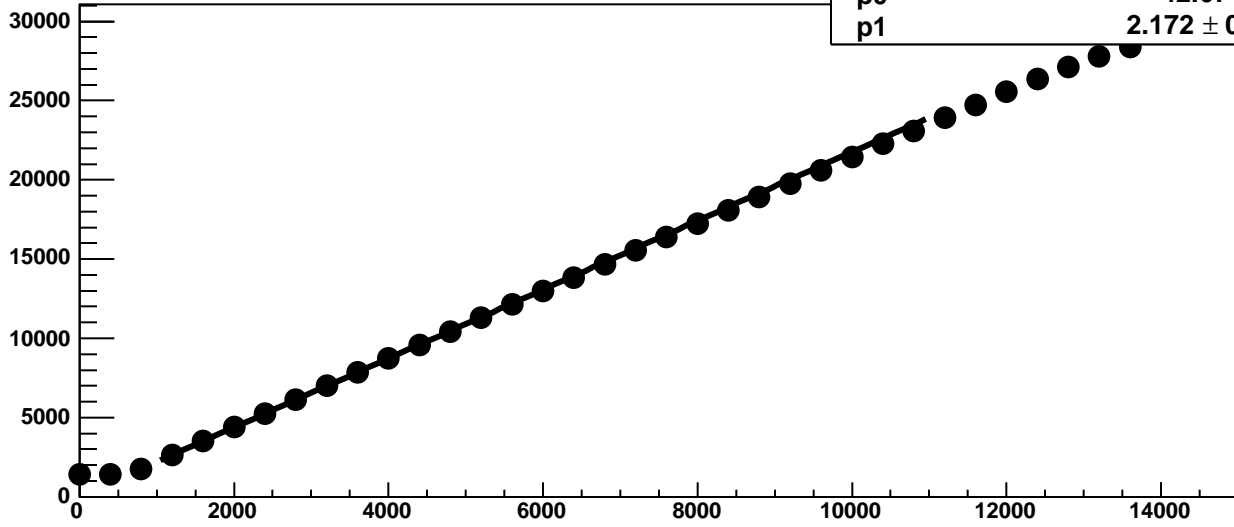
Chip 2, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

466.2 / 23

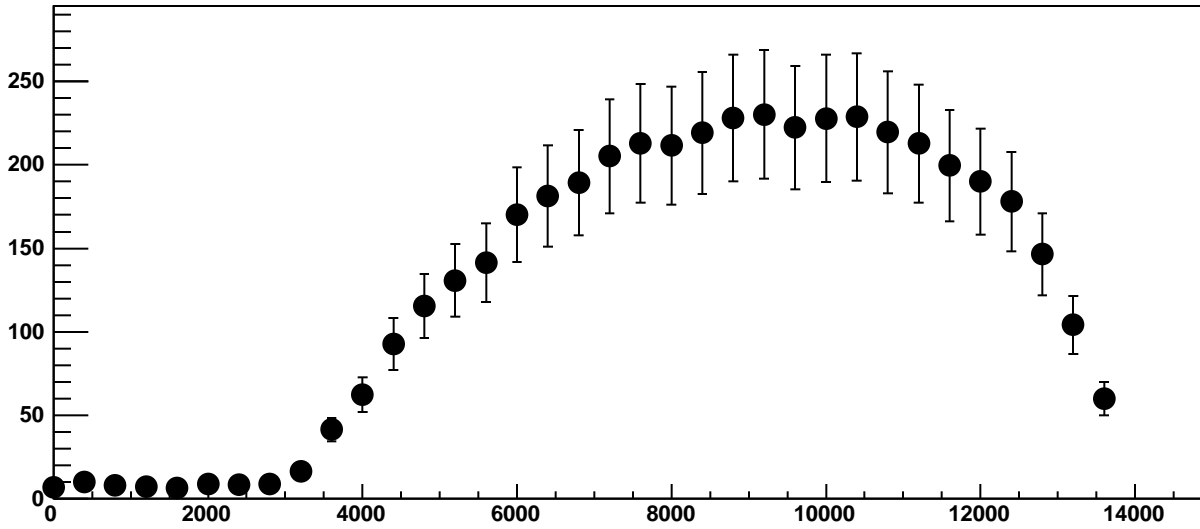
p0

42.67 ± 2.241

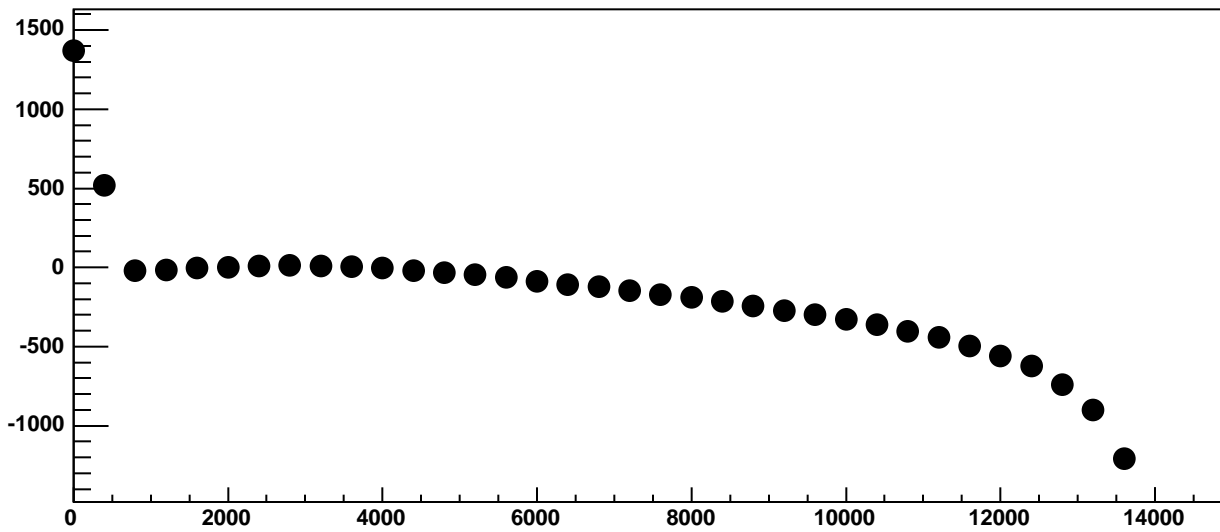
p1

2.172 ± 0.00105

Chip 2, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC

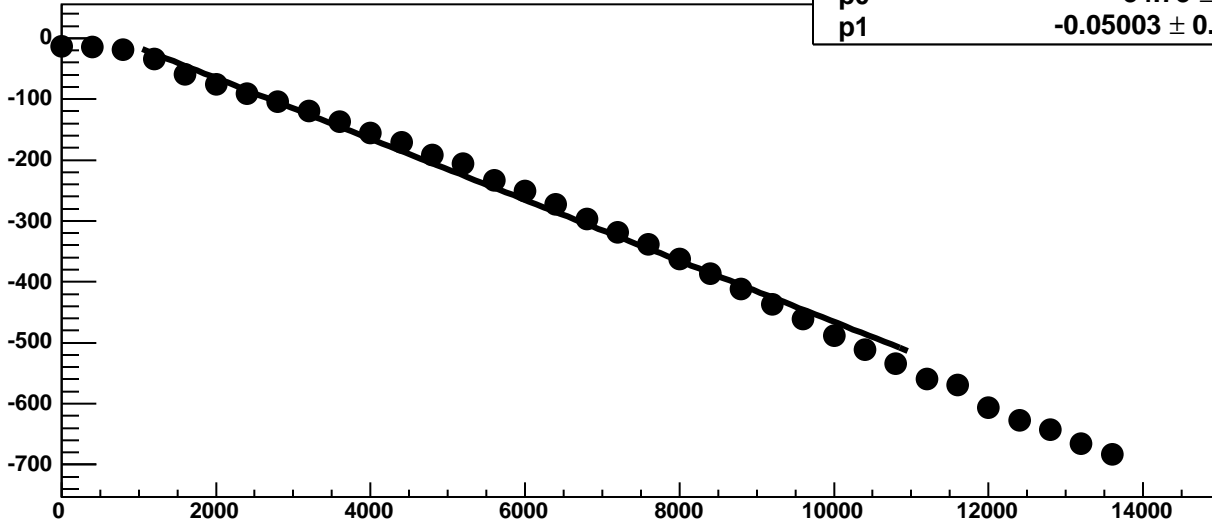


Chip 2, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC

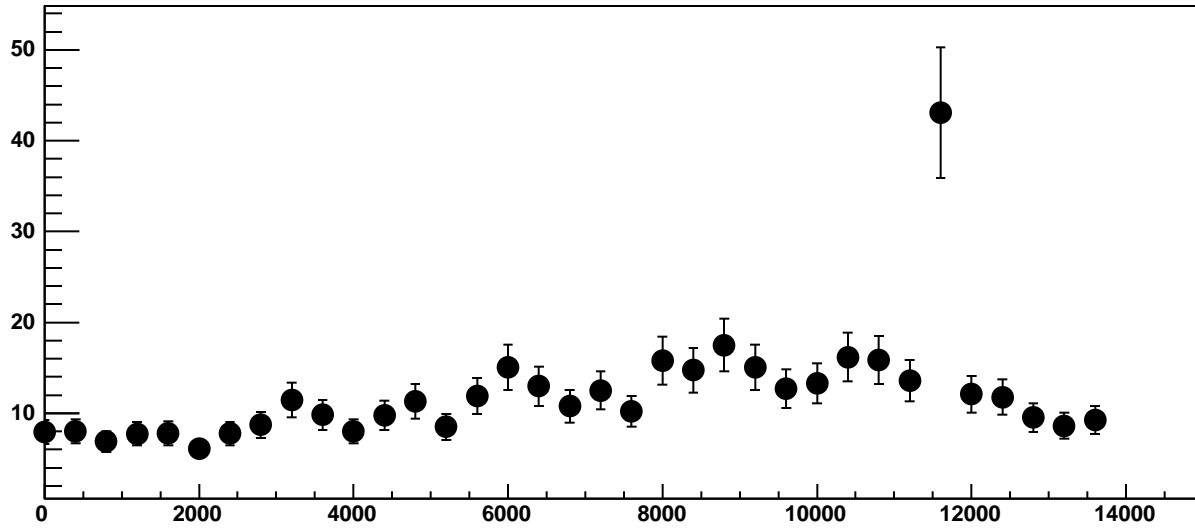


Chip 2, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

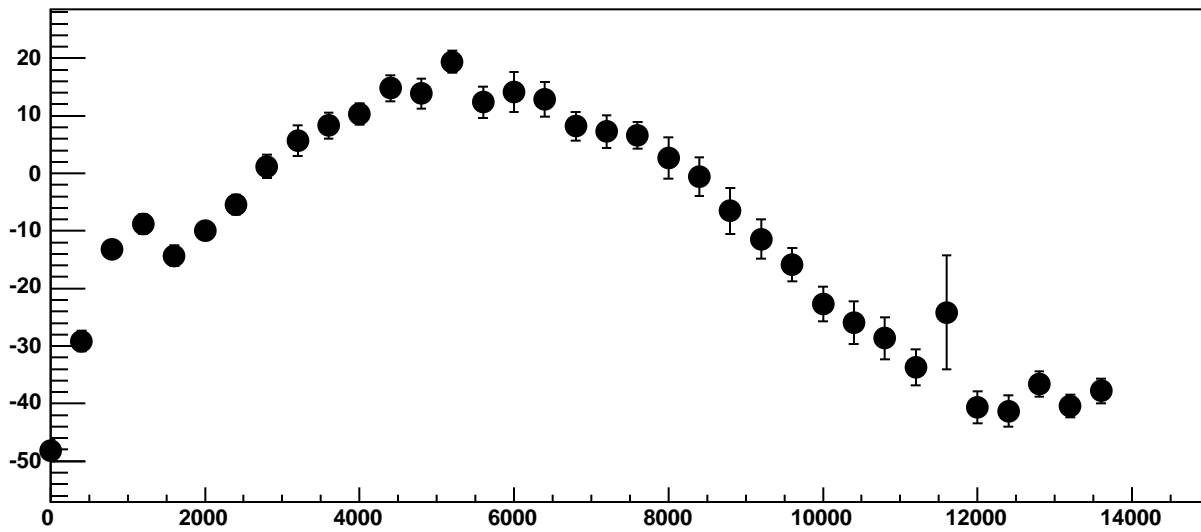
χ^2 / ndf 661.8 / 23
p0 34.73 ± 0.9282
p1 -0.05003 ± 0.000173



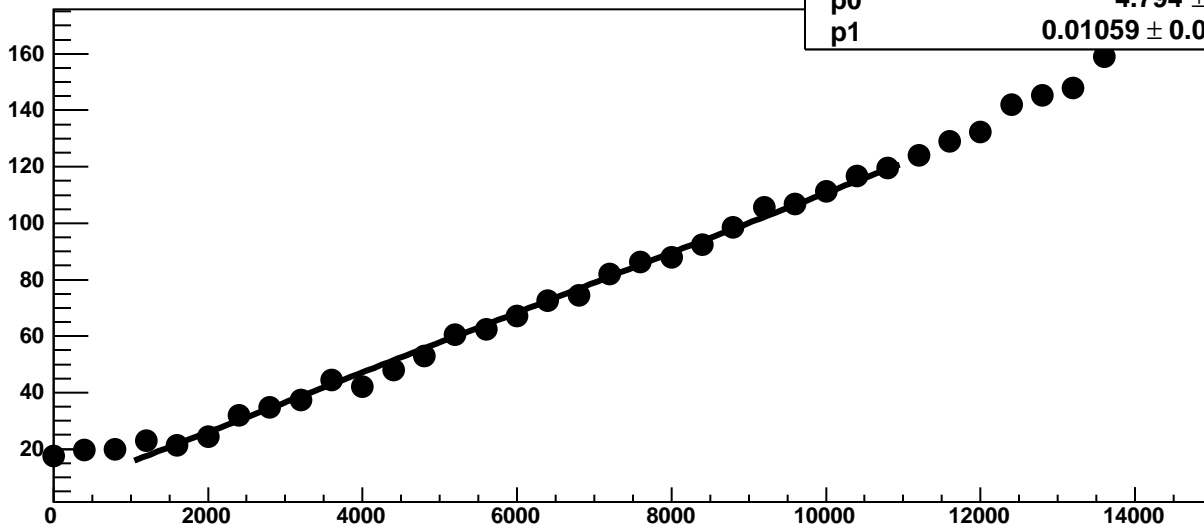
Chip 2, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



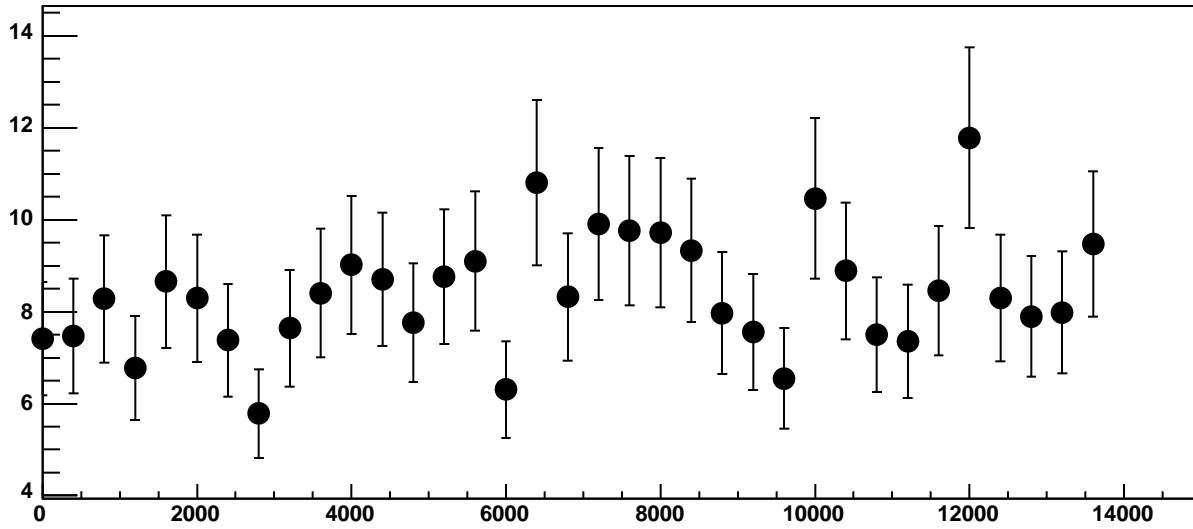
Chip 2, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



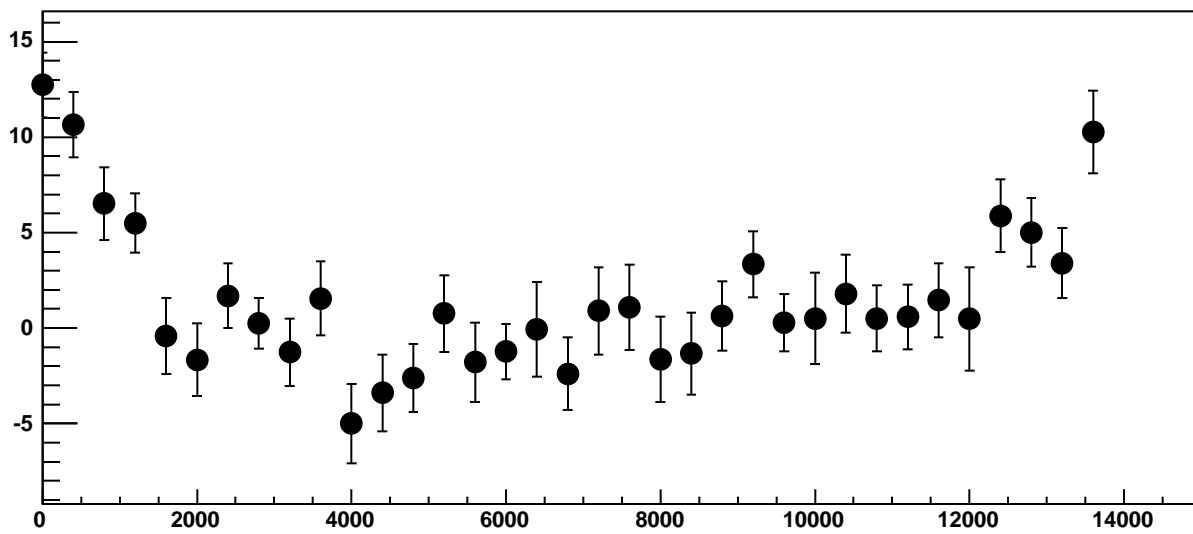
Chip 2, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



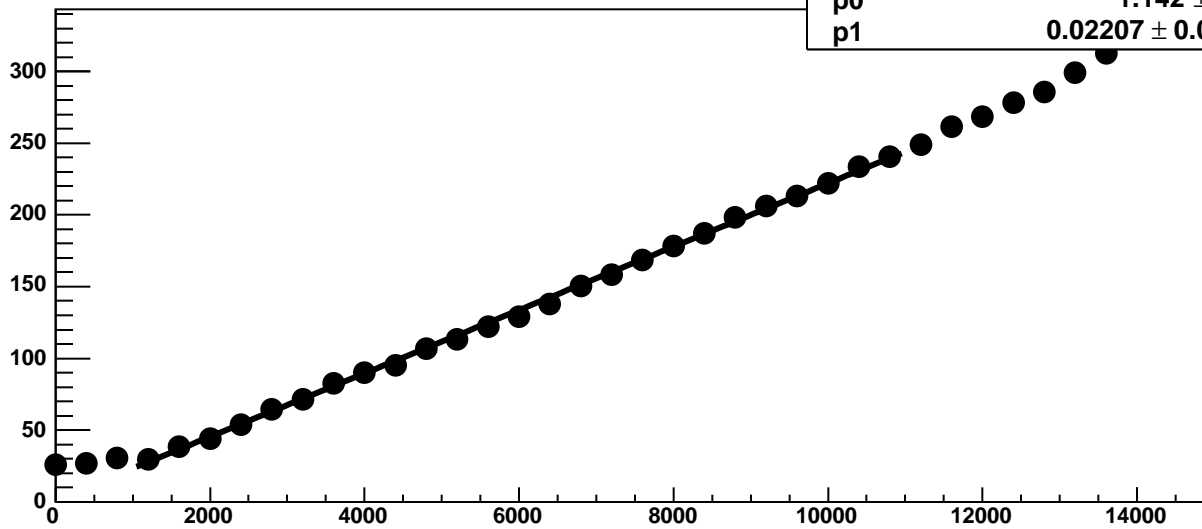
Chip 2, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

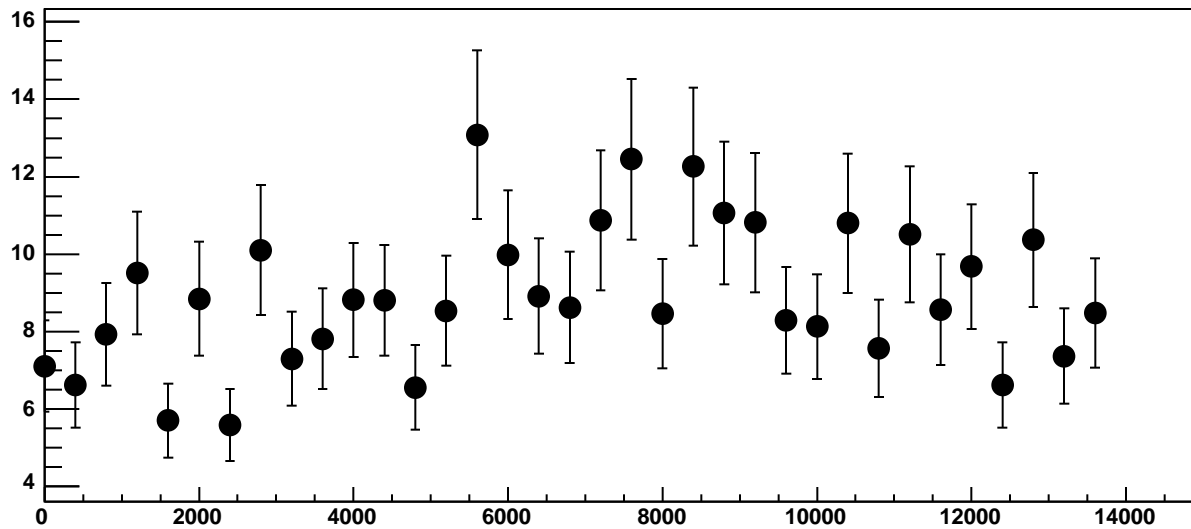


Chip 2, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

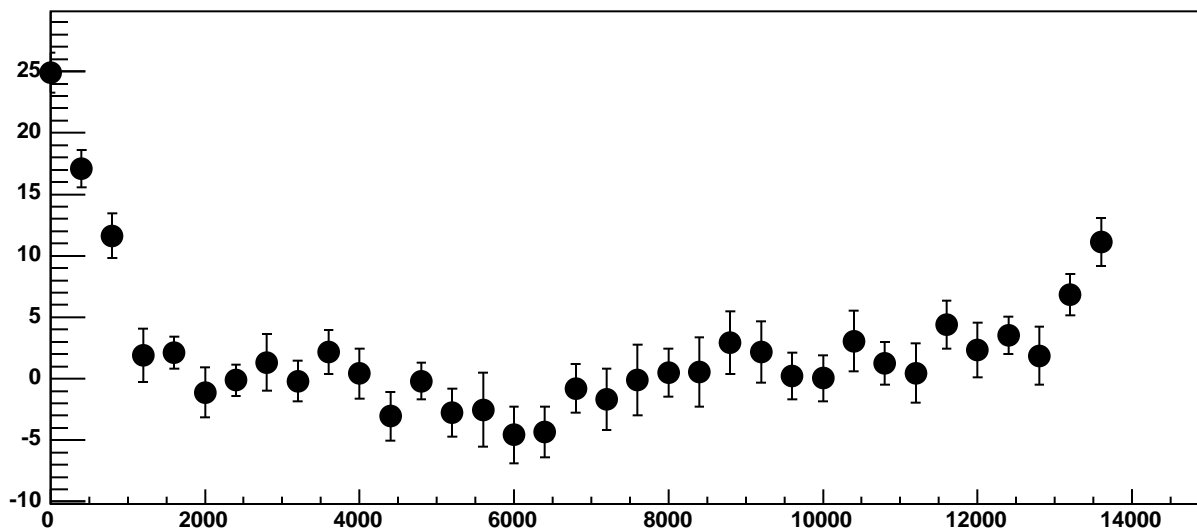


χ^2 / ndf 24.12 / 23
p0 1.142 ± 0.8056
p1 0.02207 ± 0.0001301

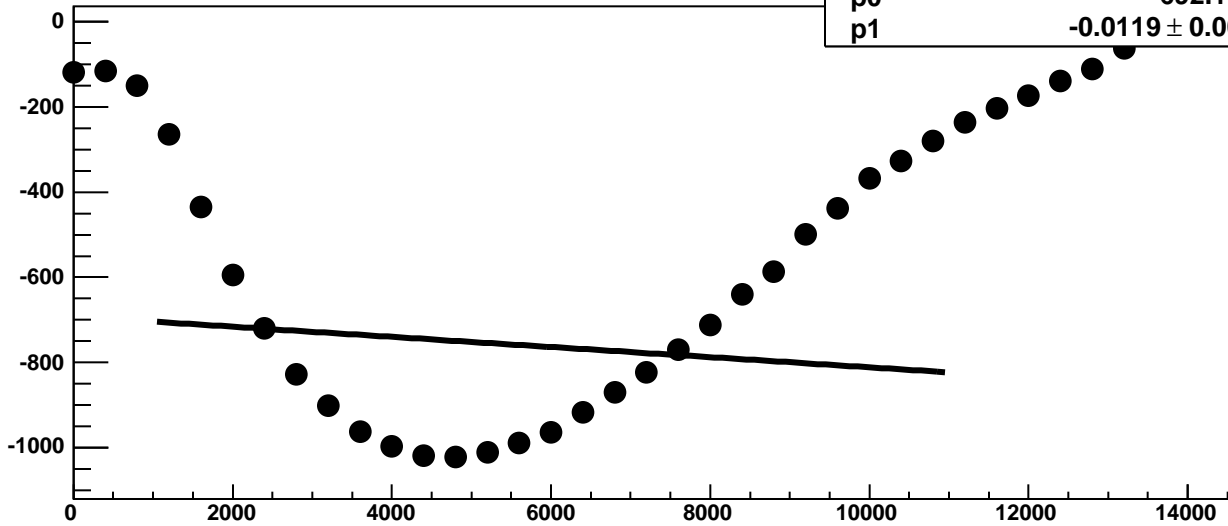
Chip 2, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC

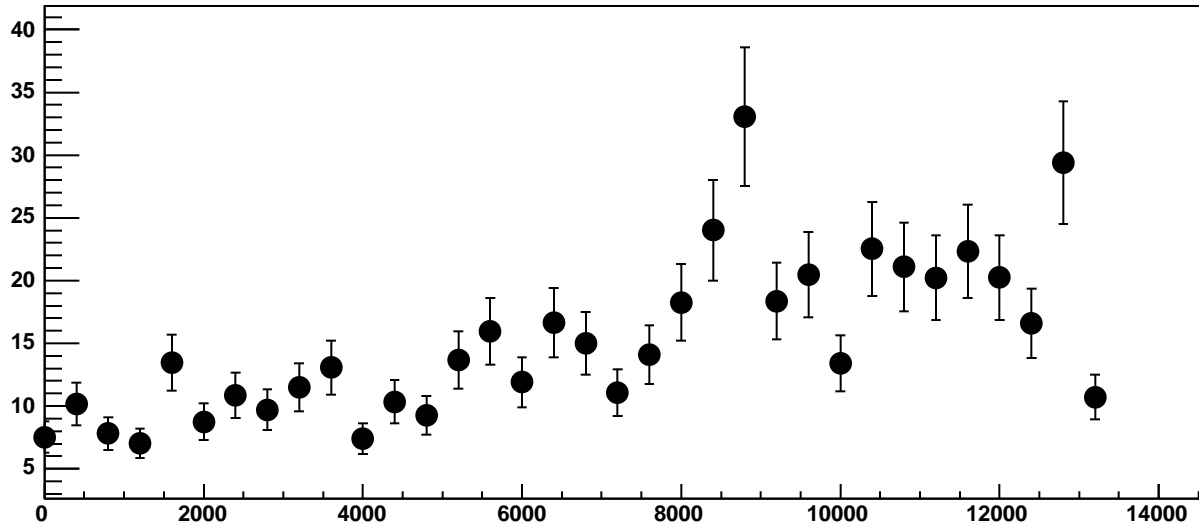


Chip 2, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

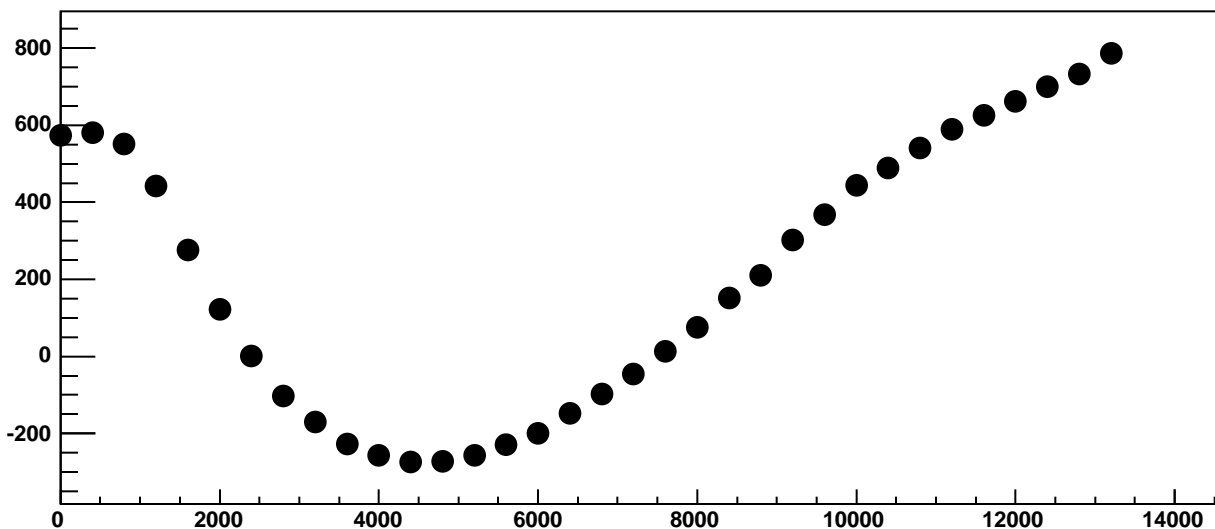


χ^2 / ndf 2.255e+05 / 23
p0 -692.1 ± 1.121
p1 -0.0119 ± 0.0002148

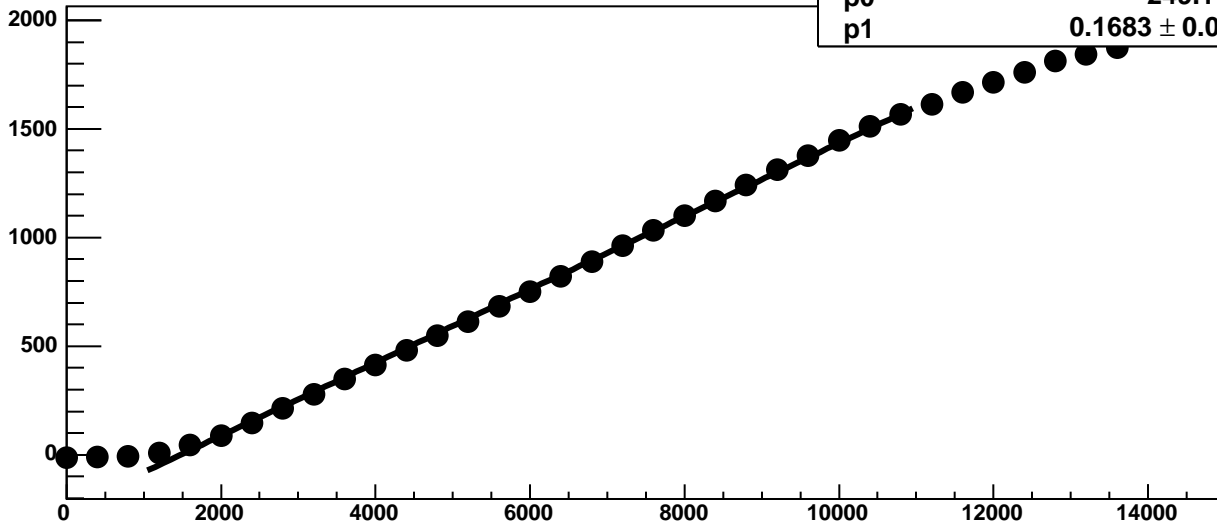
Chip 2, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



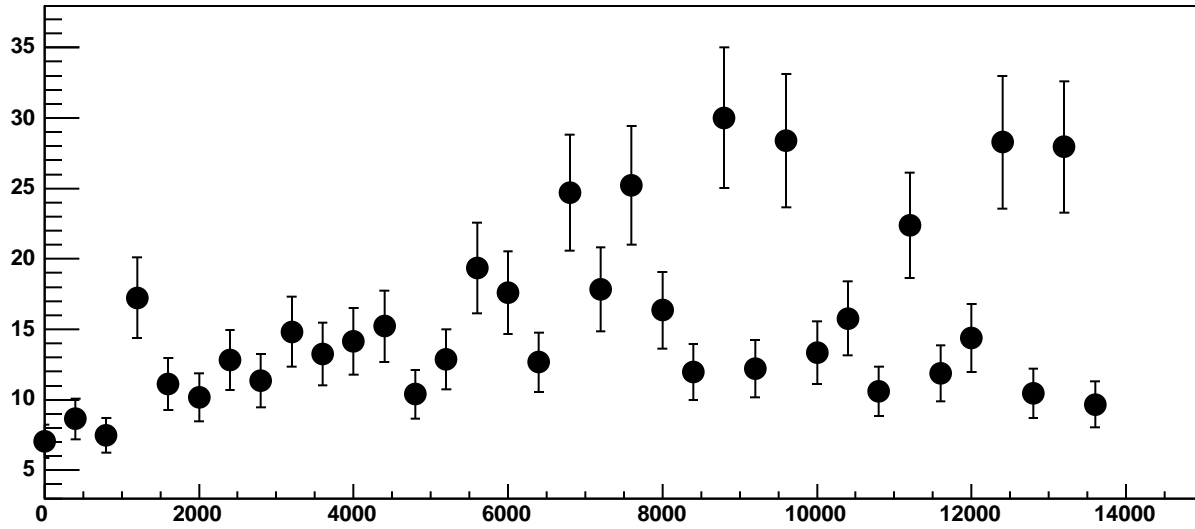
Chip 2, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



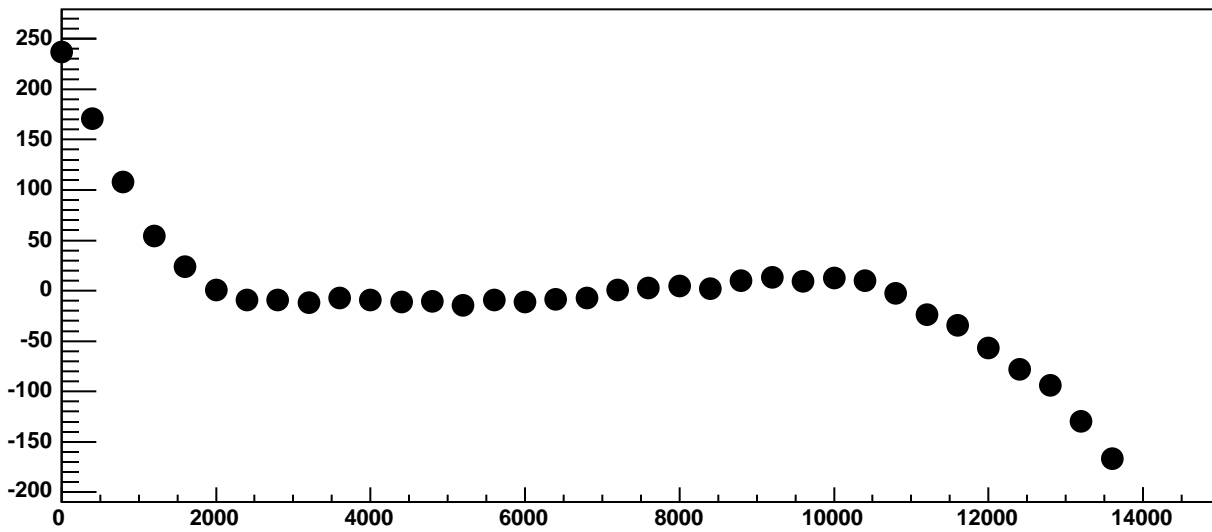
Chip 2, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



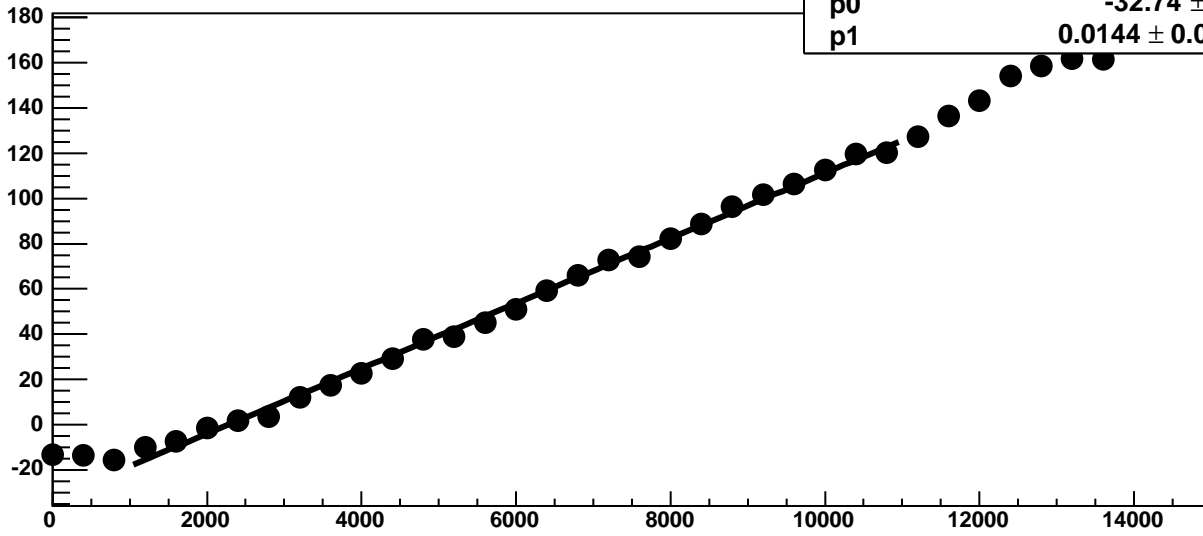
Chip 2, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



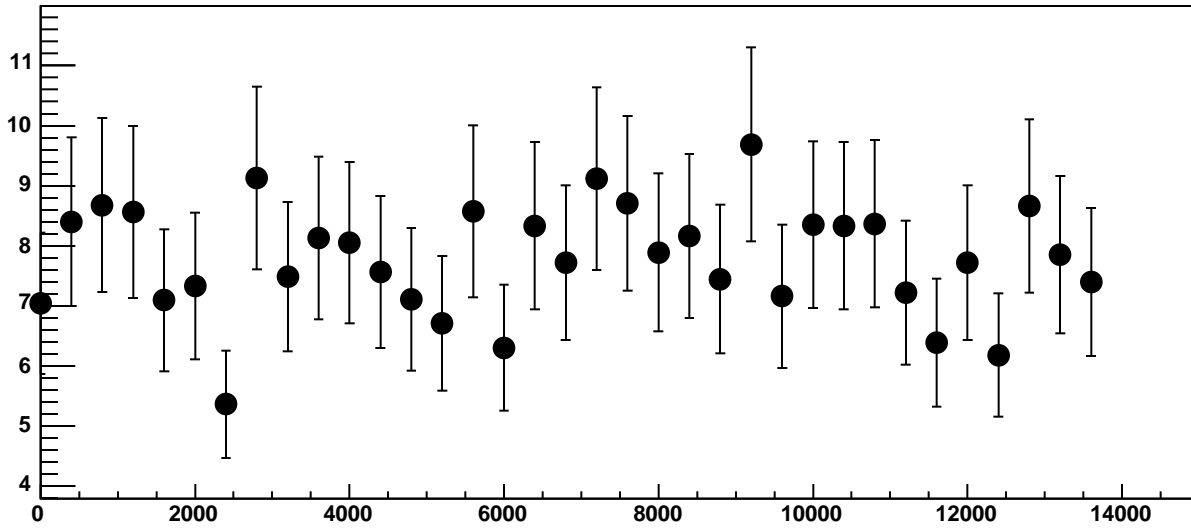
Chip 2, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



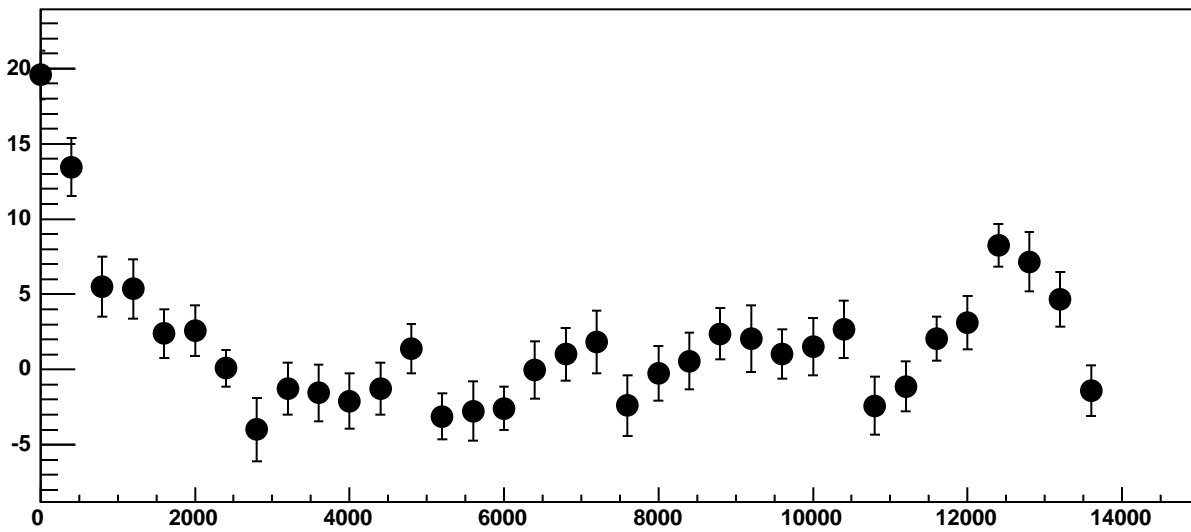
Chip 2, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



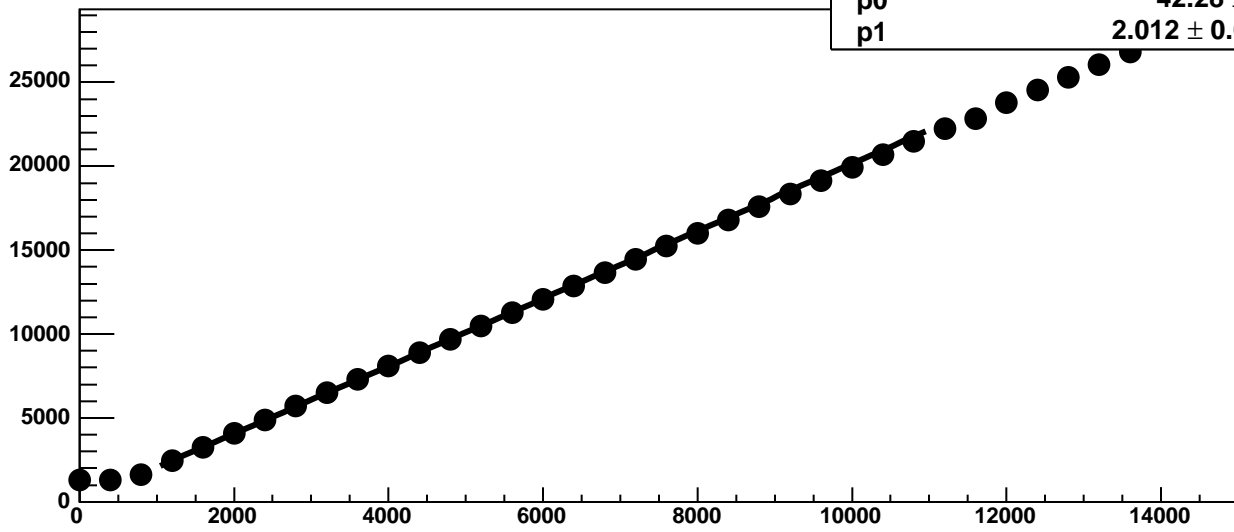
Chip 2, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



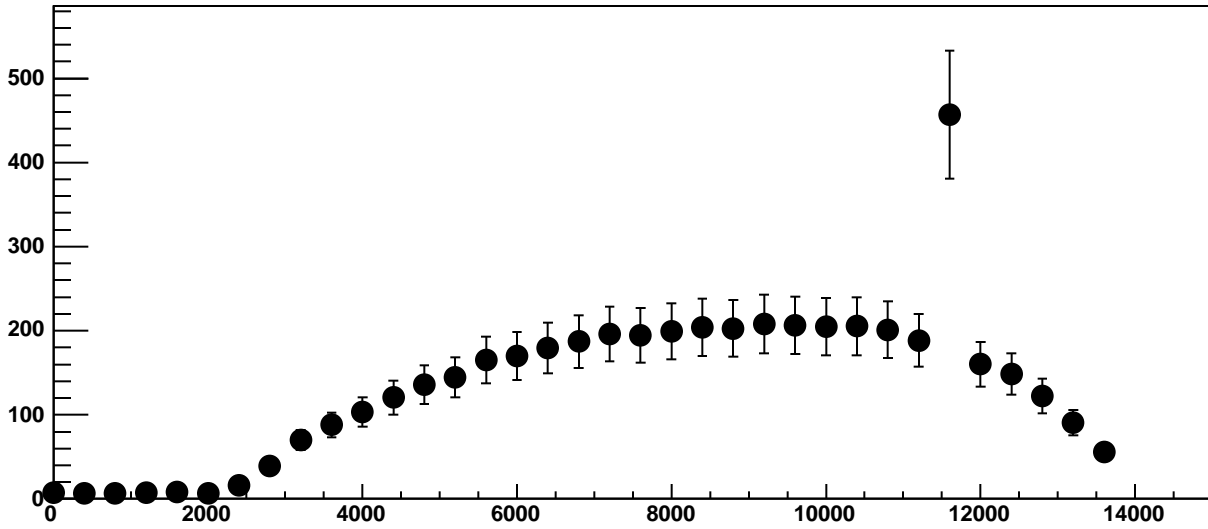
Chip 2, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



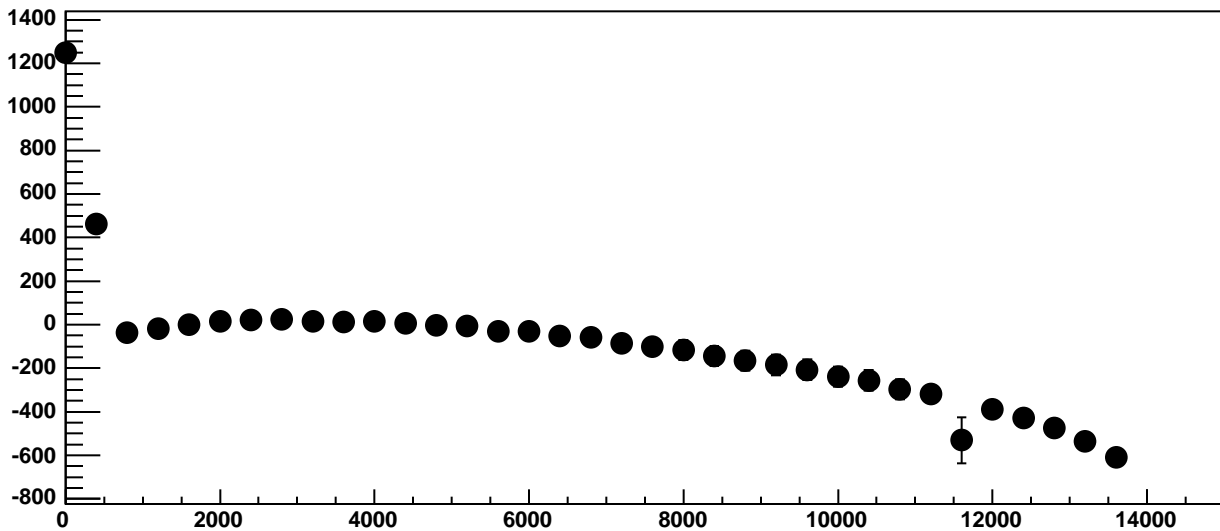
Chip 2, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC



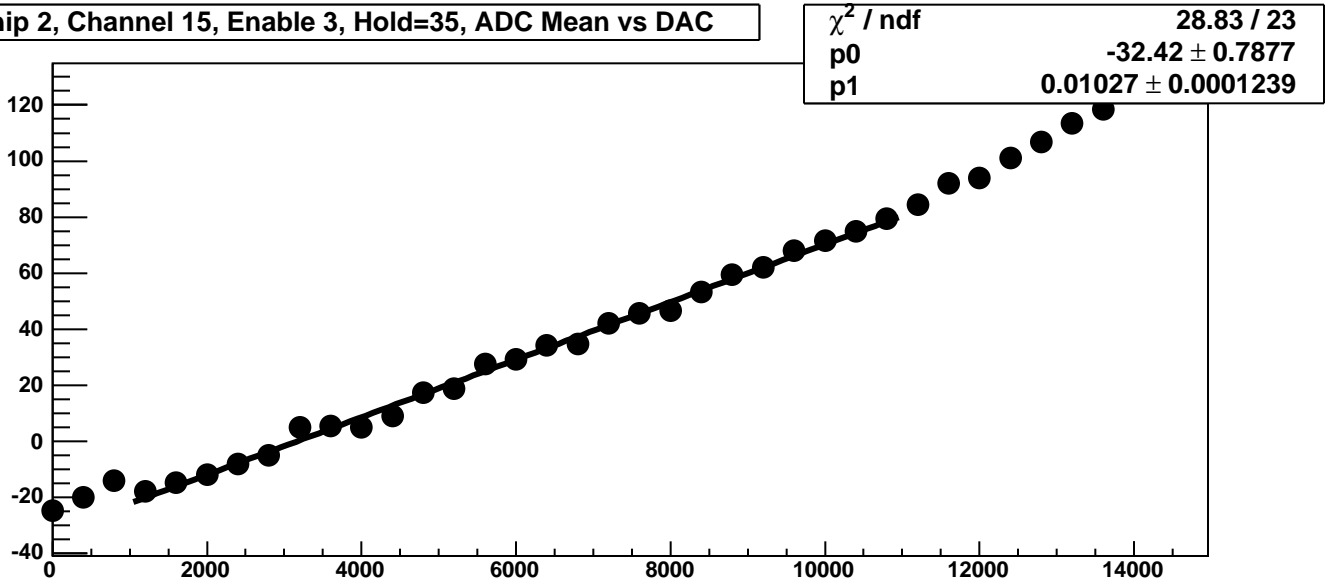
Chip 2, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



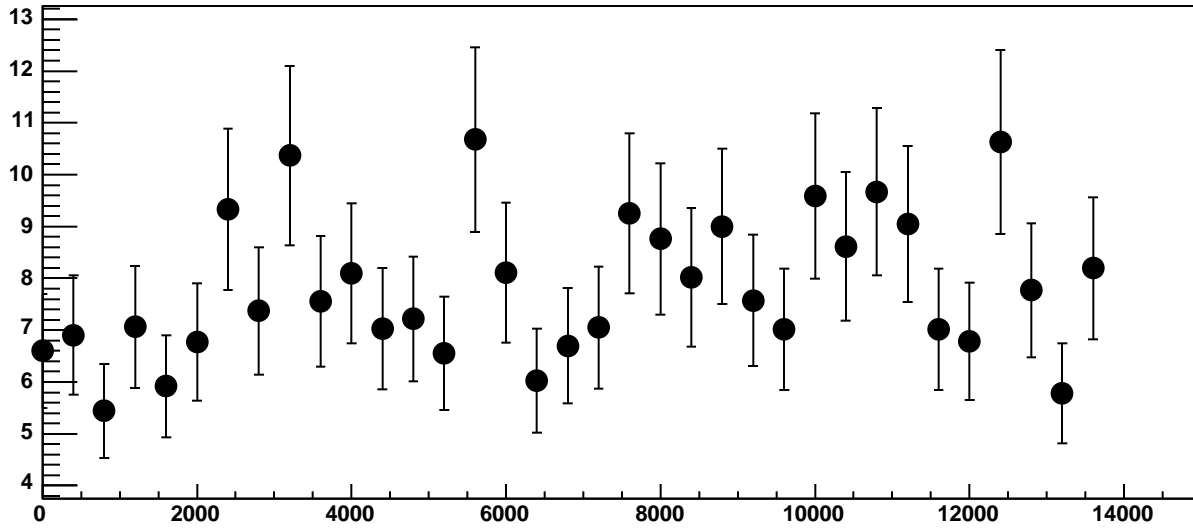
Chip 2, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC



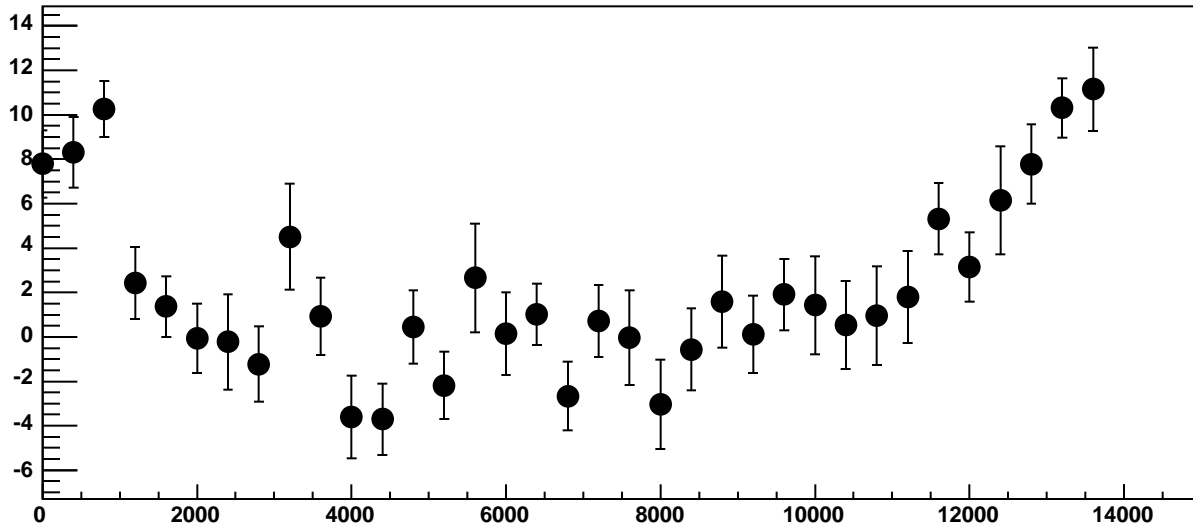
Chip 2, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



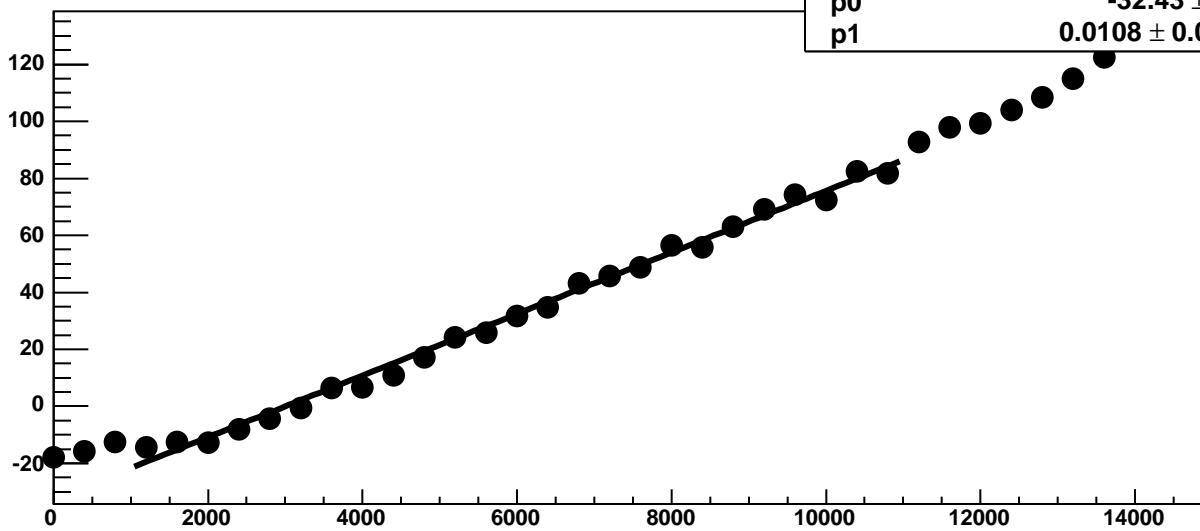
Chip 2, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



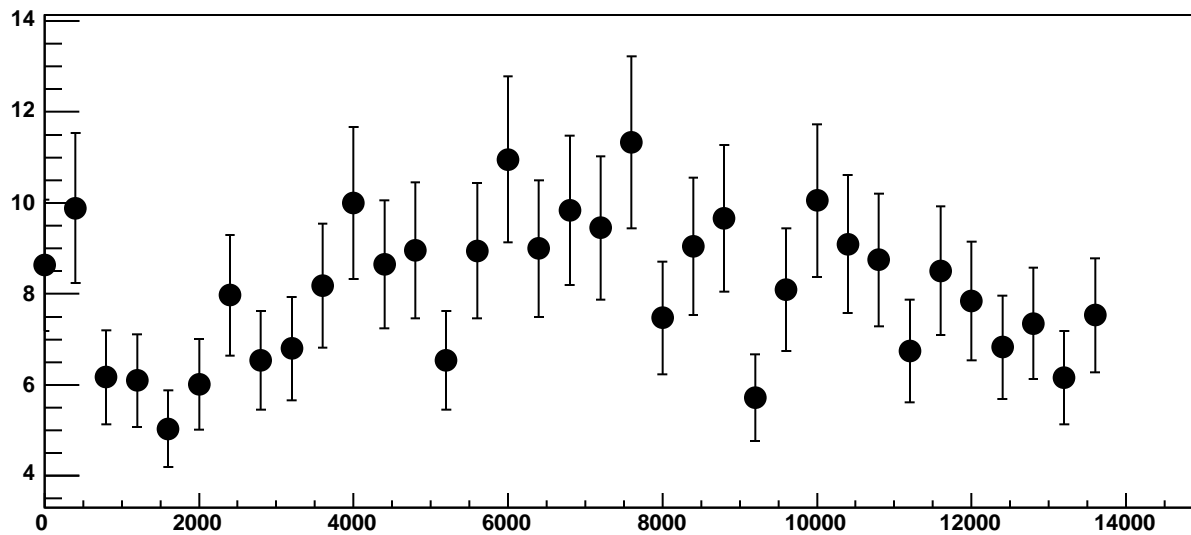
Chip 2, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



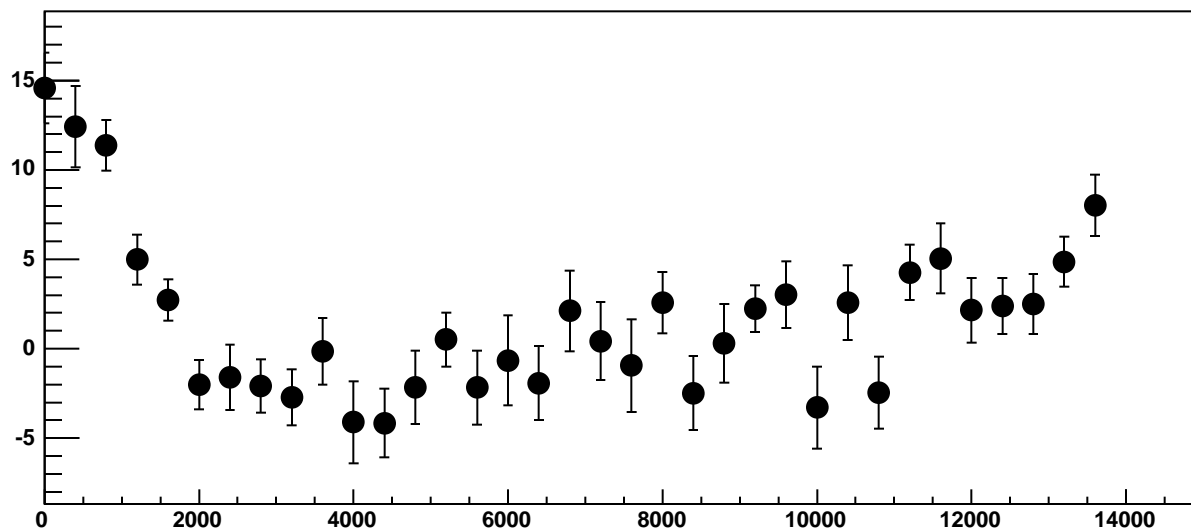
Chip 2, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



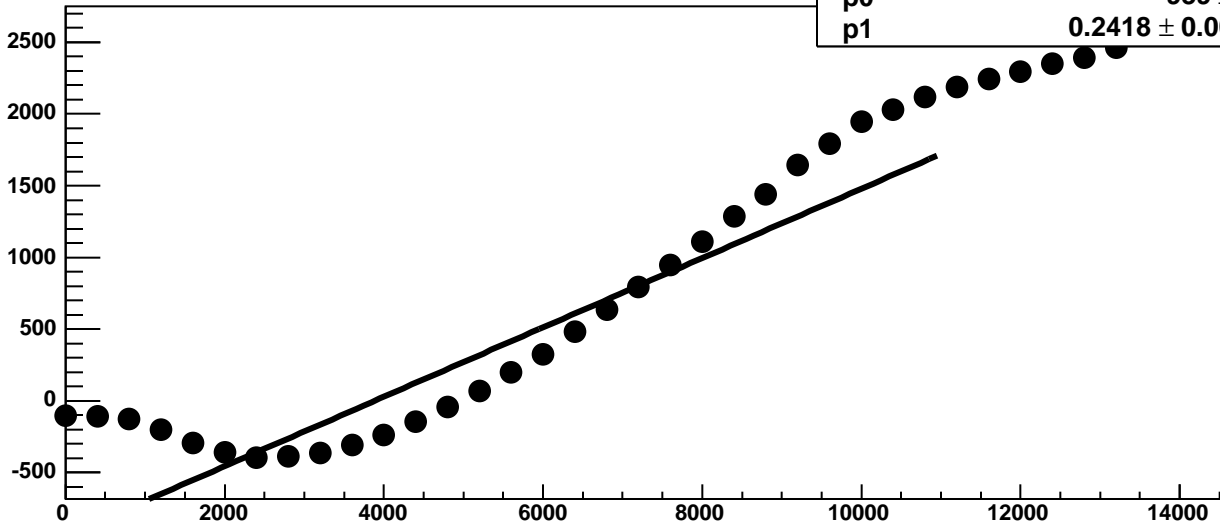
Chip 2, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

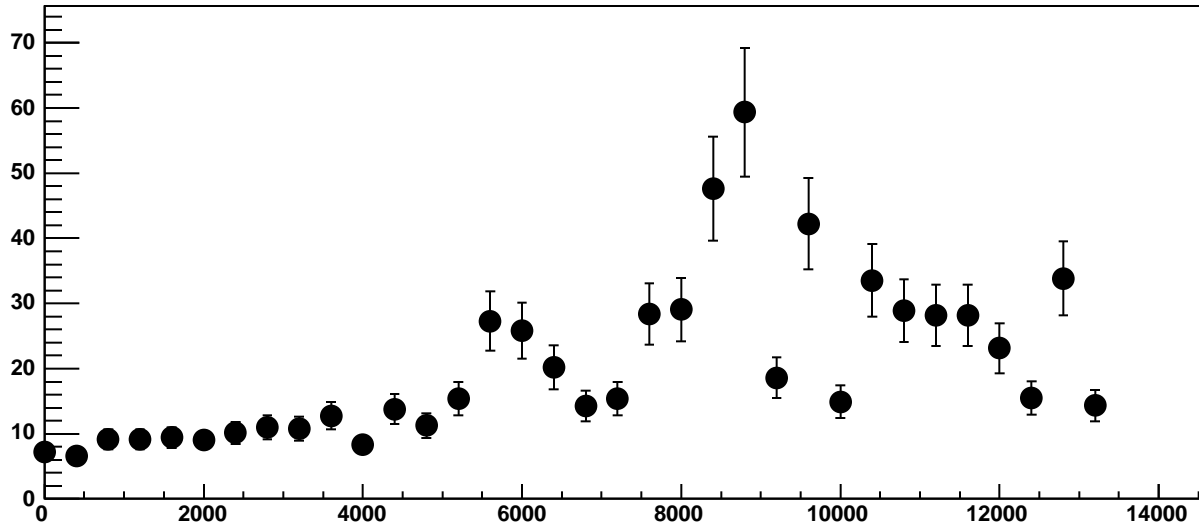


Chip 2, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC

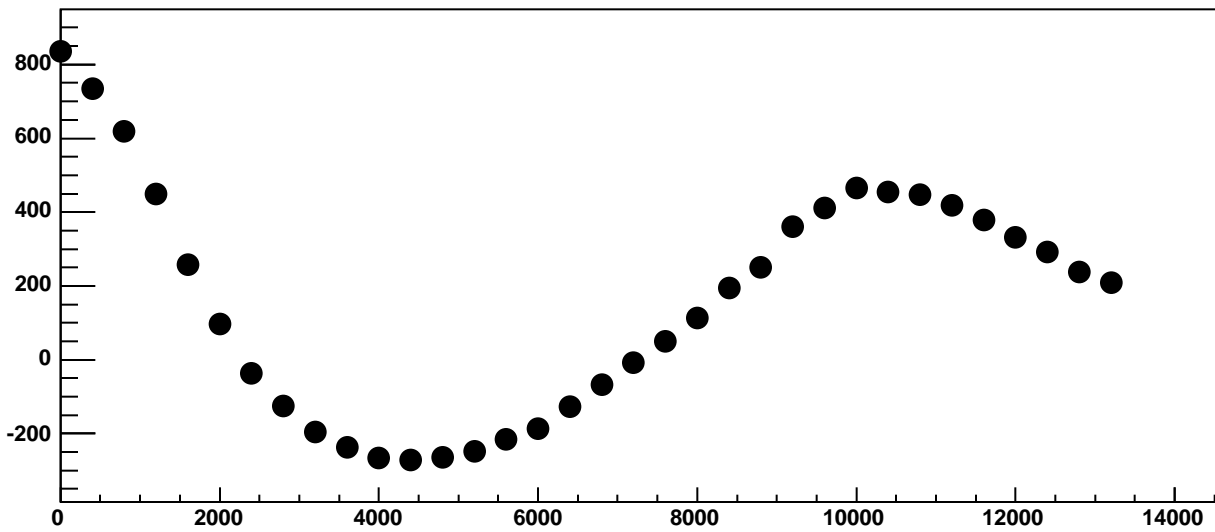


χ^2 / ndf 1.598e+05 / 23
p0 -939 ± 1.235
p1 0.2418 ± 0.0002618

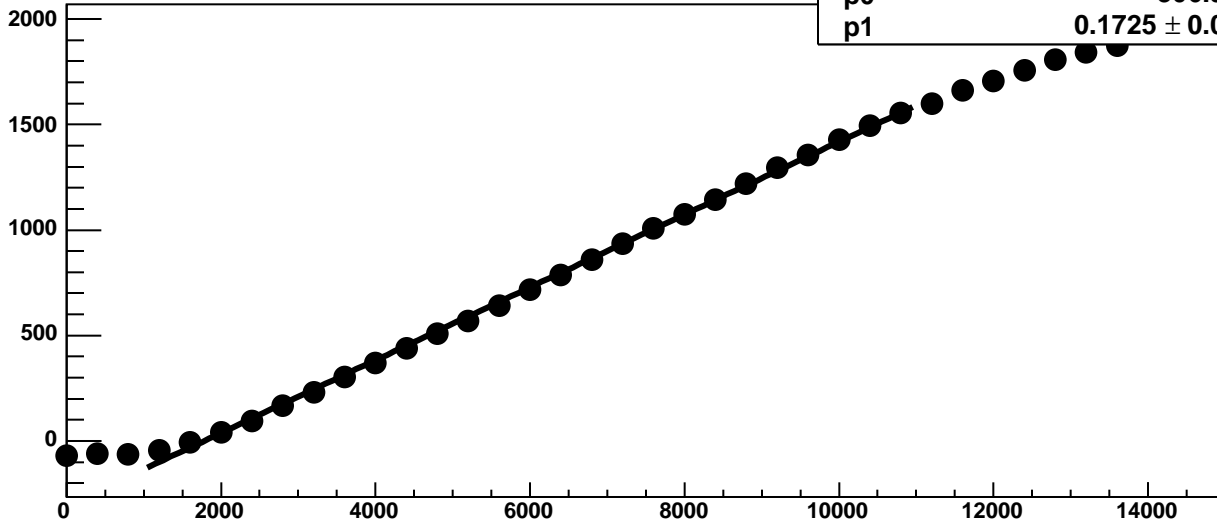
Chip 2, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



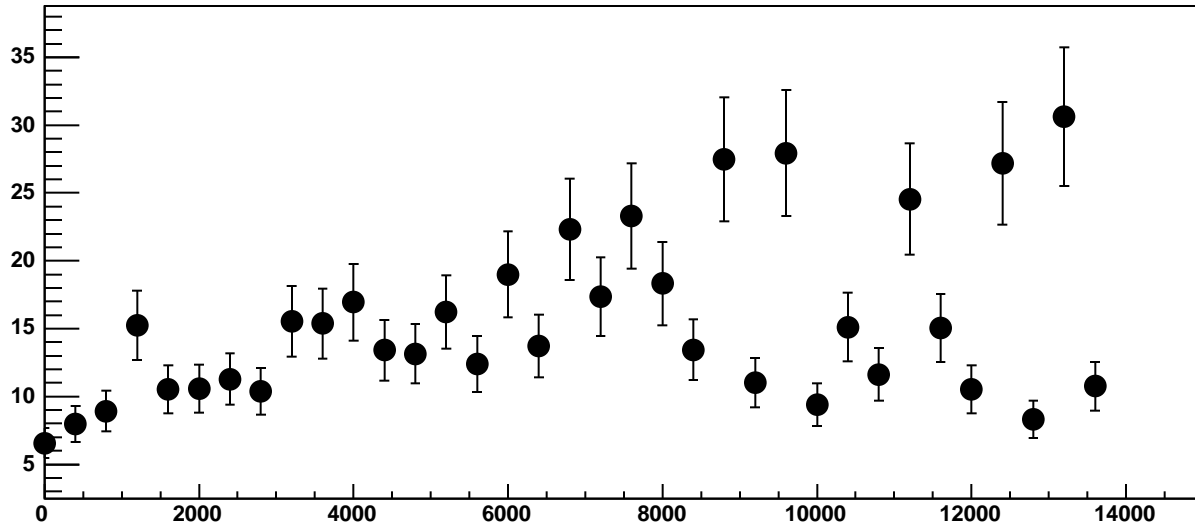
Chip 2, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC



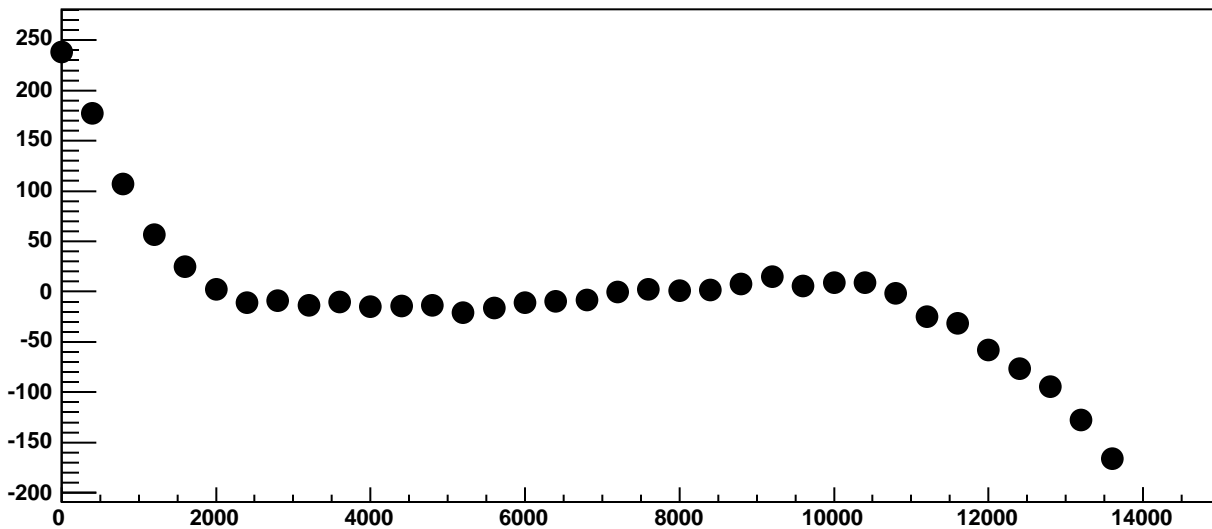
Chip 2, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



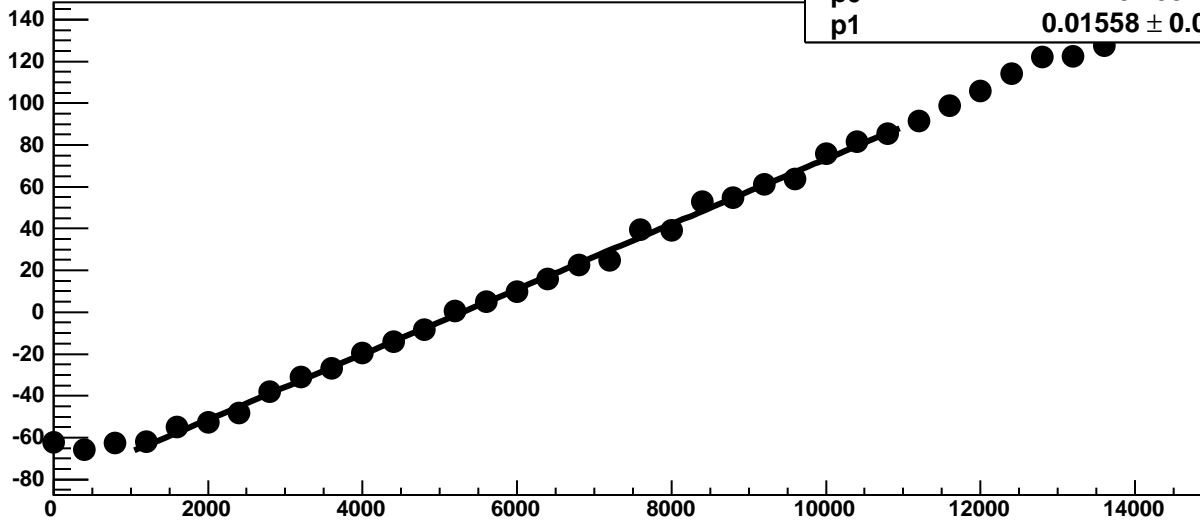
Chip 2, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



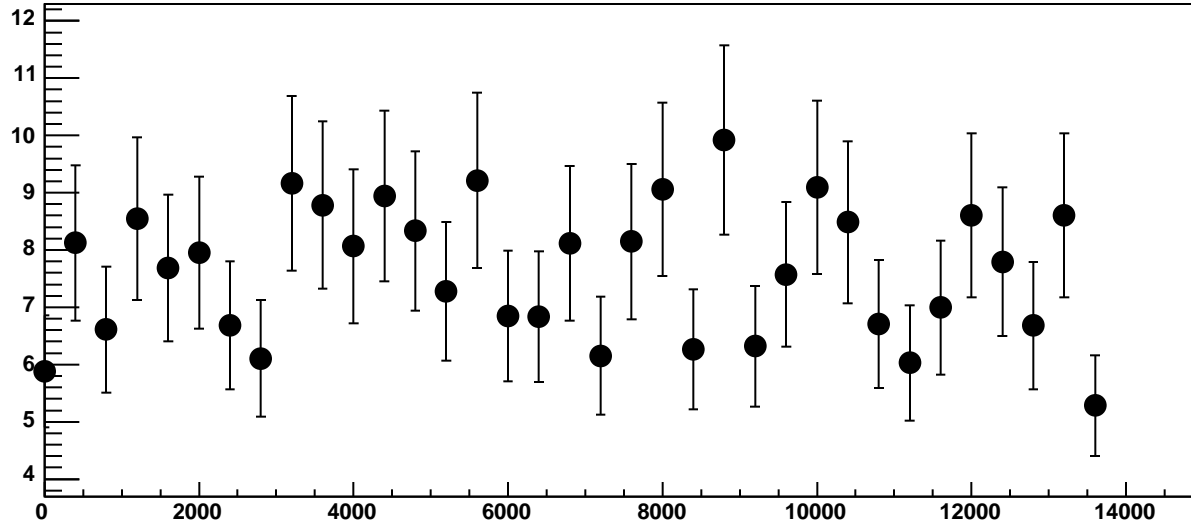
Chip 2, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



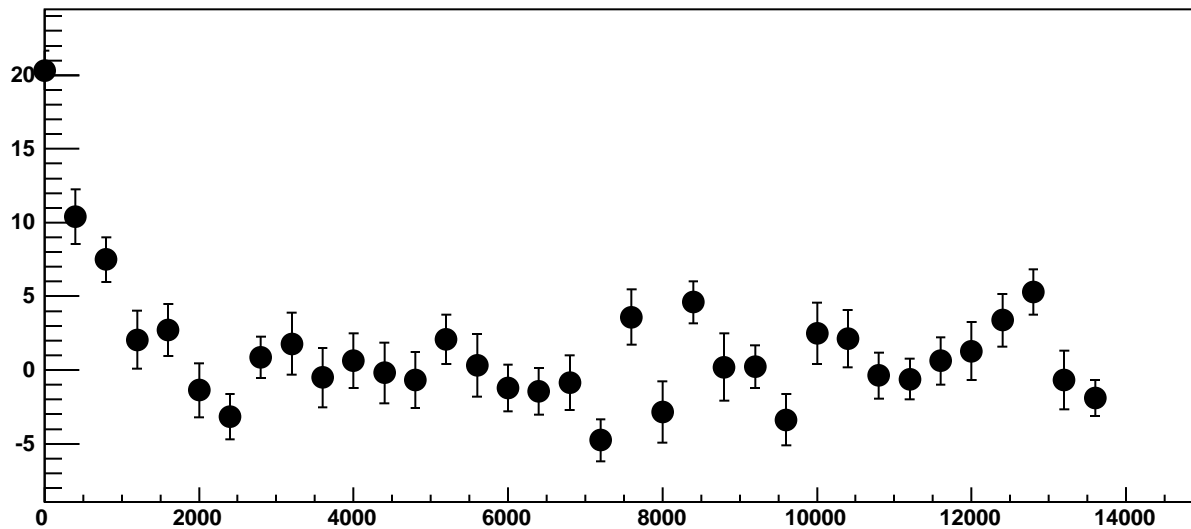
Chip 2, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



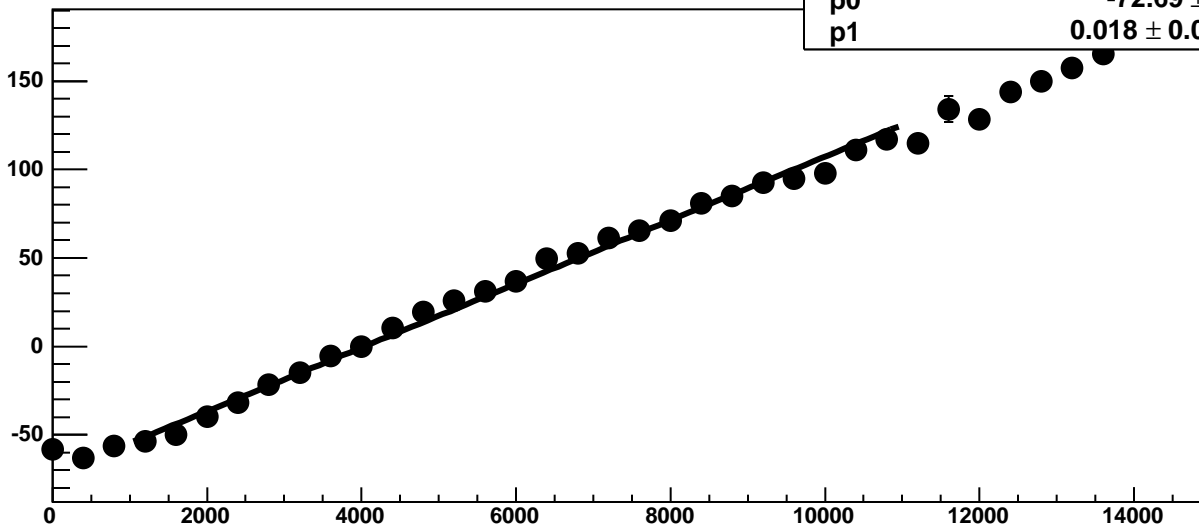
Chip 2, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



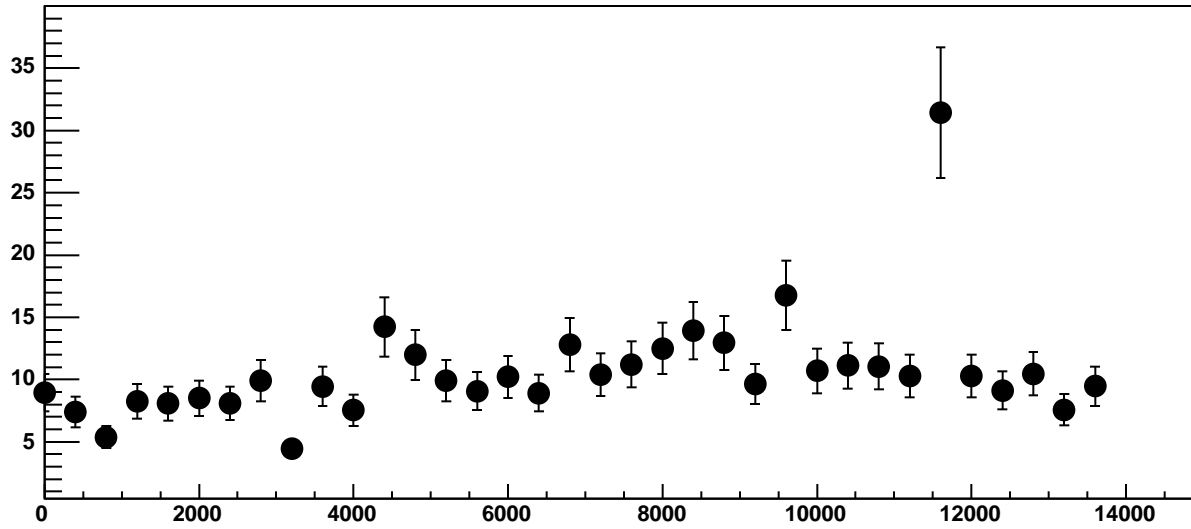
Chip 2, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



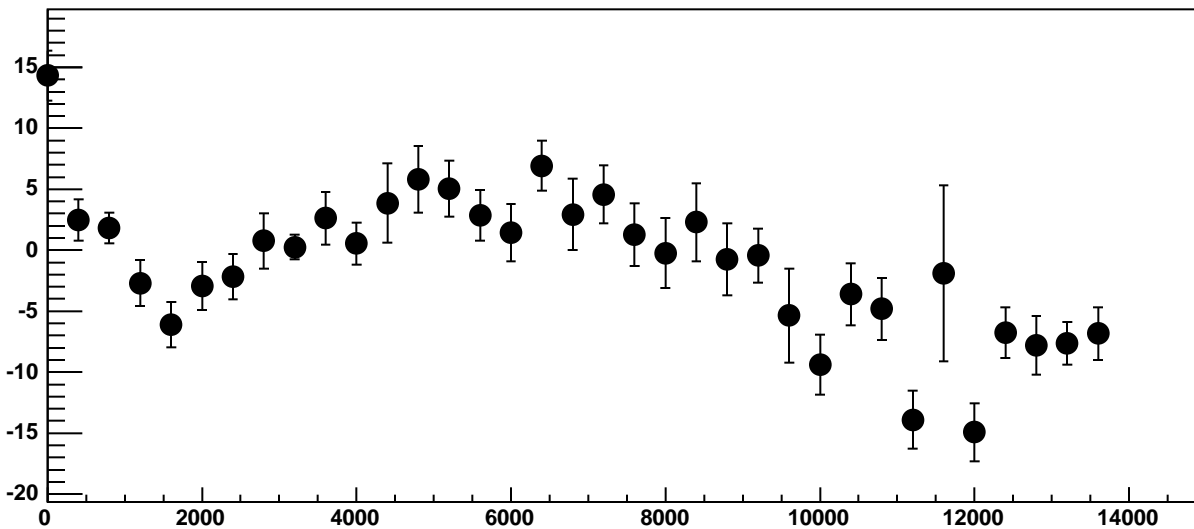
Chip 2, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC



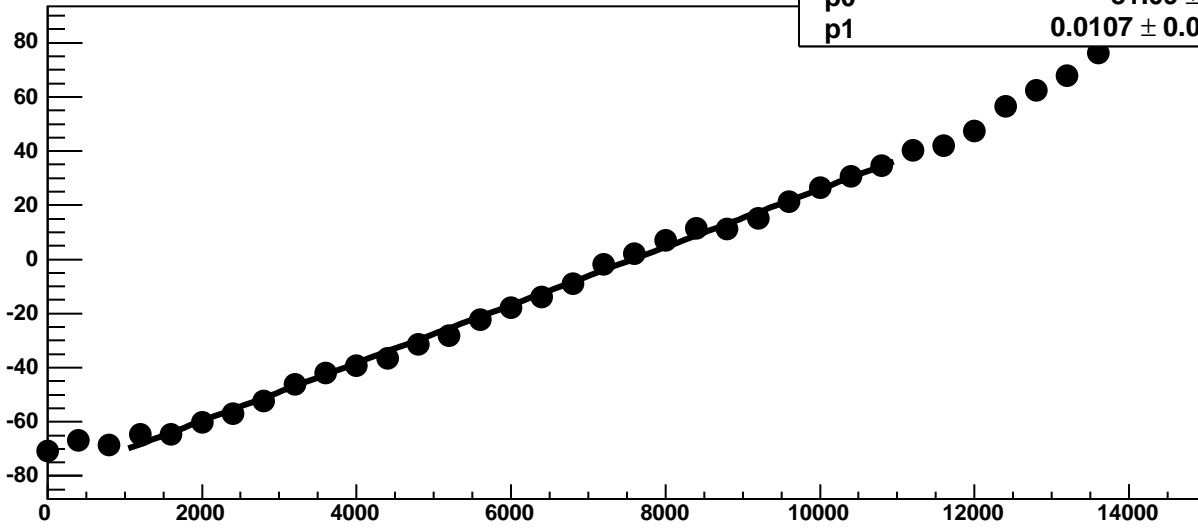
Chip 2, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

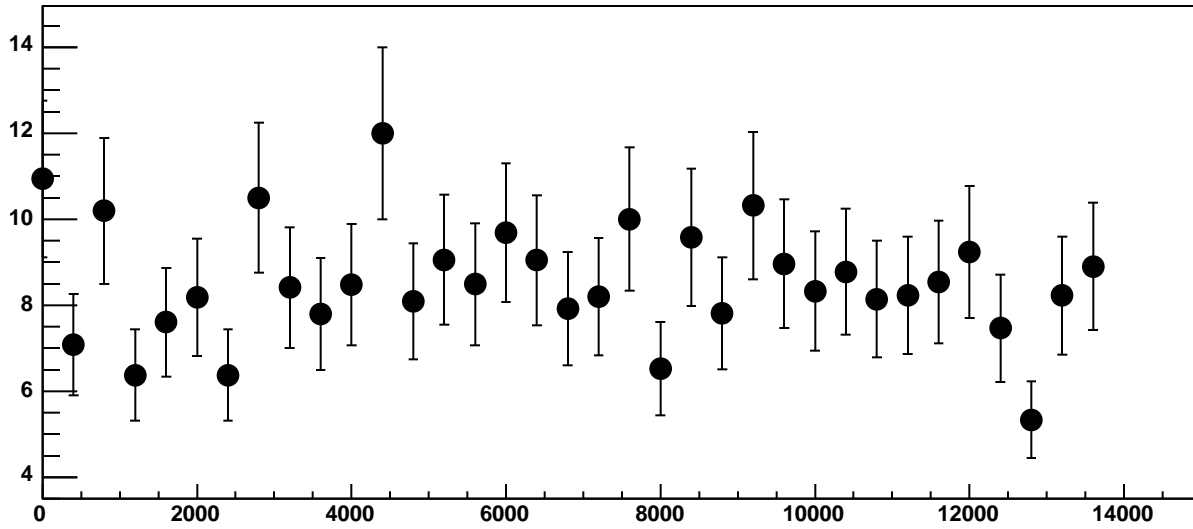


Chip 2, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

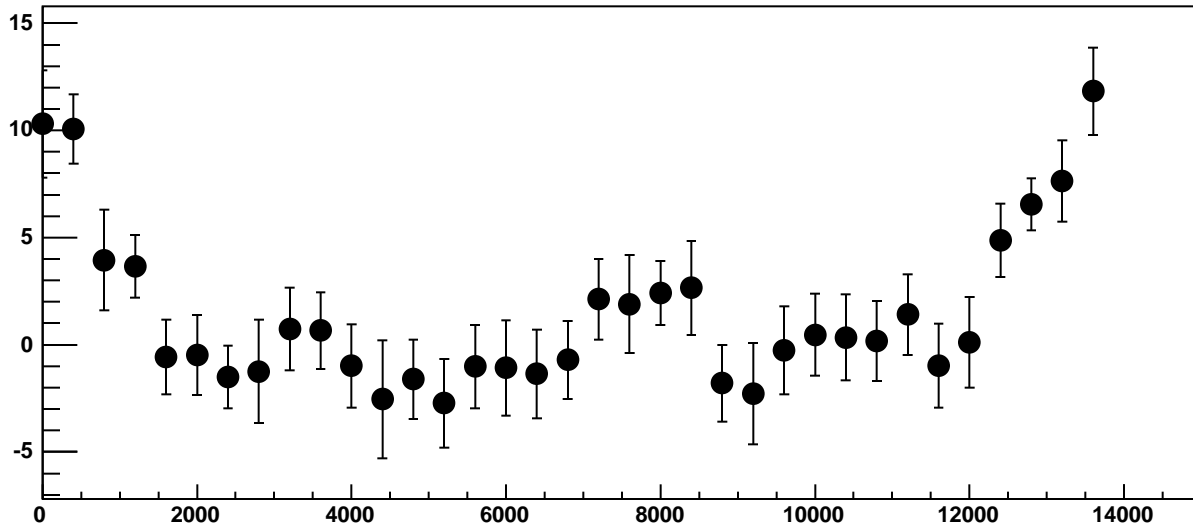


χ^2 / ndf 20.76 / 23
p0 -81.09 ± 0.8228
p1 0.0107 ± 0.0001268

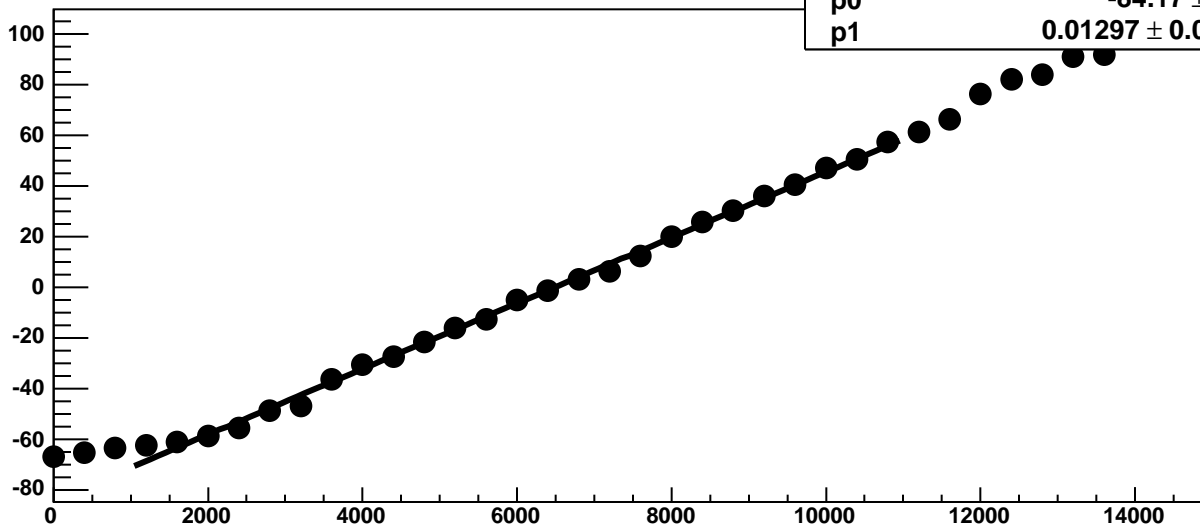
Chip 2, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



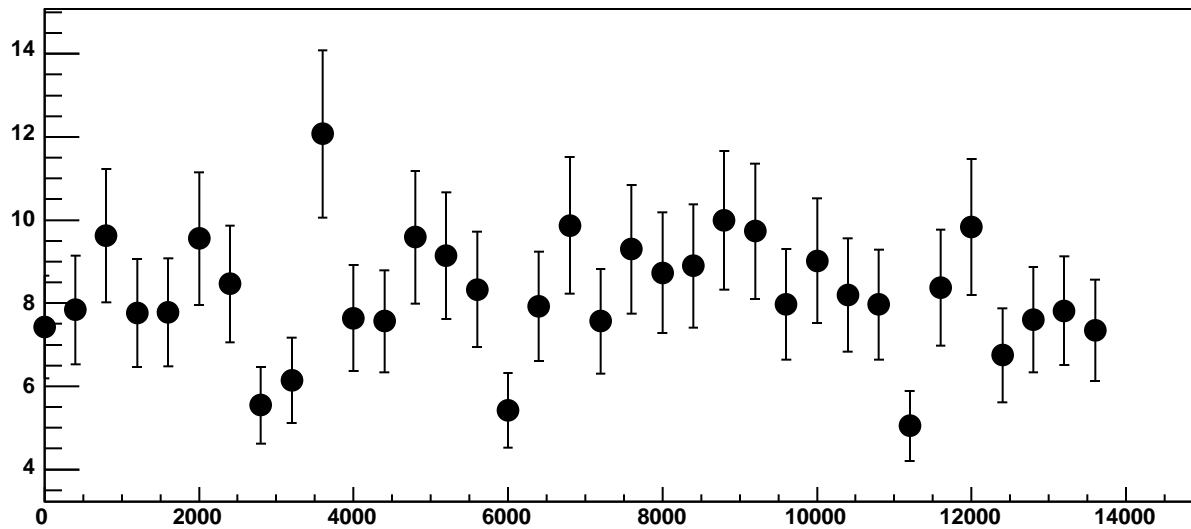
Chip 2, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



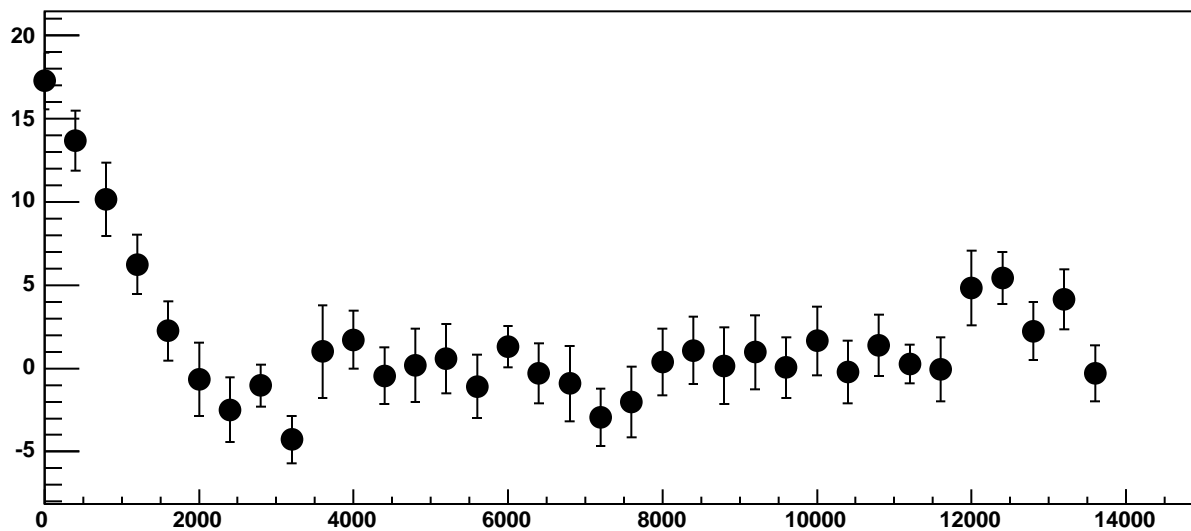
Chip 2, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



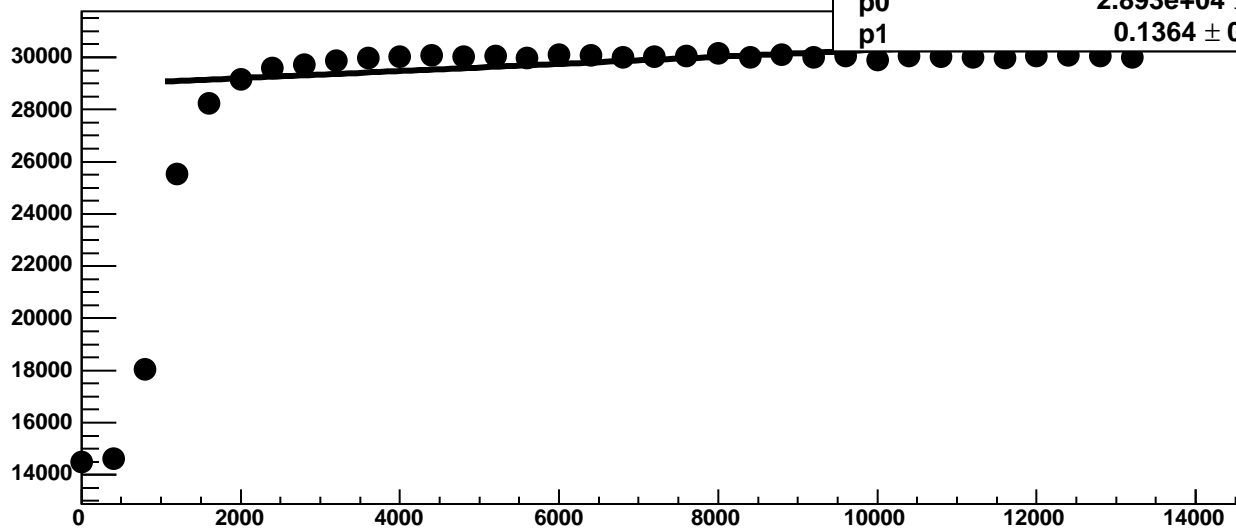
Chip 2, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



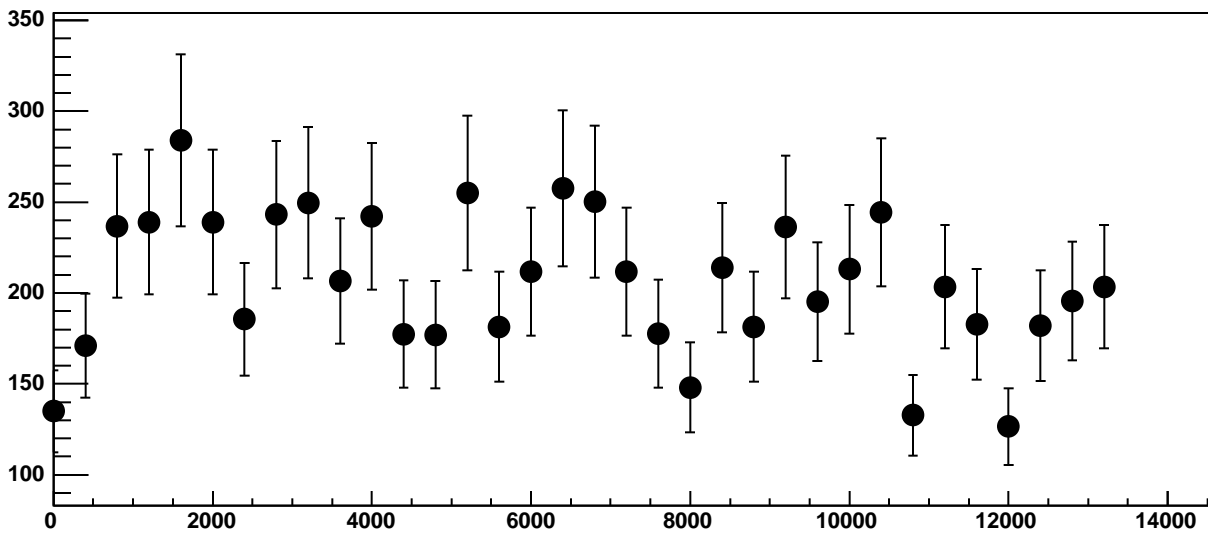
Chip 2, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



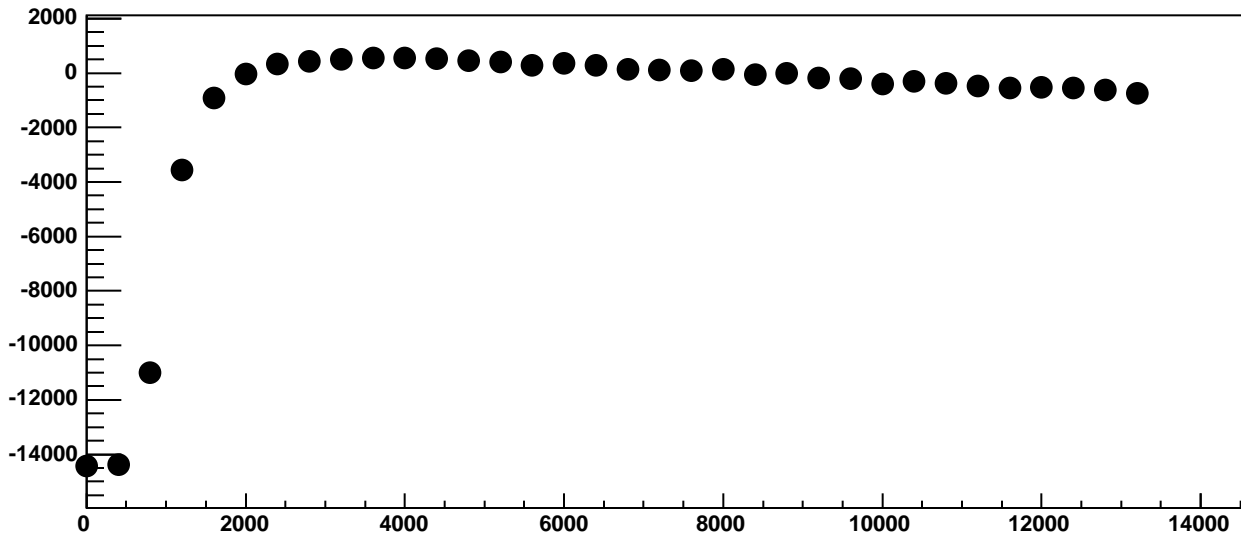
Chip 2, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC



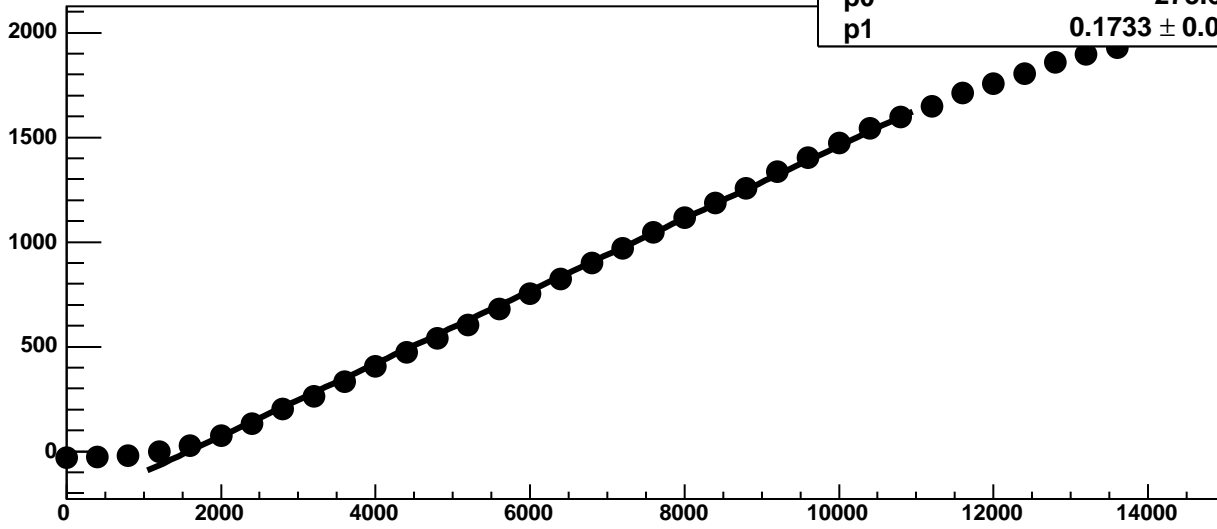
Chip 2, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC

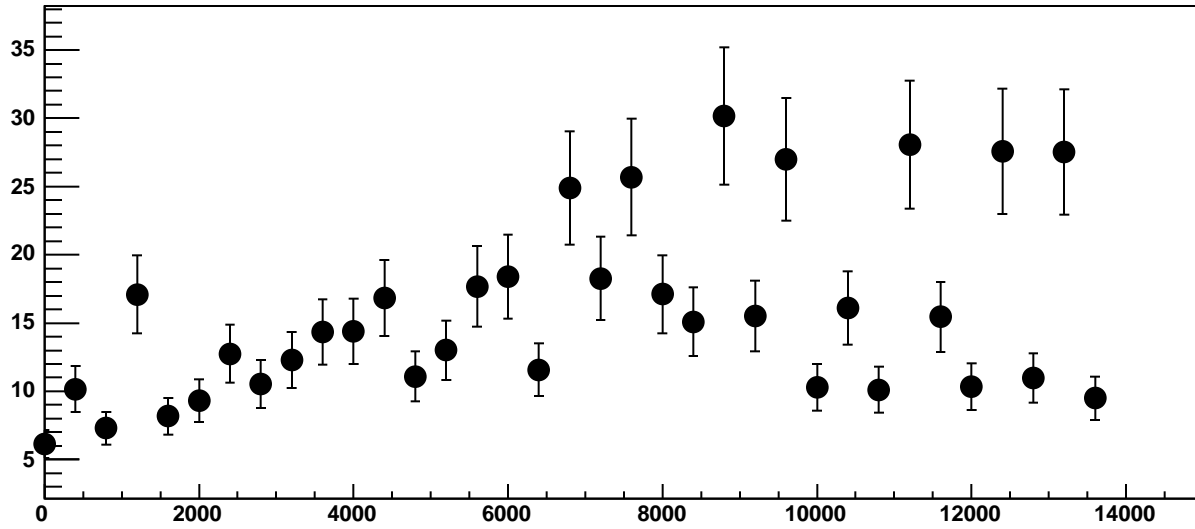


Chip 2, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

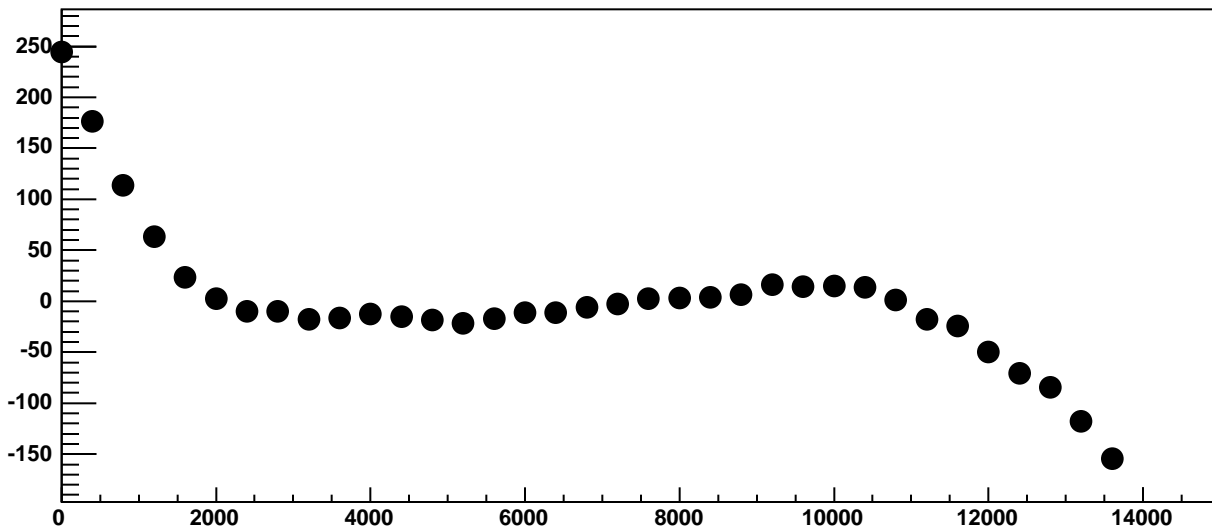


χ^2 / ndf 780.4 / 23
p0 -273.6 ± 1.22
p1 0.1733 ± 0.0001967

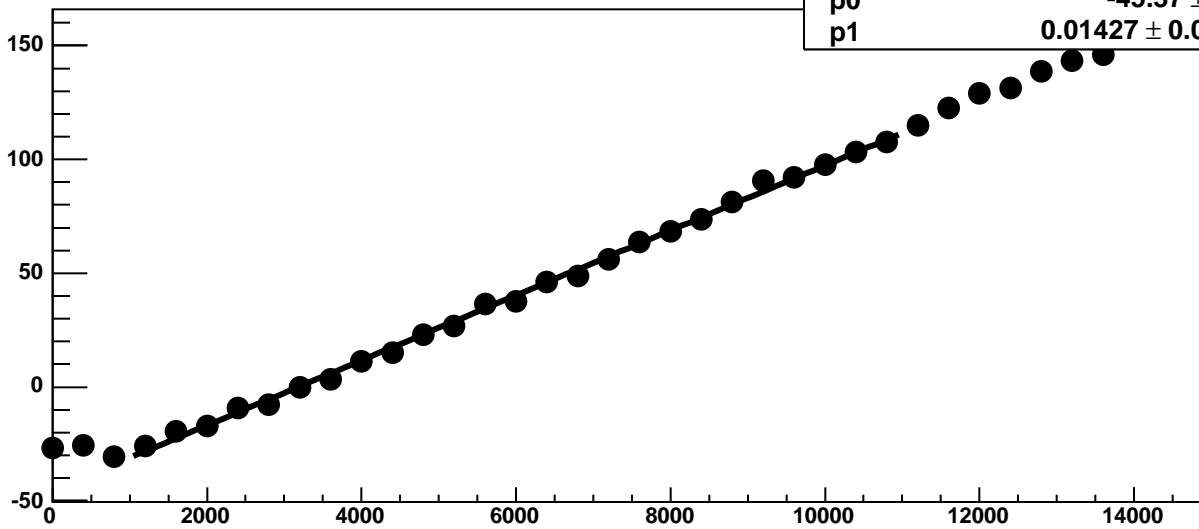
Chip 2, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

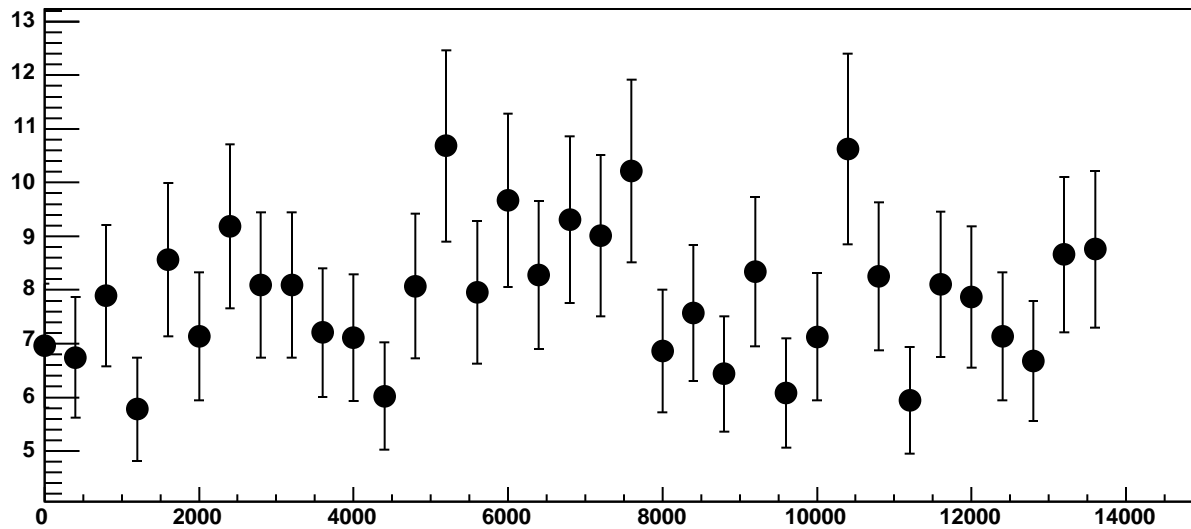


Chip 2, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

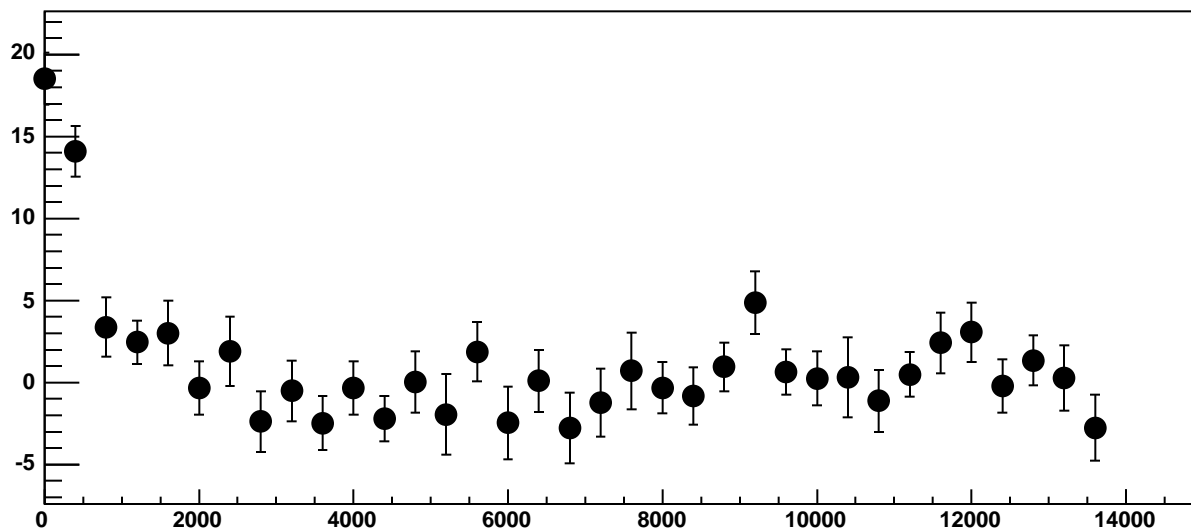


χ^2 / ndf 26 / 23
p0 -45.37 ± 0.7835
p1 0.01427 ± 0.0001188

Chip 2, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

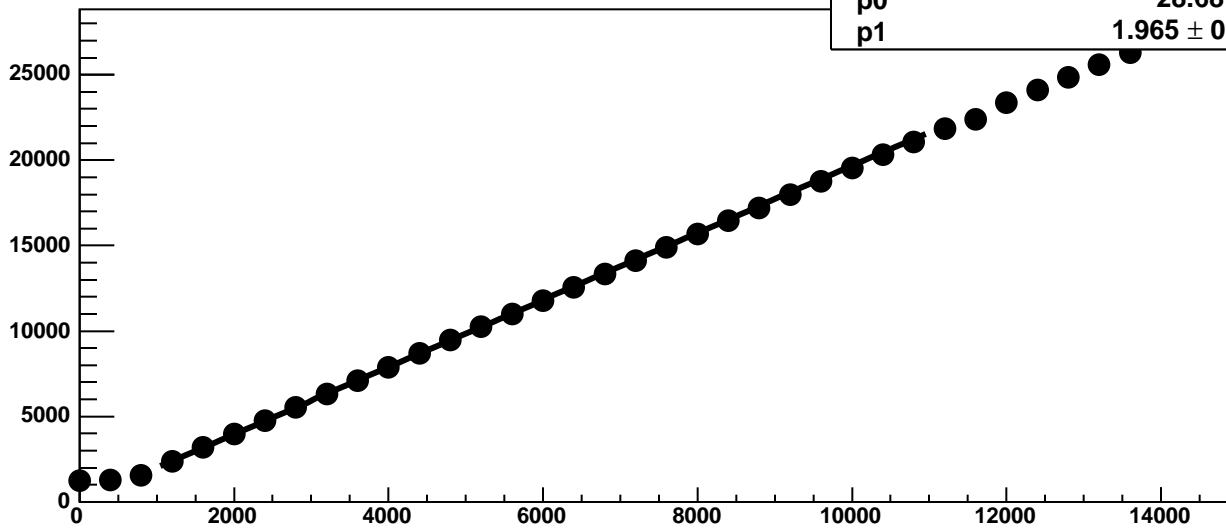


Chip 2, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

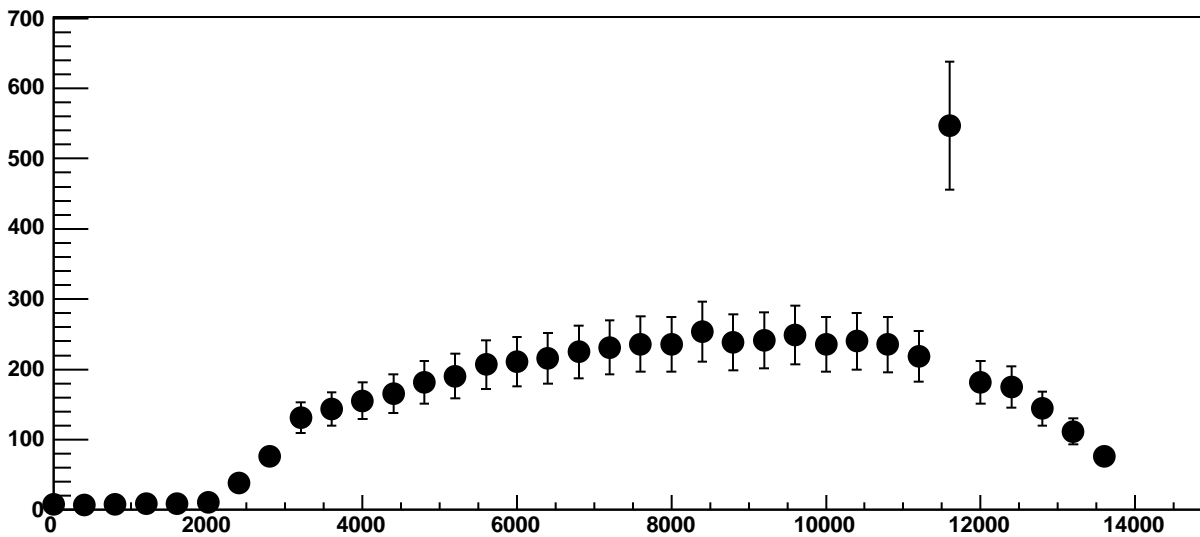


Chip 2, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC

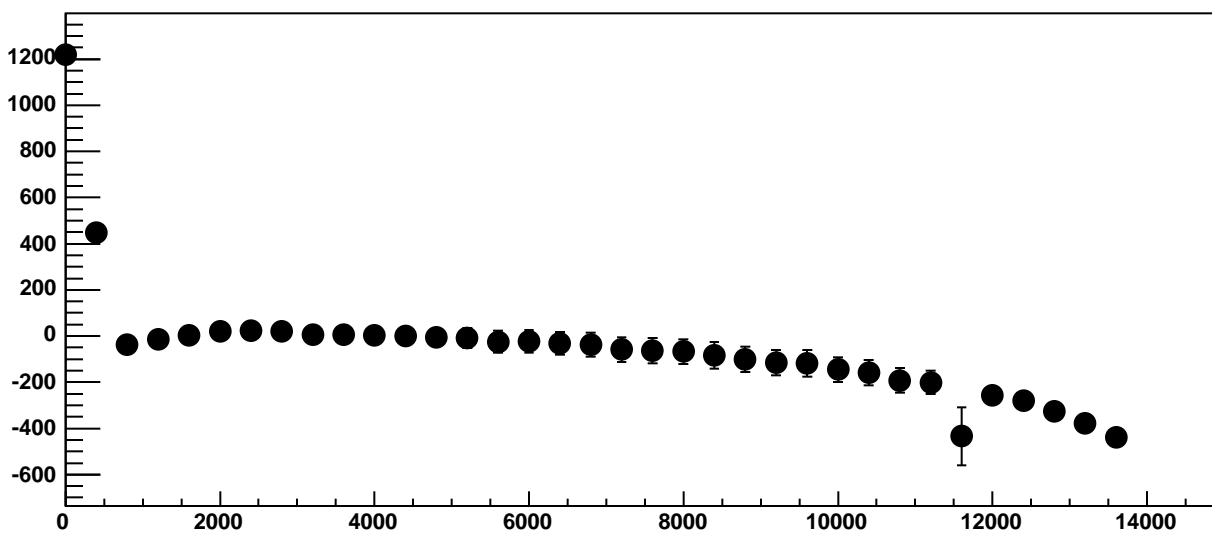
χ^2 / ndf 176.8 / 23
p0 28.68 ± 3.129
p1 1.965 ± 0.001752



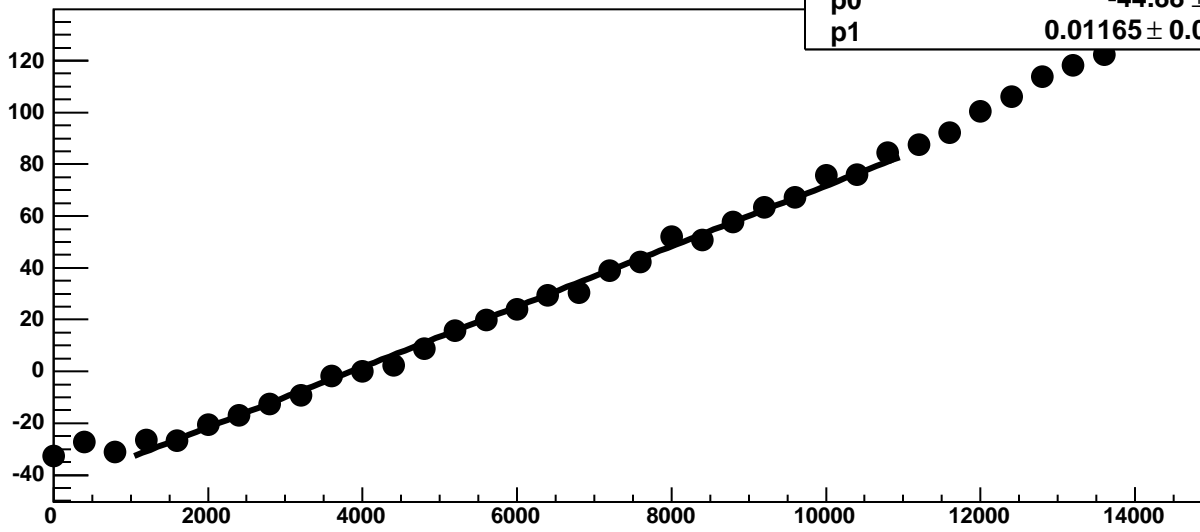
Chip 2, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

34 / 23

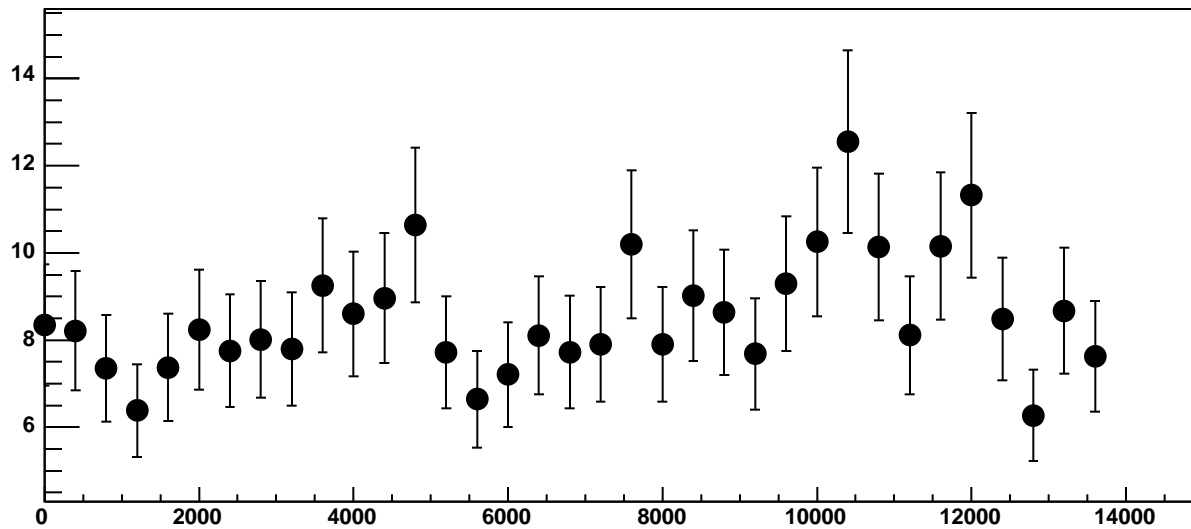
p0

-44.88 ± 0.8405

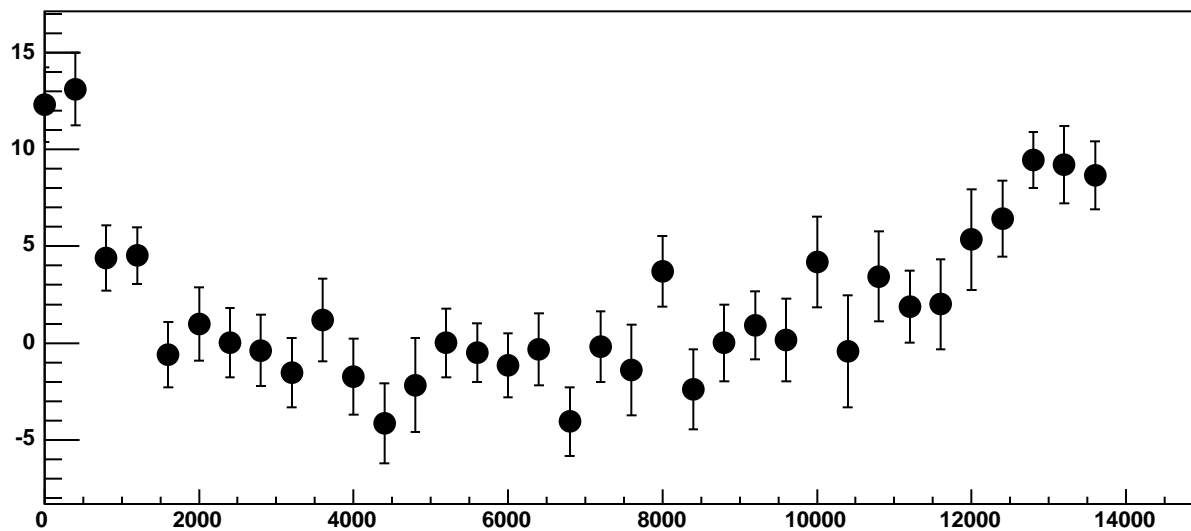
p1

0.01165 ± 0.0001348

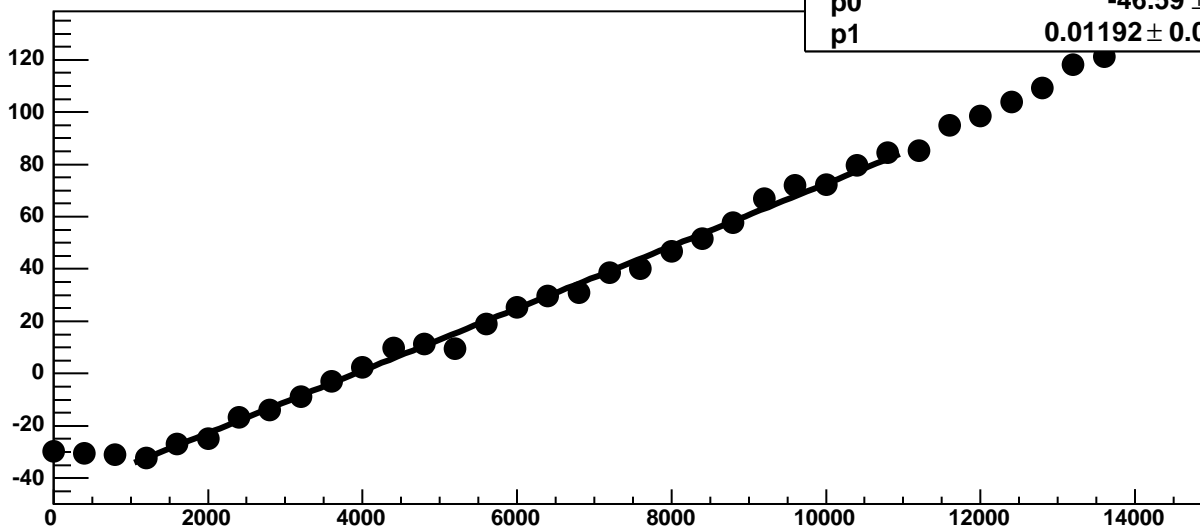
Chip 2, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



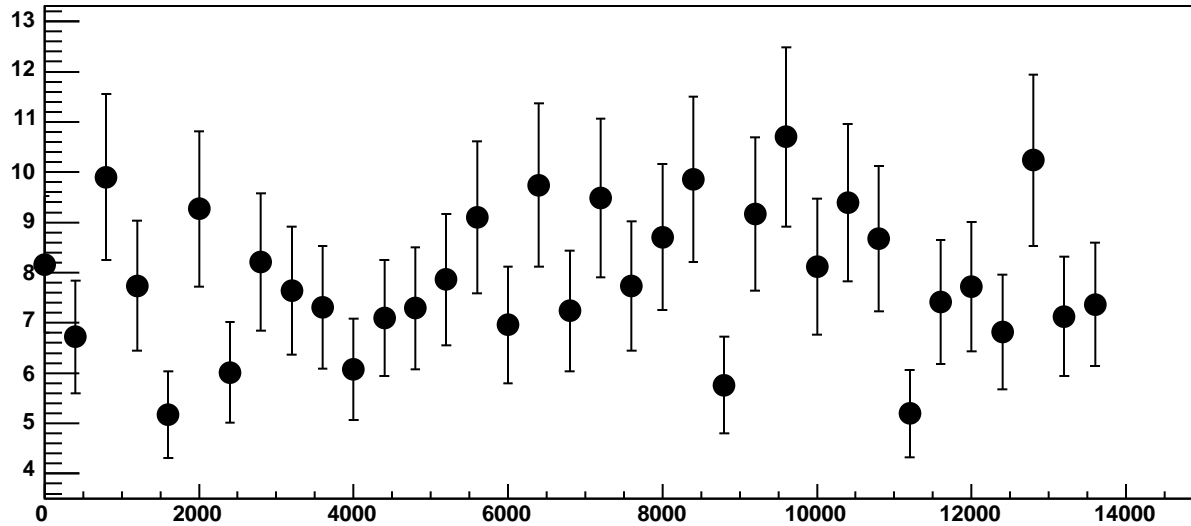
Chip 2, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



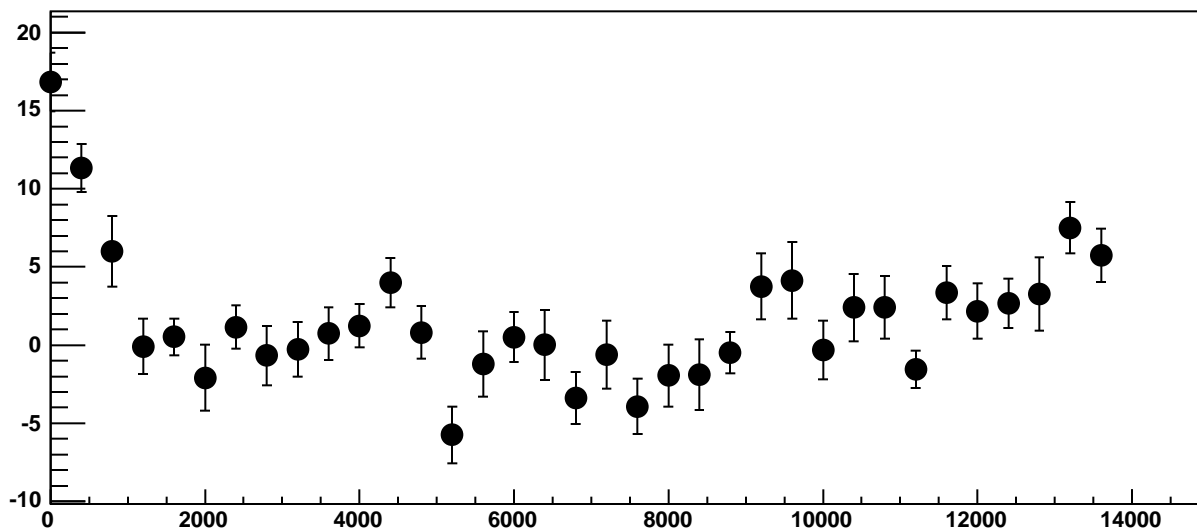
Chip 2, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



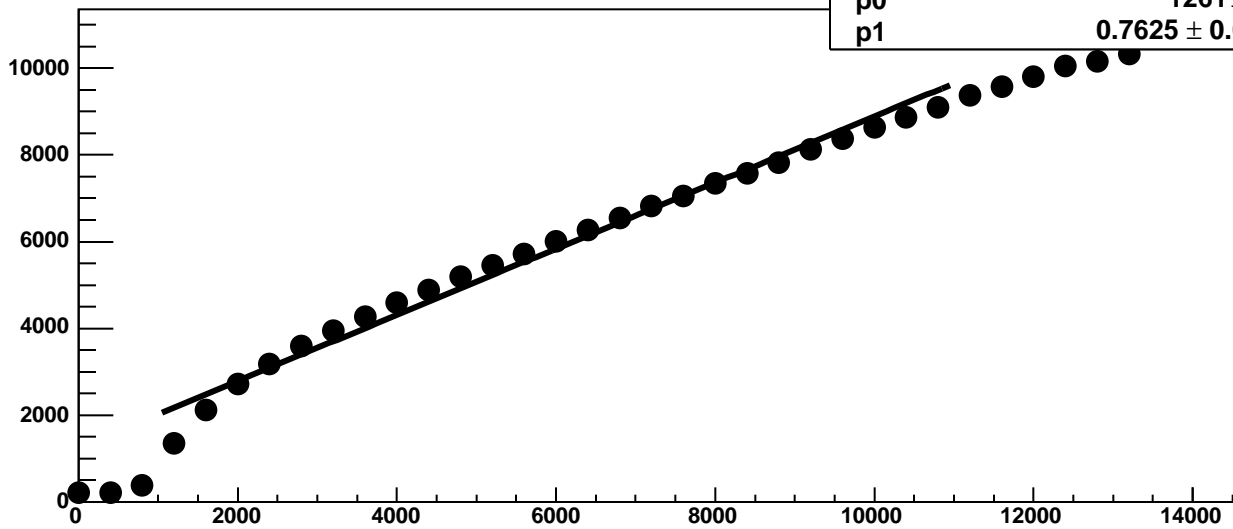
Chip 2, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



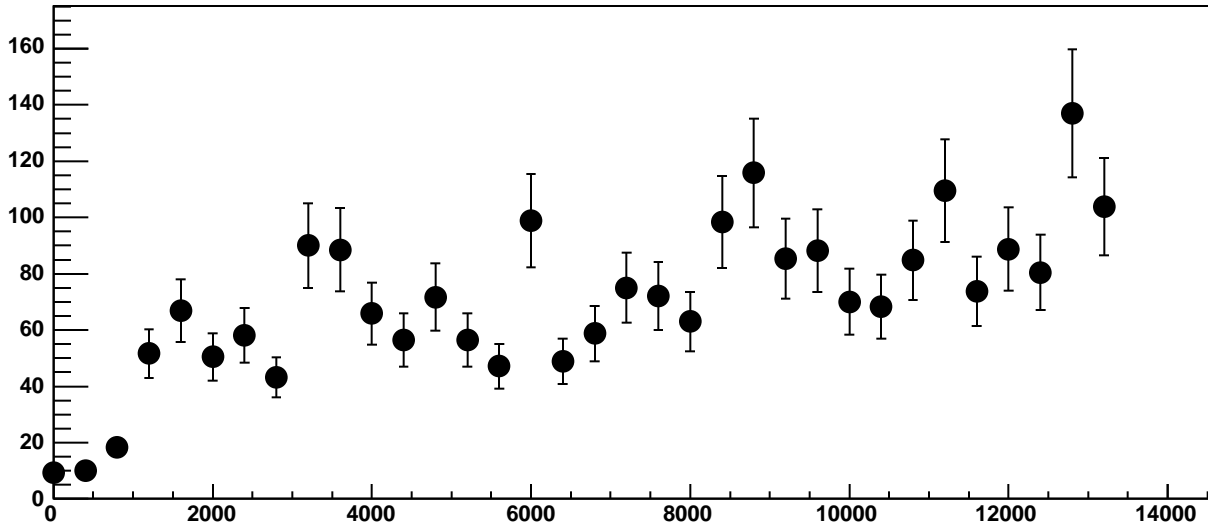
Chip 2, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



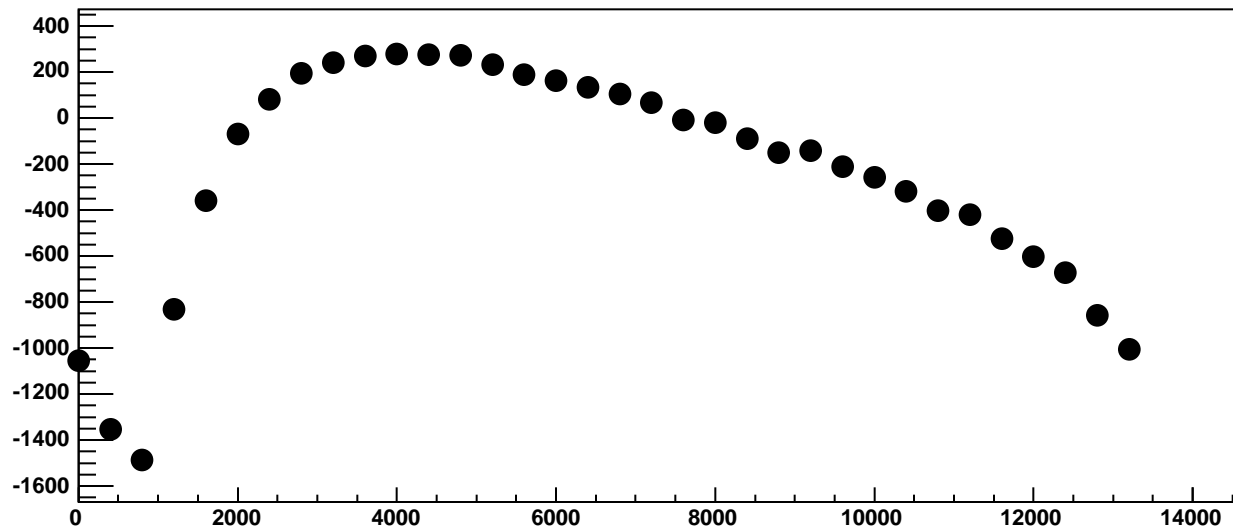
Chip 2, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC



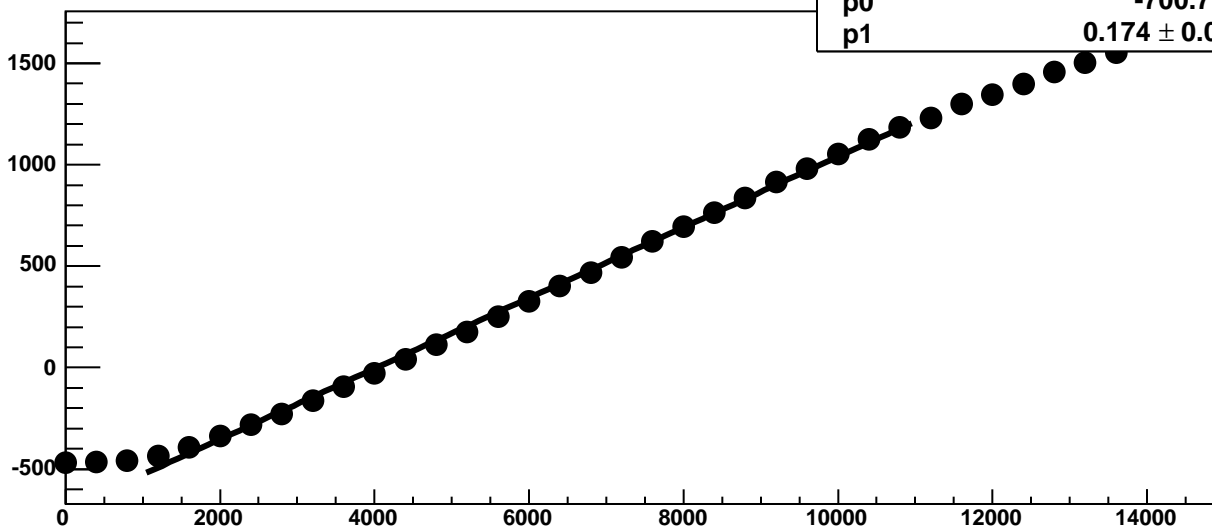
Chip 2, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC



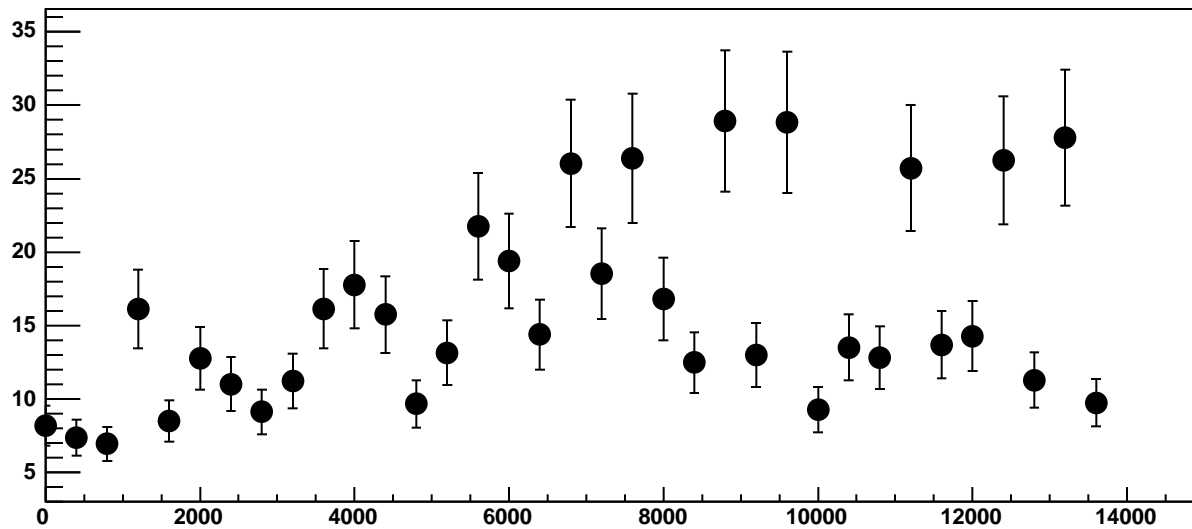
Chip 2, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC



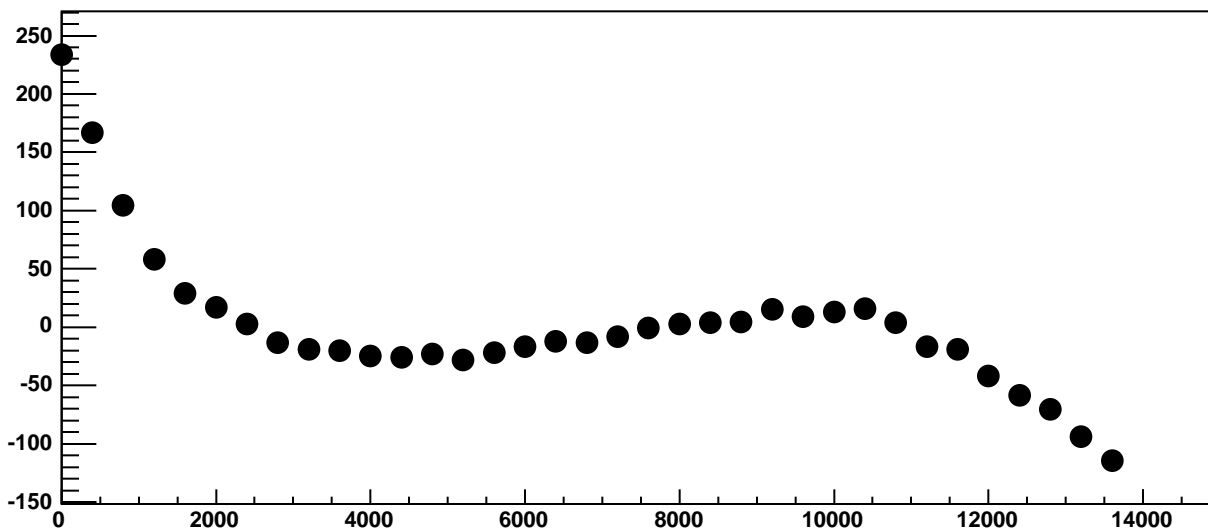
Chip 3, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC



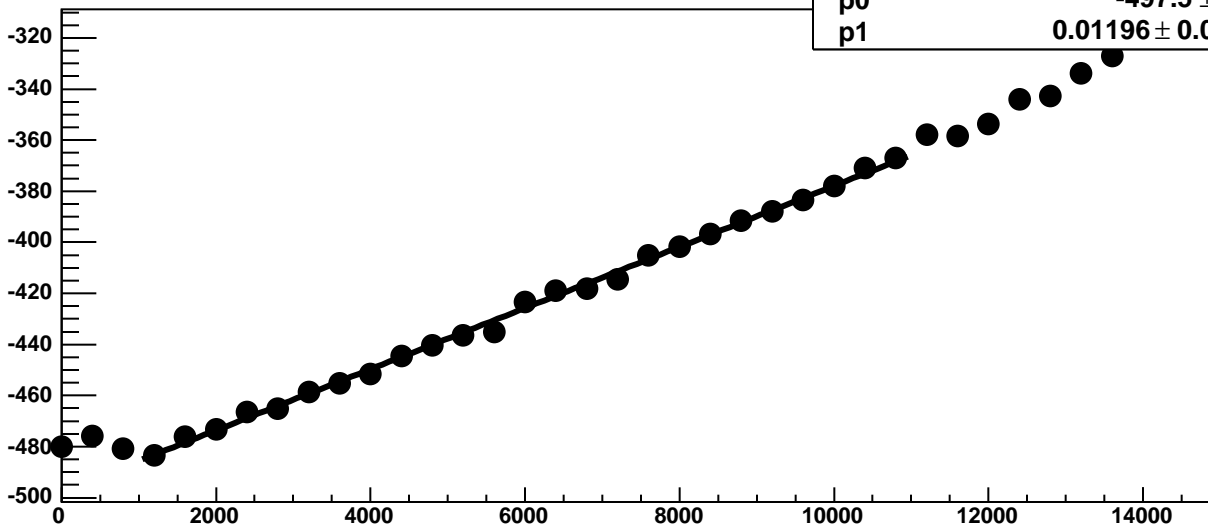
Chip 3, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

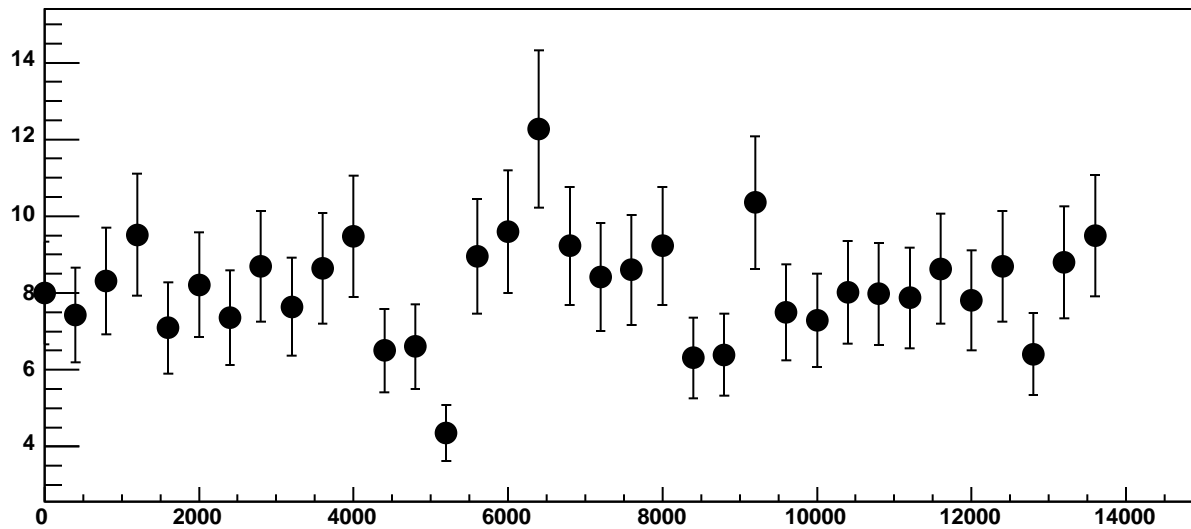


Chip 3, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

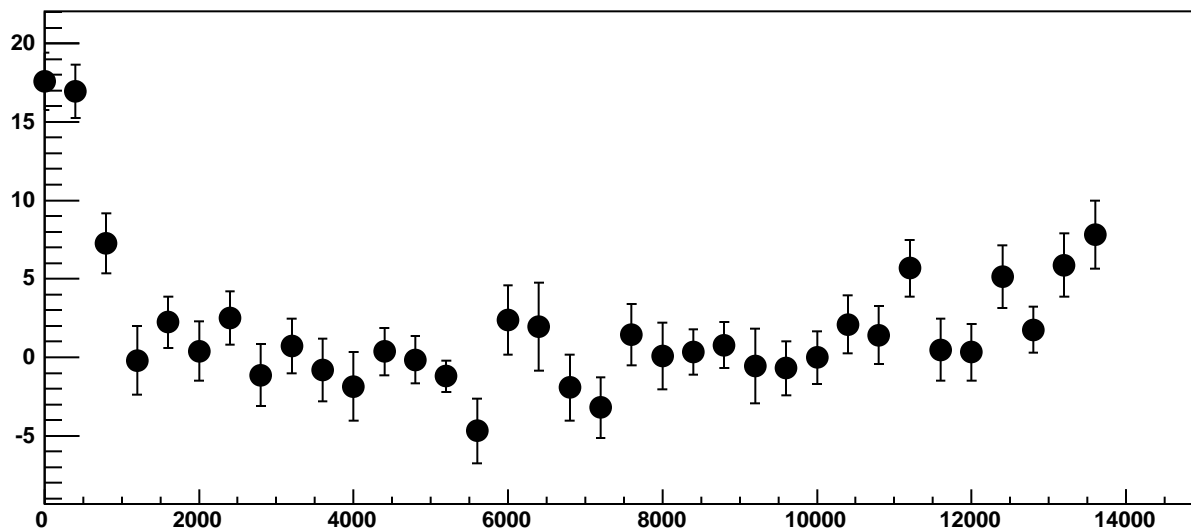


χ^2 / ndf 20.43 / 23
p0 -497.5 ± 0.8256
p1 0.01196 ± 0.0001249

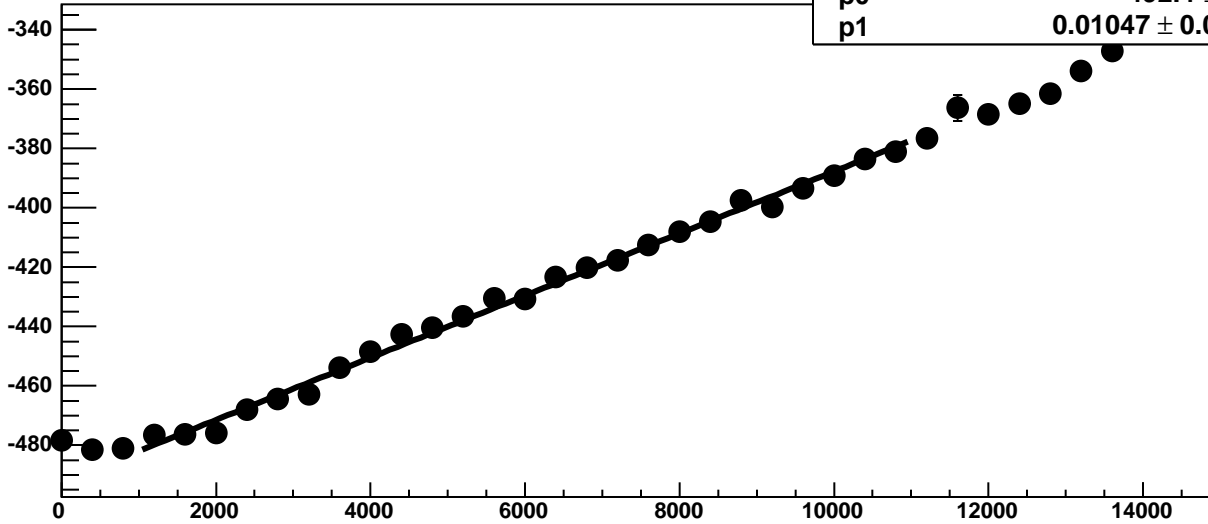
Chip 3, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



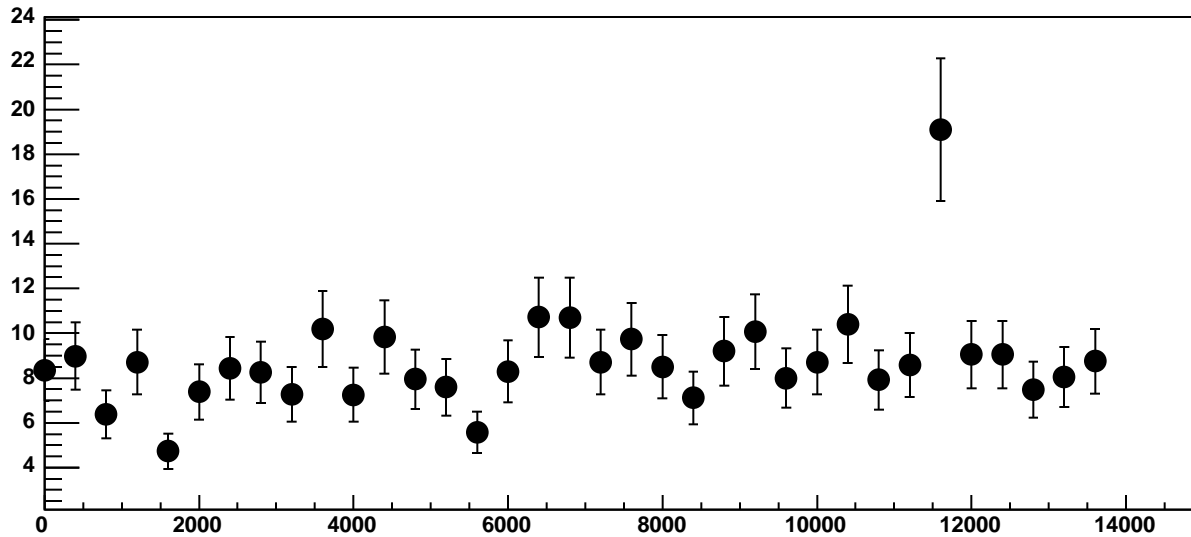
Chip 3, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



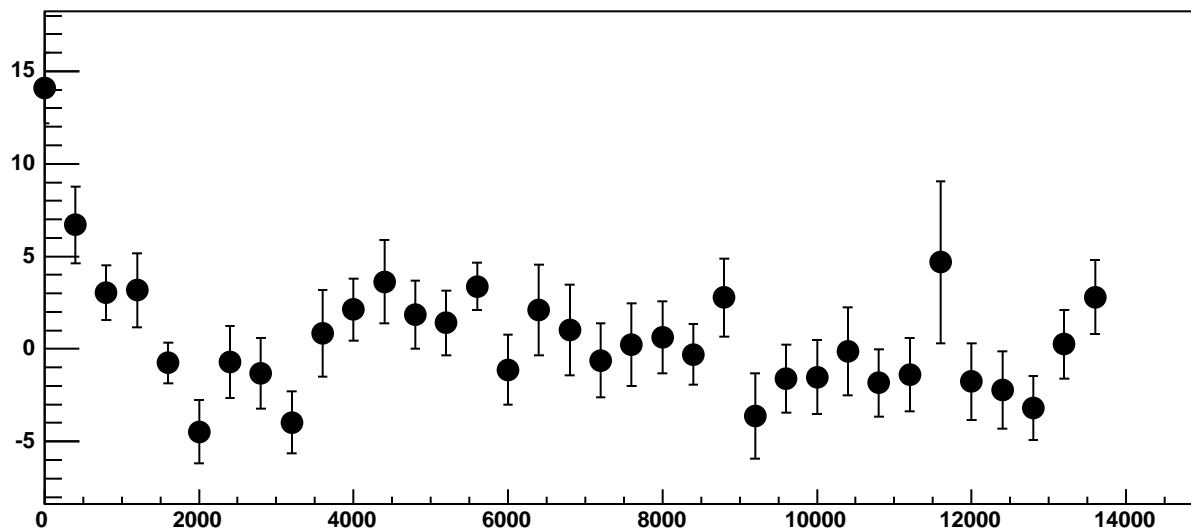
Chip 3, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



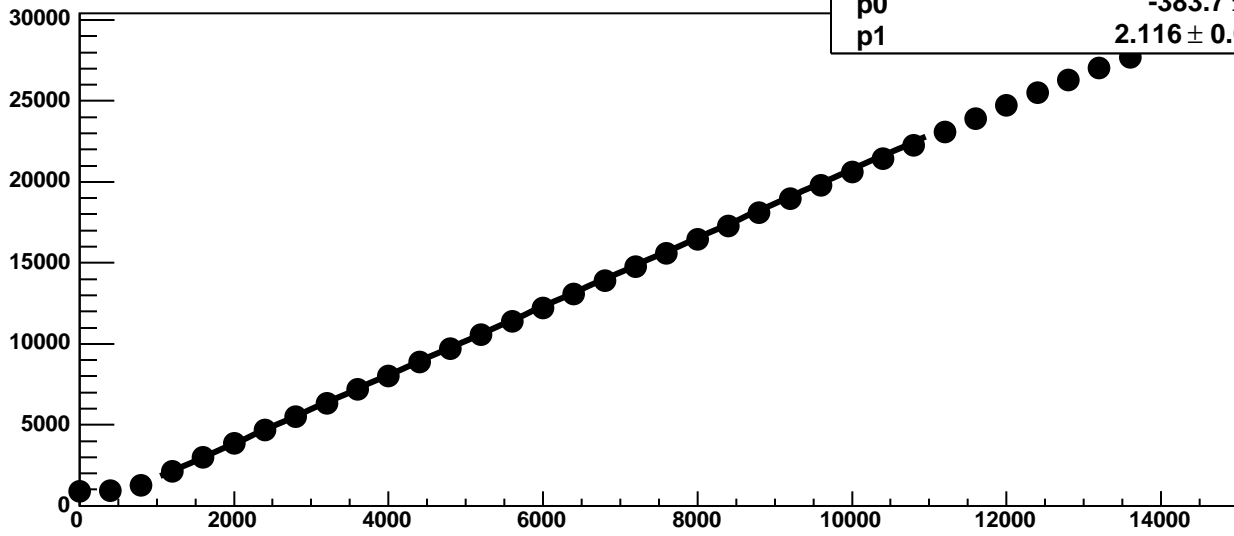
Chip 3, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

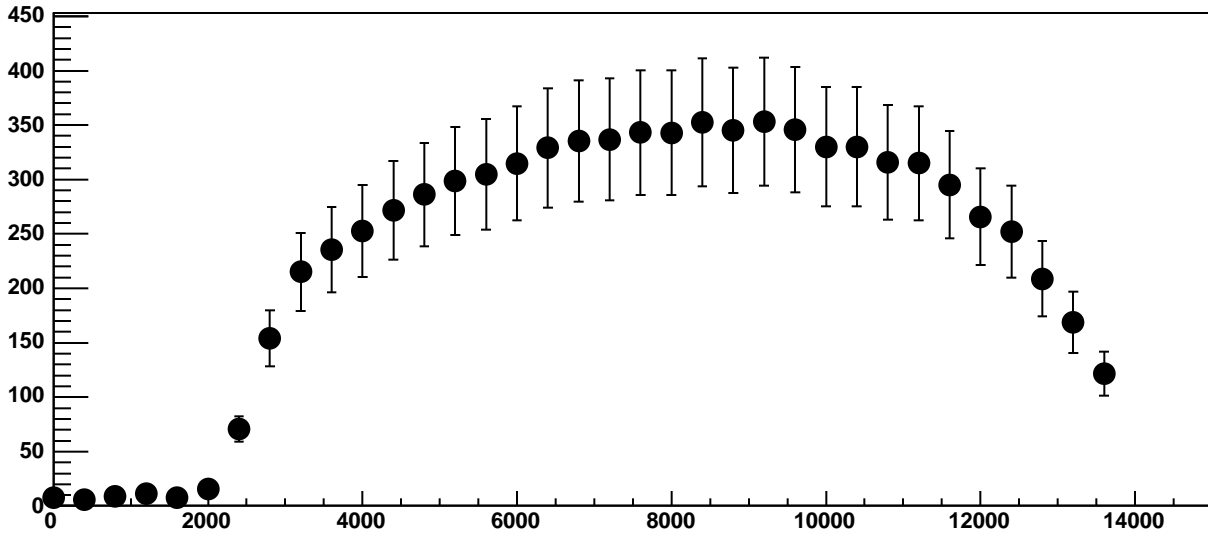


Chip 3, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC

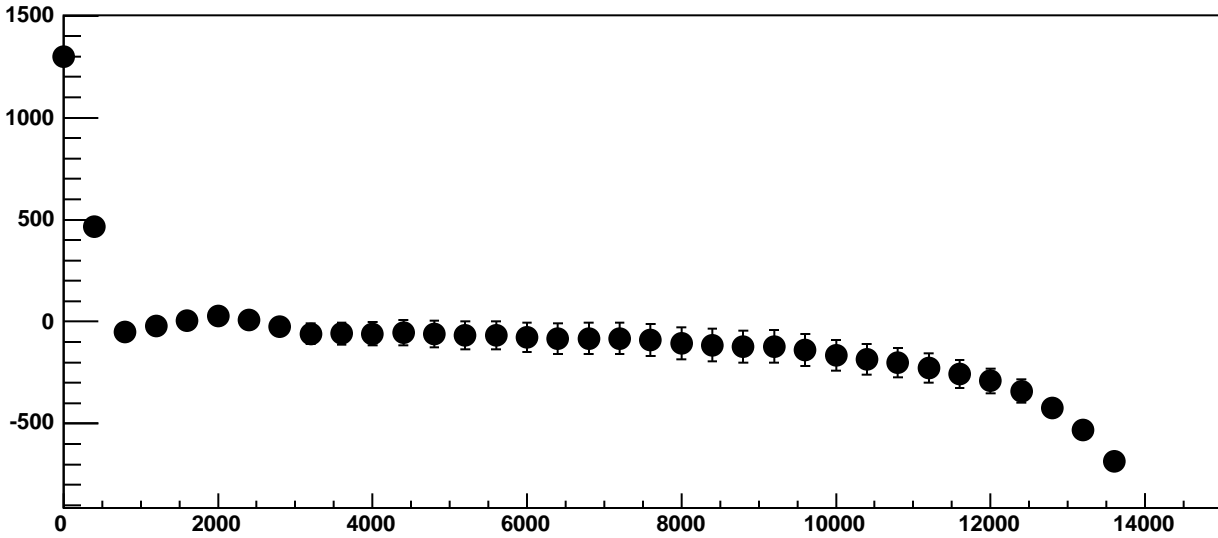


χ^2 / ndf 191 / 23
p0 -383.7 ± 4.176
p1 2.116 ± 0.002489

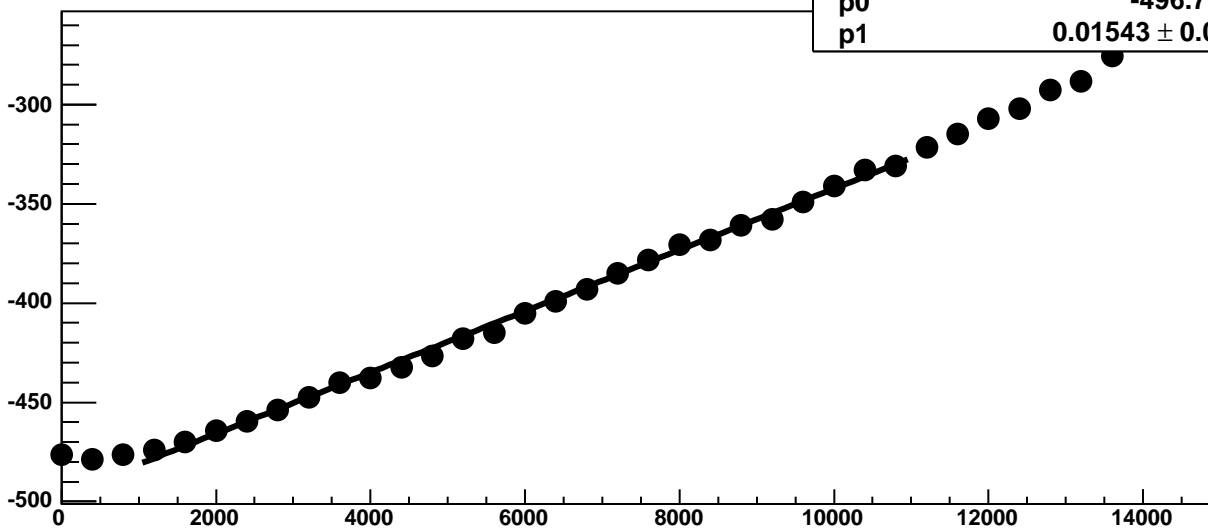
Chip 3, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC

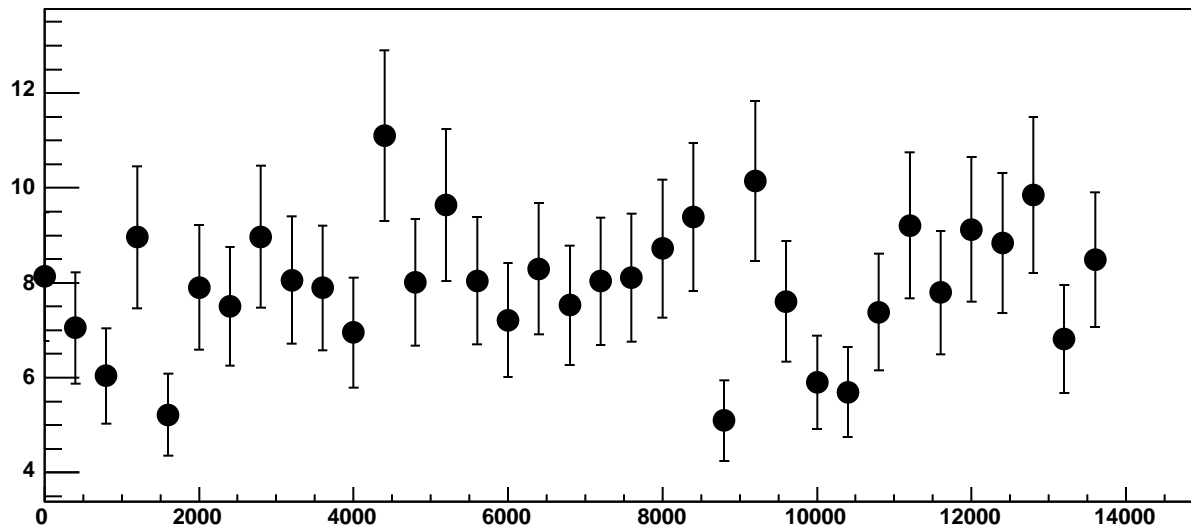


Chip 3, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

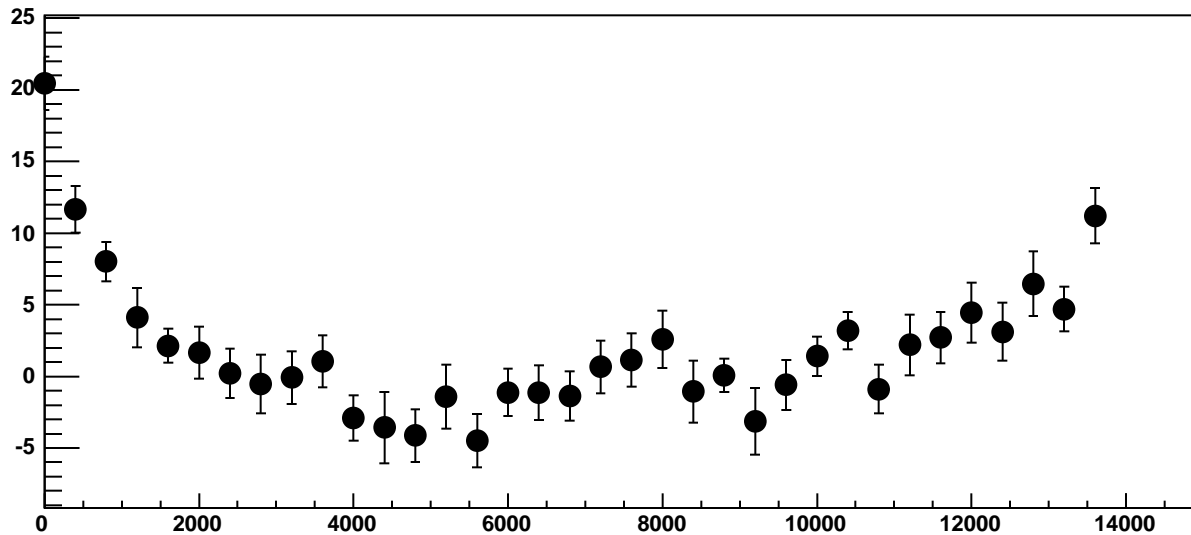


χ^2 / ndf 38.28 / 23
p0 -496.7 ± 0.773
p1 0.01543 ± 0.0001114

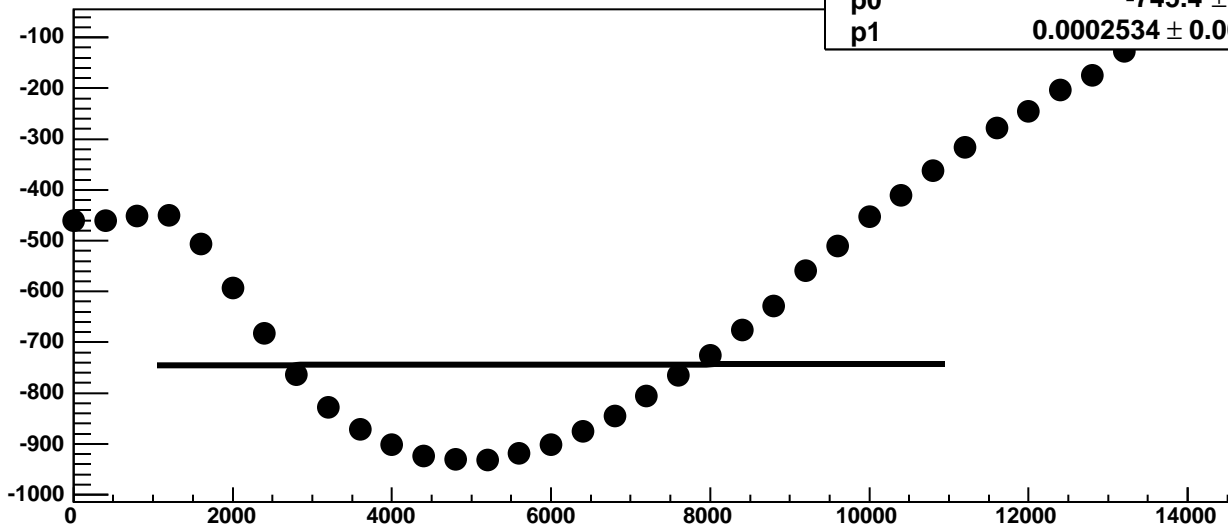
Chip 3, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

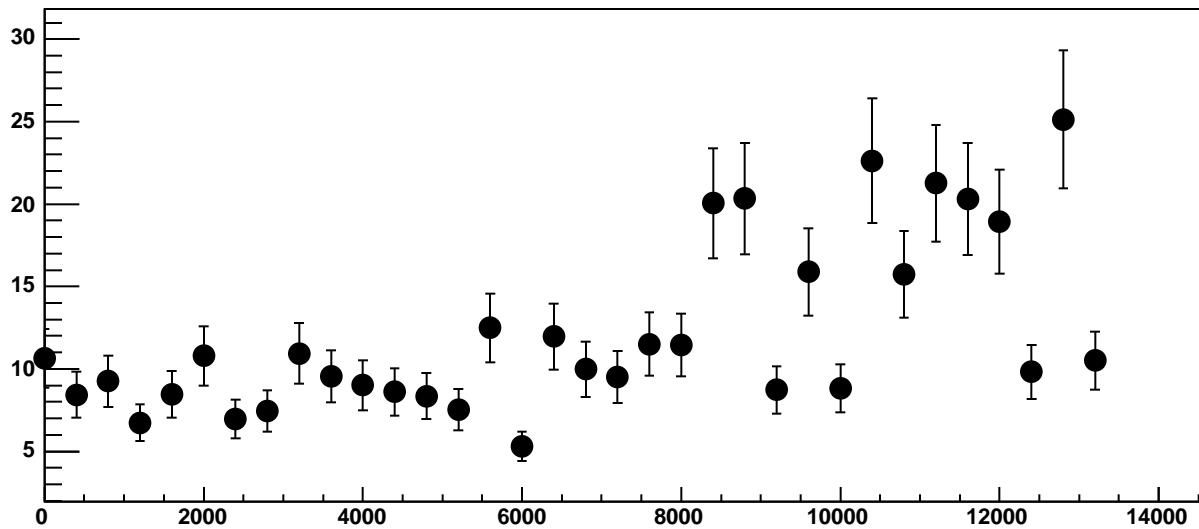


Chip 3, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

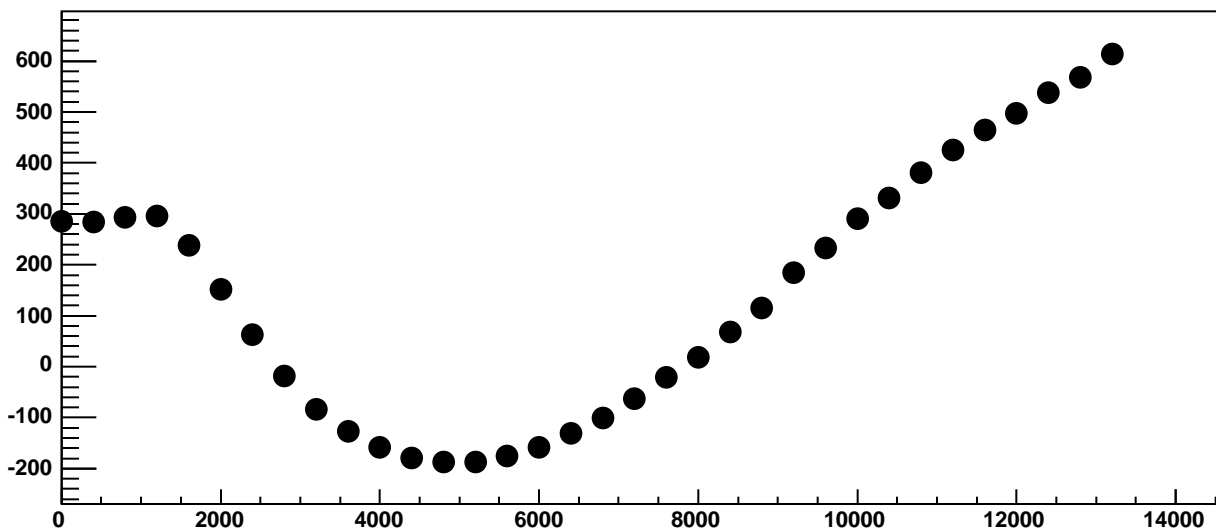


χ^2 / ndf 1.716e+05 / 23
p0 -745.4 ± 0.9292
p1 0.0002534 ± 0.0001623

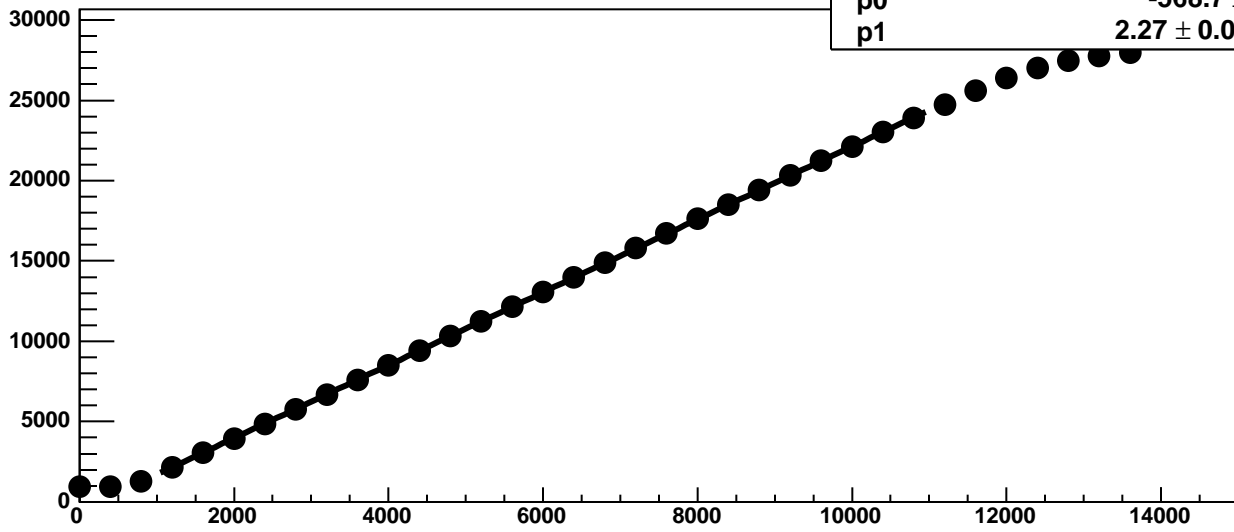
Chip 3, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



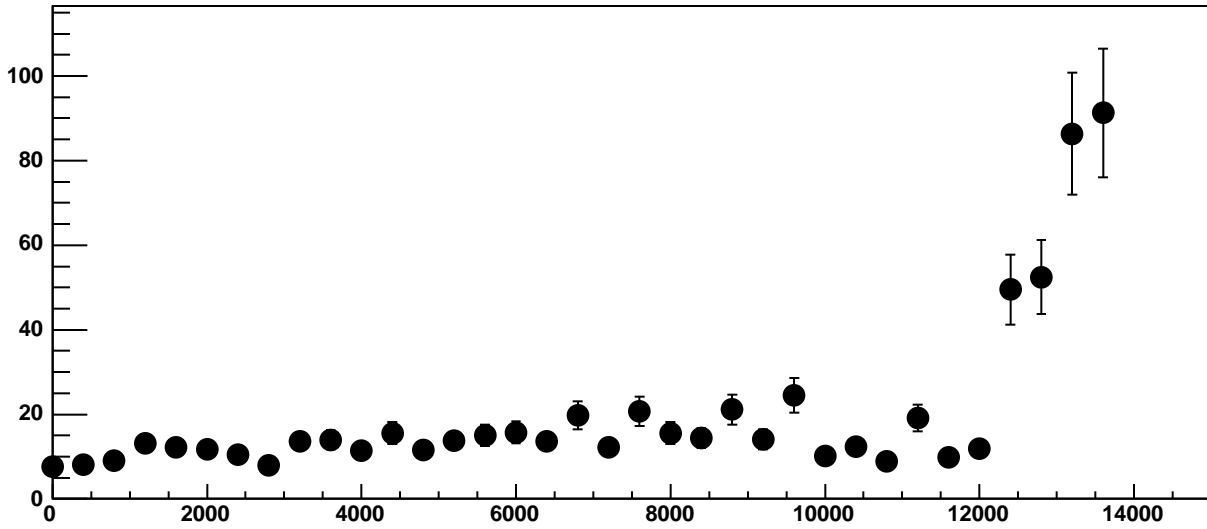
Chip 3, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



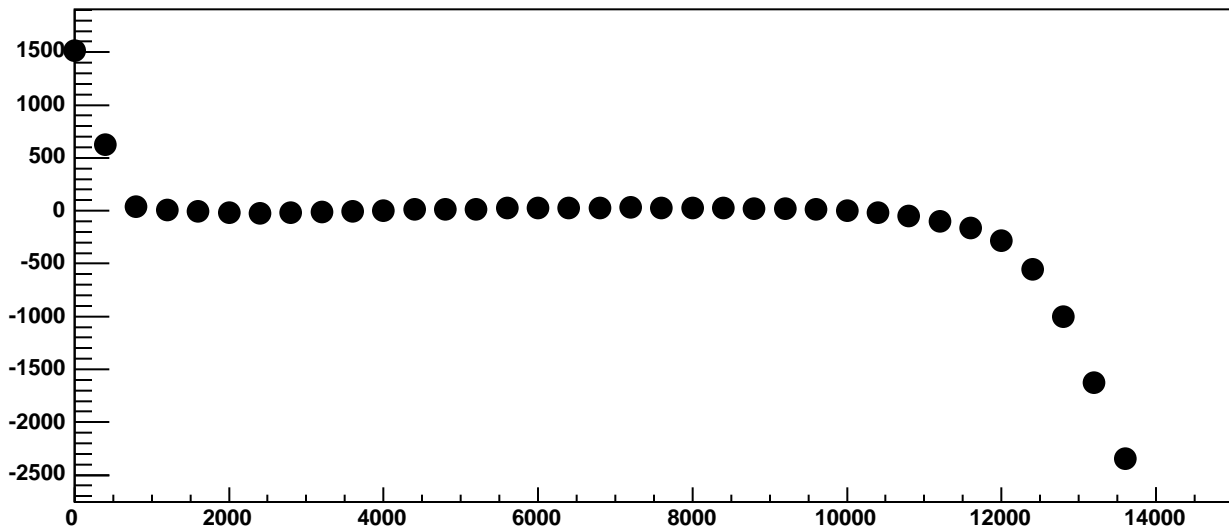
Chip 3, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



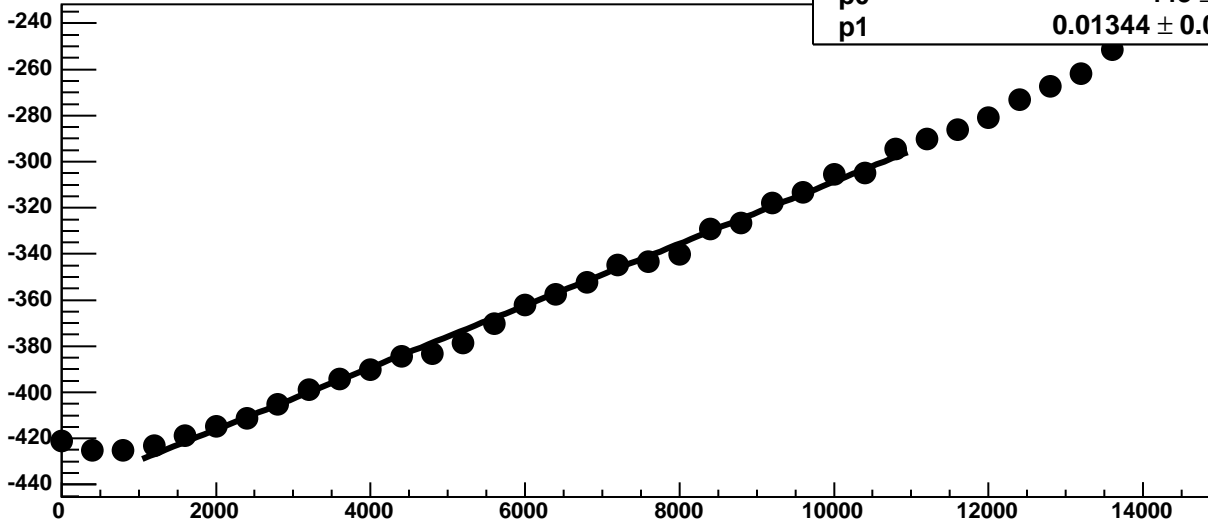
Chip 3, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC

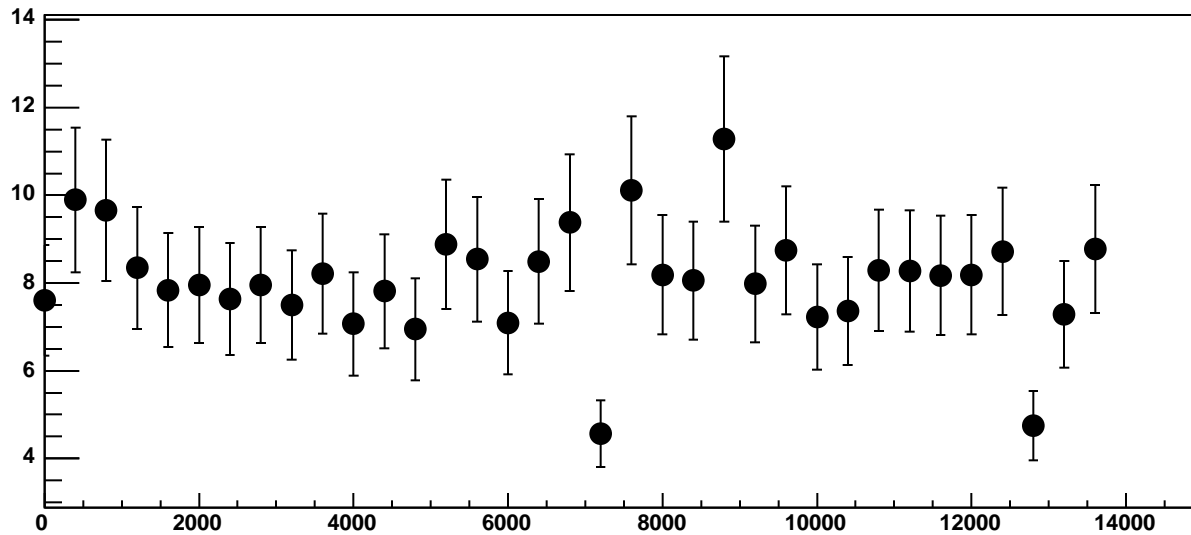


Chip 3, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

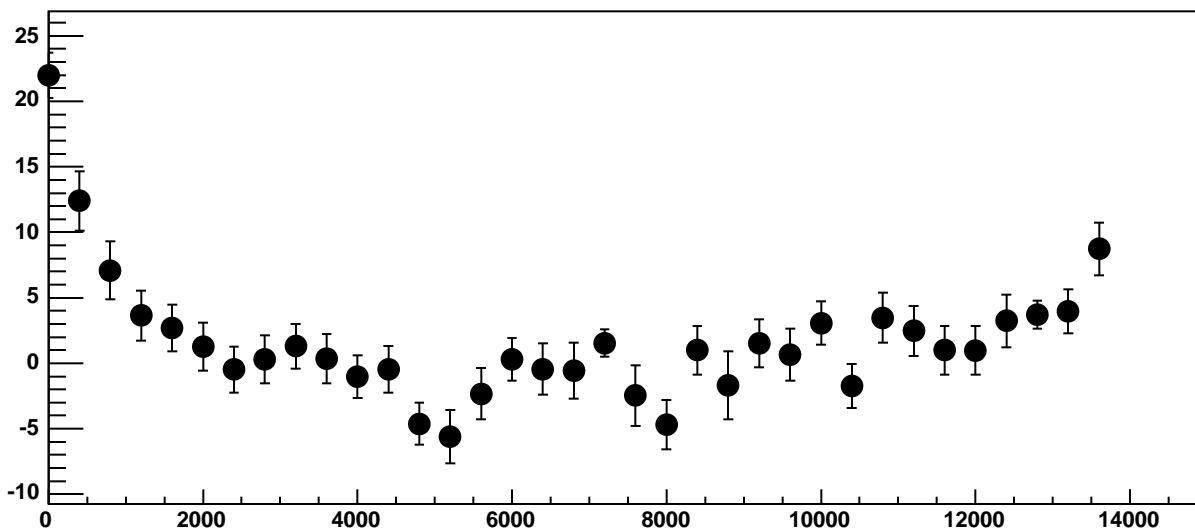


χ^2 / ndf 43.85 / 23
p0 -443 ± 0.8338
p1 0.01344 ± 0.0001261

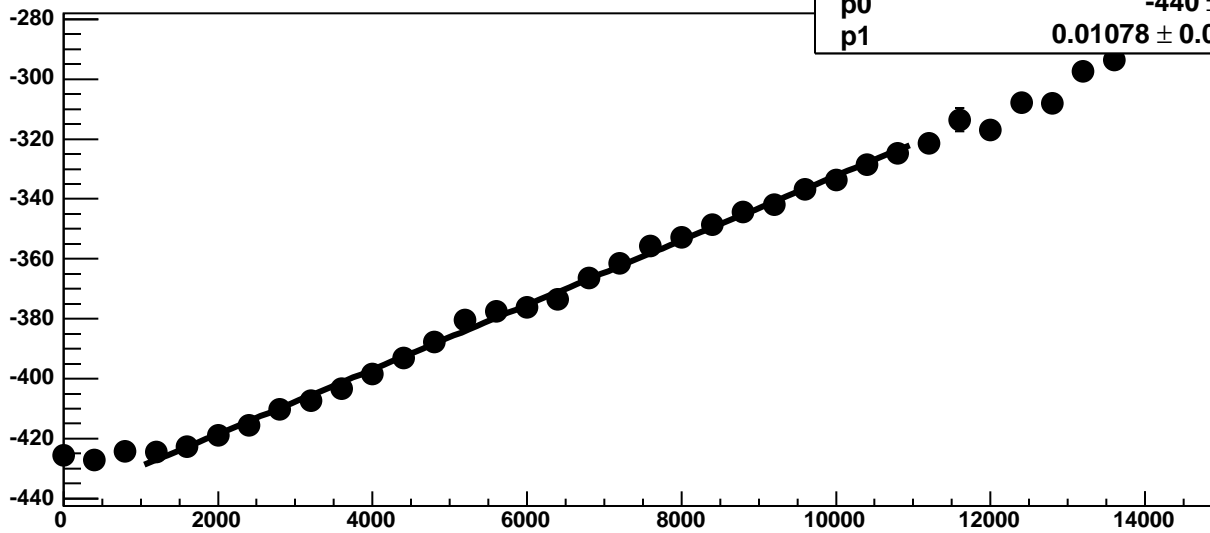
Chip 3, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



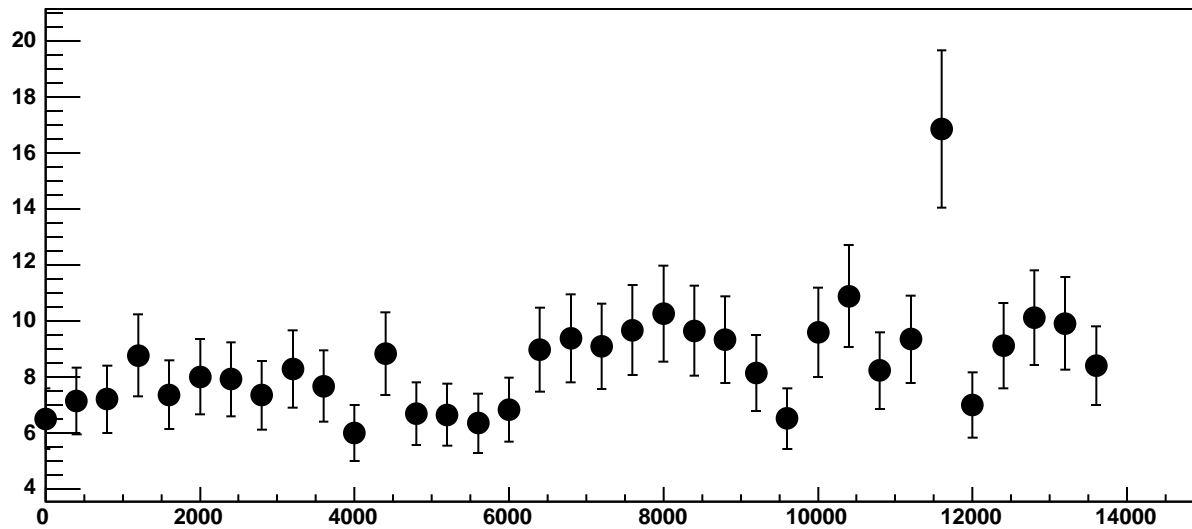
Chip 3, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



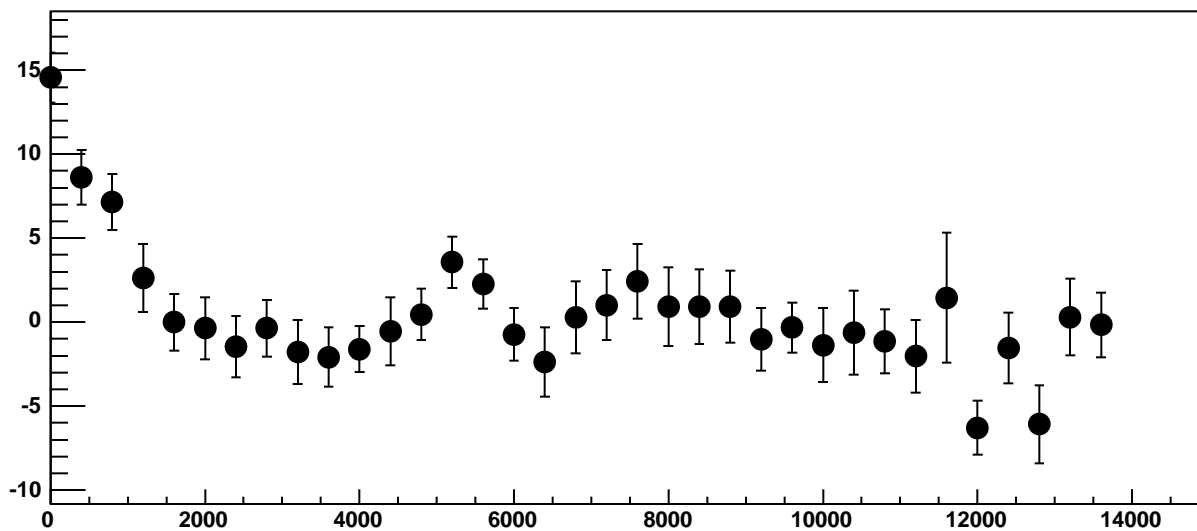
Chip 3, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



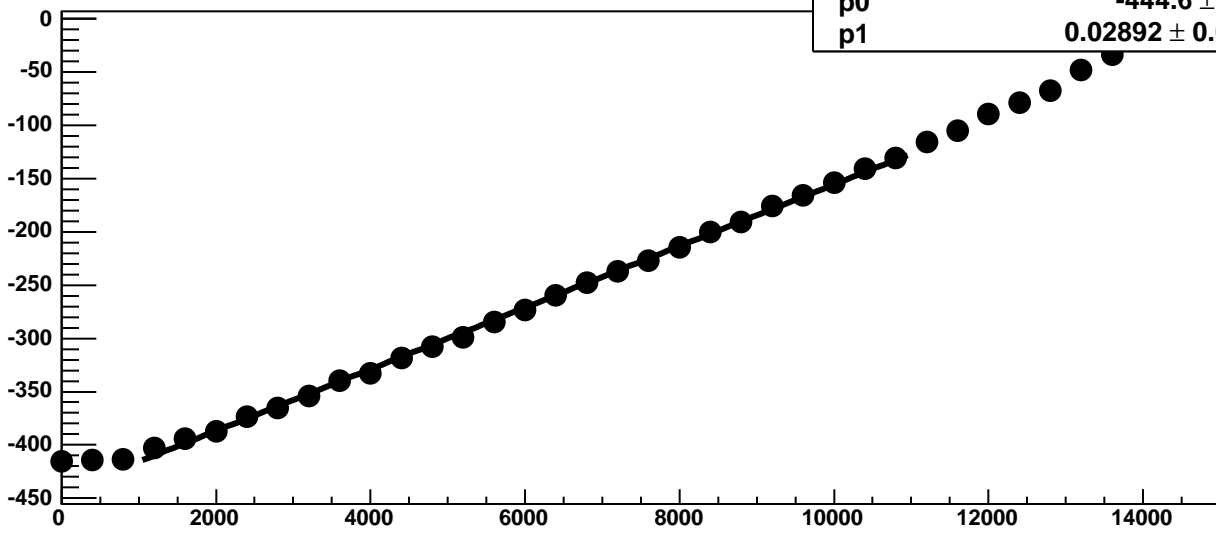
Chip 3, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



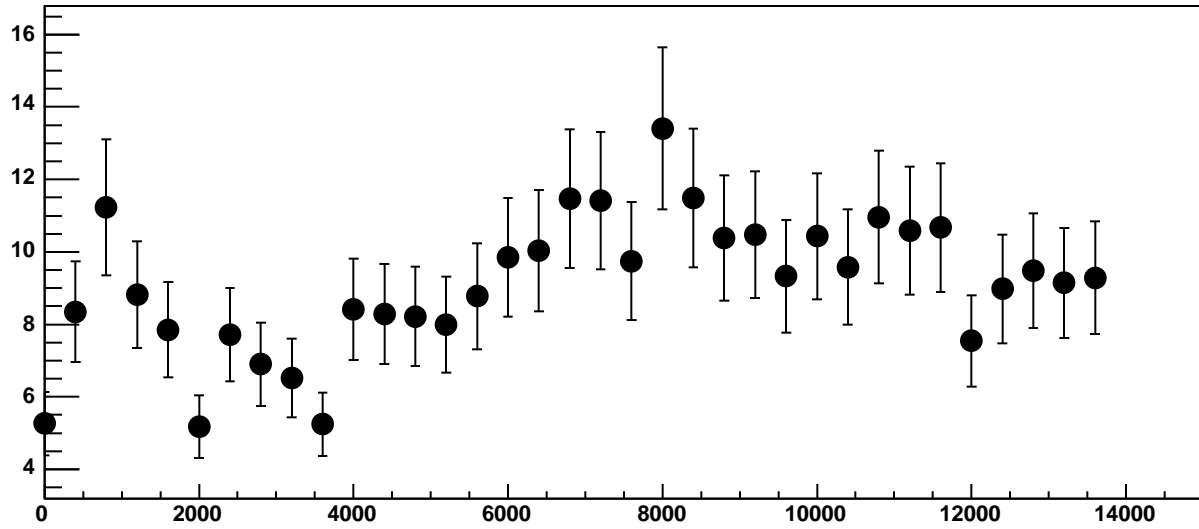
Chip 3, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



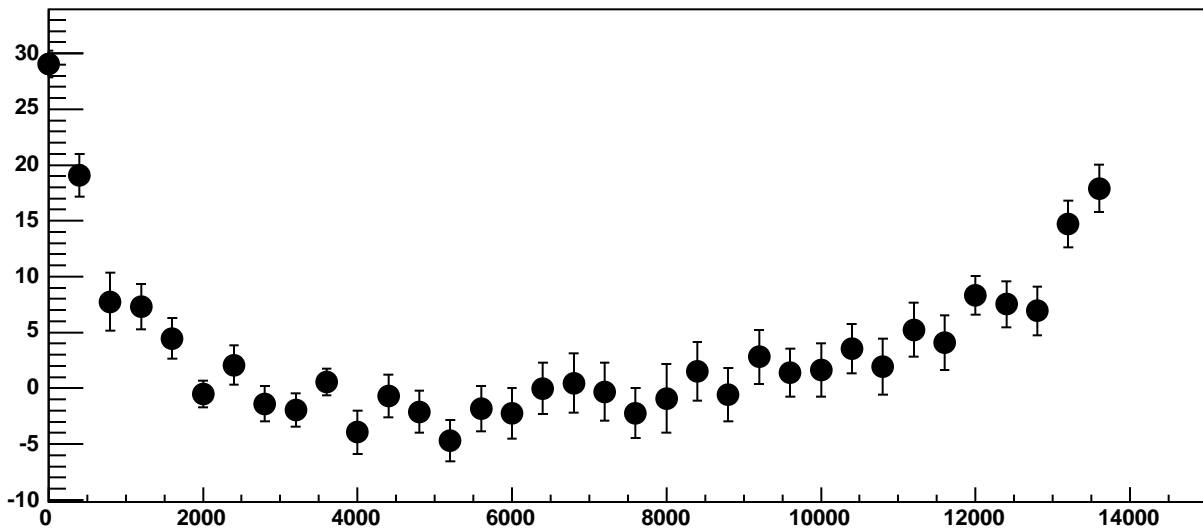
Chip 3, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC



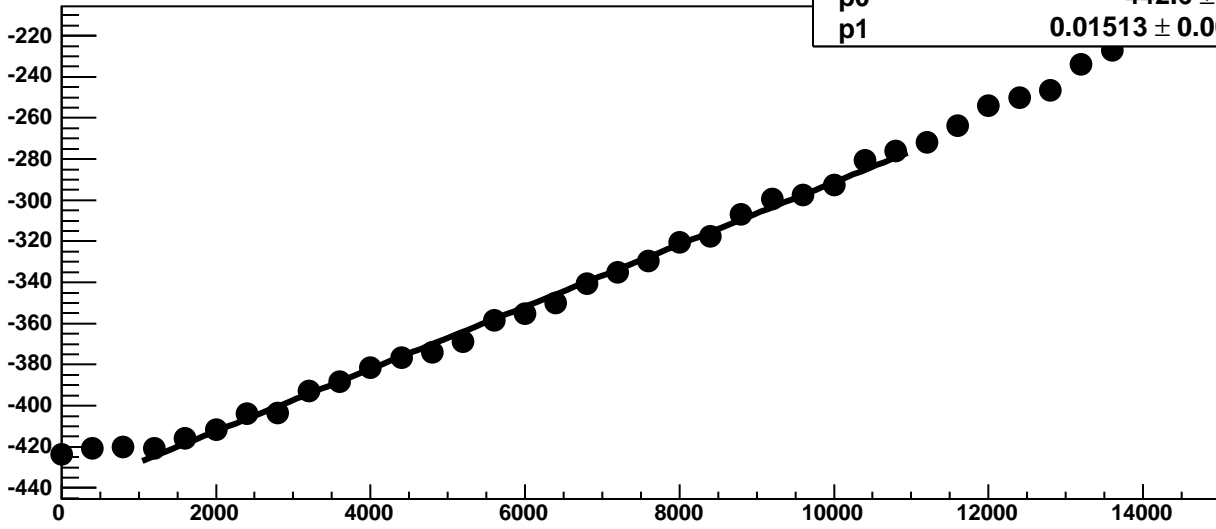
Chip 3, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC

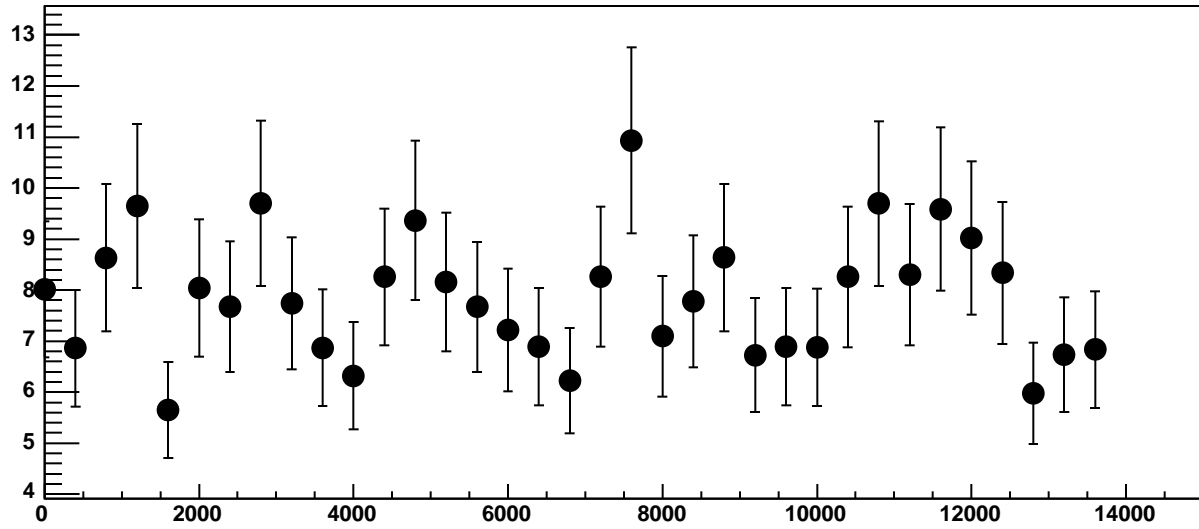


Chip 3, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

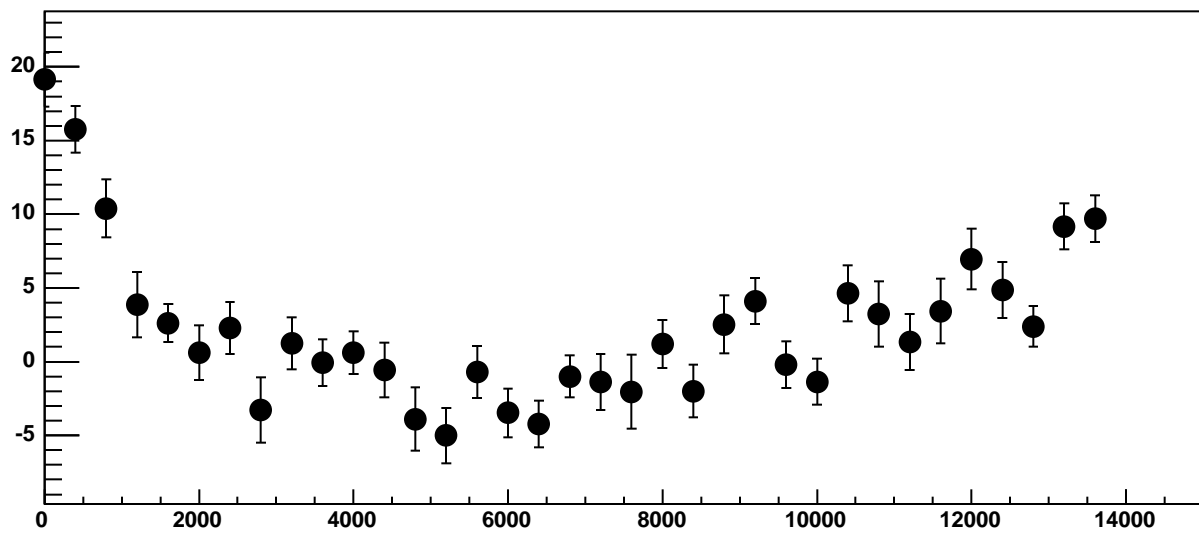


χ^2 / ndf 55.1 / 23
p0 -442.6 ± 0.8015
p1 0.01513 ± 0.0001216

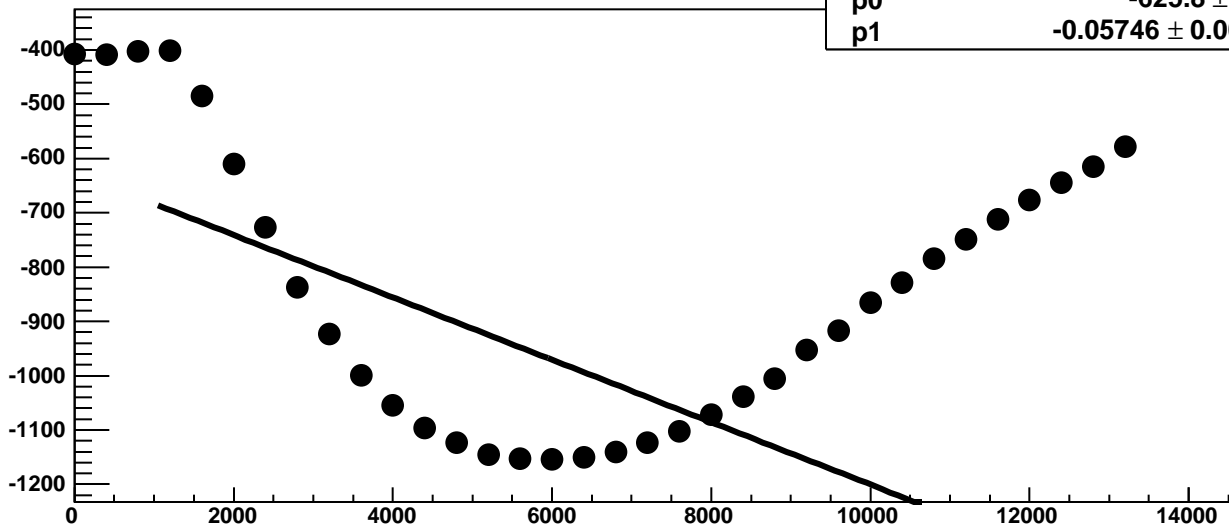
Chip 3, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

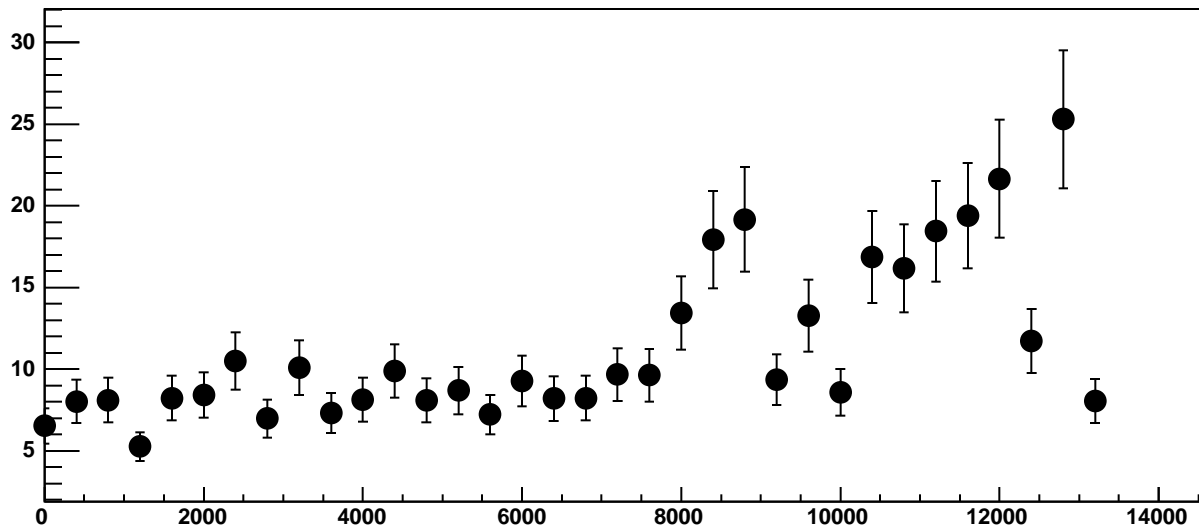


Chip 3, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

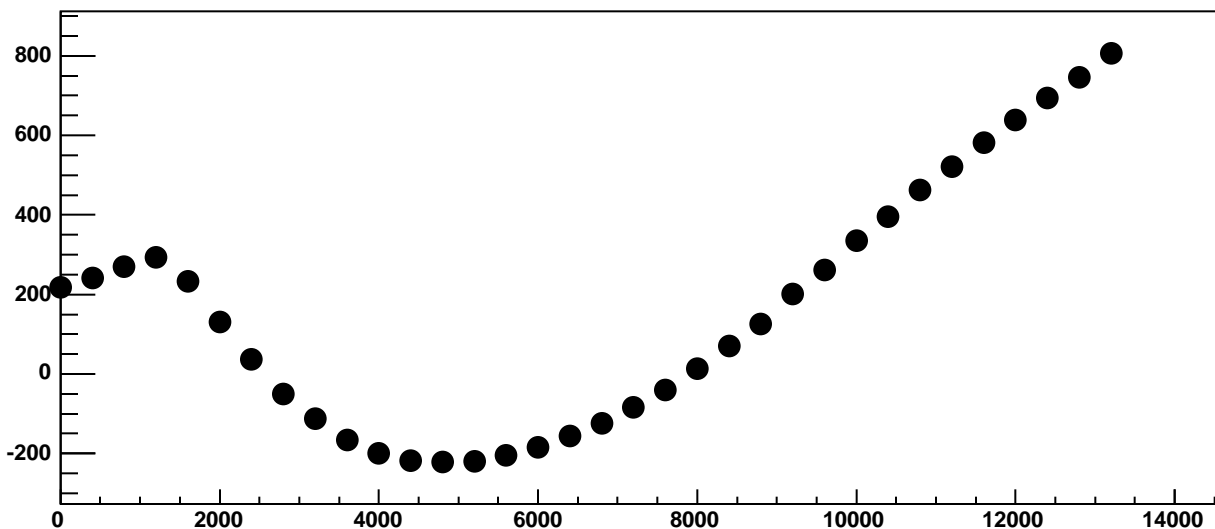


χ^2 / ndf 2.472e+05 / 23
p0 -625.8 ± 0.8525
p1 -0.05746 ± 0.0001512

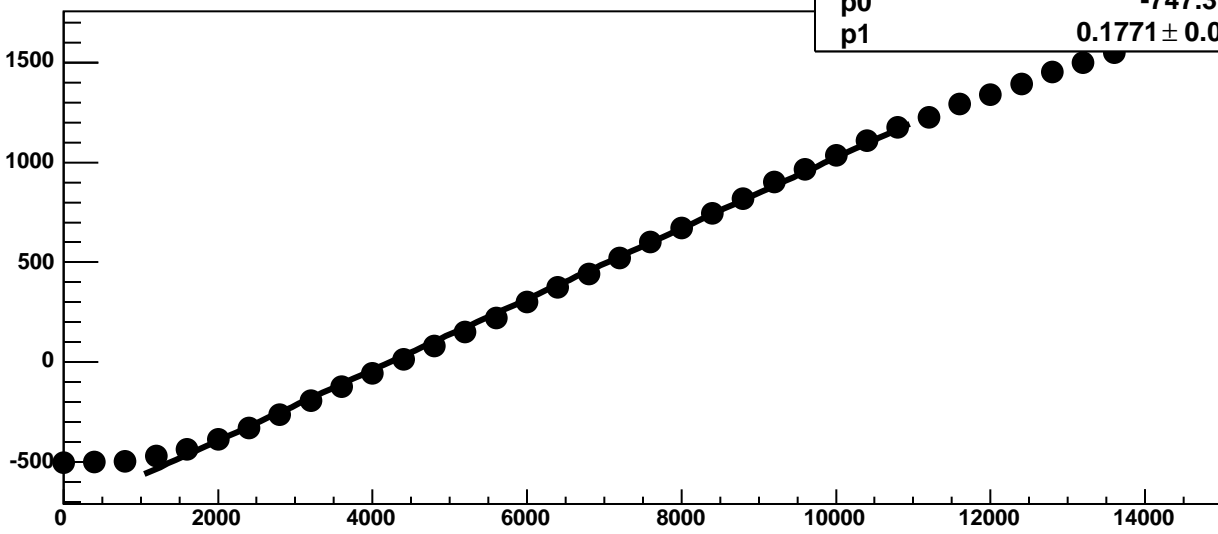
Chip 3, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

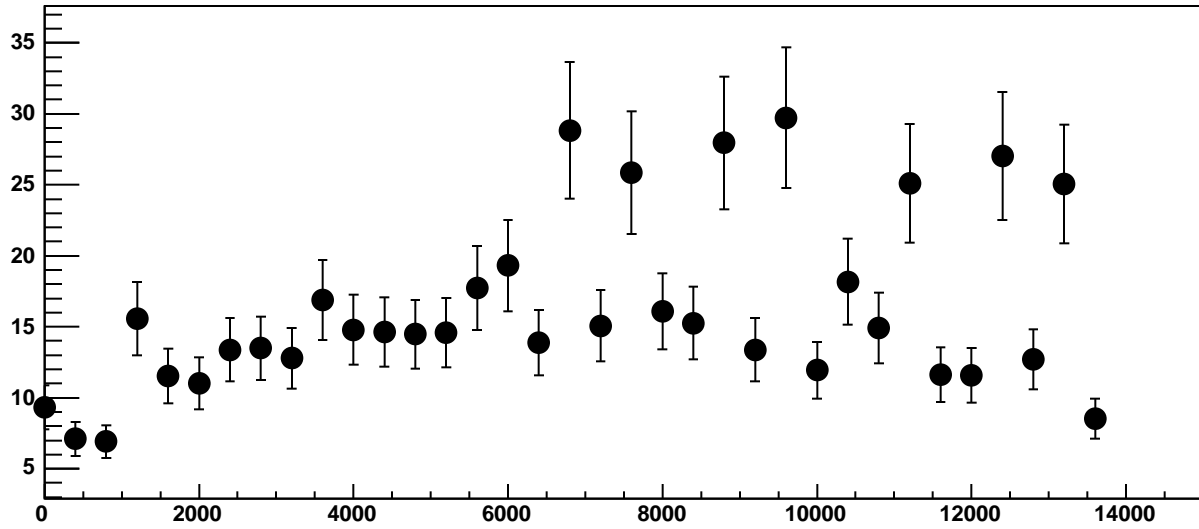


Chip 3, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC

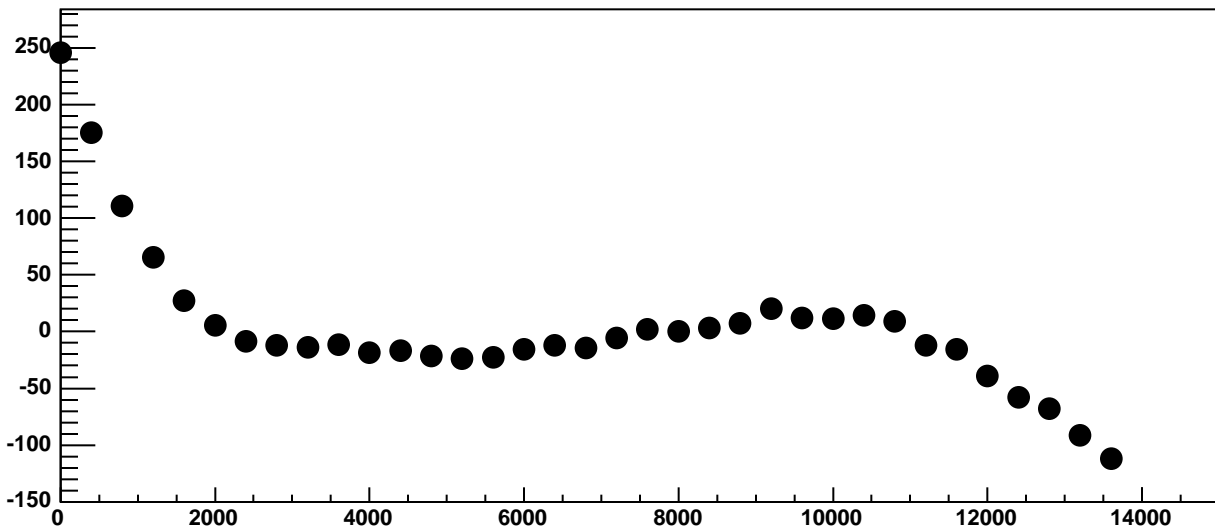


χ^2 / ndf 793 / 23
p0 -747.3 ± 1.44
p1 0.1771 ± 0.0002311

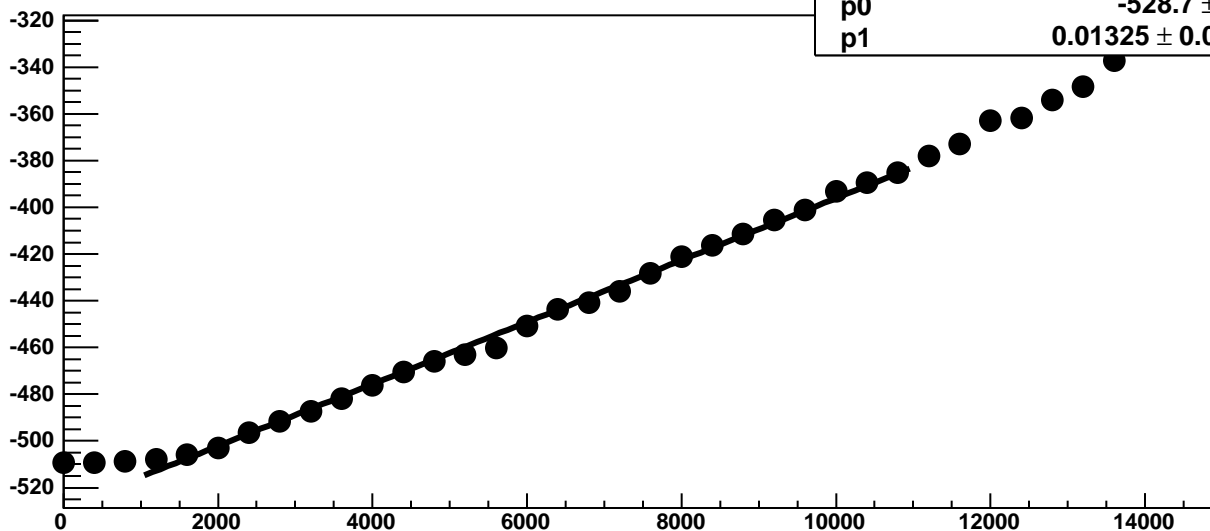
Chip 3, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



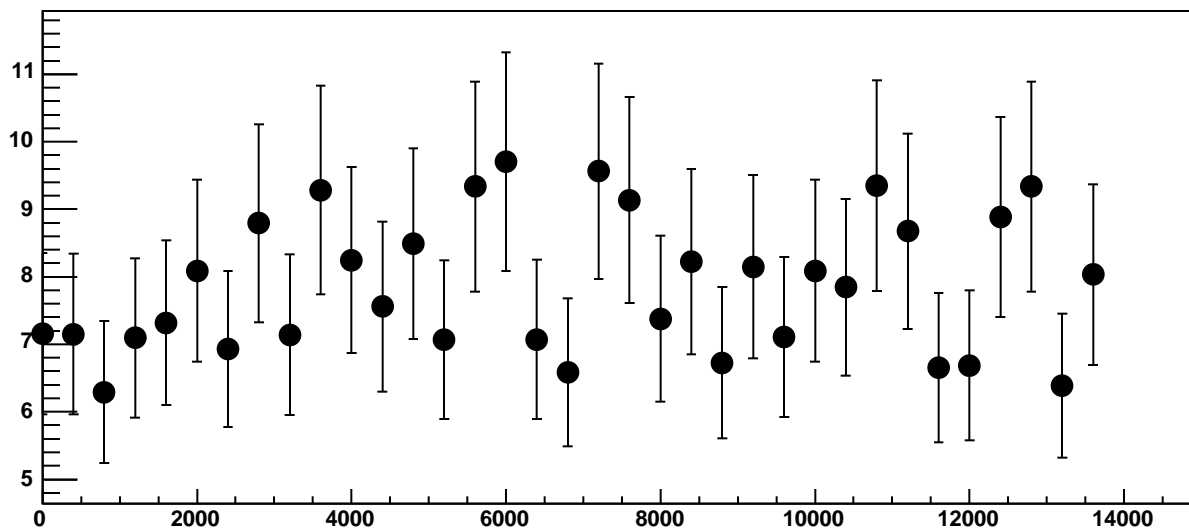
Chip 3, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC



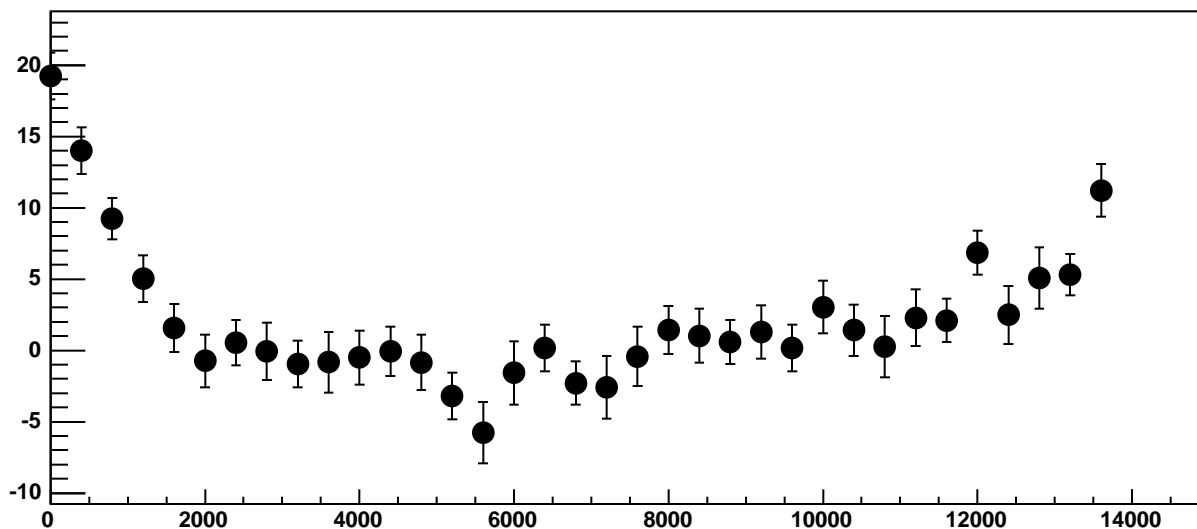
Chip 3, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



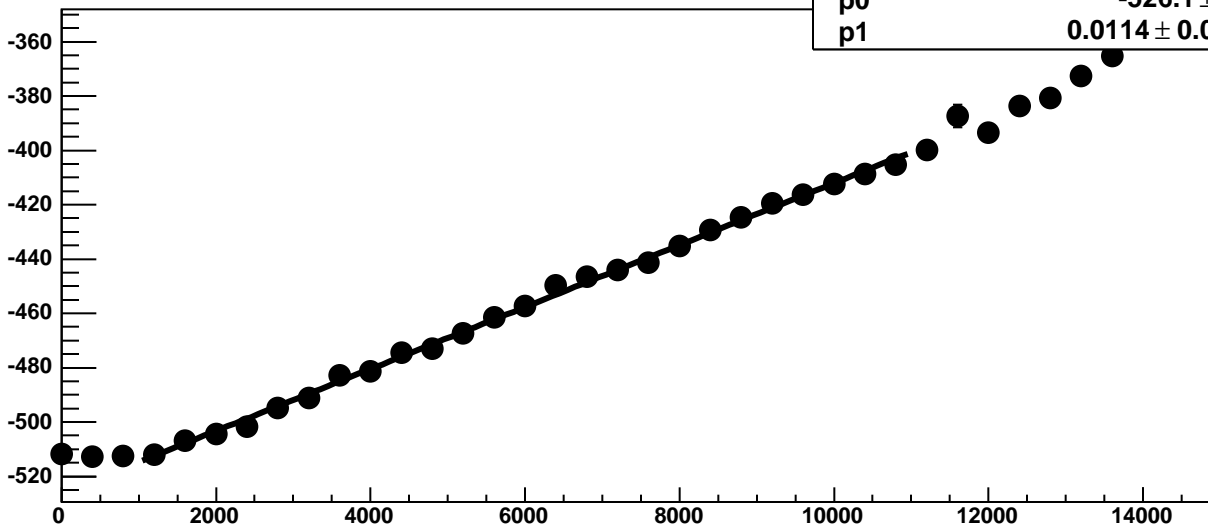
Chip 3, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



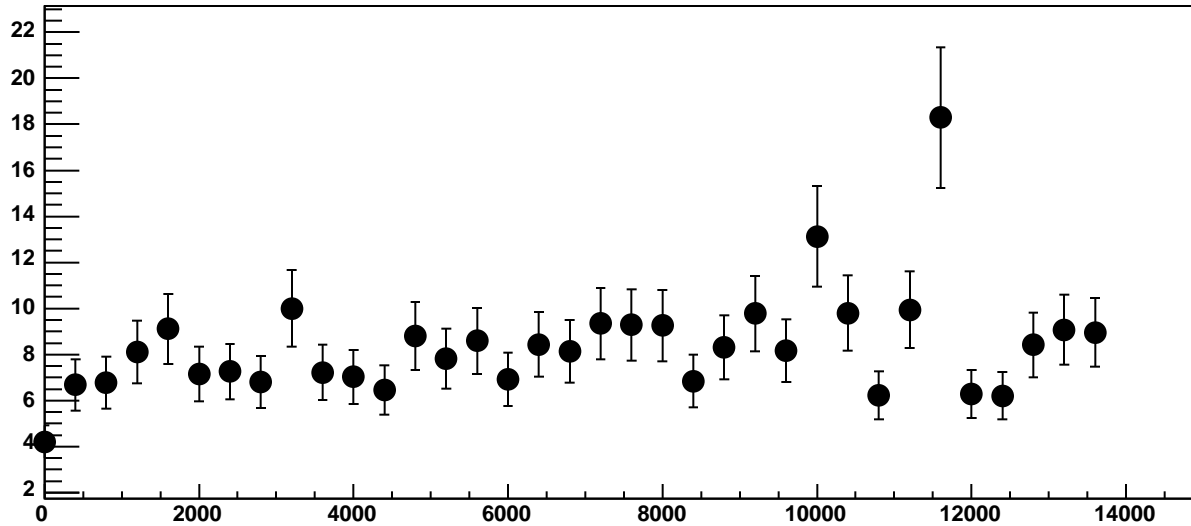
Chip 3, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



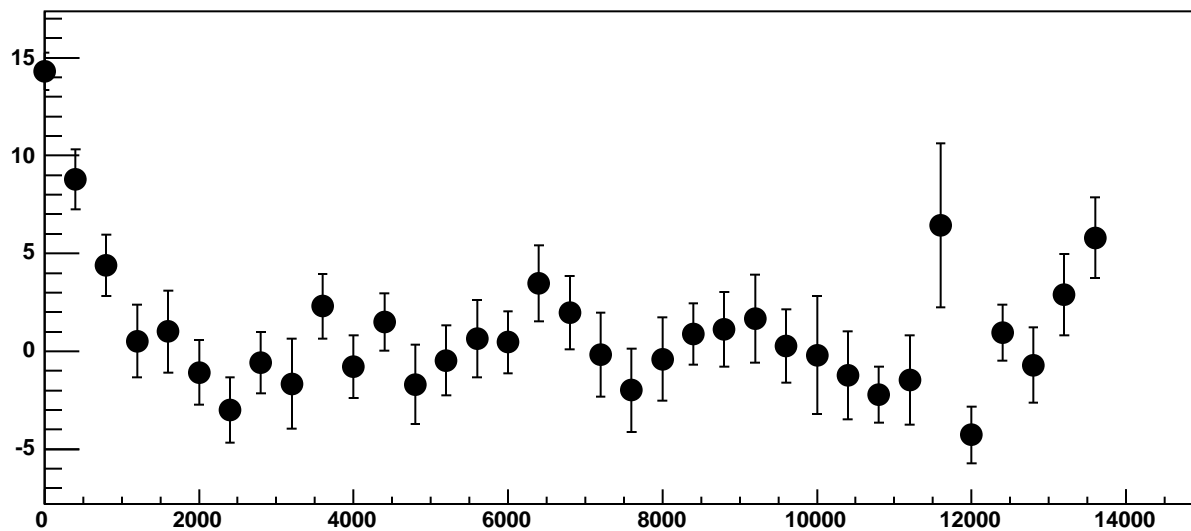
Chip 3, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



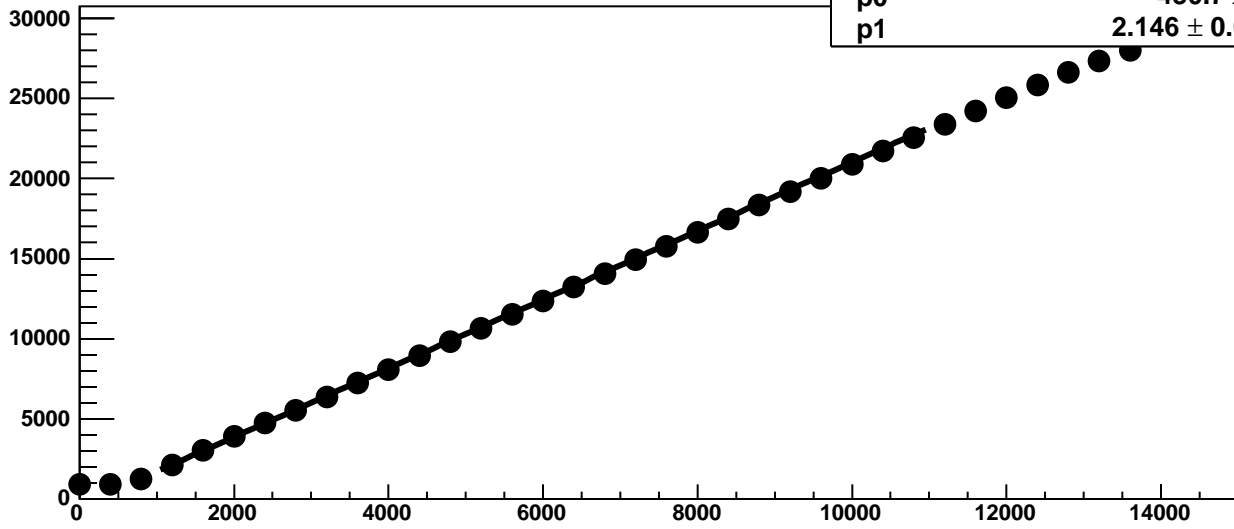
Chip 3, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



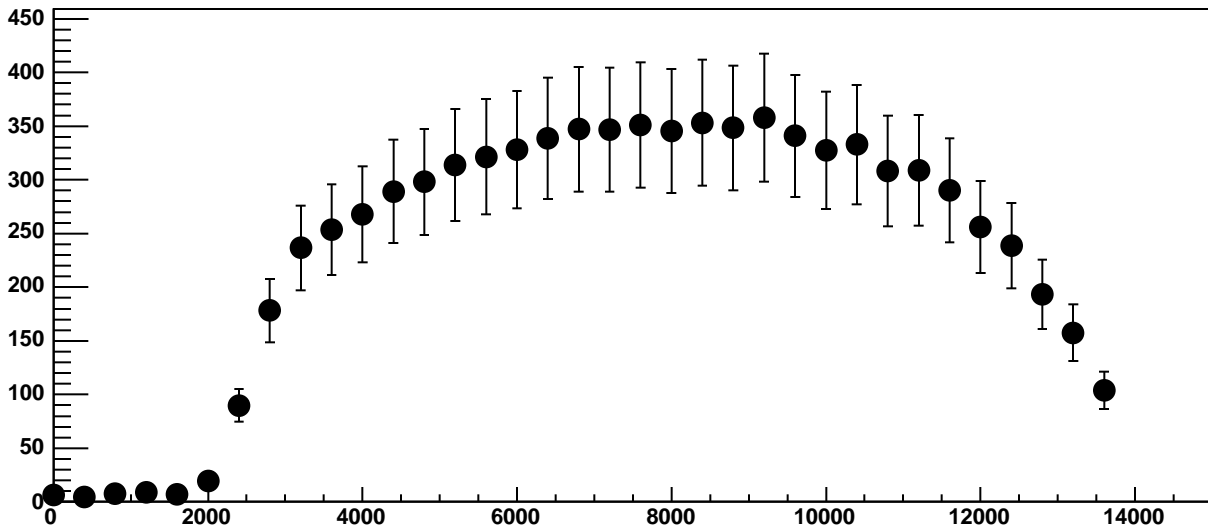
Chip 3, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



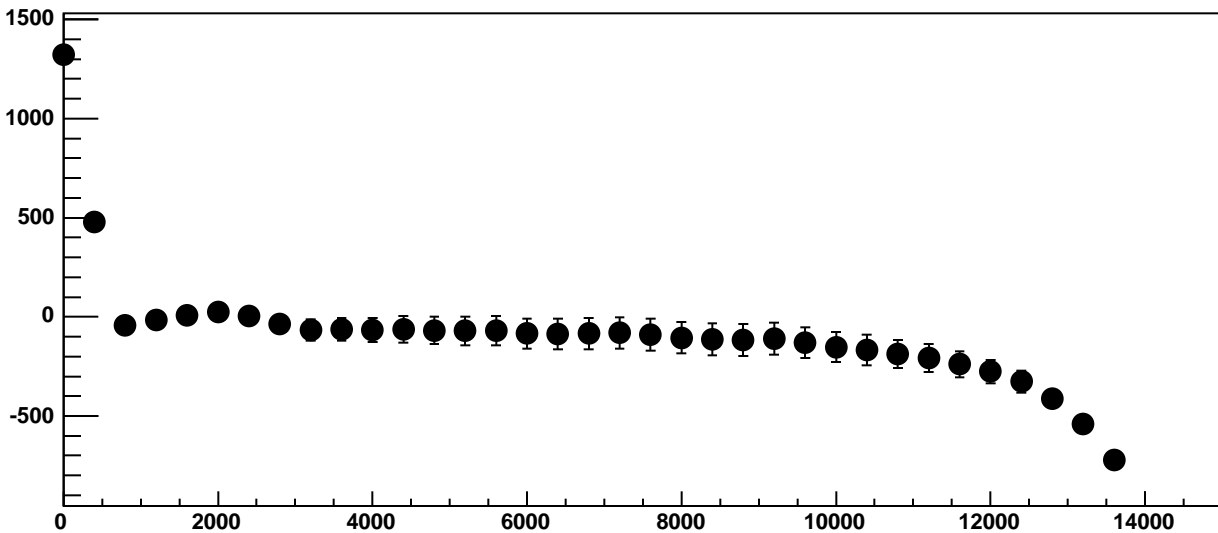
Chip 3, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC



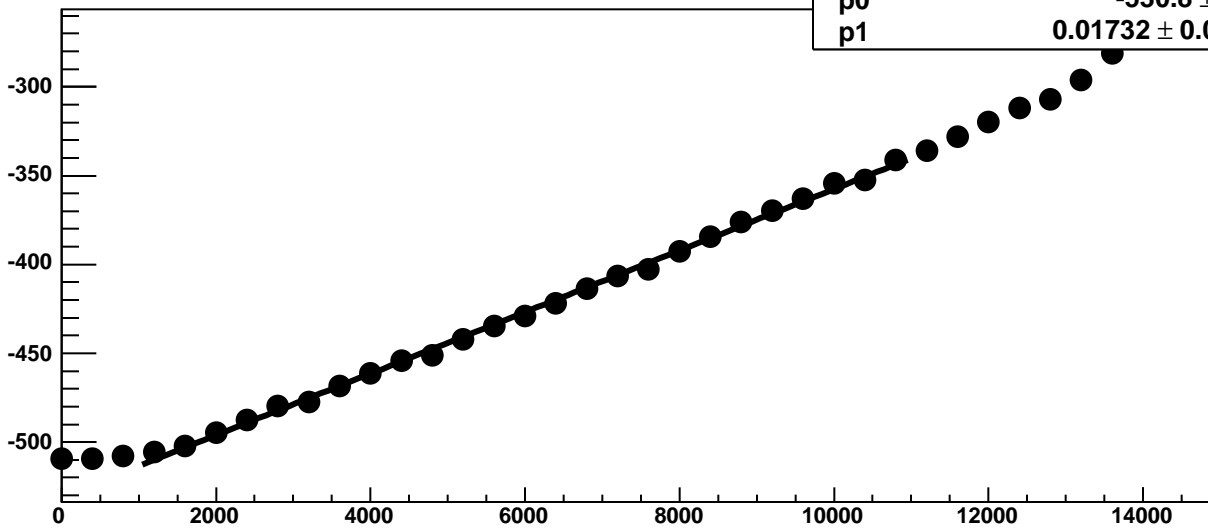
Chip 3, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC

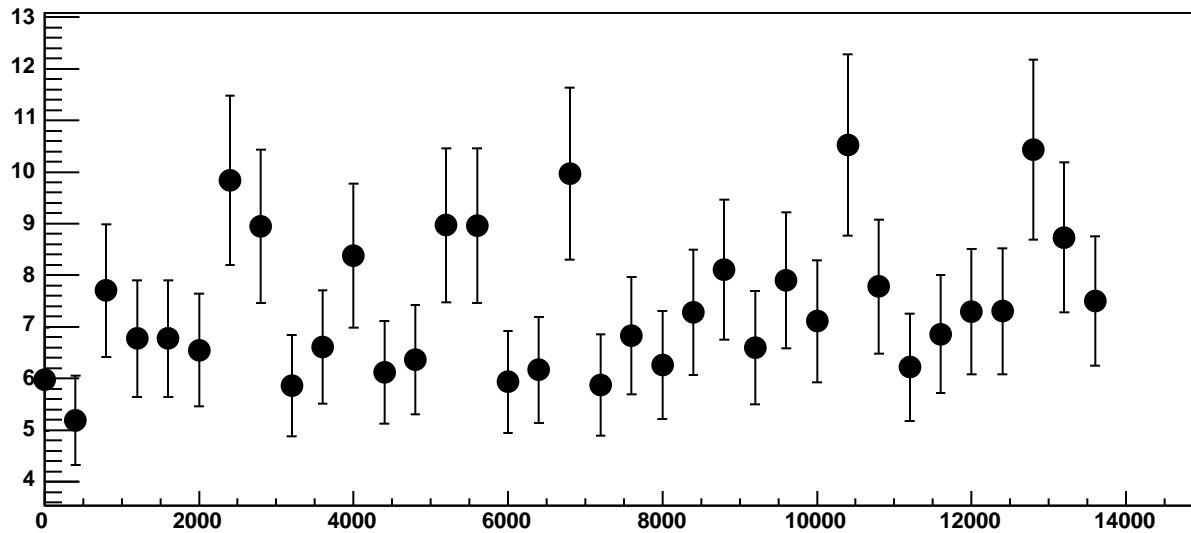


Chip 3, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

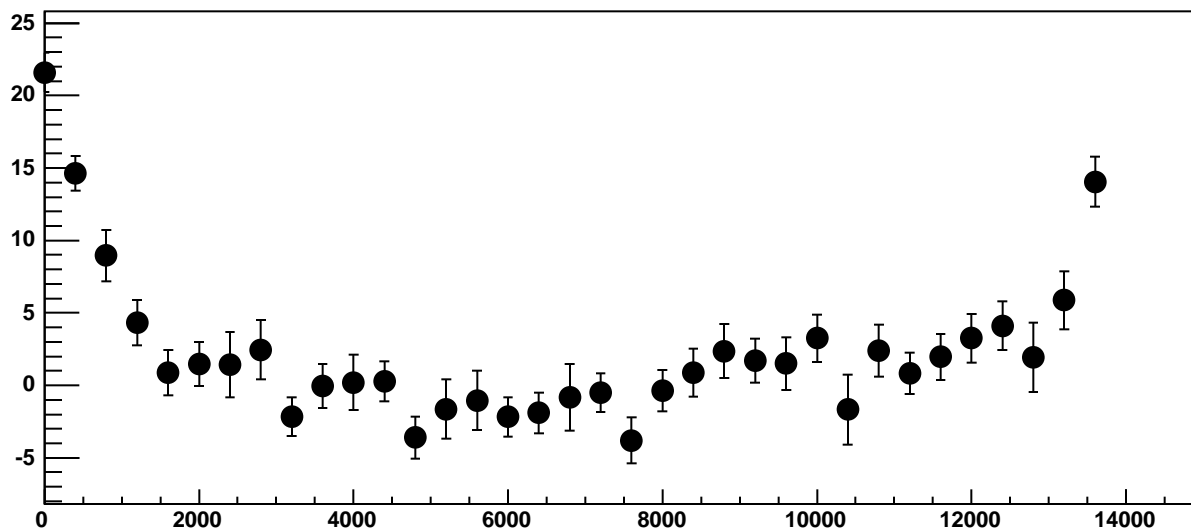


χ^2 / ndf 41.22 / 23
p0 -530.8 ± 0.7603
p1 0.01732 ± 0.0001172

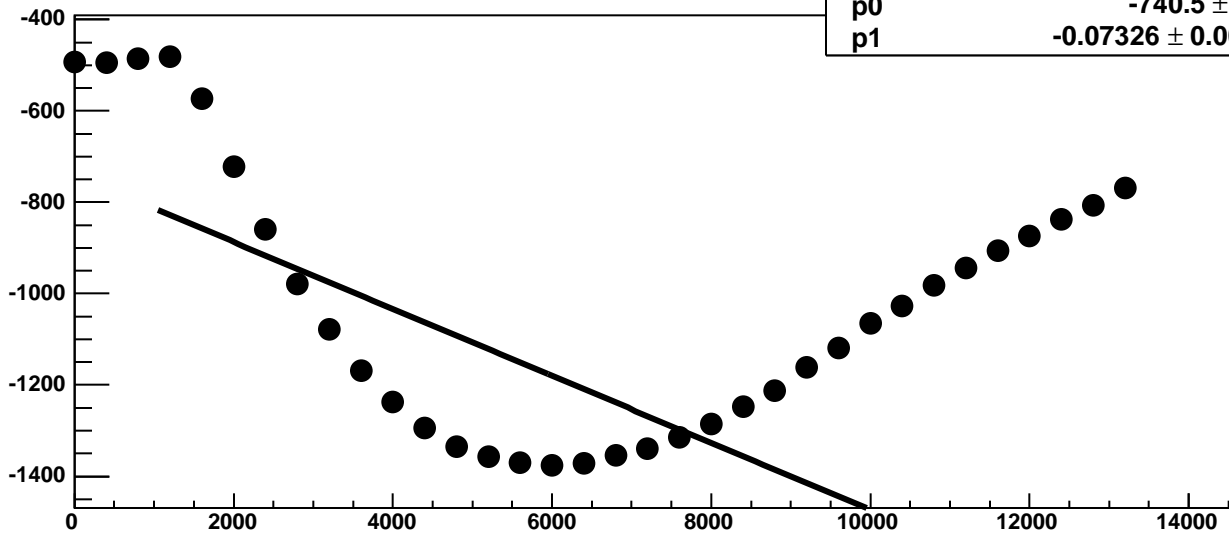
Chip 3, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

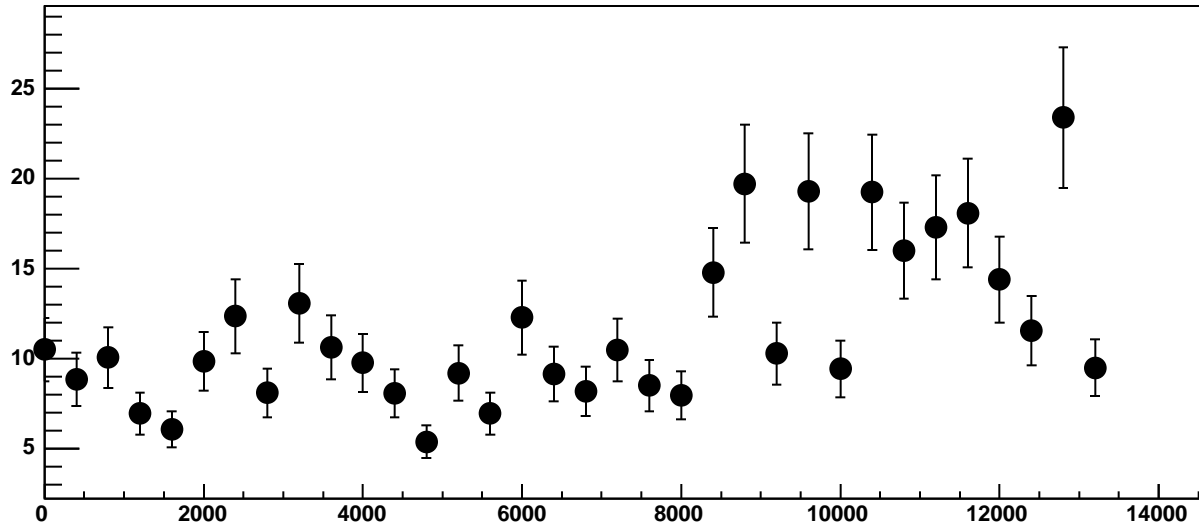


Chip 3, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

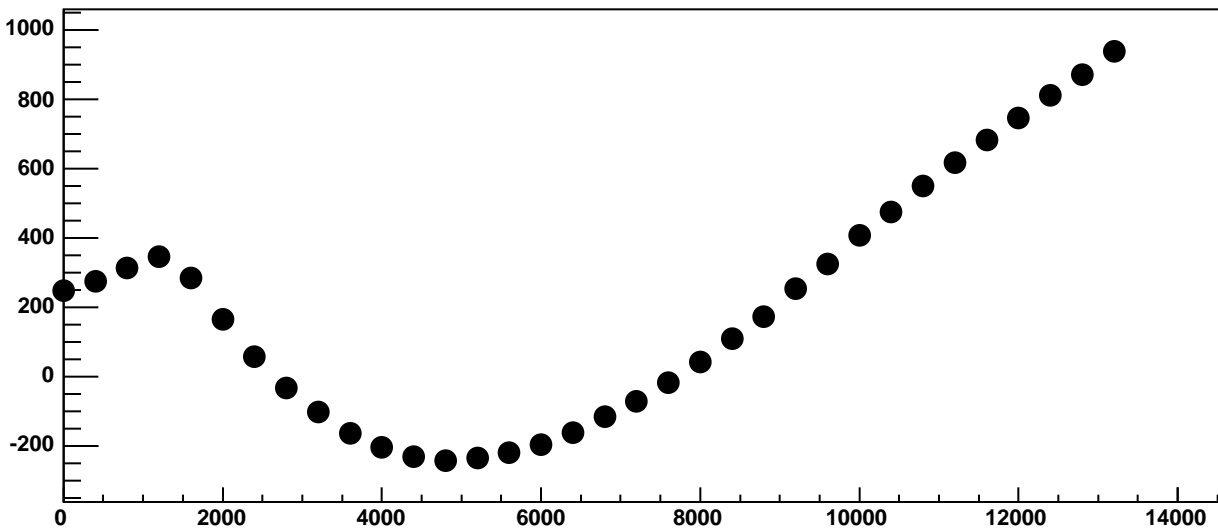


χ^2 / ndf 2.998e+05 / 23
p0 -740.5 ± 0.9242
p1 -0.07326 ± 0.0001605

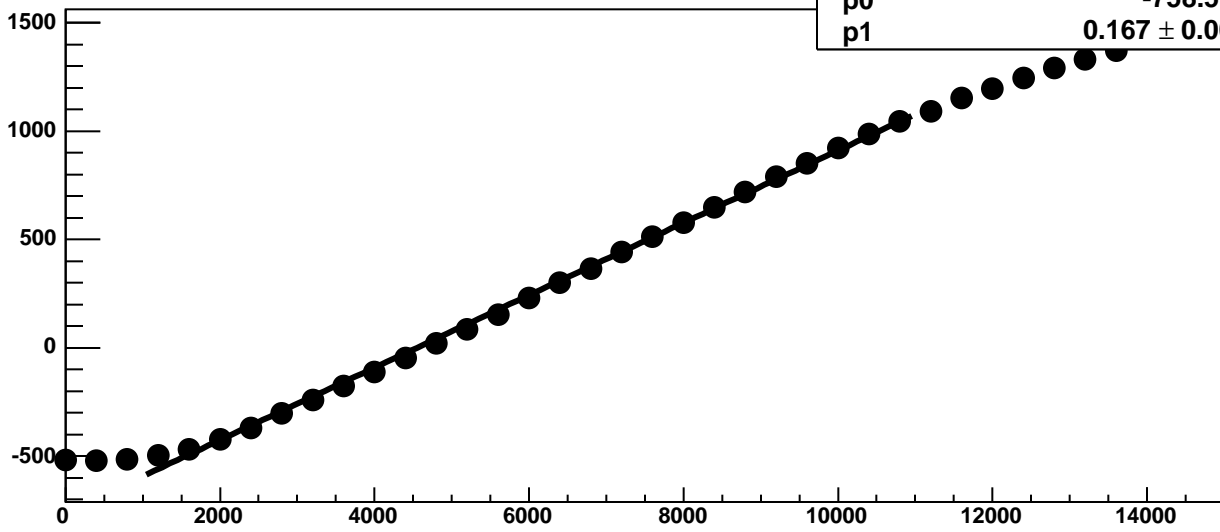
Chip 3, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



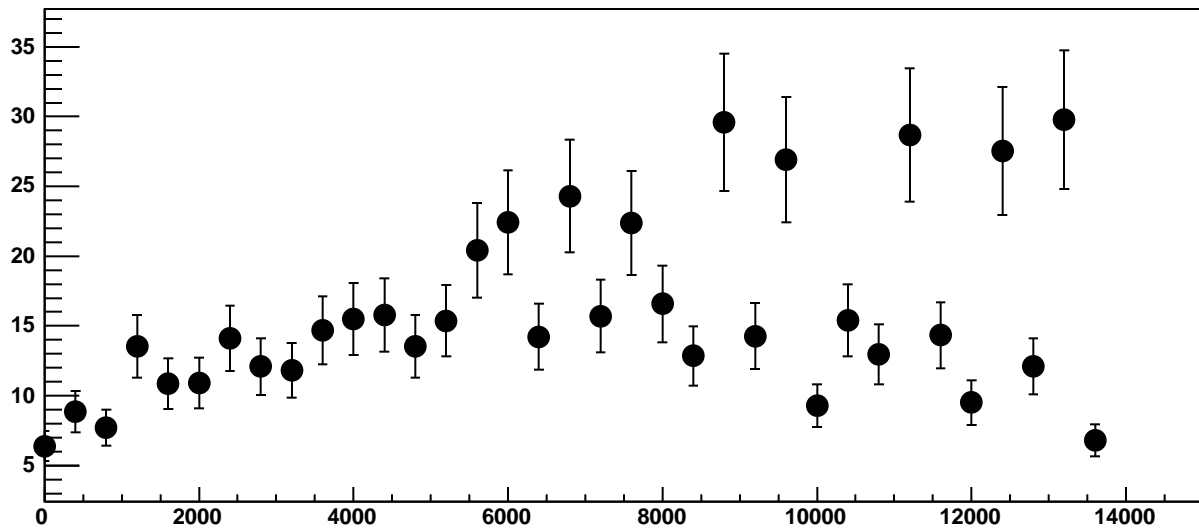
Chip 3, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



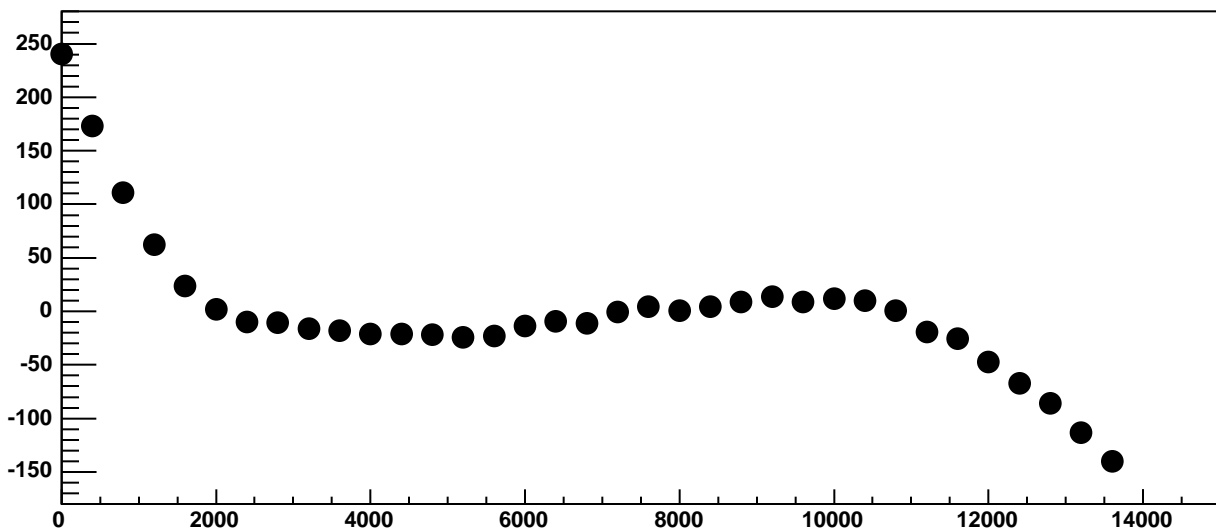
Chip 3, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



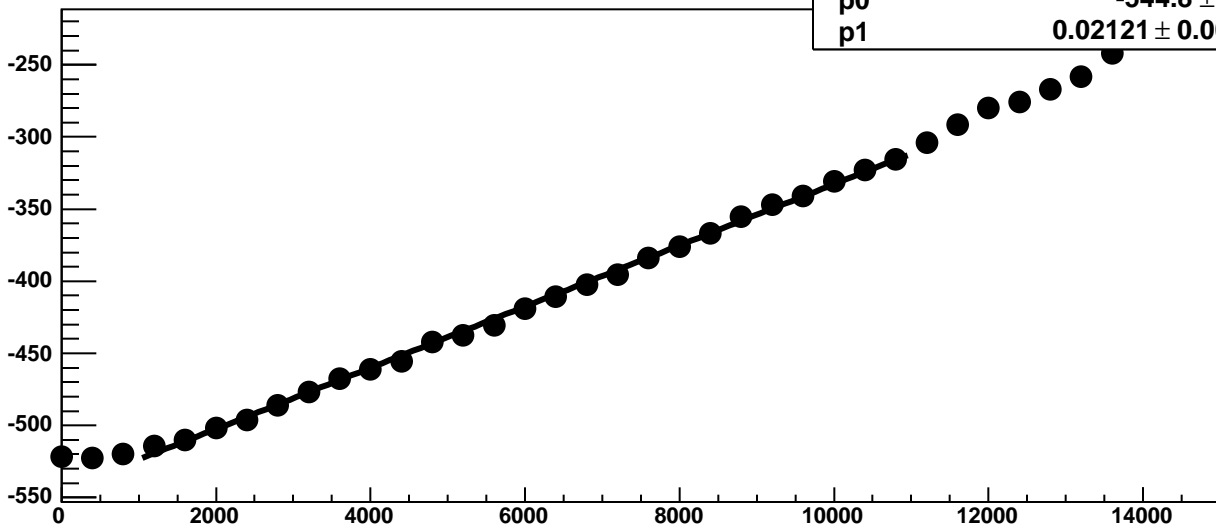
Chip 3, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



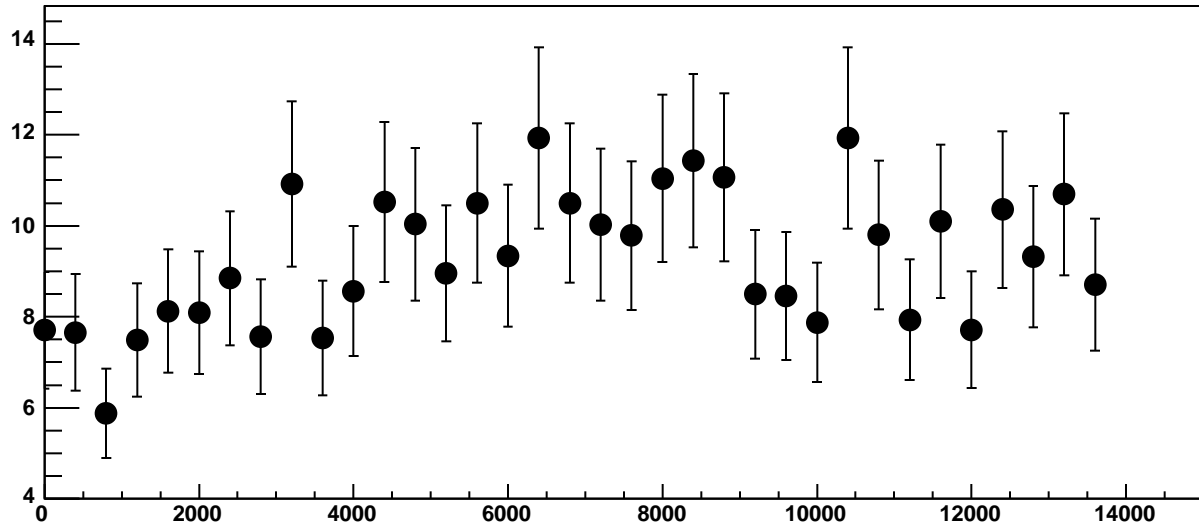
Chip 3, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



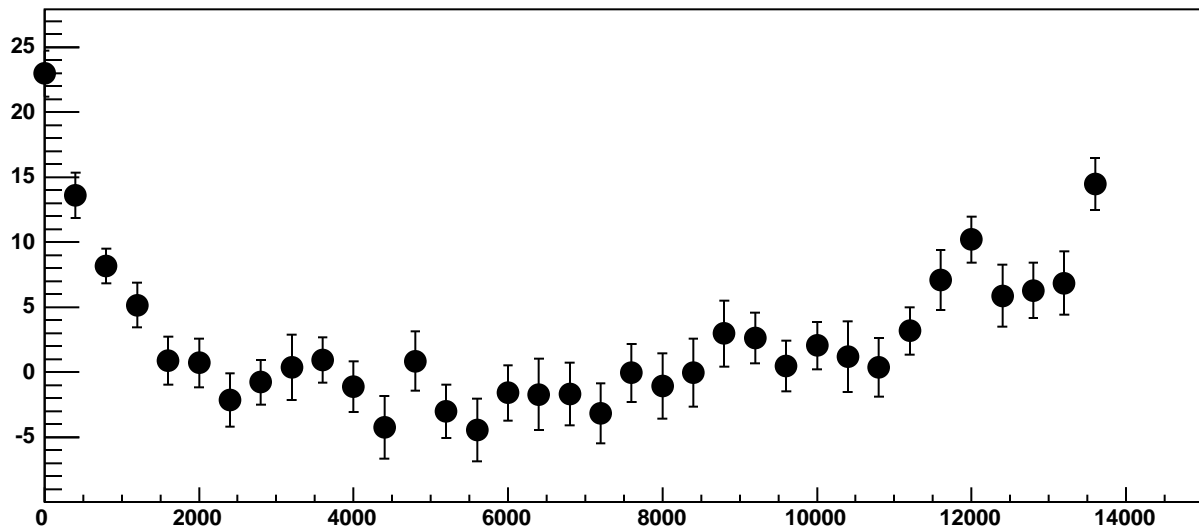
Chip 3, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC



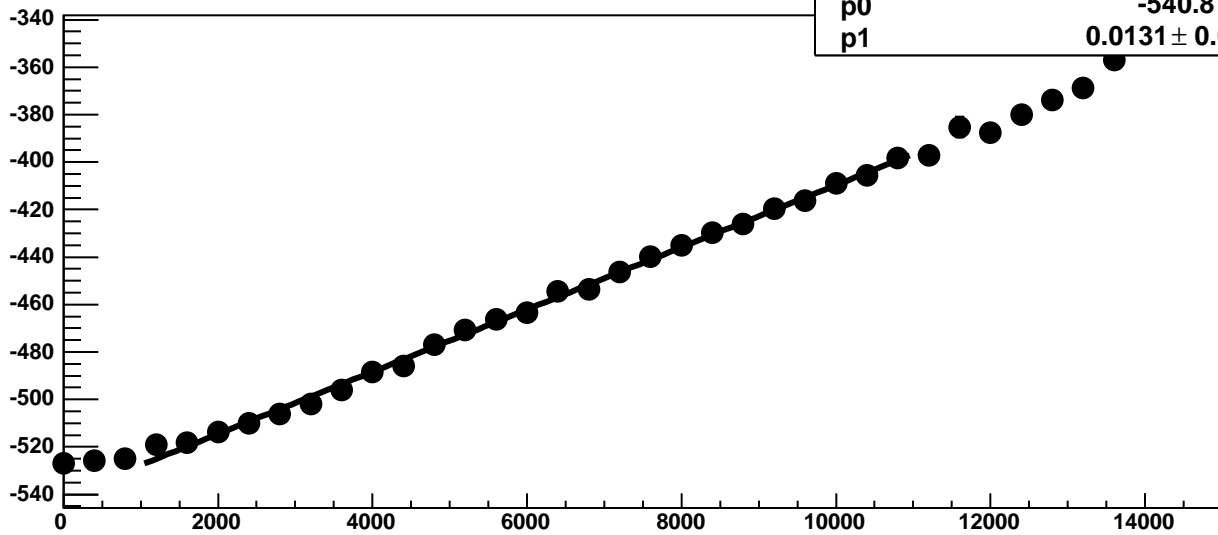
Chip 3, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



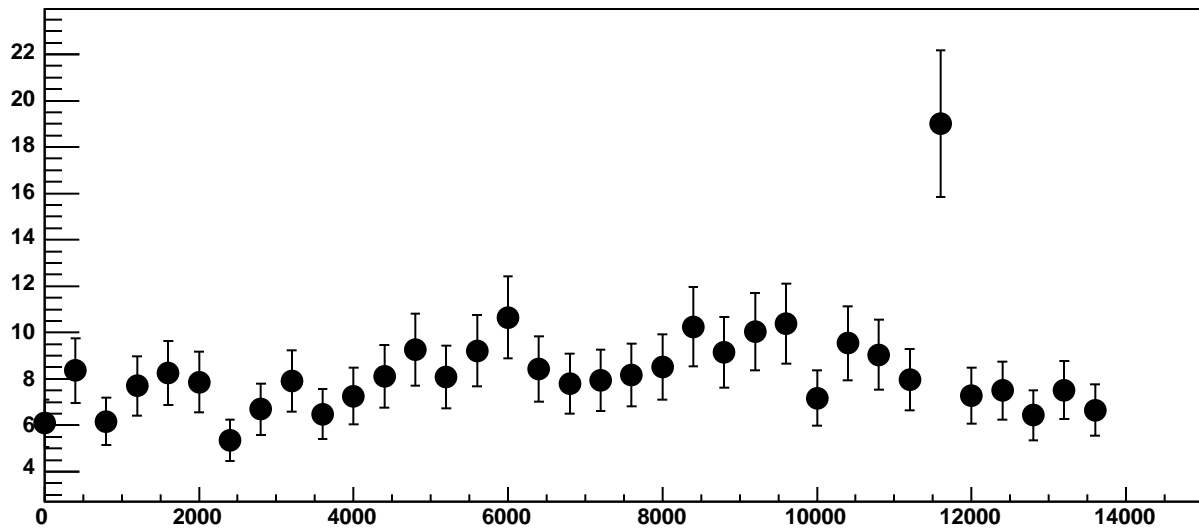
Chip 3, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC



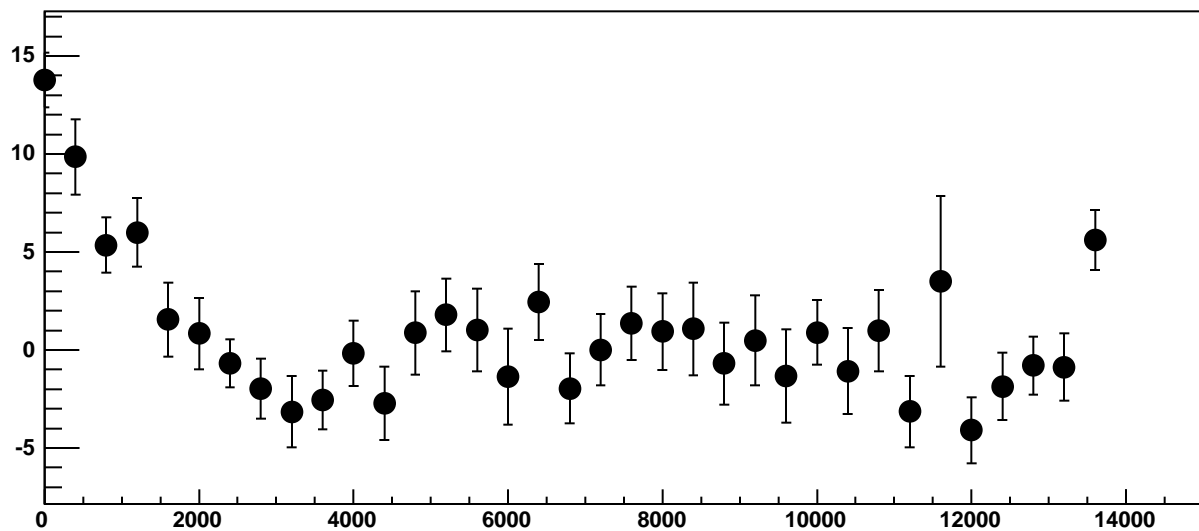
Chip 3, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



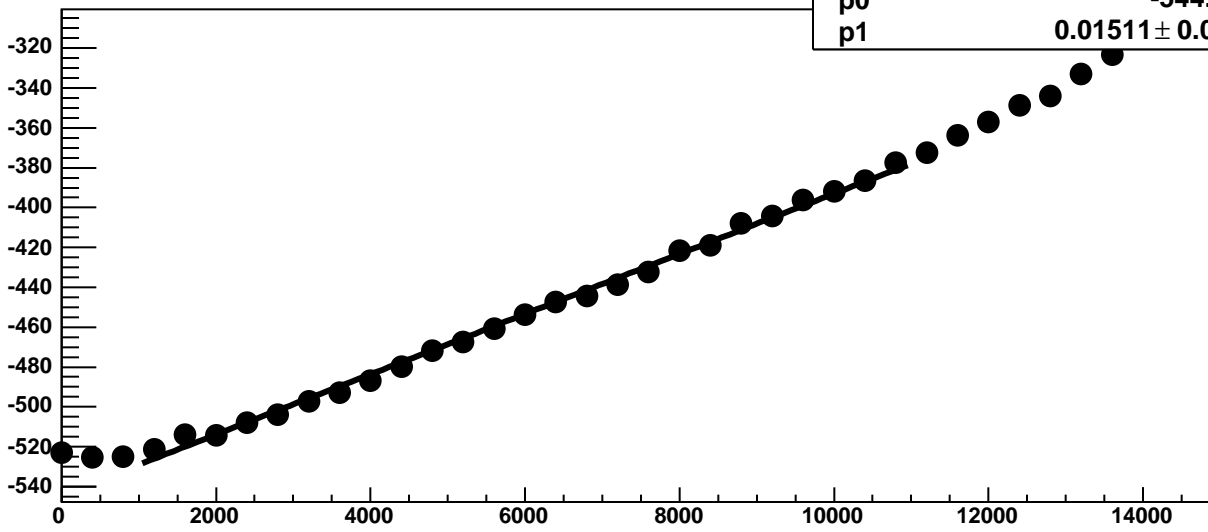
Chip 3, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

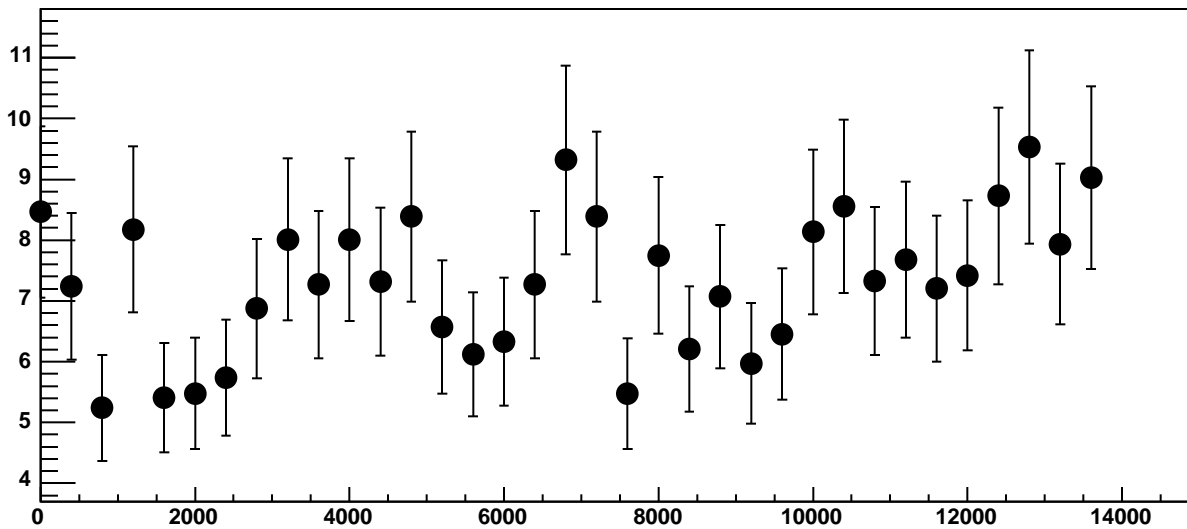


Chip 3, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

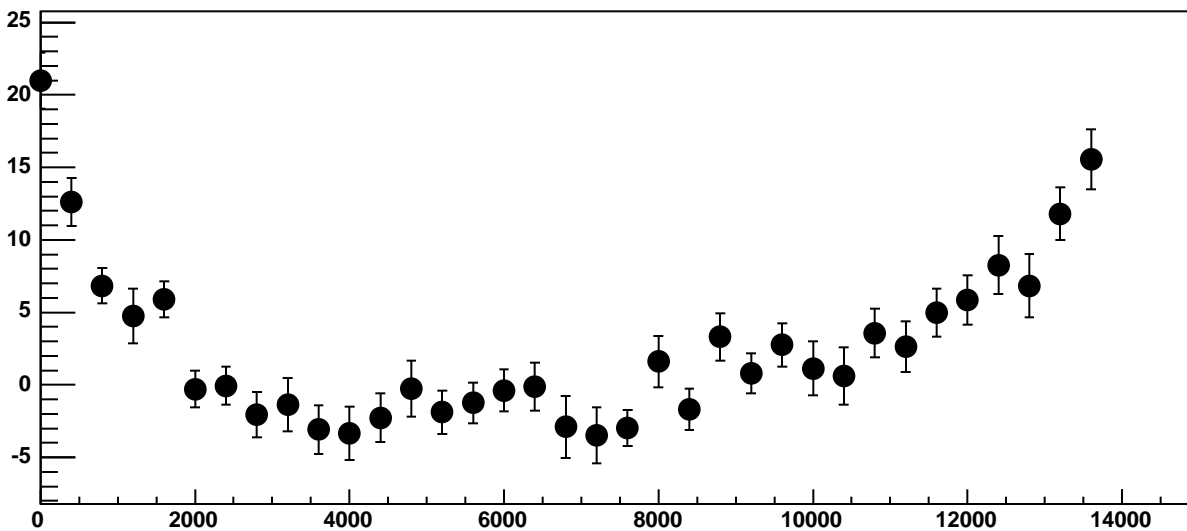


χ^2 / ndf 68.37 / 23
p0 -544.1 ± 0.7
p1 0.01511 ± 0.0001077

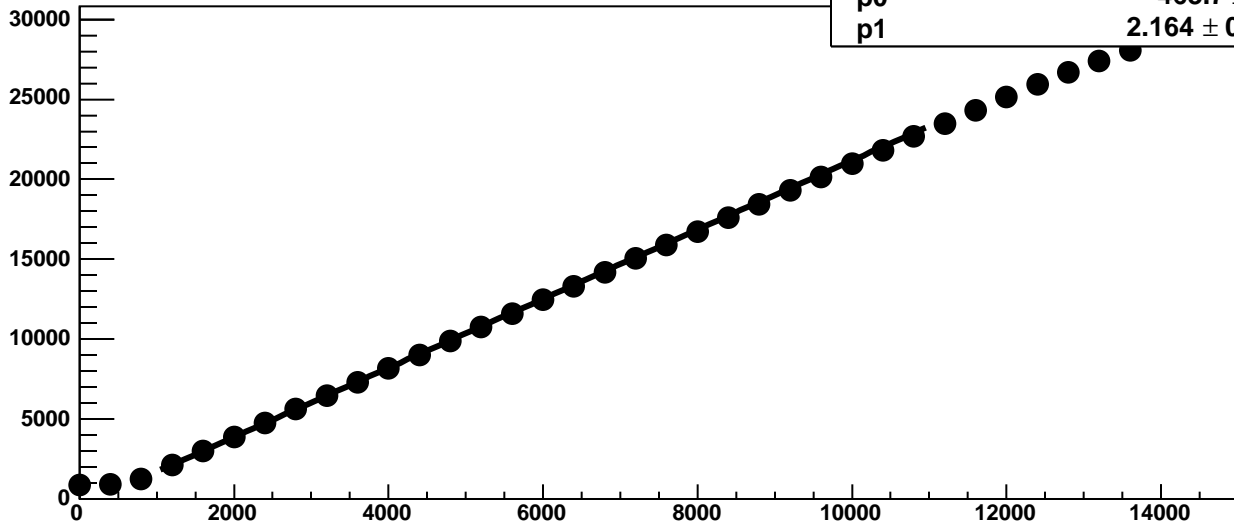
Chip 3, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



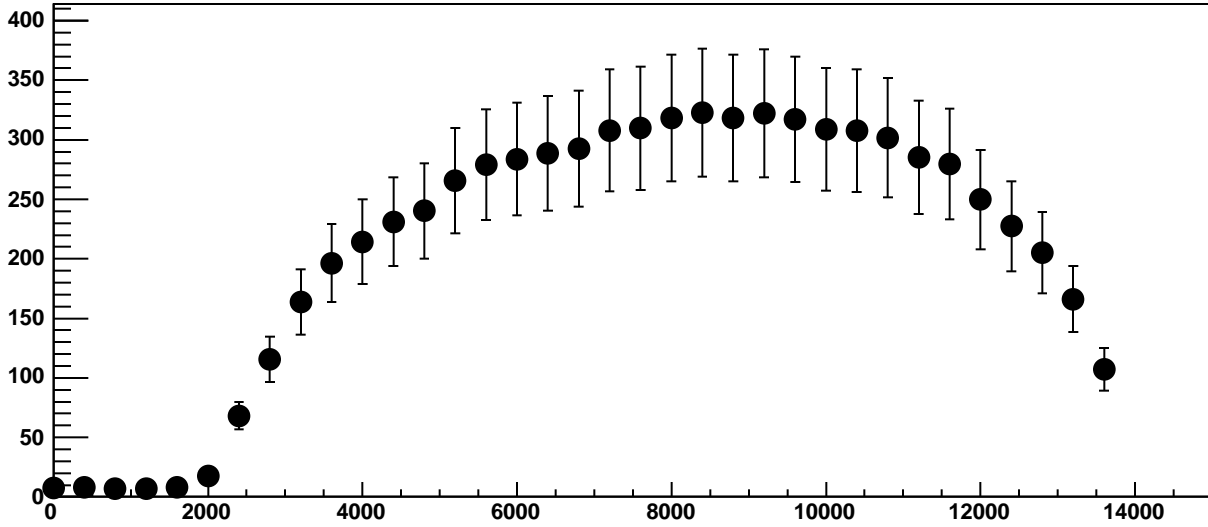
Chip 3, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



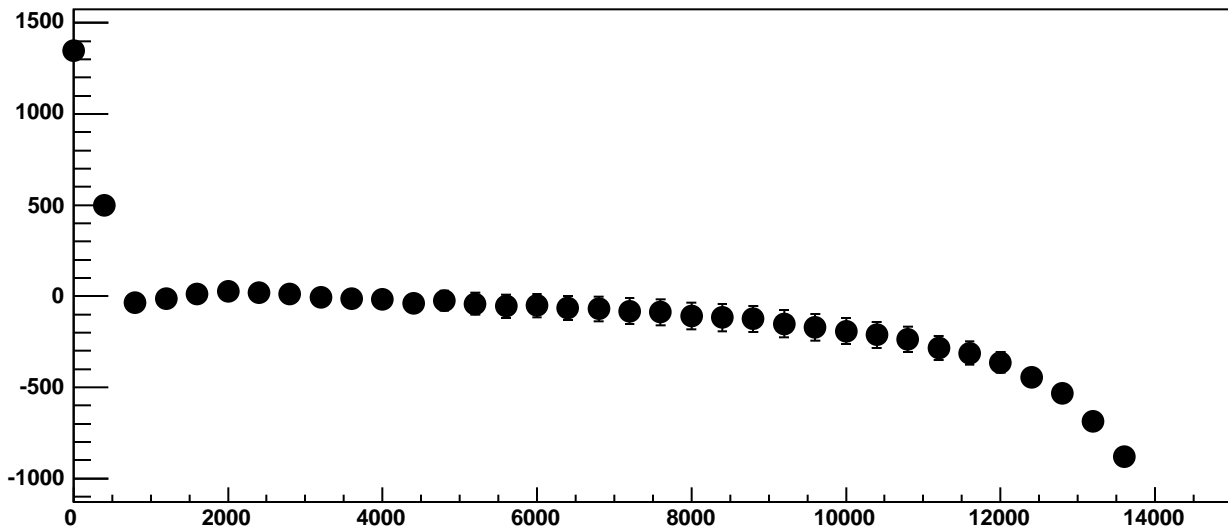
Chip 3, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC



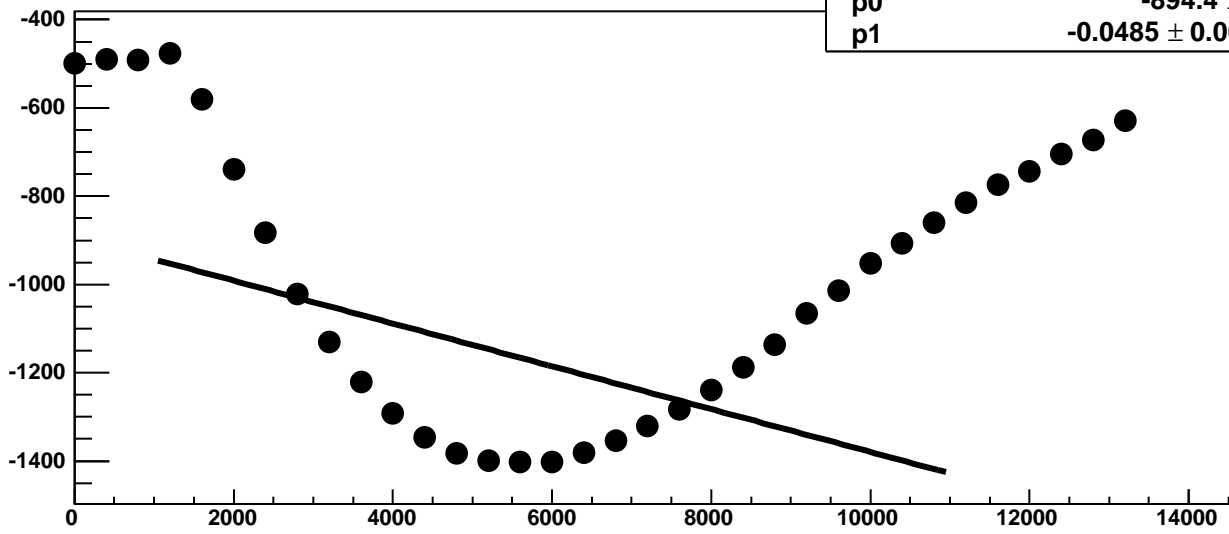
Chip 3, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC

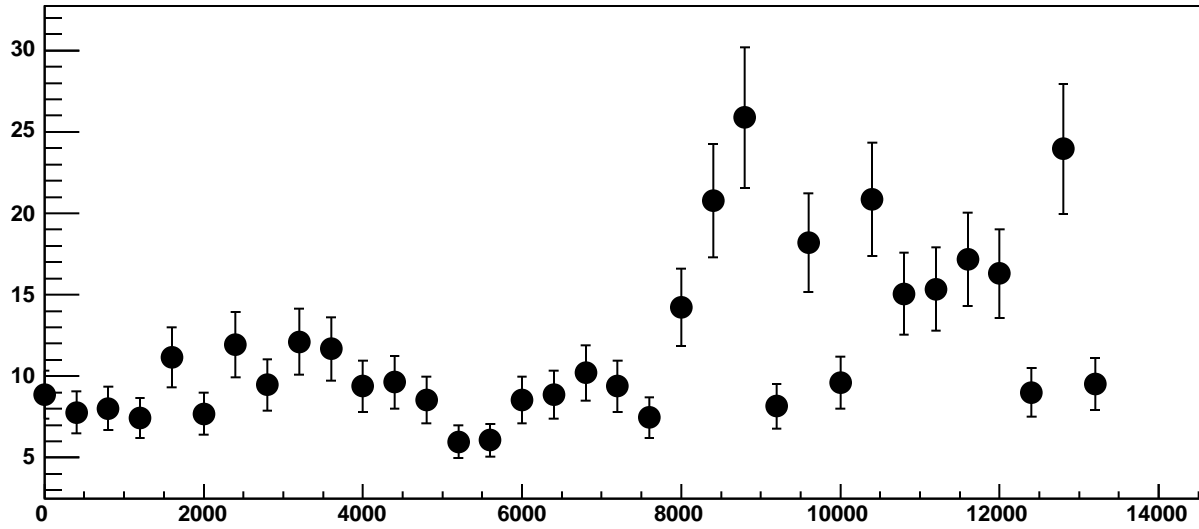


Chip 3, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

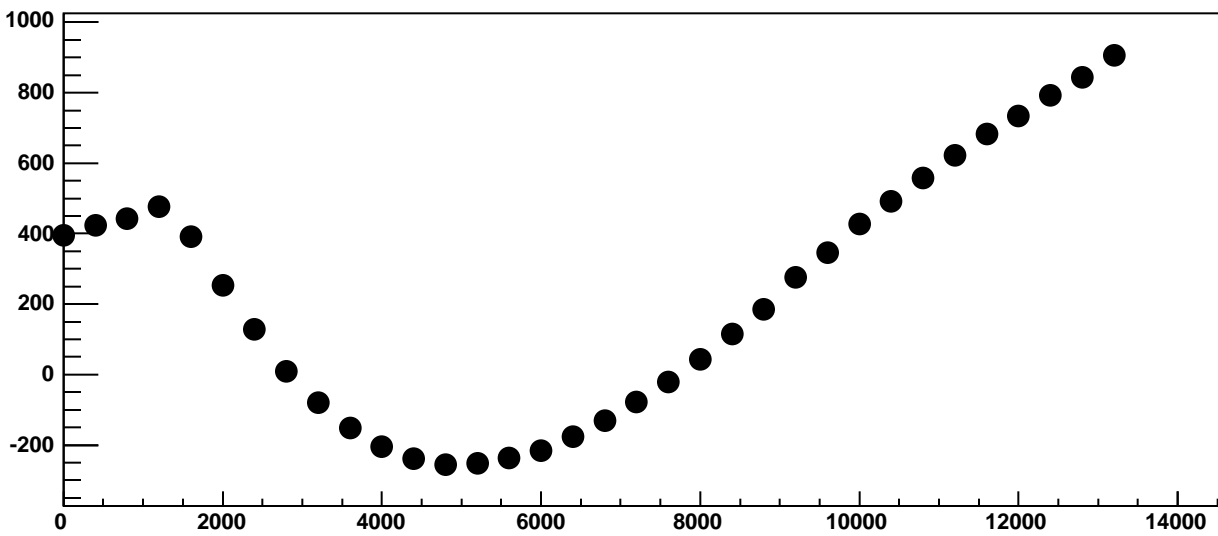


χ^2 / ndf 3.575e+05 / 23
p0 -894.4 ± 1.023
p1 -0.0485 ± 0.0001717

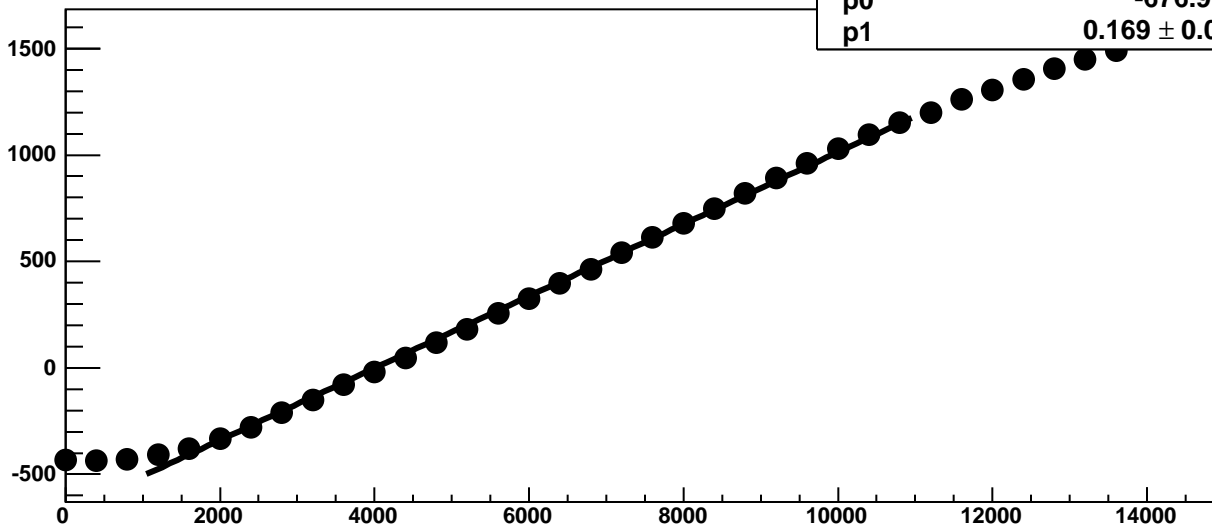
Chip 3, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

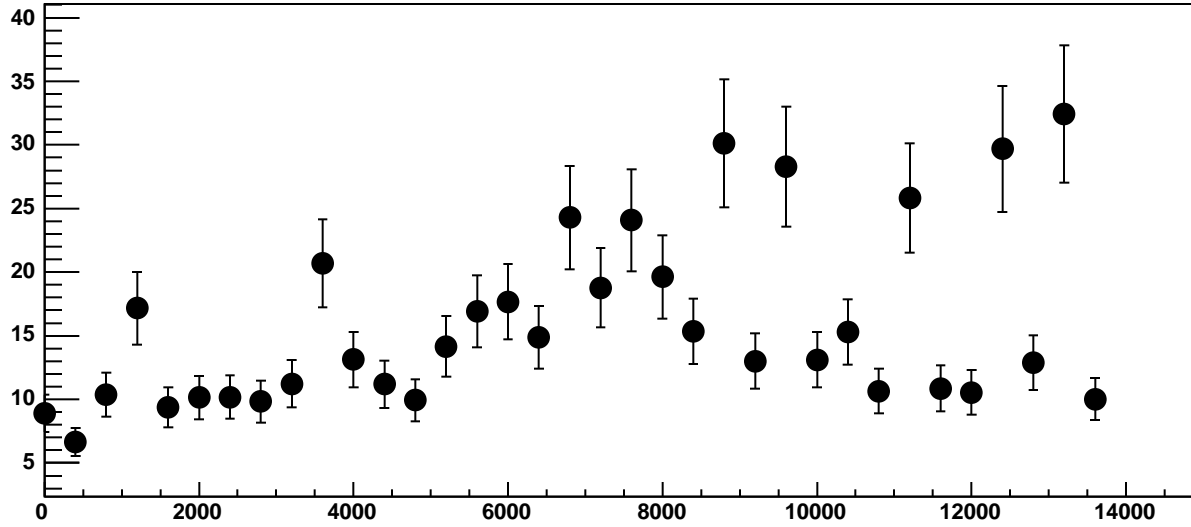


Chip 3, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

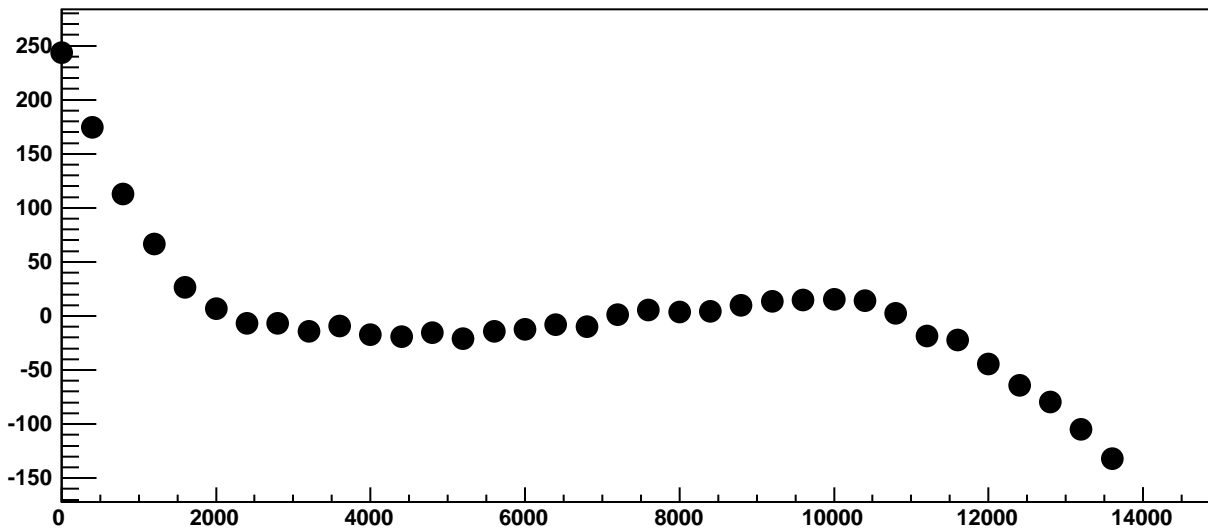


χ^2 / ndf 785.5 / 23
p0 -676.9 ± 1.238
p1 0.169 ± 0.0002054

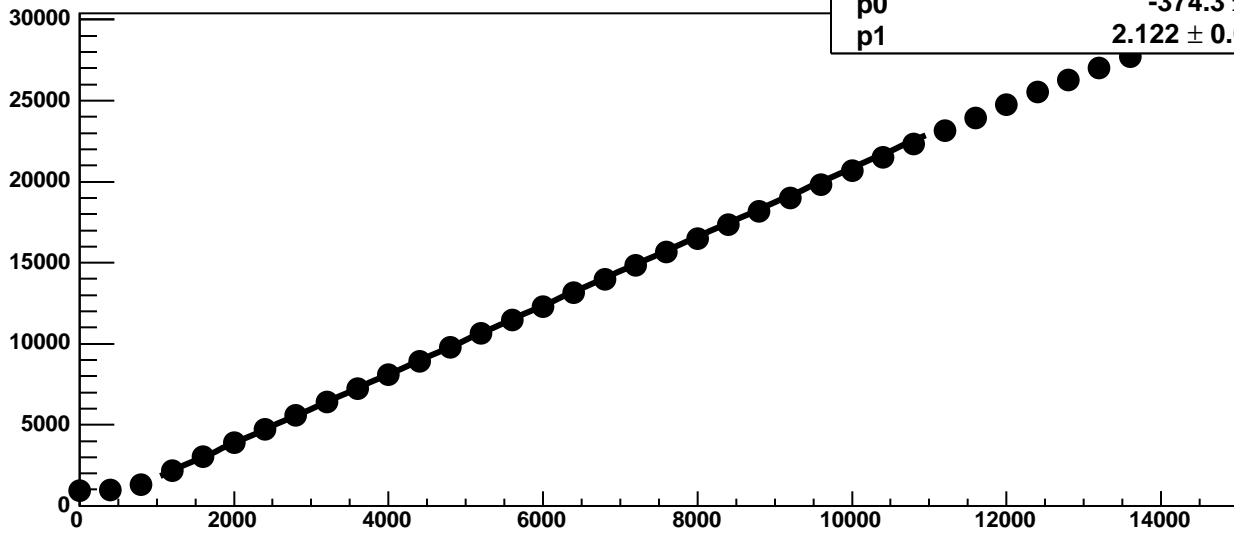
Chip 3, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



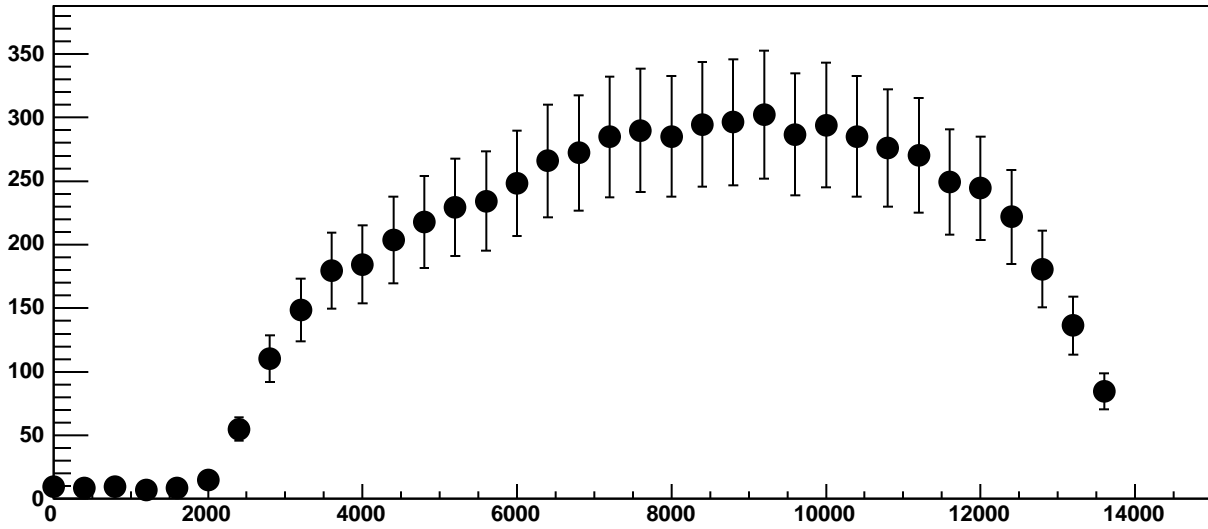
Chip 3, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



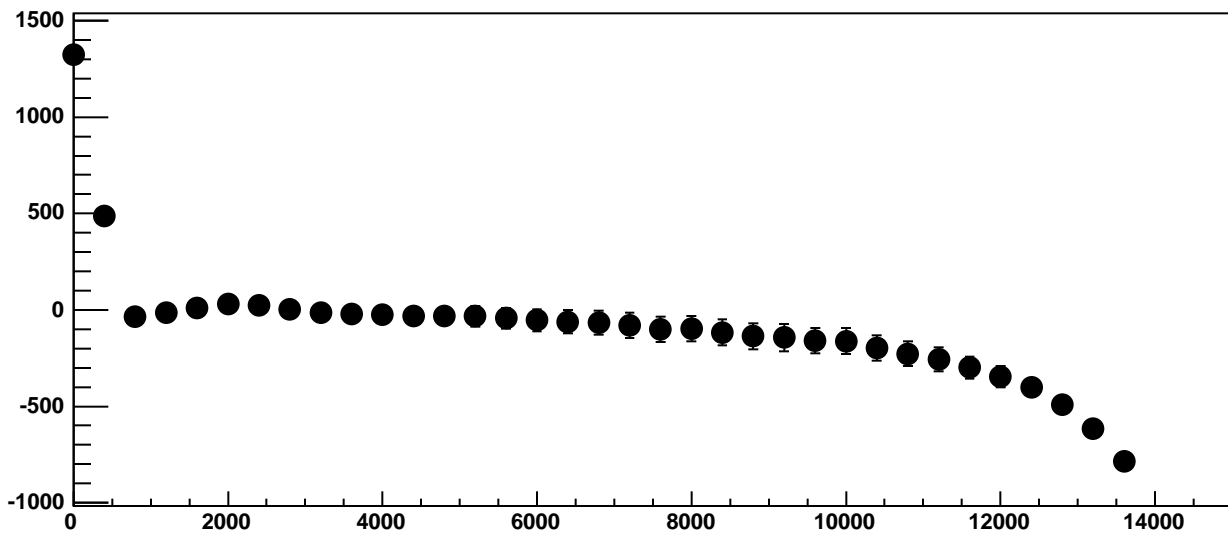
Chip 3, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC



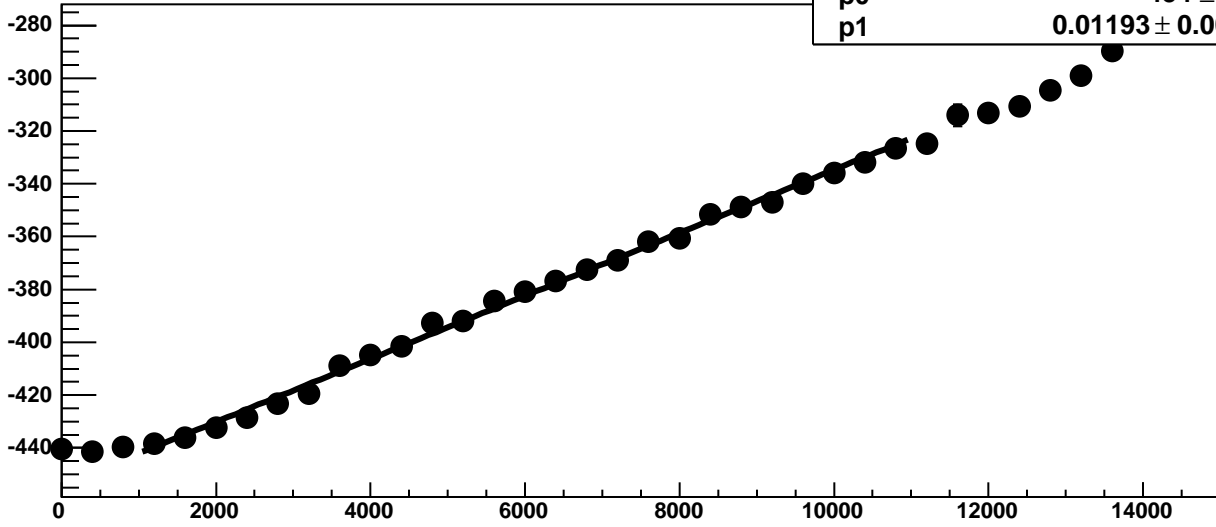
Chip 3, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



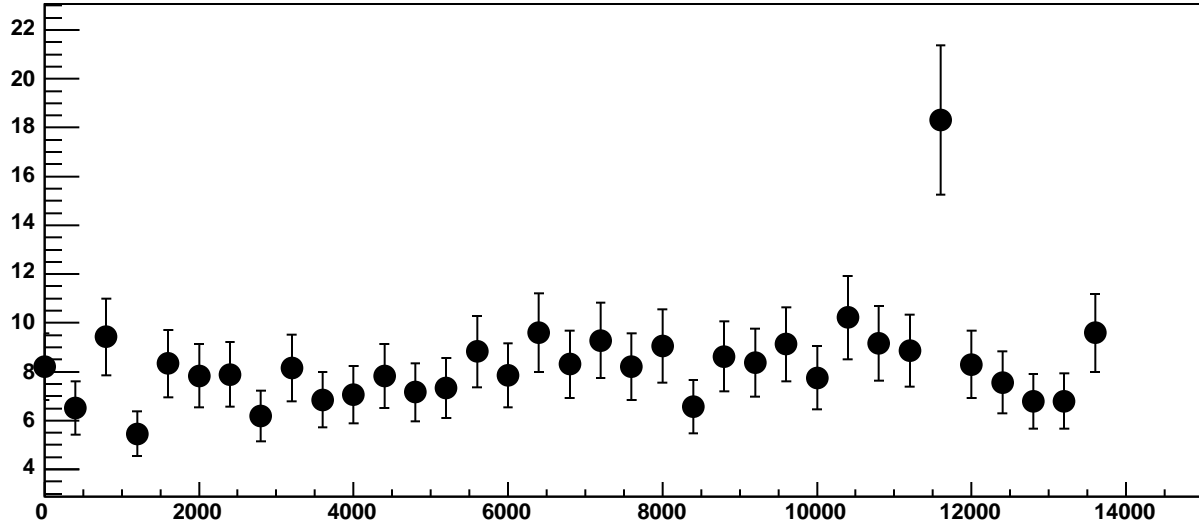
Chip 3, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC



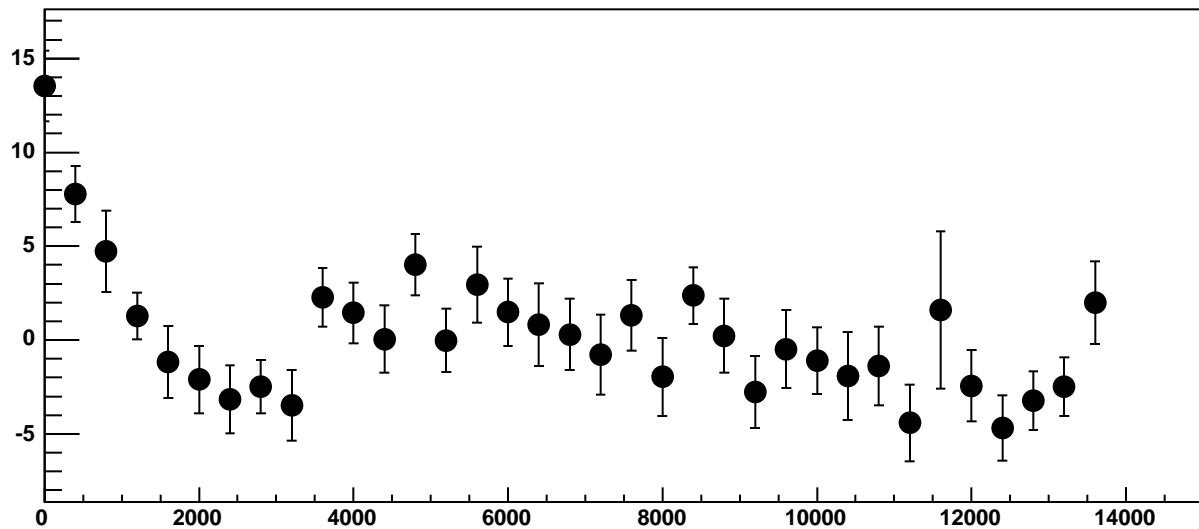
Chip 3, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



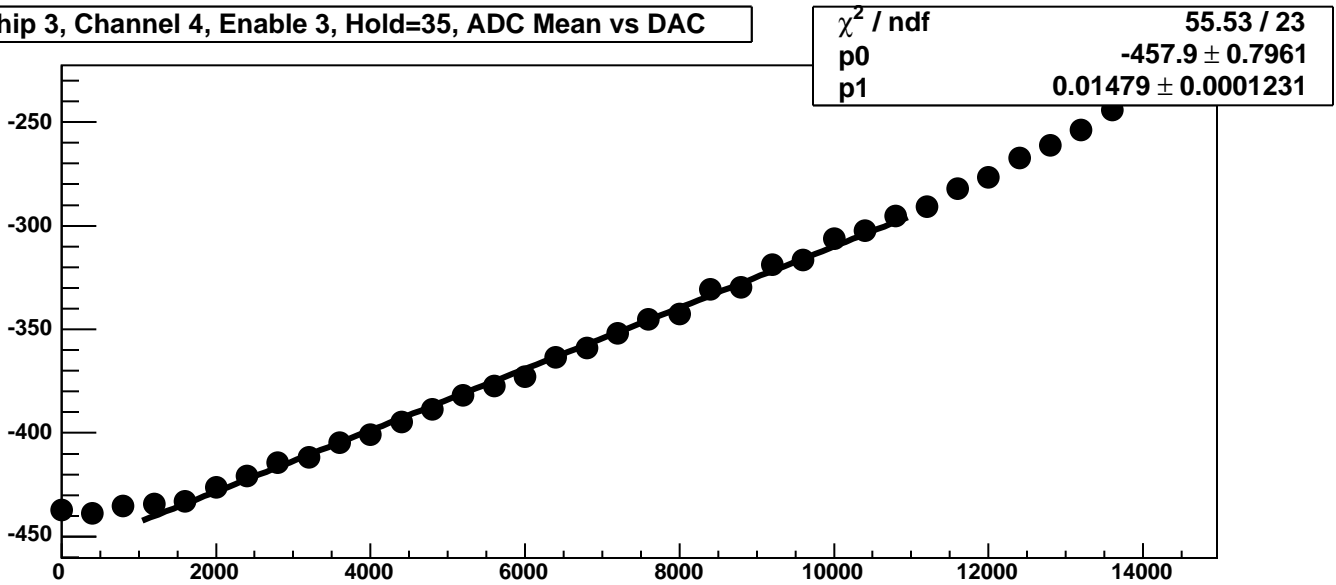
Chip 3, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



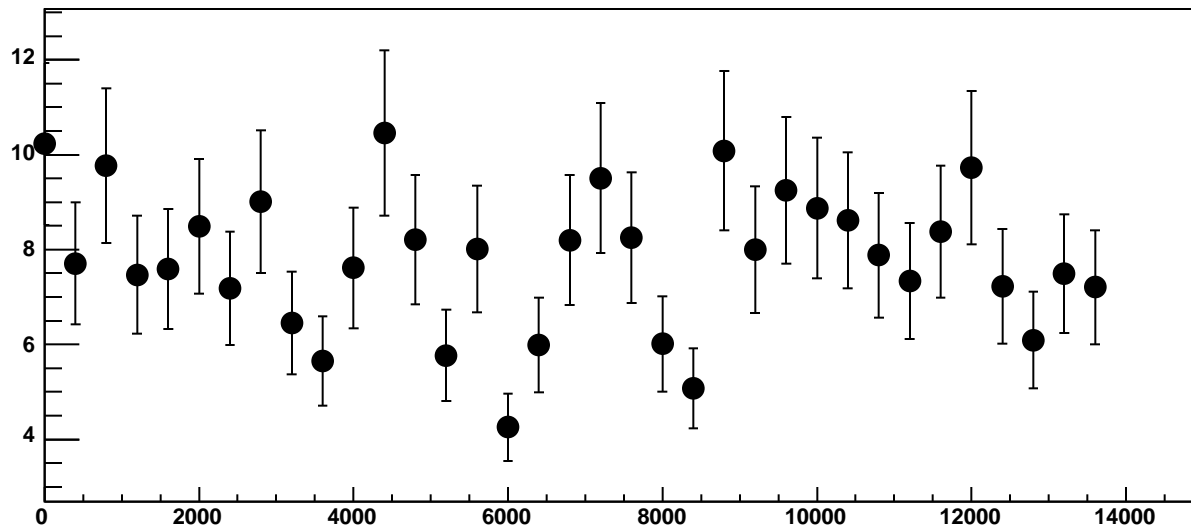
Chip 3, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



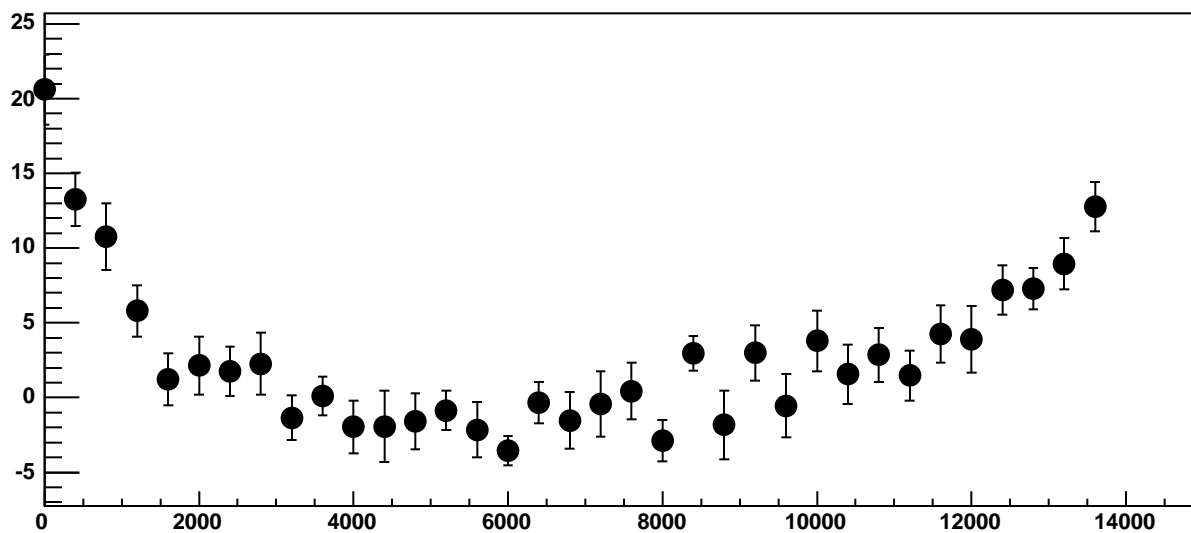
Chip 3, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



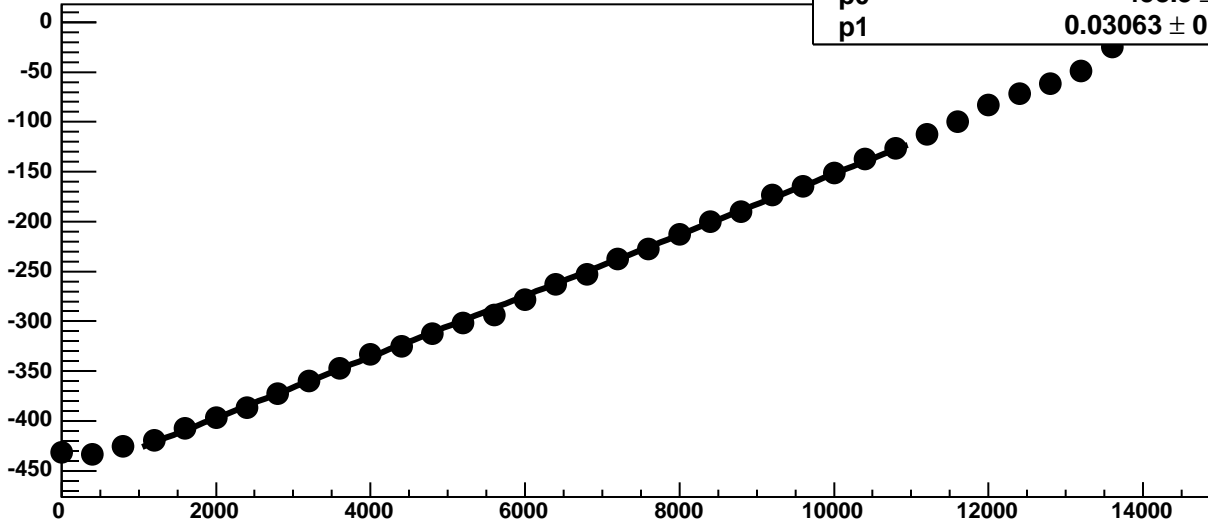
Chip 3, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

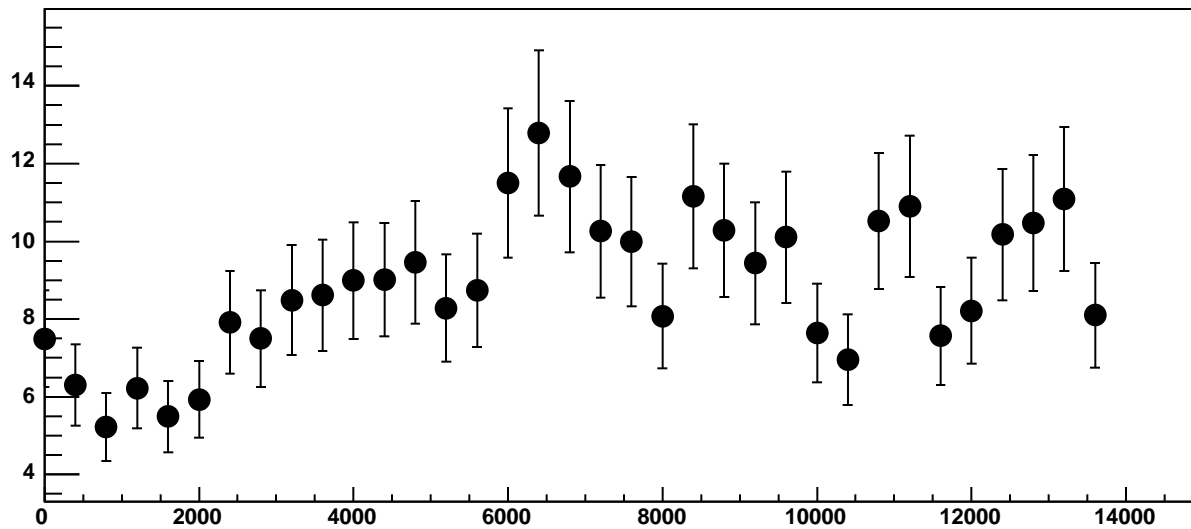


Chip 3, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

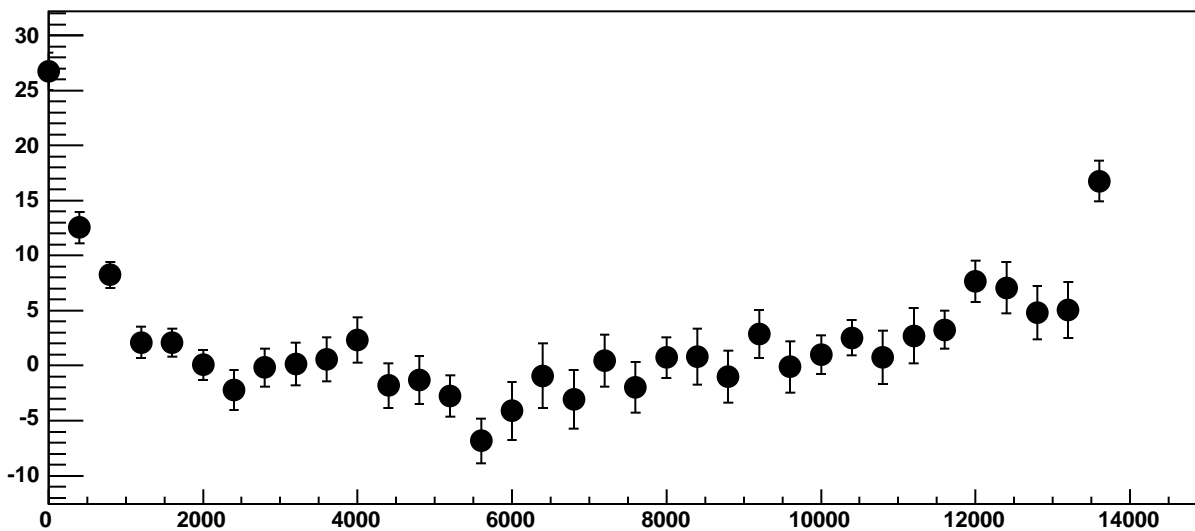


χ^2 / ndf 32.44 / 23
p0 -458.3 ± 0.7495
p1 0.03063 ± 0.000122

Chip 3, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC

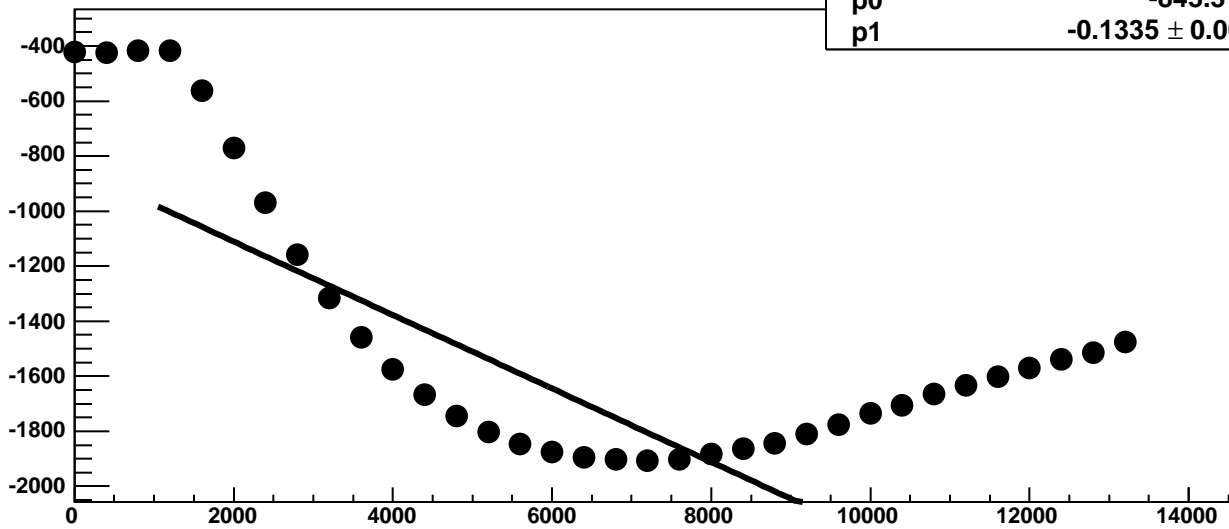


Chip 3, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC

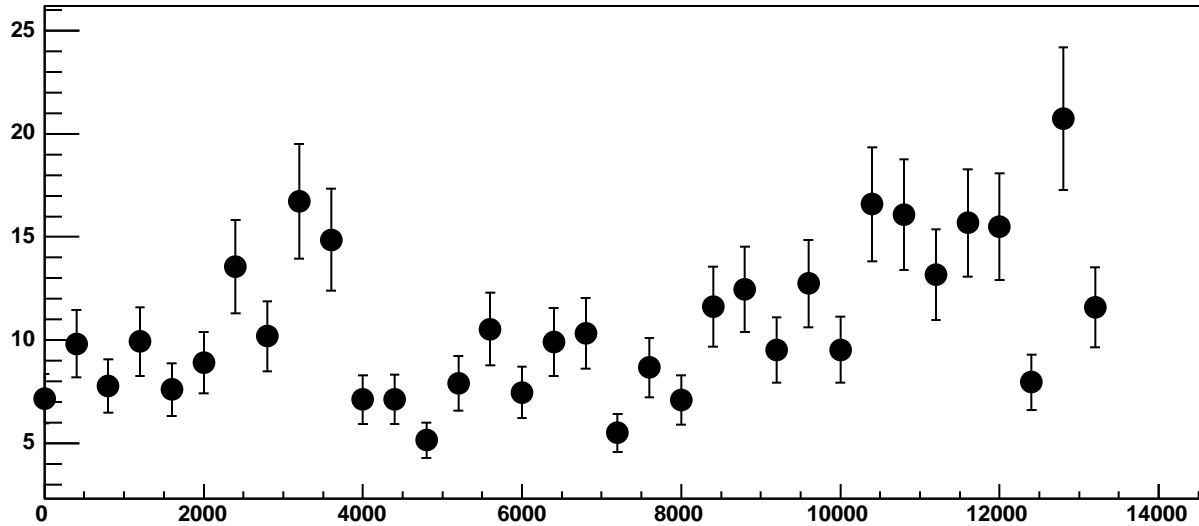


Chip 3, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

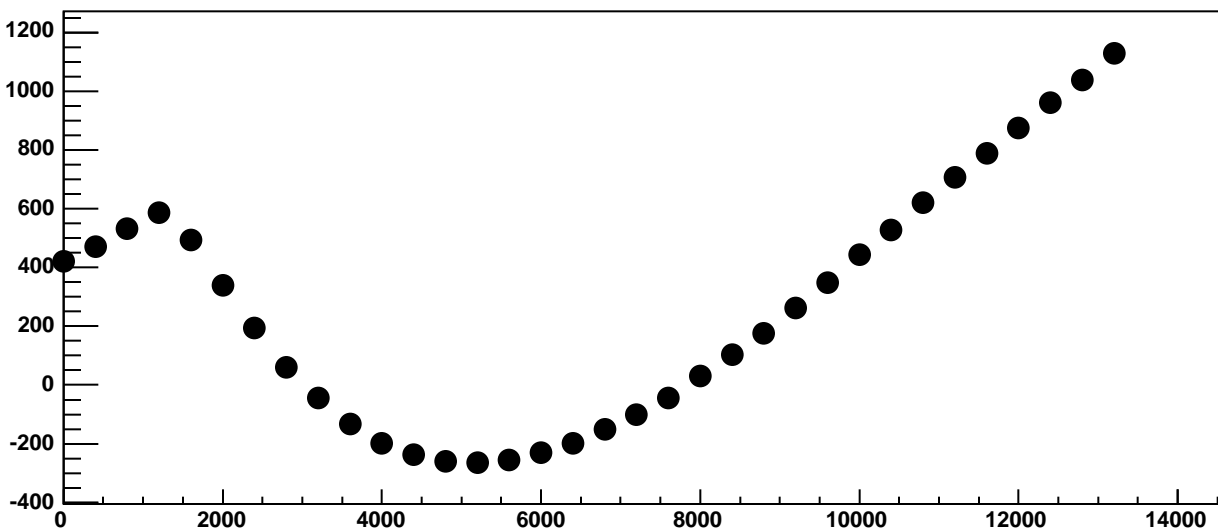
χ^2 / ndf 4.562e+05 / 23
p0 -843.3 ± 1.021
p1 -0.1335 ± 0.0001643



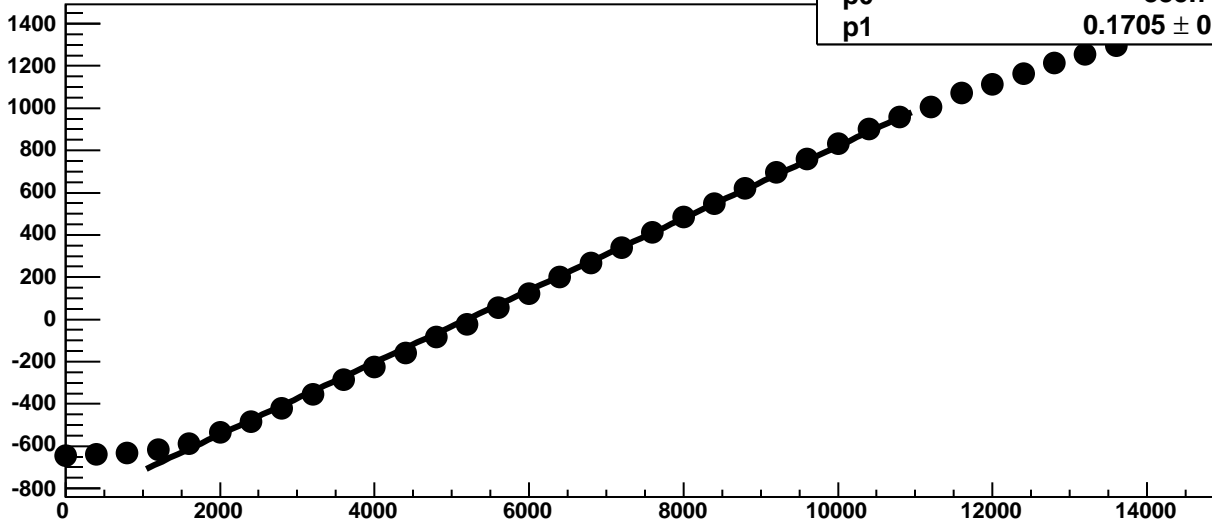
Chip 3, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



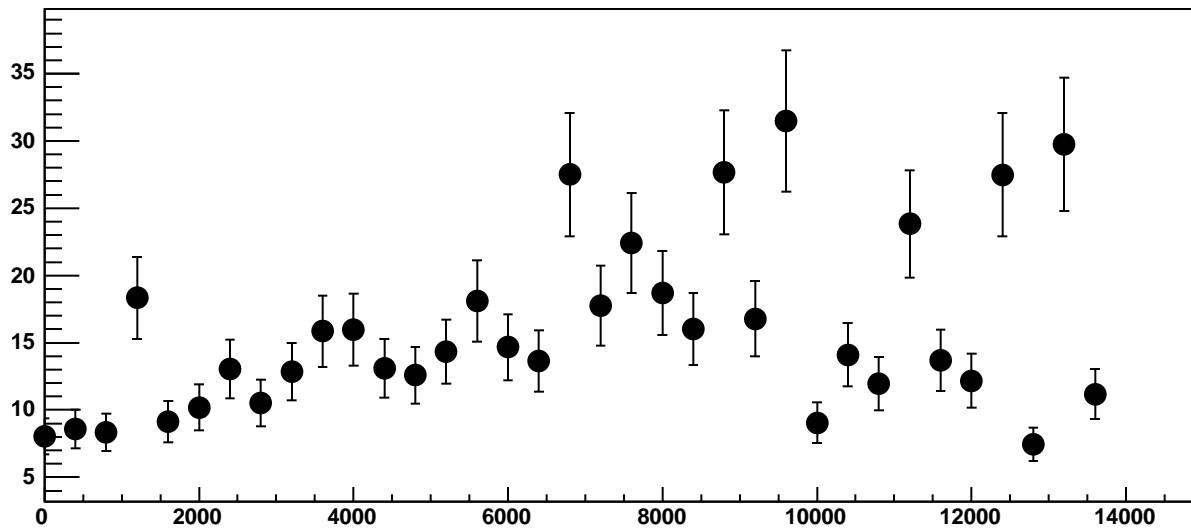
Chip 3, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



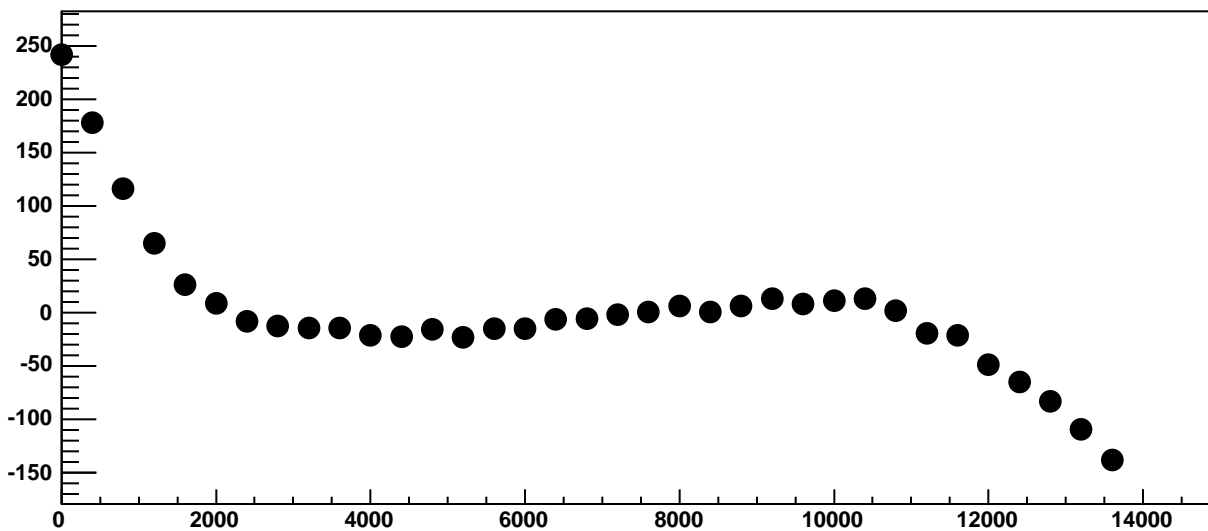
Chip 3, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



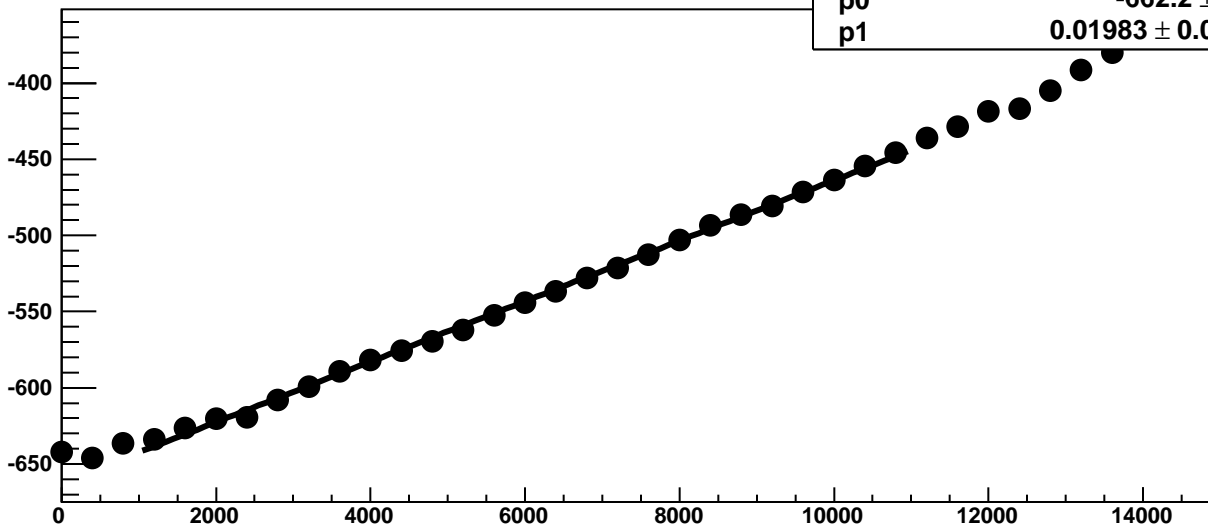
Chip 3, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

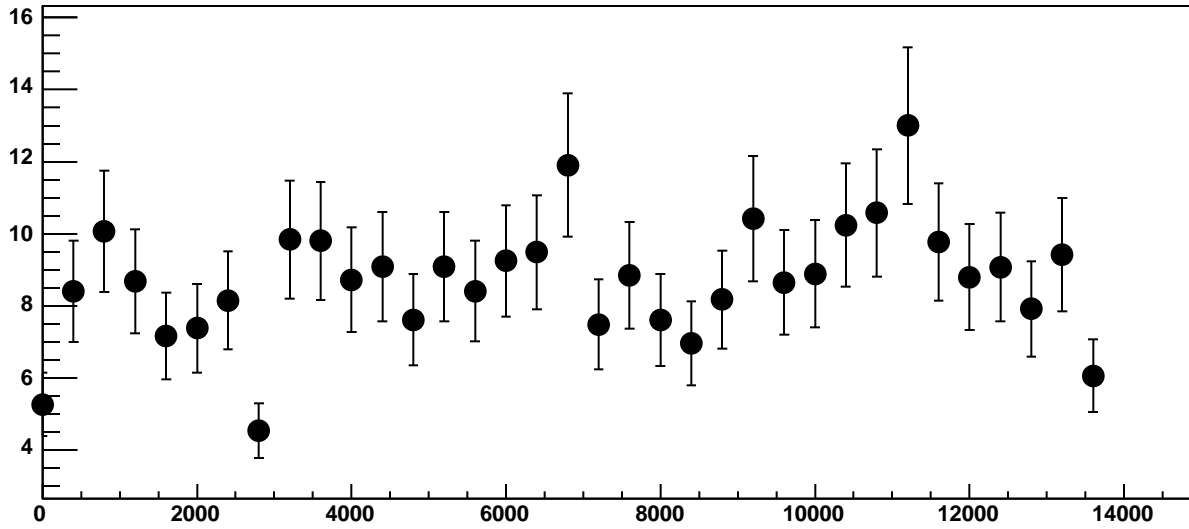


Chip 3, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

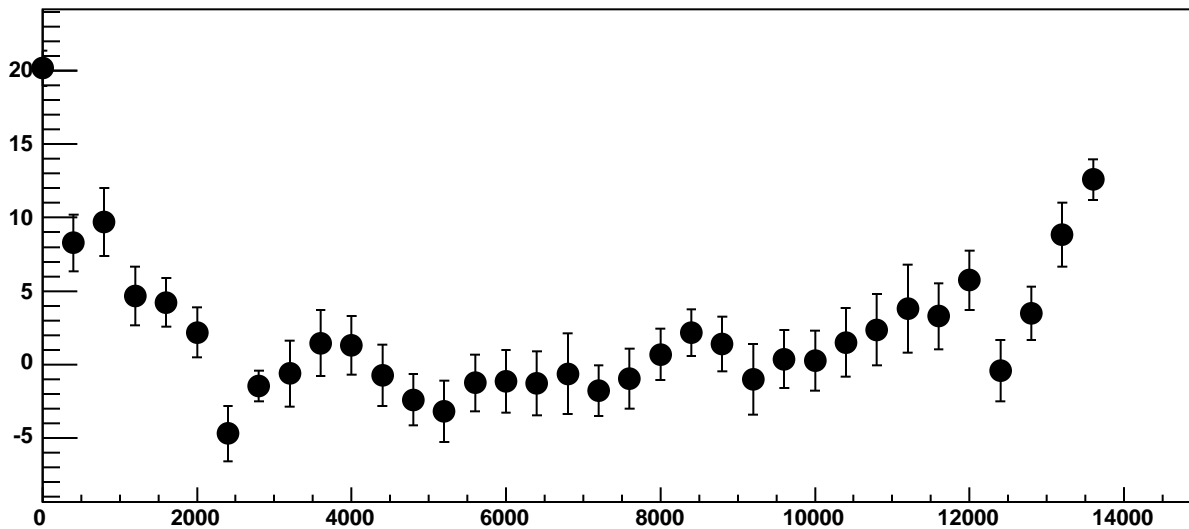


χ^2 / ndf 33.81 / 23
p0 -662.2 ± 0.8155
p1 0.01983 ± 0.0001309

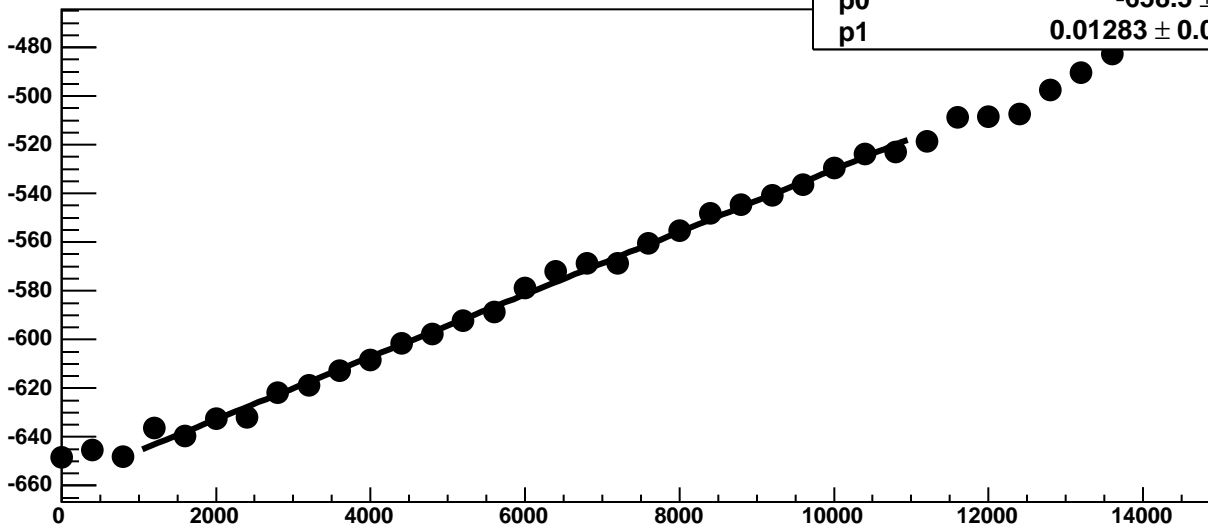
Chip 3, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

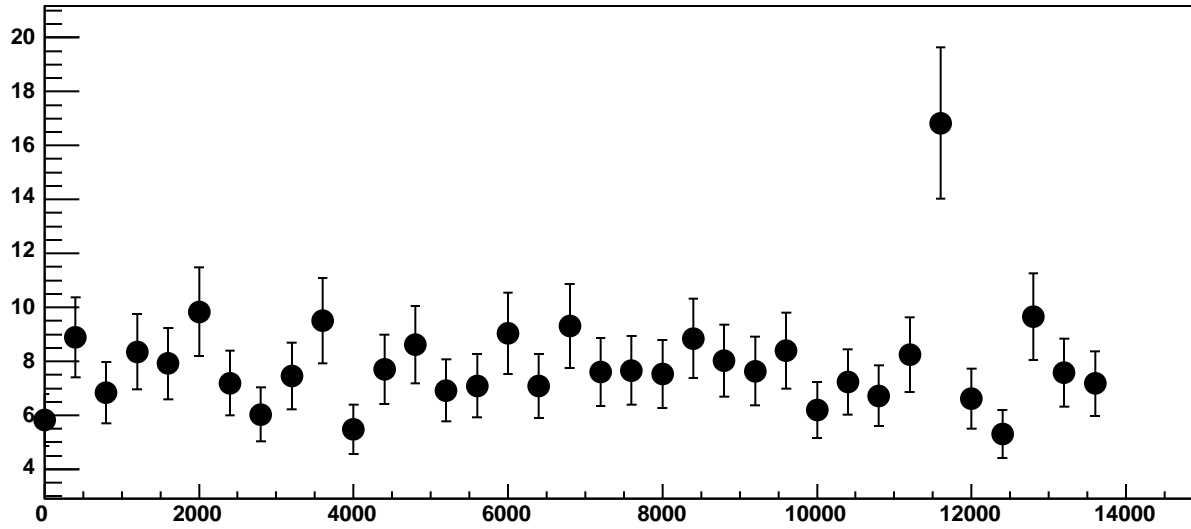


Chip 3, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

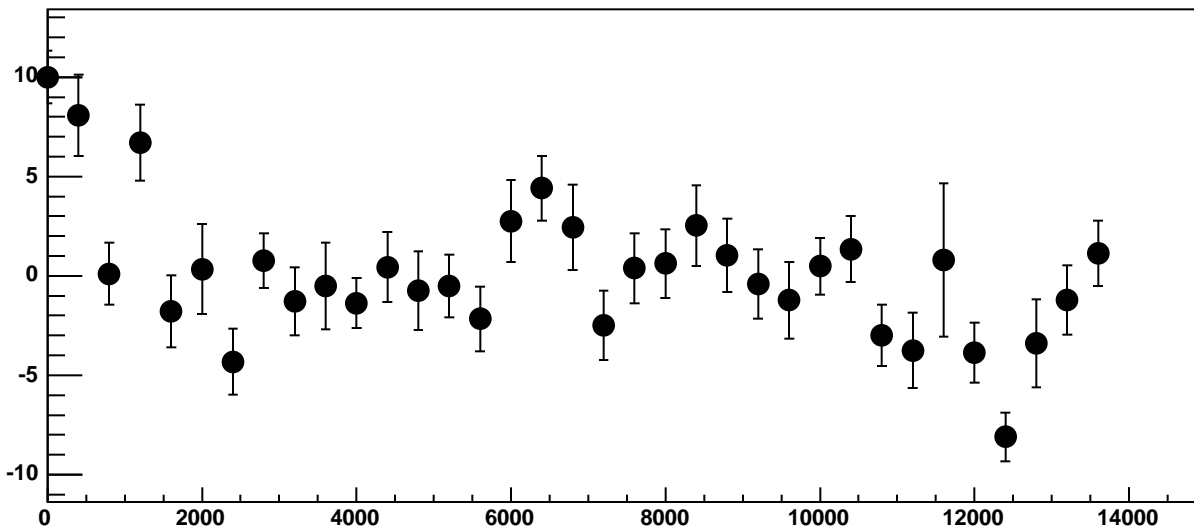


χ^2 / ndf 43.79 / 23
p0 -658.5 ± 0.7962
p1 0.01283 ± 0.0001185

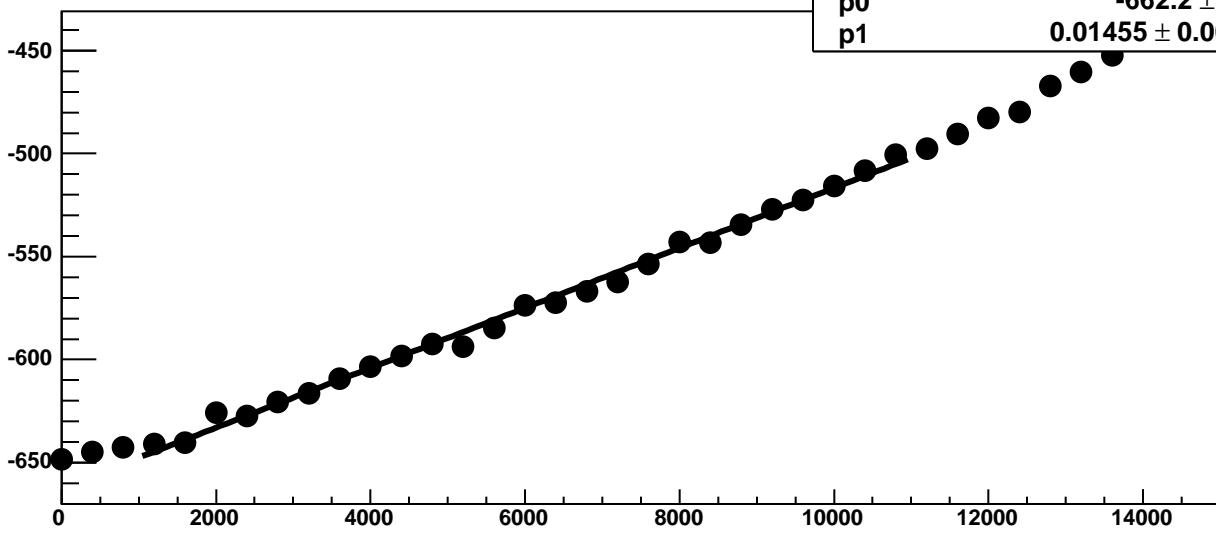
Chip 3, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

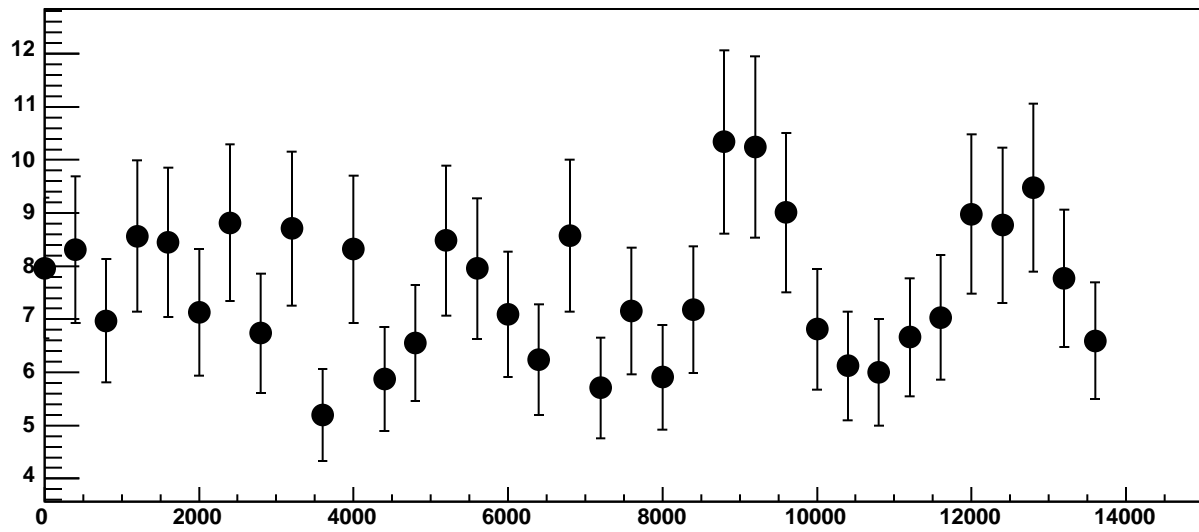


Chip 3, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

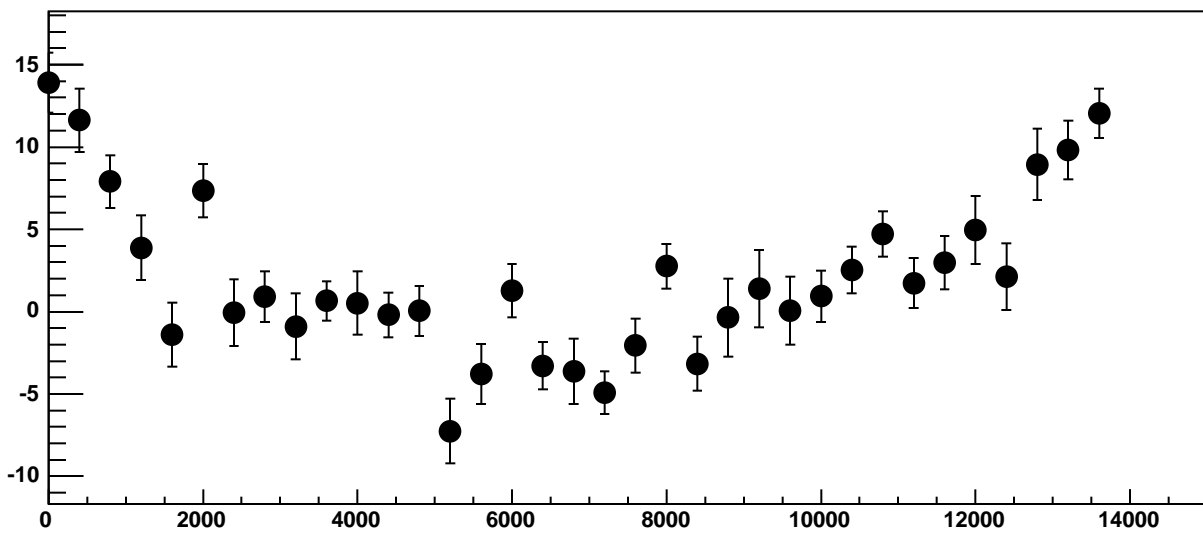


χ^2 / ndf 92.29 / 23
p0 -662.2 ± 0.7795
p1 0.01455 ± 0.0001155

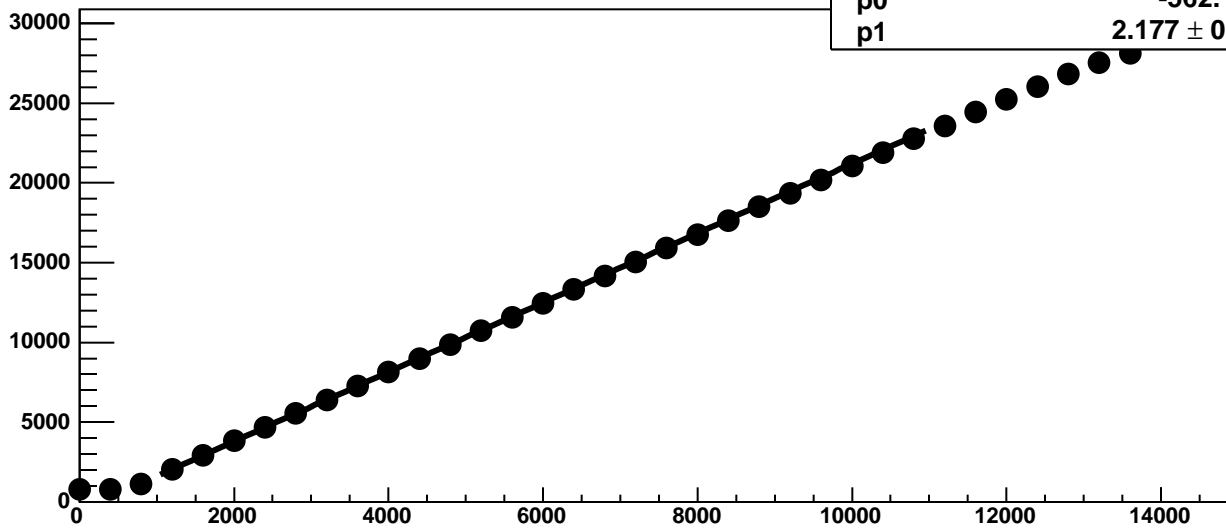
Chip 3, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



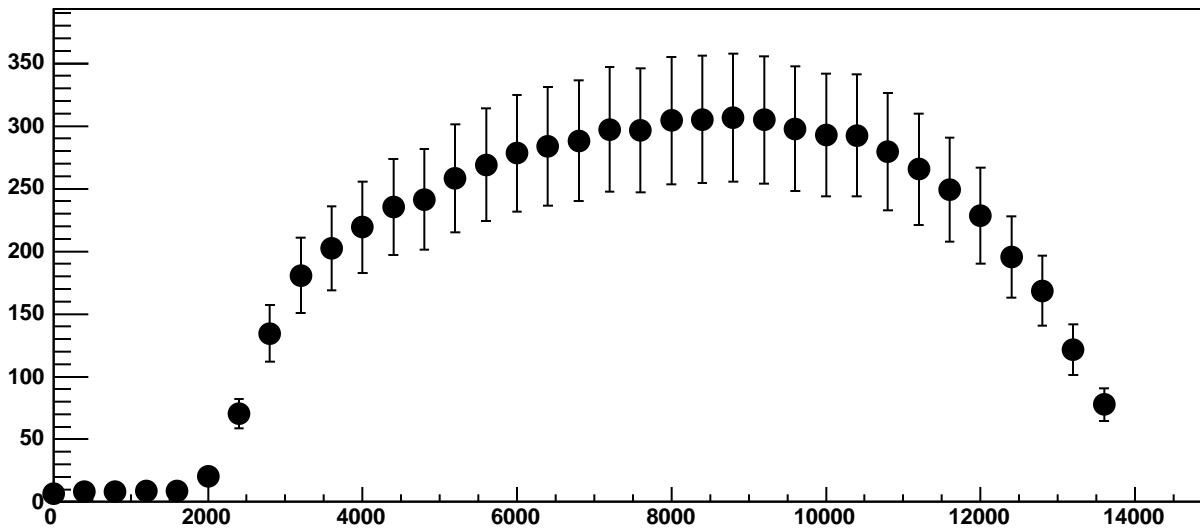
Chip 3, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



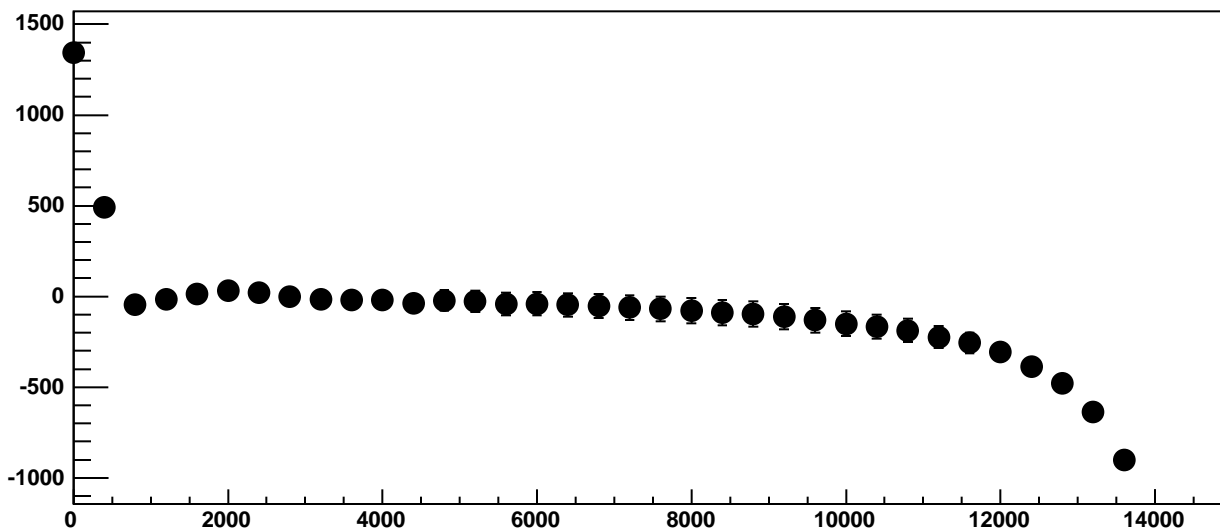
Chip 3, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC



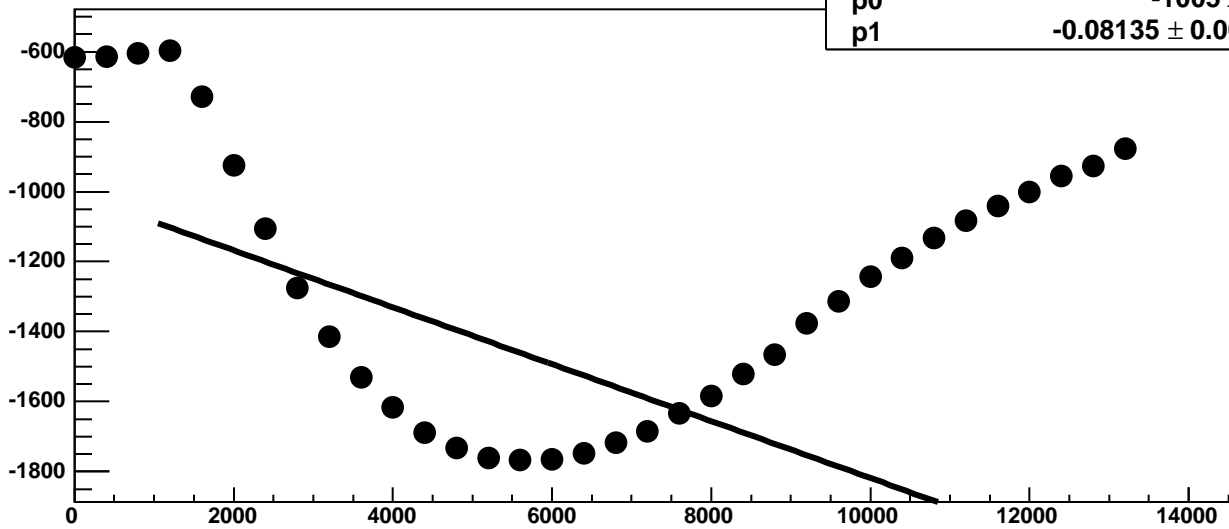
Chip 3, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC

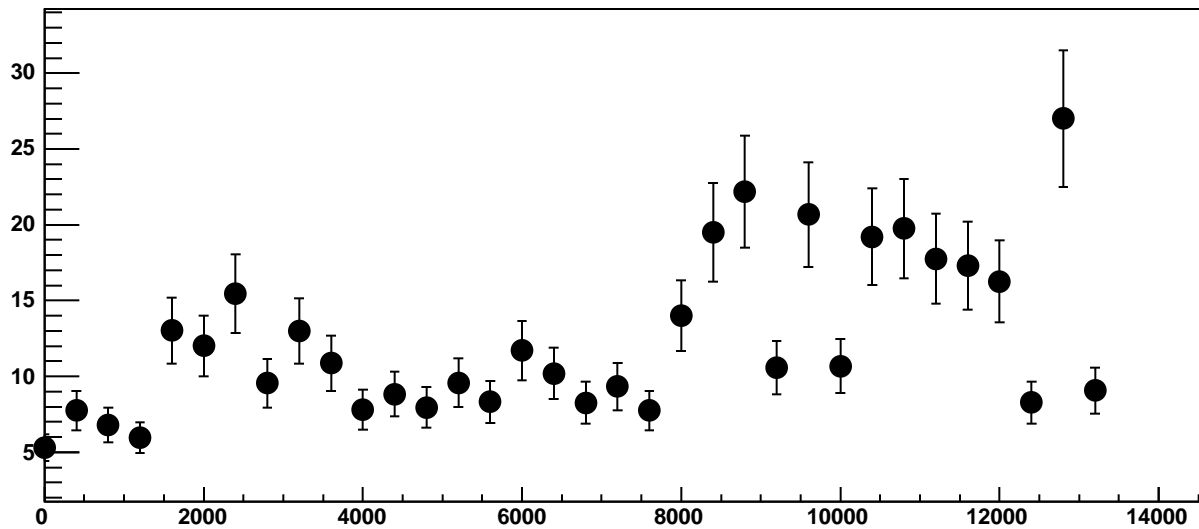


Chip 3, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

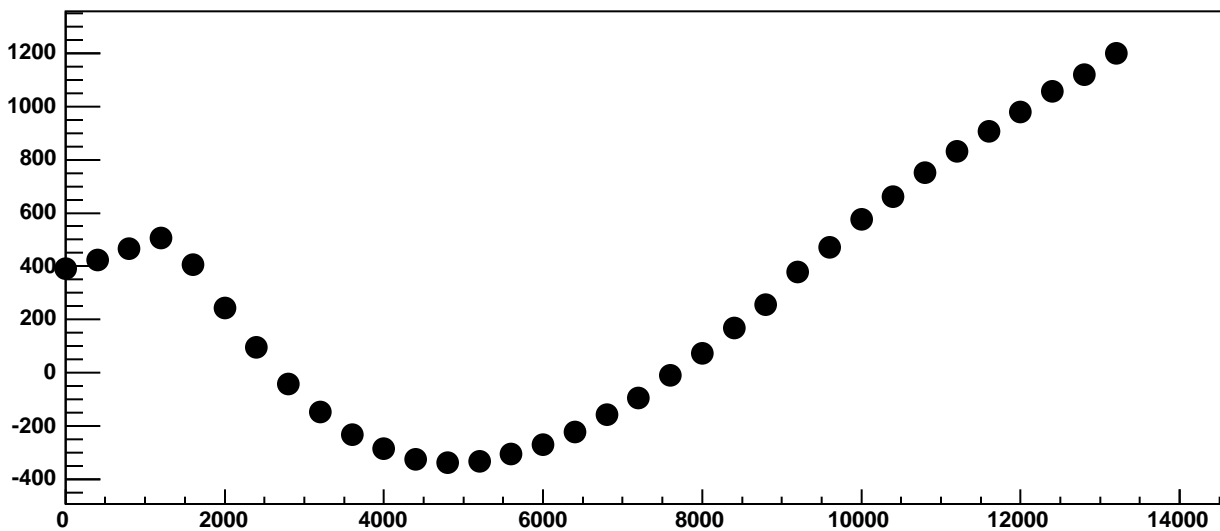


χ^2 / ndf 4.819e+05 / 23
p0 -1005 ± 1.053
p1 -0.08135 ± 0.0001808

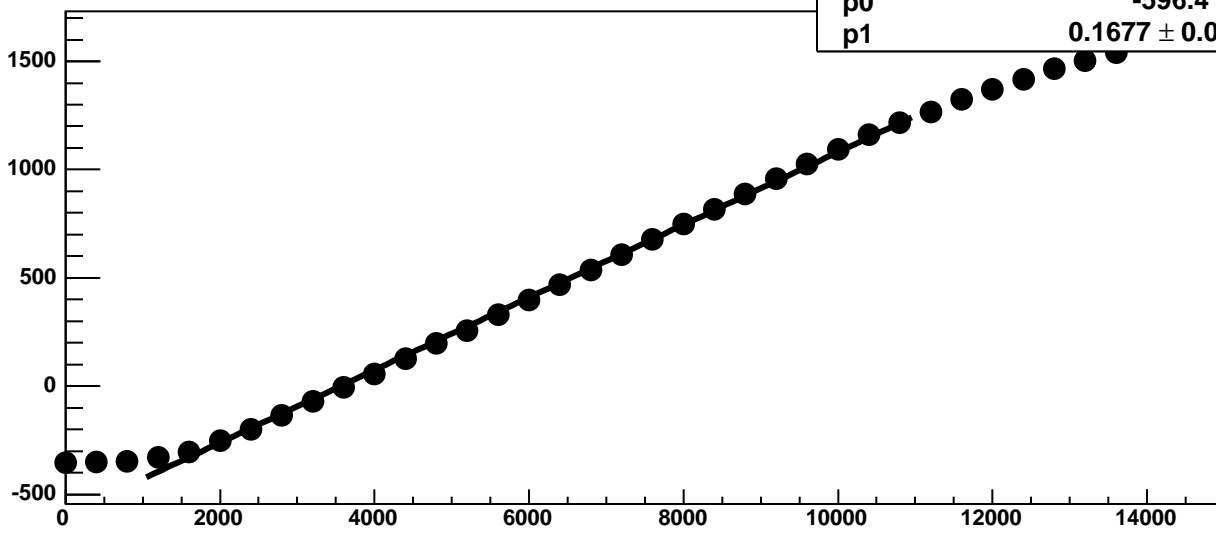
Chip 3, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



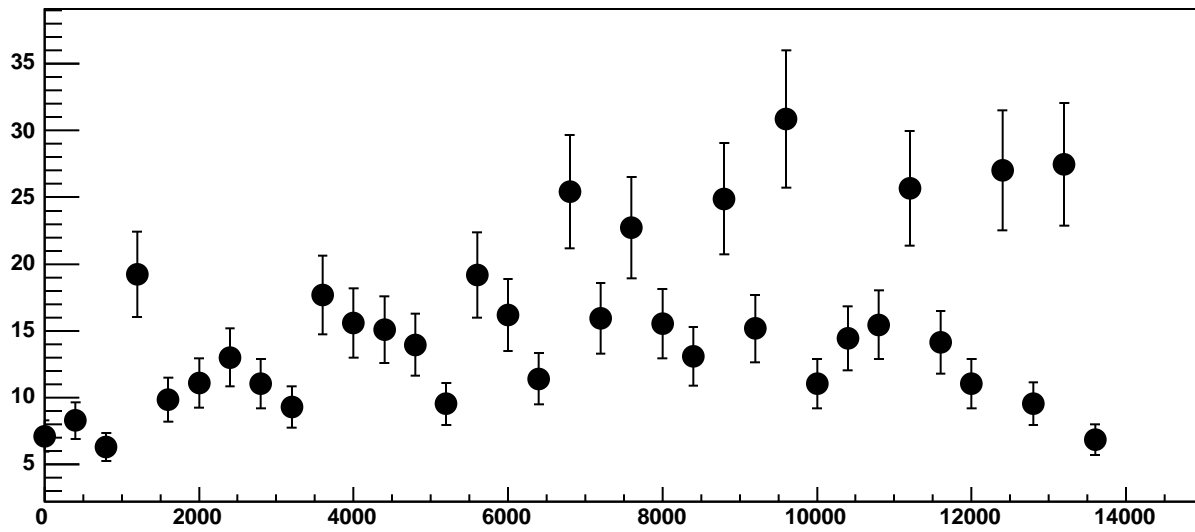
Chip 3, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



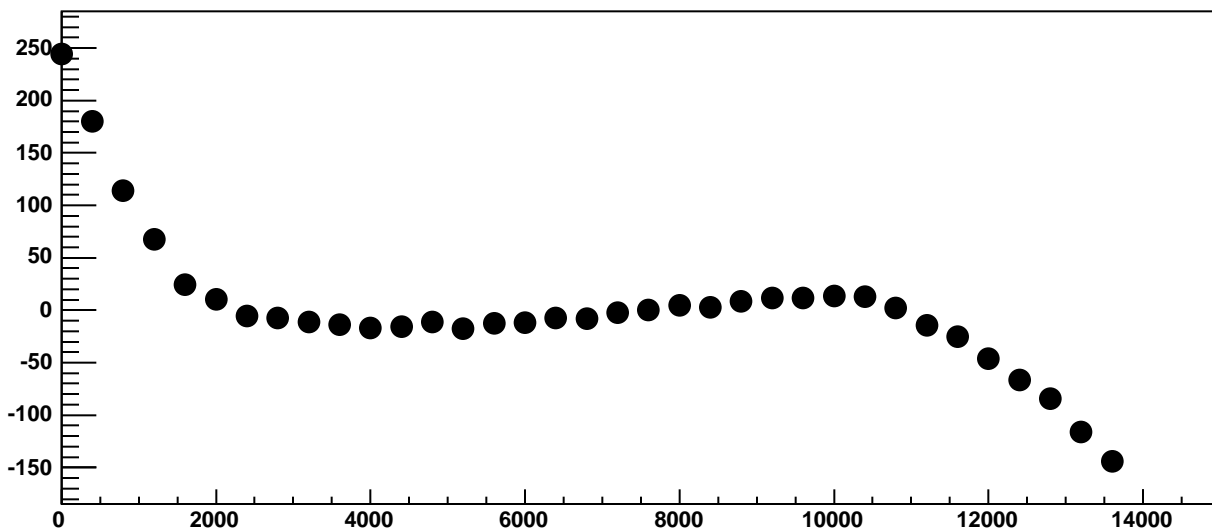
Chip 3, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



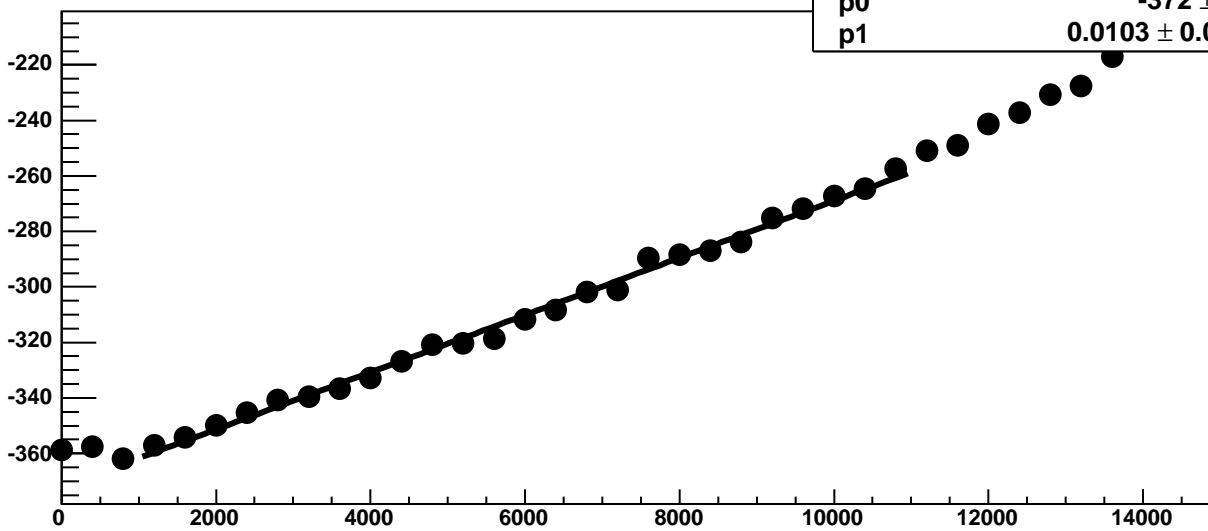
Chip 3, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

44 / 23

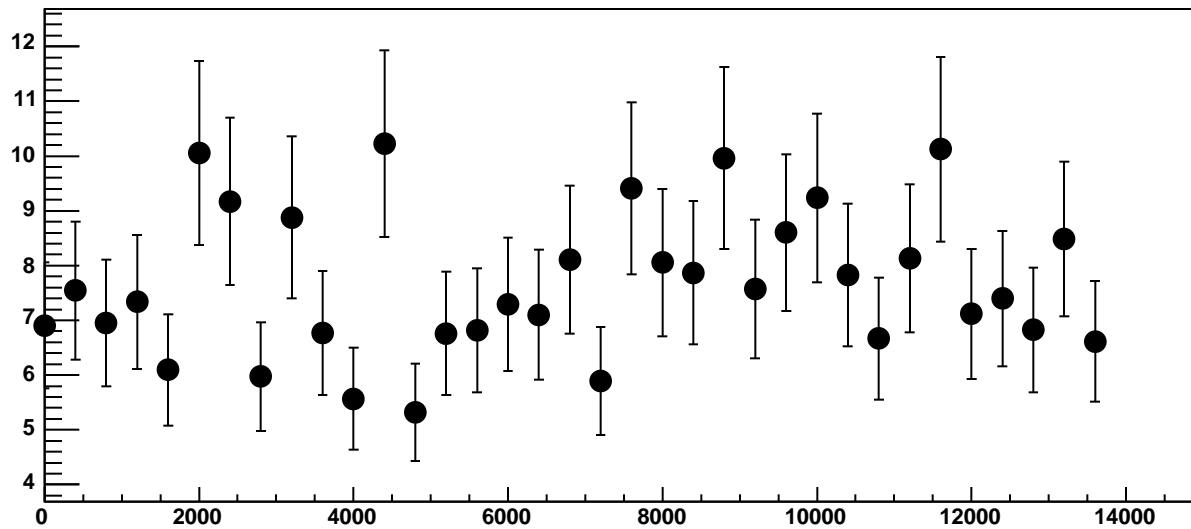
p0

-372 ± 0.7674

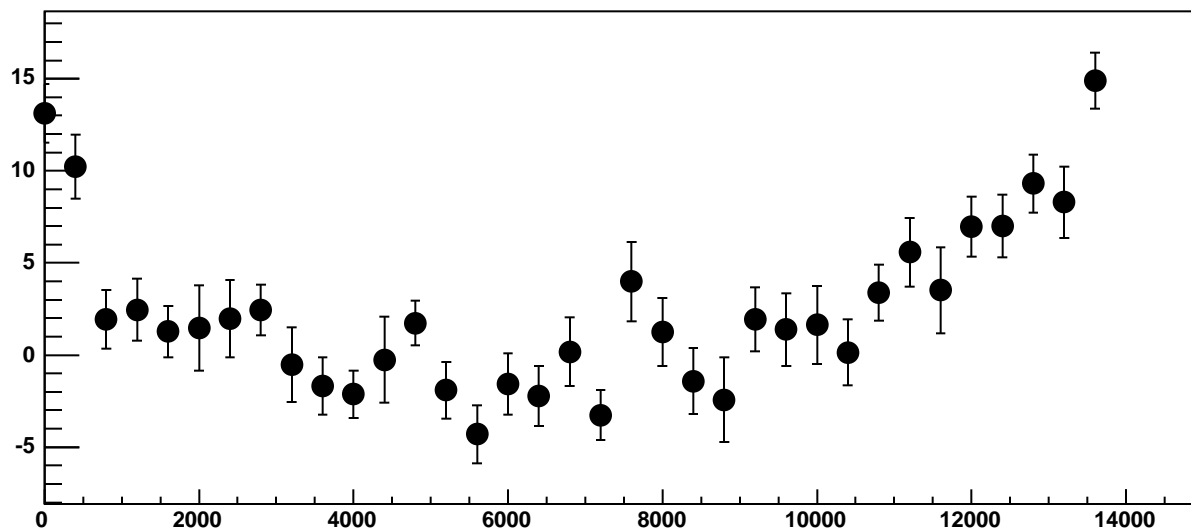
p1

0.0103 ± 0.0001199

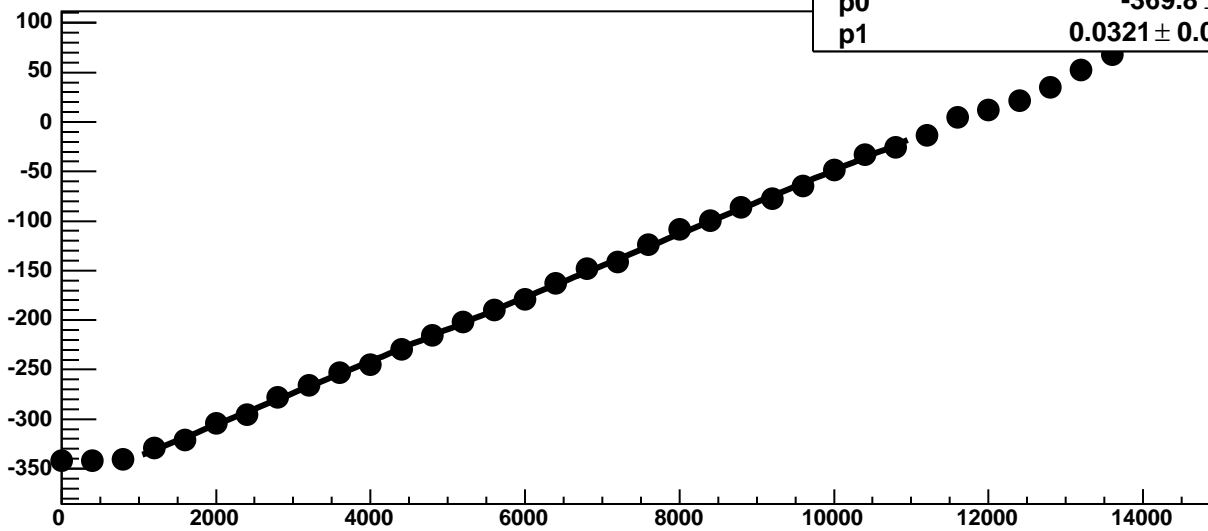
Chip 3, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



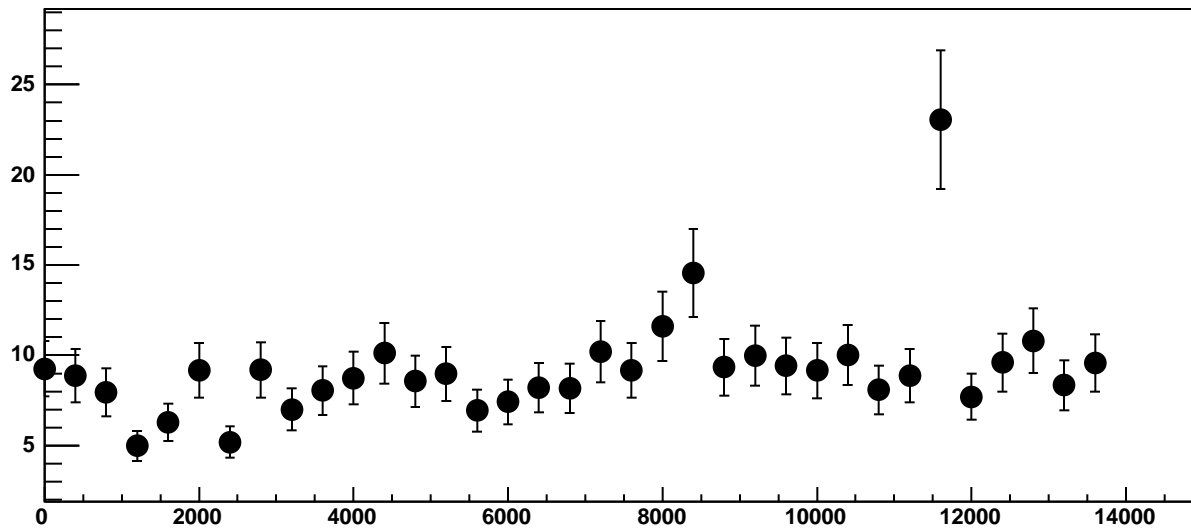
Chip 3, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



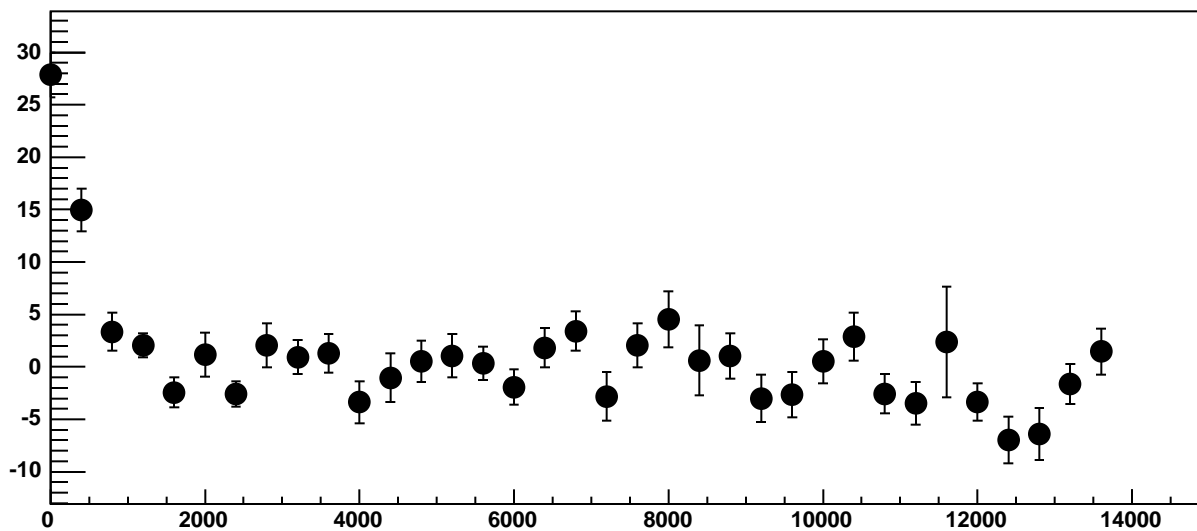
Chip 3, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC



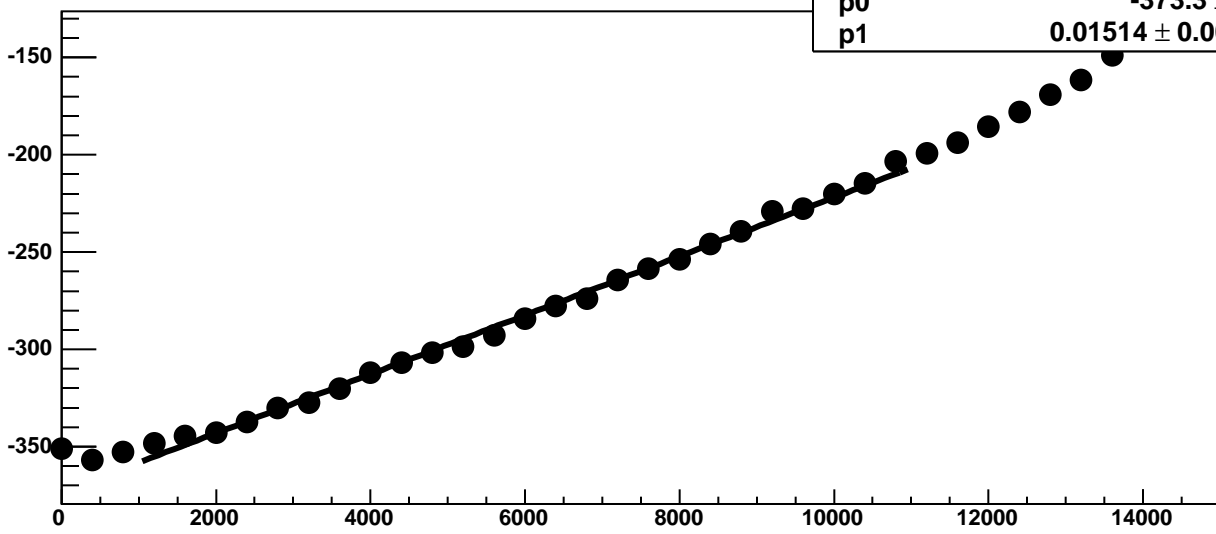
Chip 3, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC

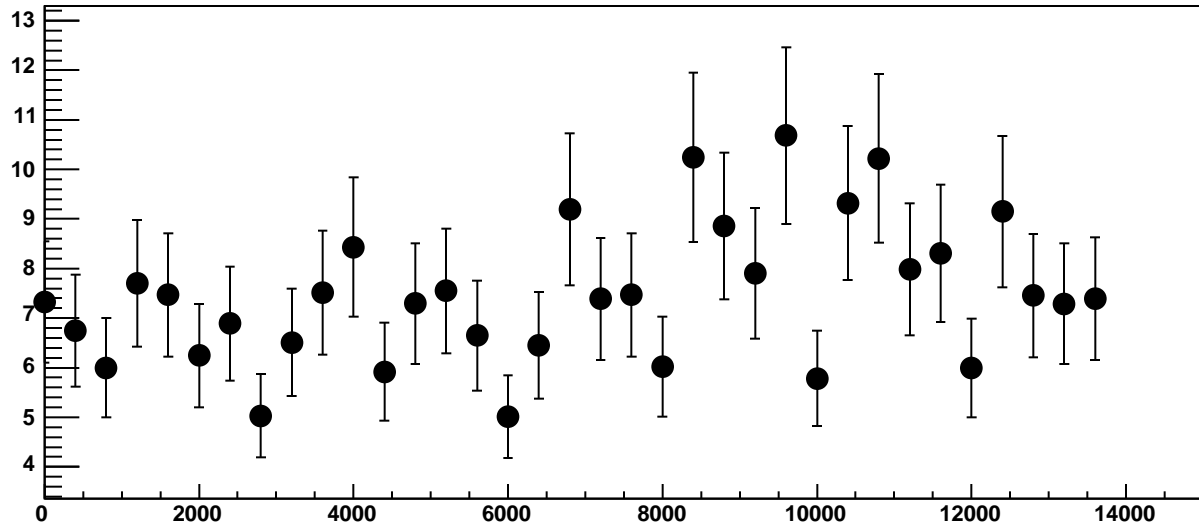


Chip 3, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

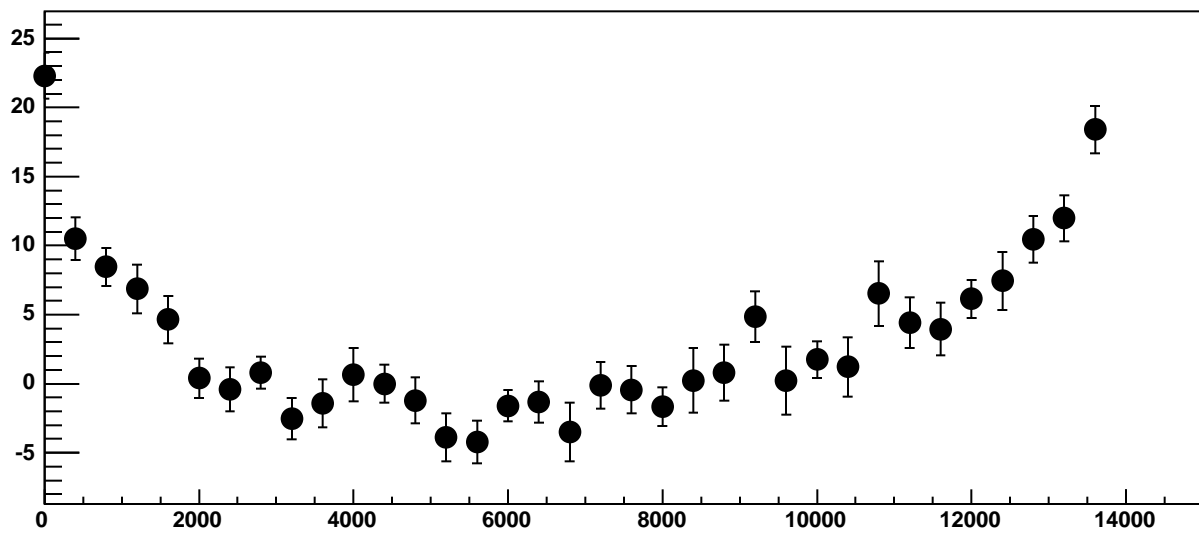


χ^2 / ndf 64.11 / 23
p0 -373.3 ± 0.737
p1 0.01514 ± 0.0001186

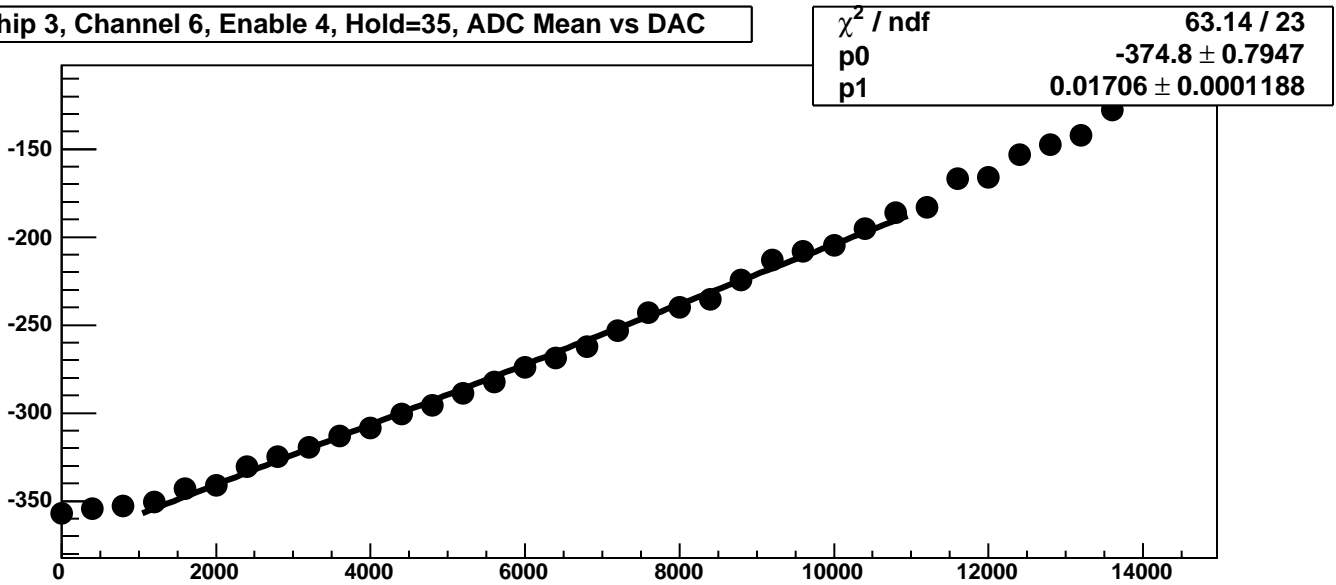
Chip 3, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



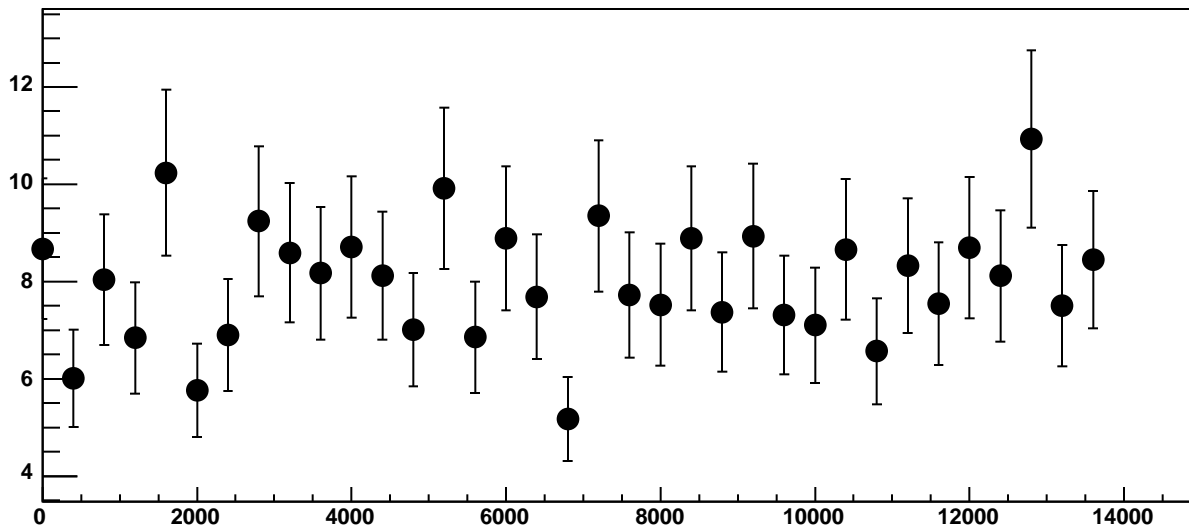
Chip 3, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



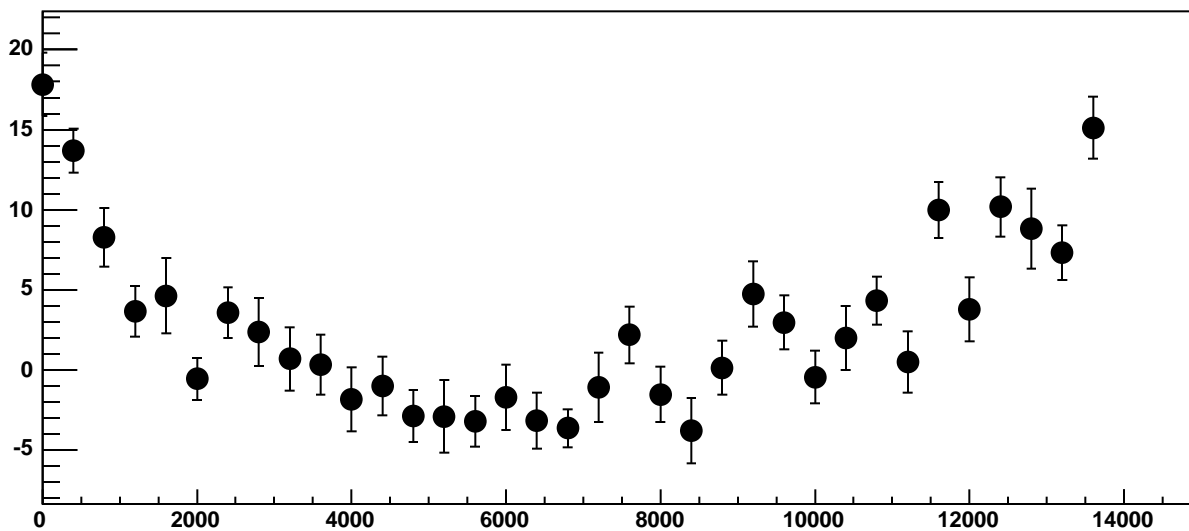
Chip 3, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



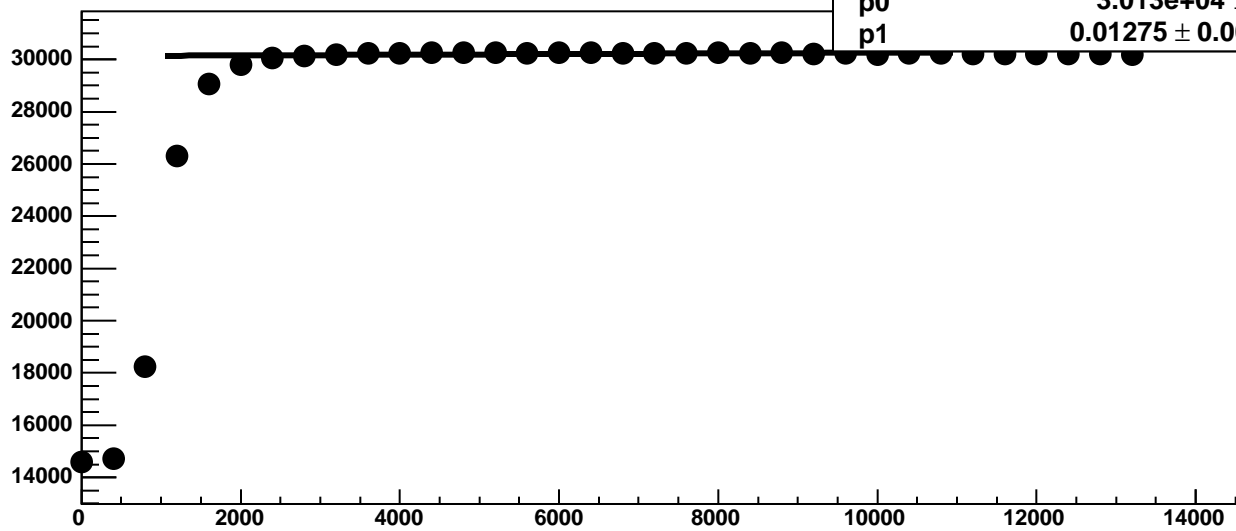
Chip 3, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



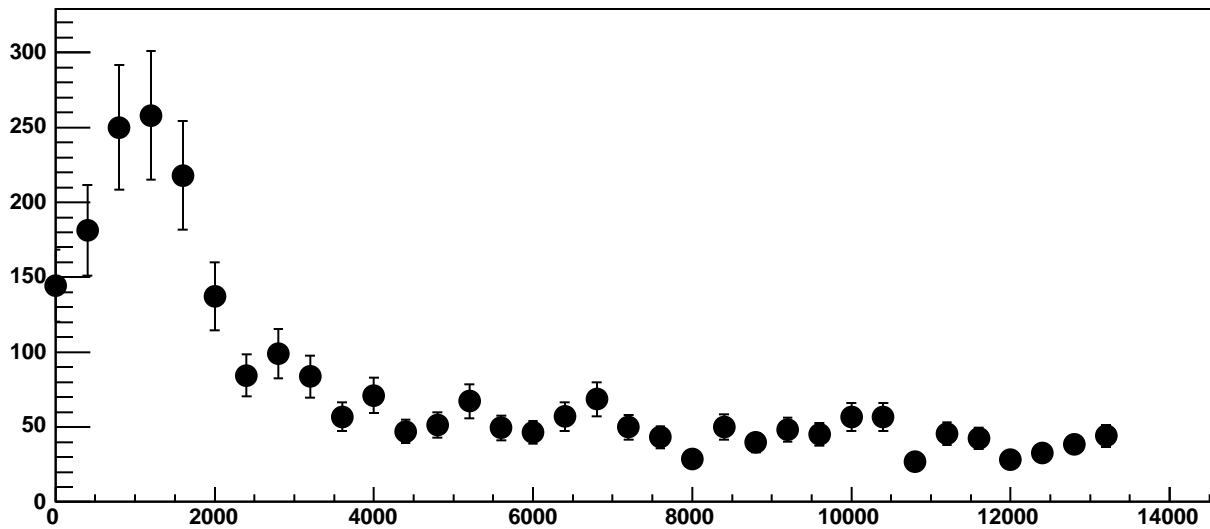
Chip 3, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



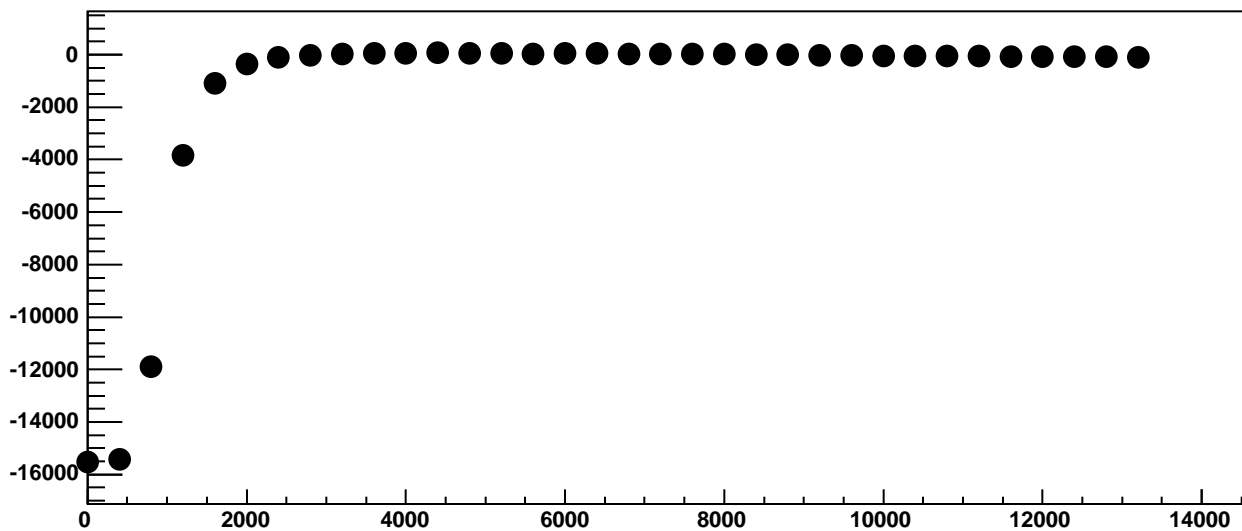
Chip 3, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC



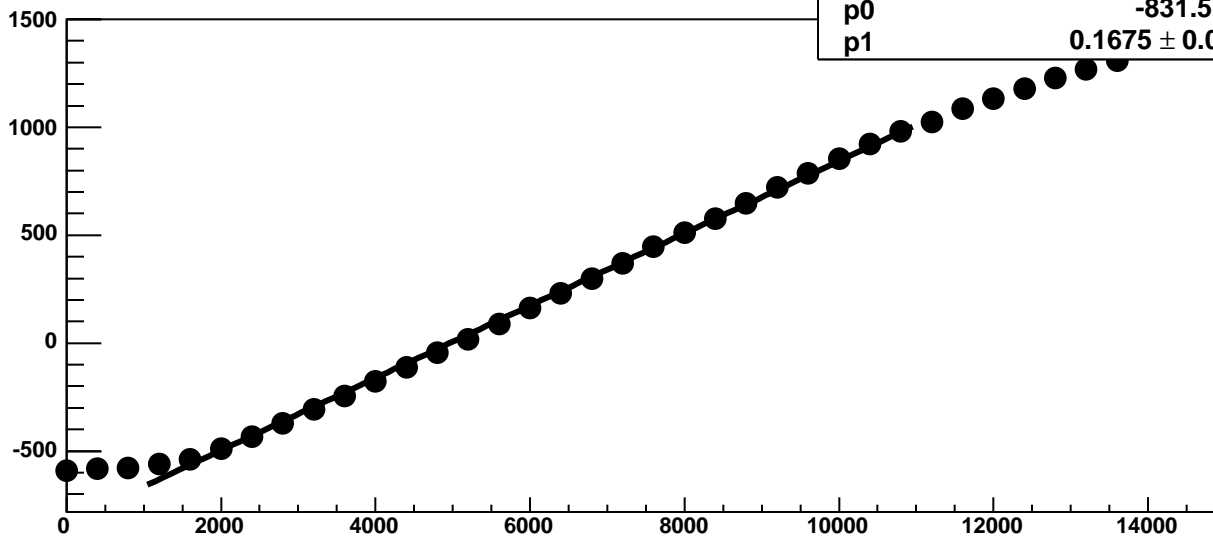
Chip 3, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC



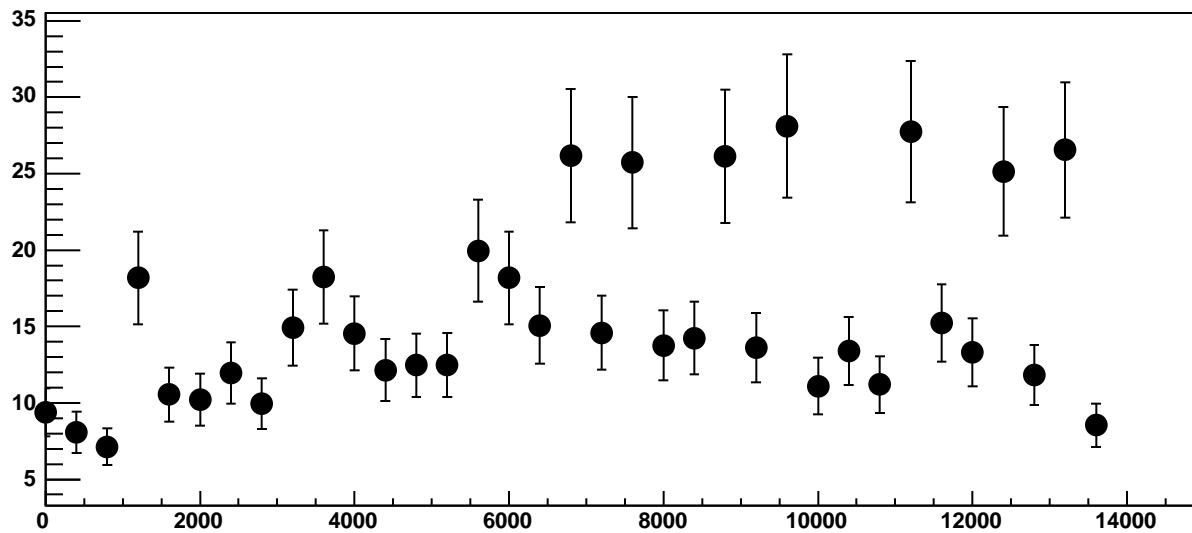
Chip 3, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC



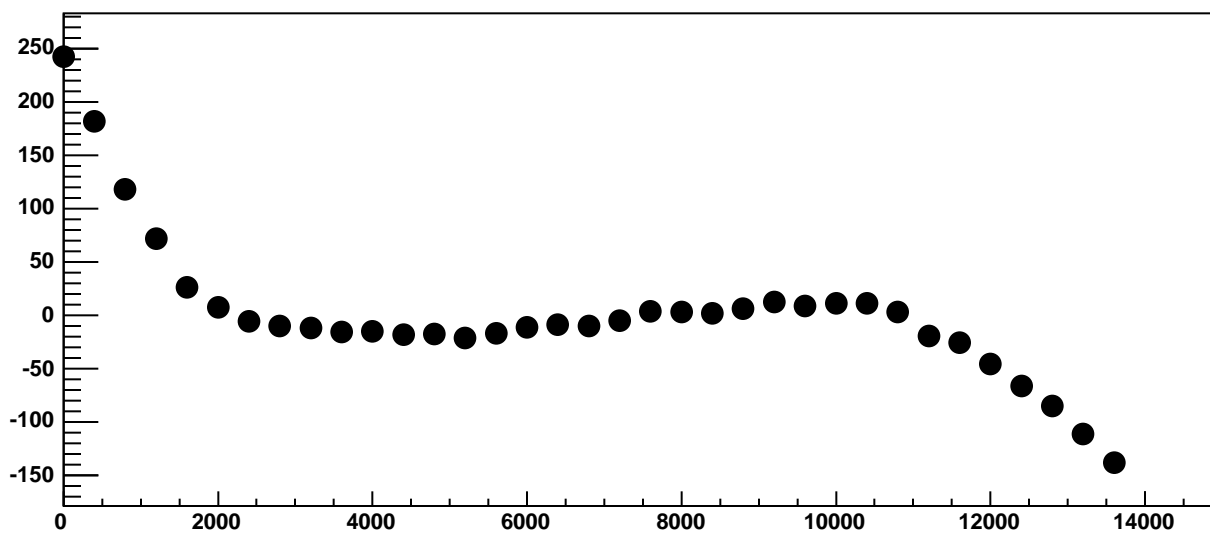
Chip 3, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



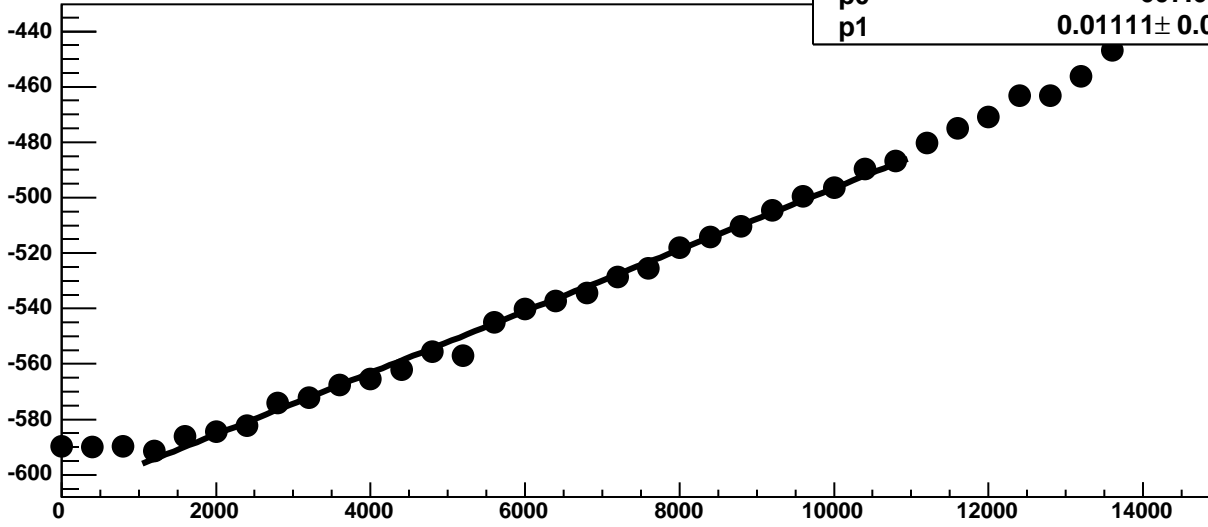
Chip 3, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



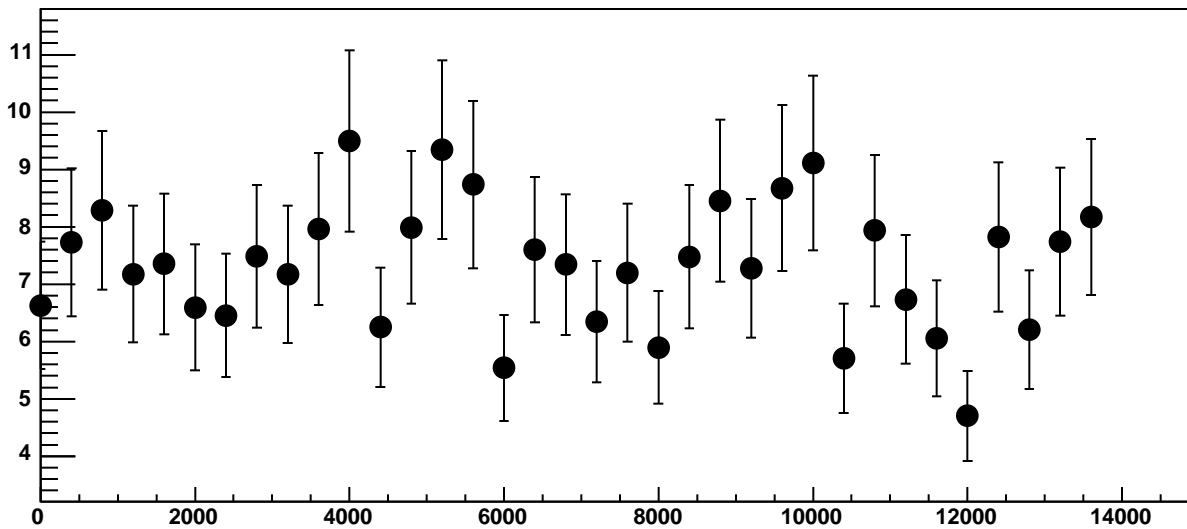
Chip 3, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



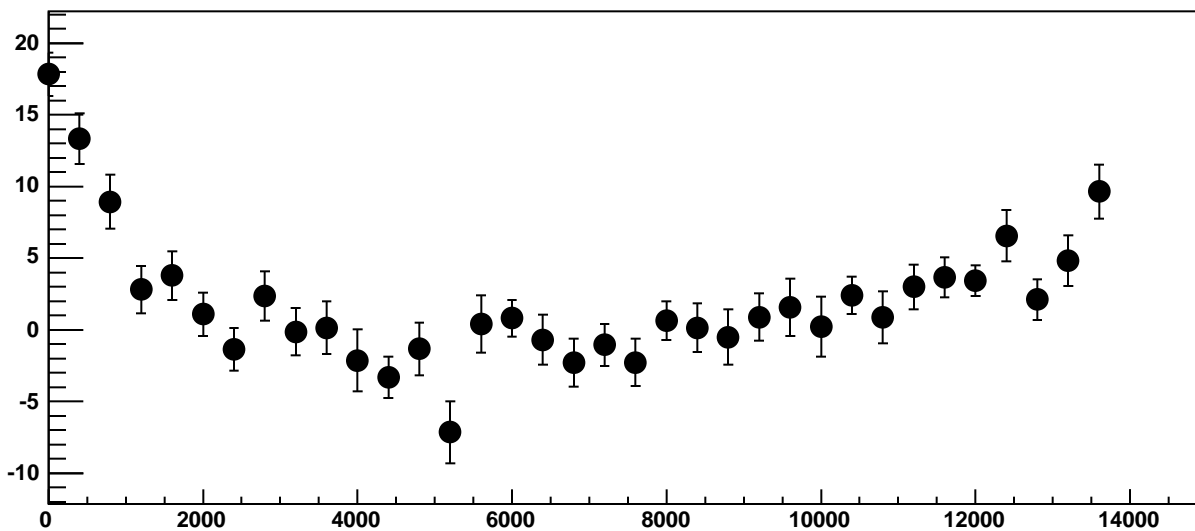
Chip 3, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



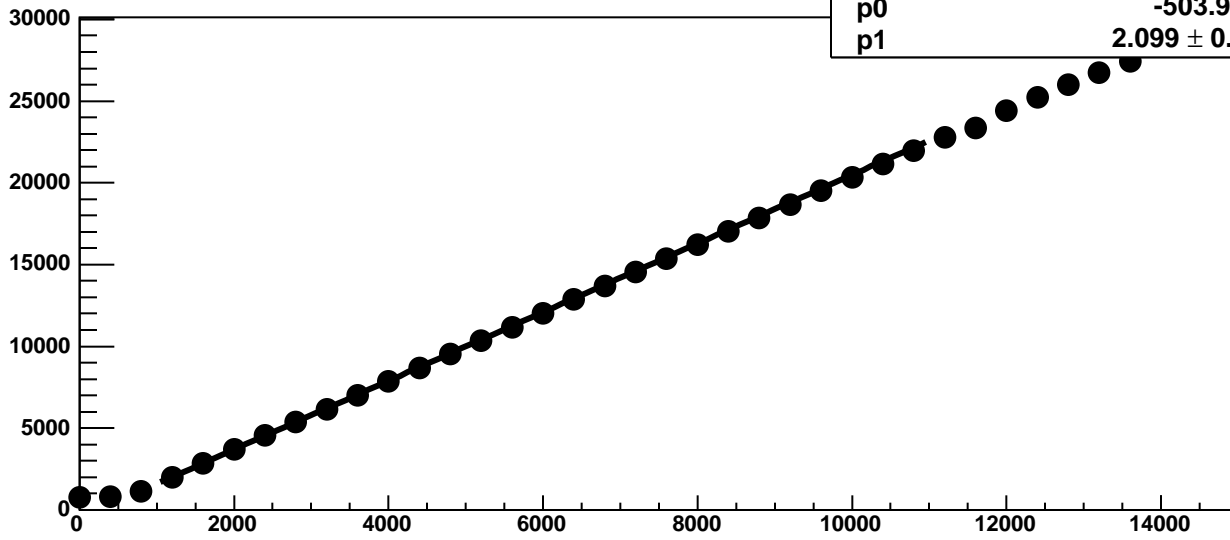
Chip 3, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

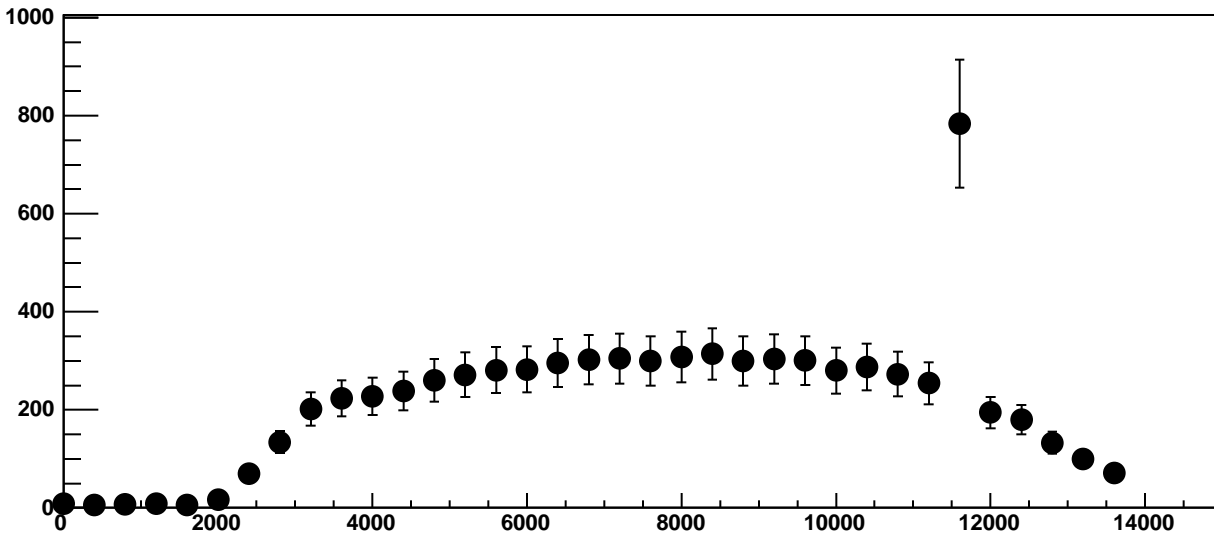


Chip 3, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC

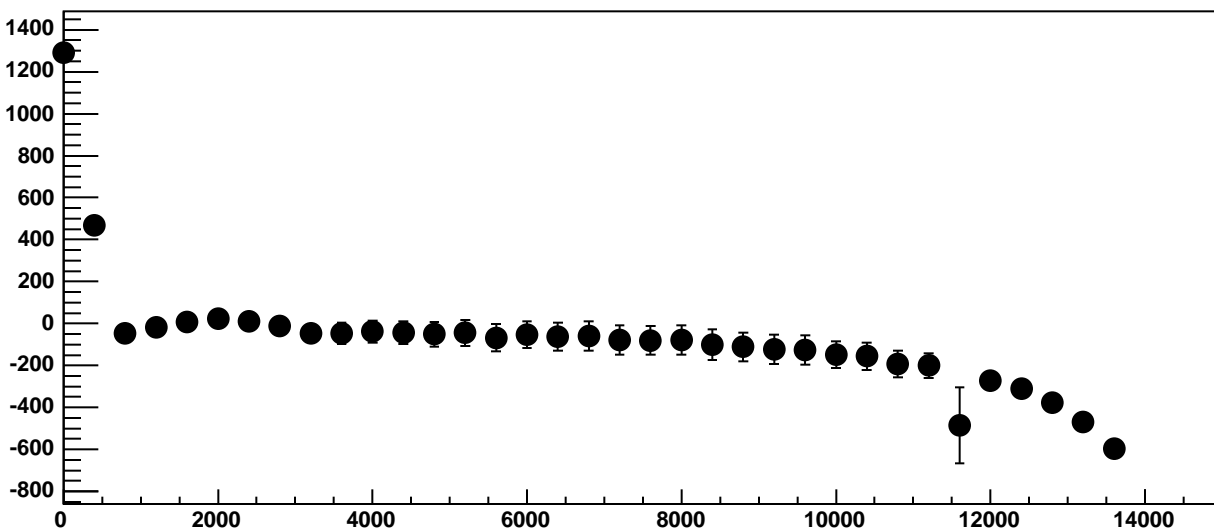


χ^2 / ndf 181.2 / 23
p0 -503.9 ± 3.491
p1 2.099 ± 0.002165

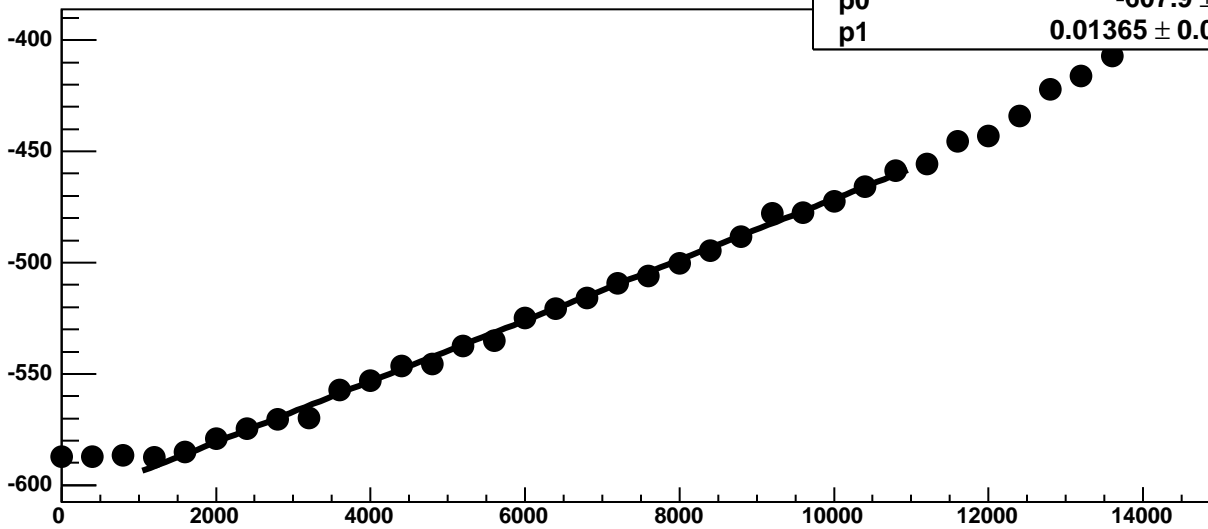
Chip 3, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC

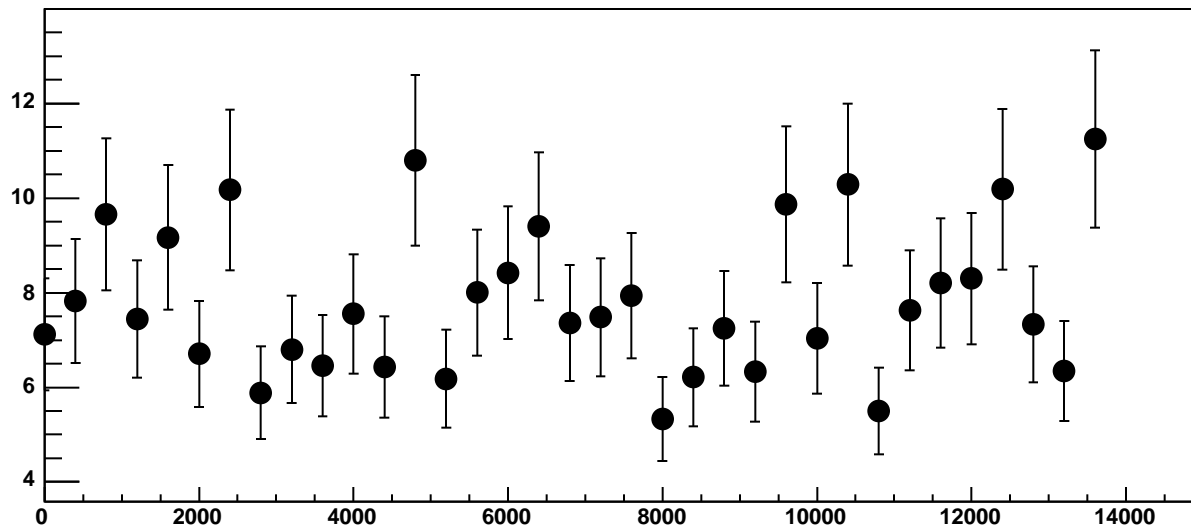


Chip 3, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

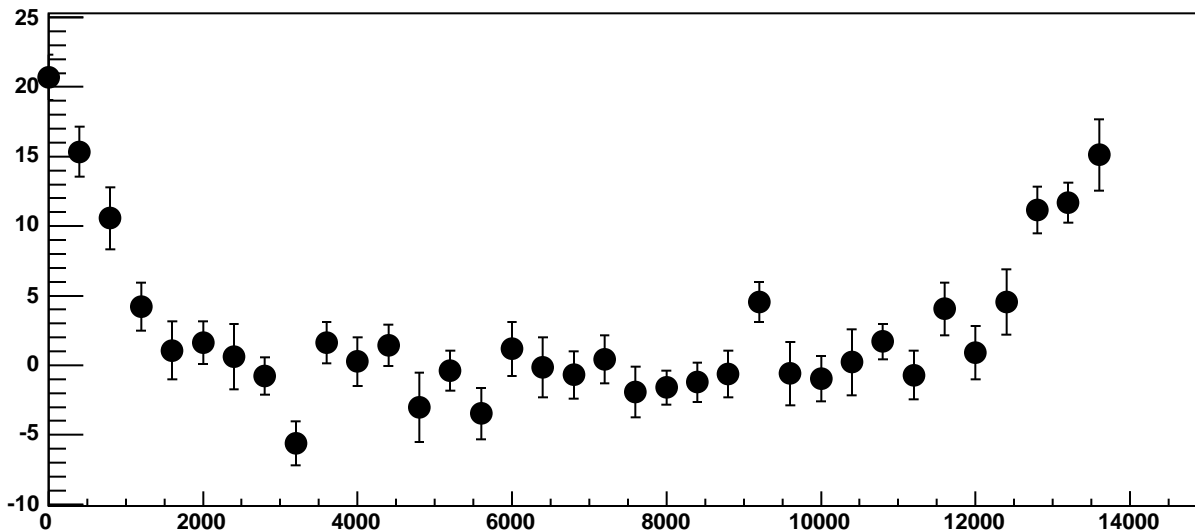


χ^2 / ndf 44.45 / 23
p0 -607.9 ± 0.7723
p1 0.01365 ± 0.0001138

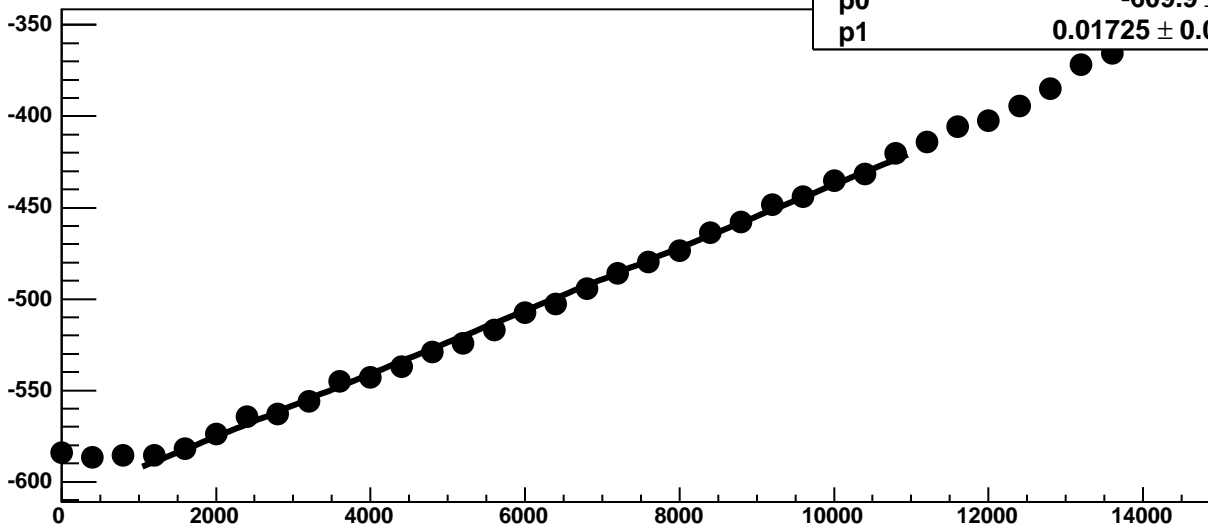
Chip 3, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

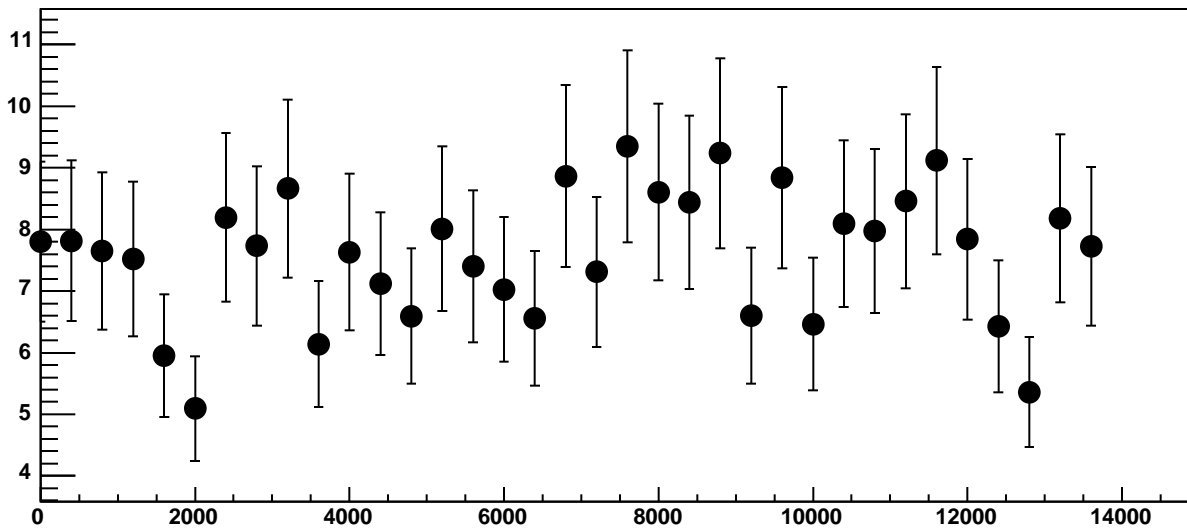


Chip 3, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

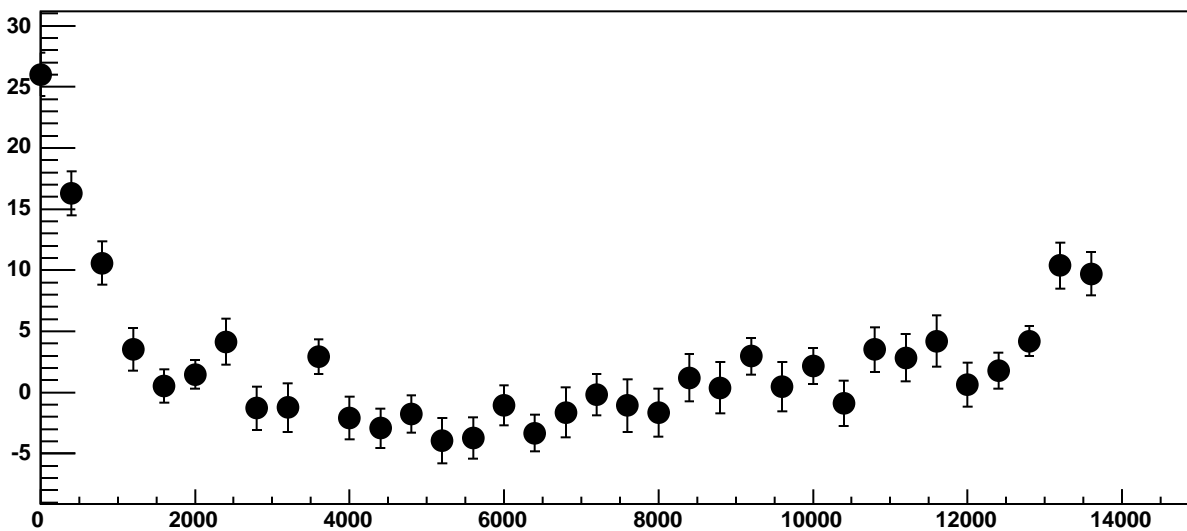


χ^2 / ndf 48.88 / 23
p0 -609.9 ± 0.7251
p1 0.01725 ± 0.0001142

Chip 3, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

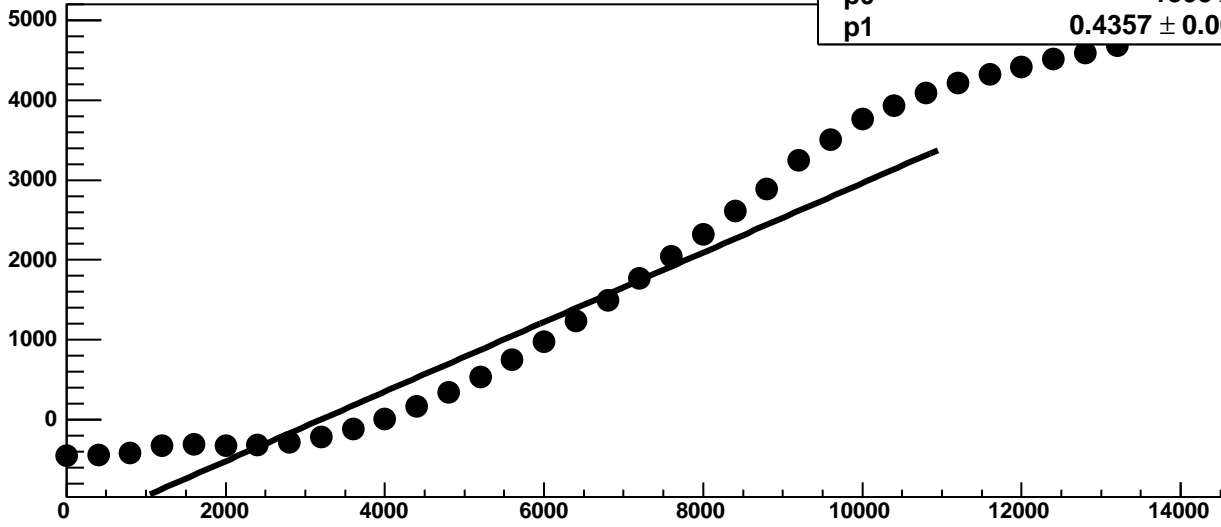


Chip 3, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

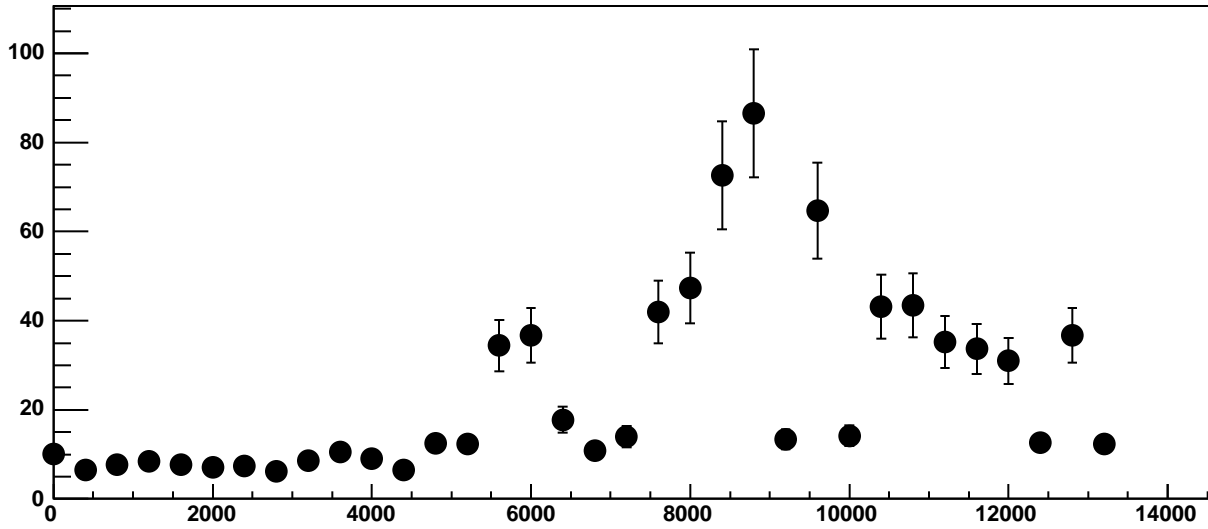


Chip 3, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC

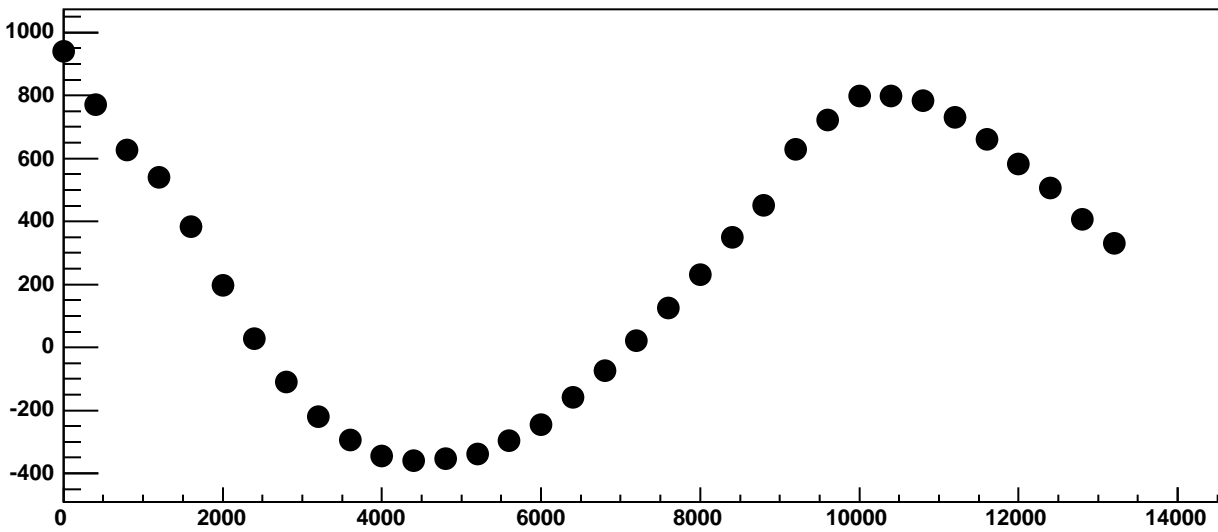
χ^2 / ndf 4.128e+05 / 23
p0 -1393 ± 1.029
p1 0.4357 ± 0.0002375



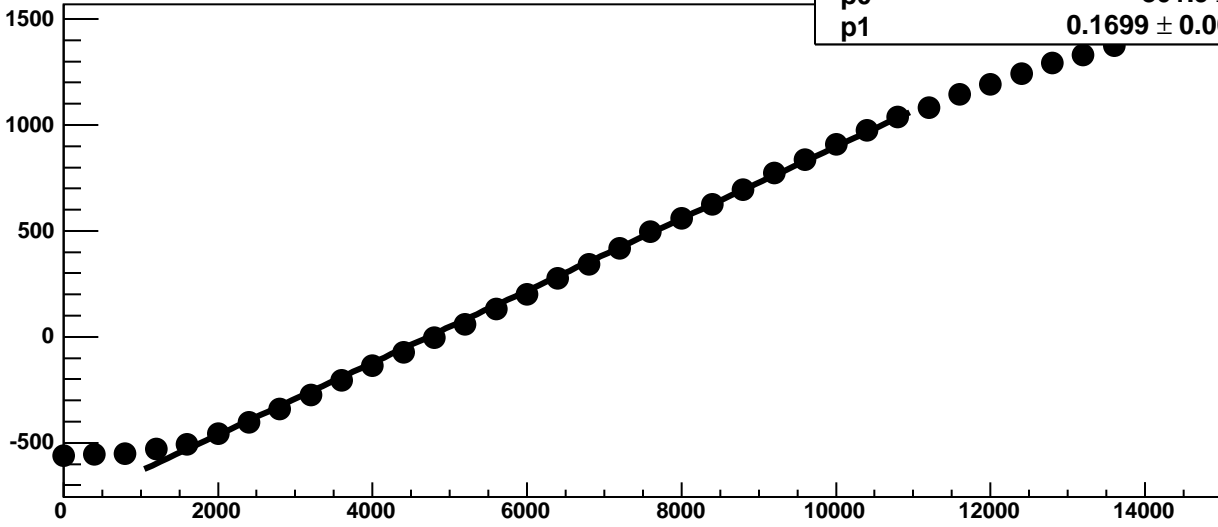
Chip 3, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



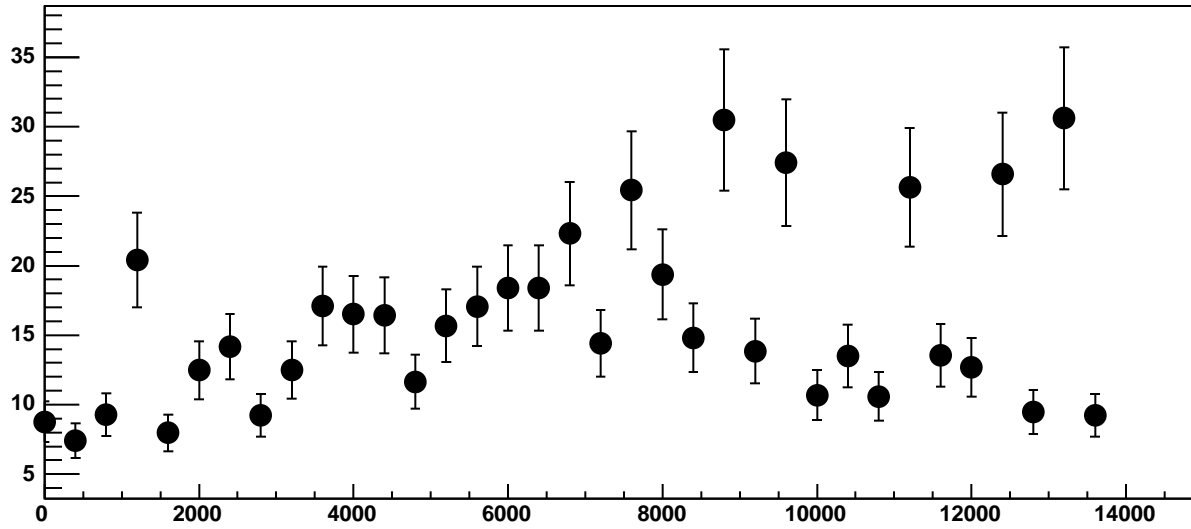
Chip 3, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC



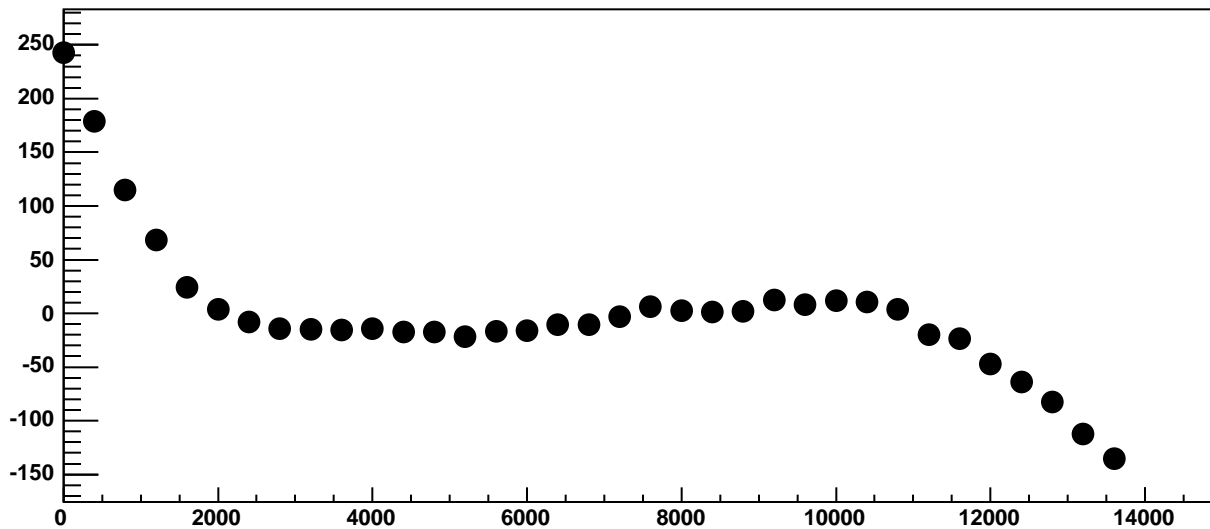
Chip 3, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



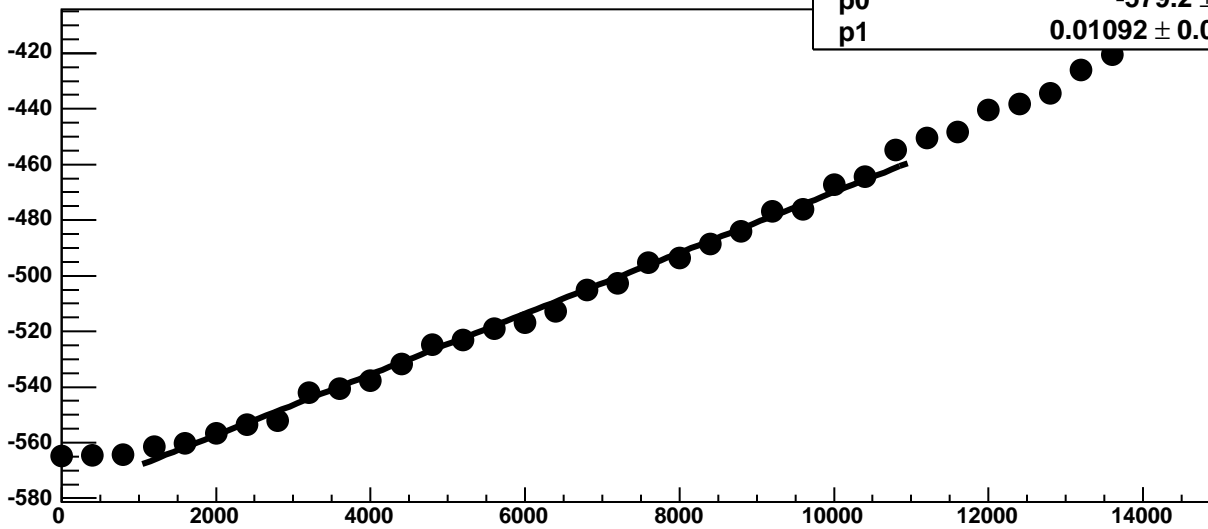
Chip 3, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

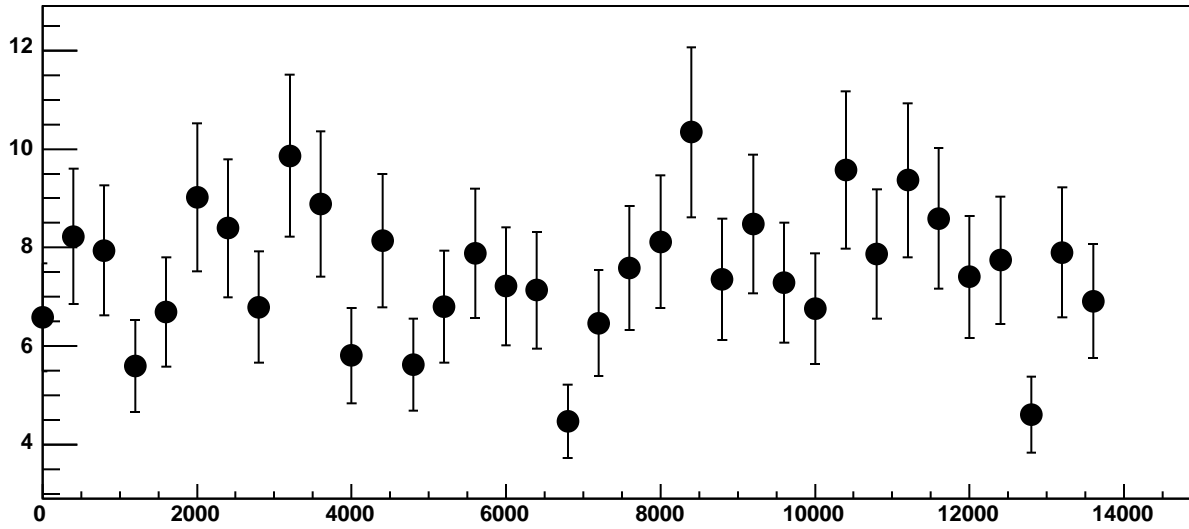


Chip 3, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

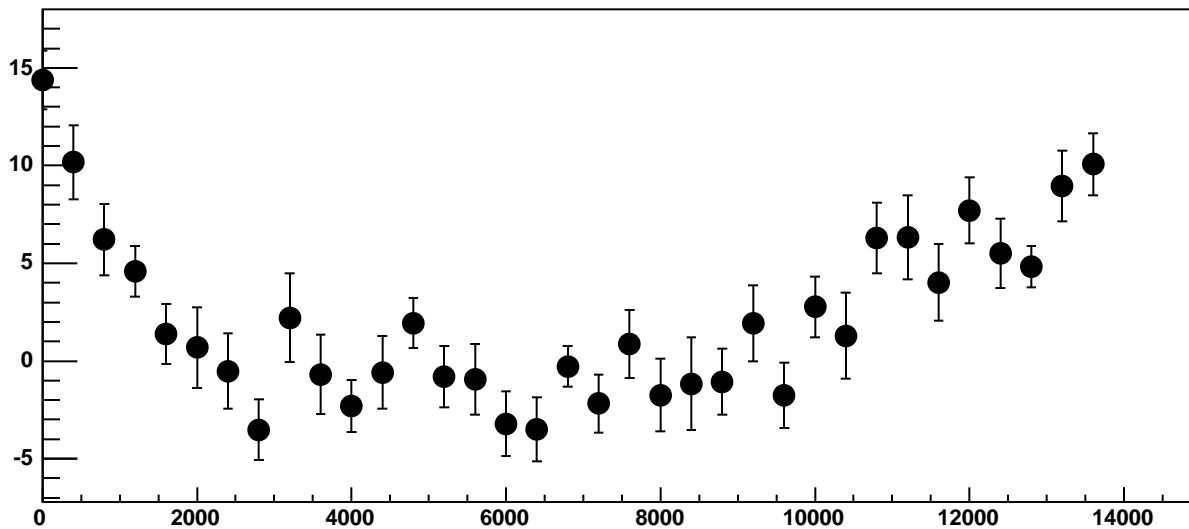


χ^2 / ndf 55.95 / 23
p0 -579.2 ± 0.7564
p1 0.01092 ± 0.0001174

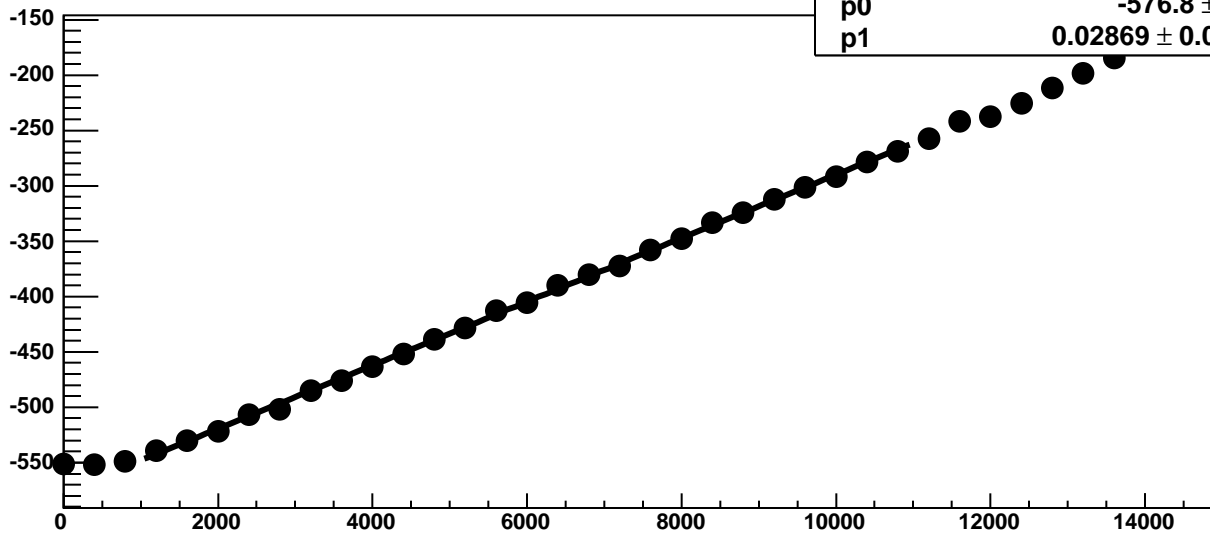
Chip 3, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

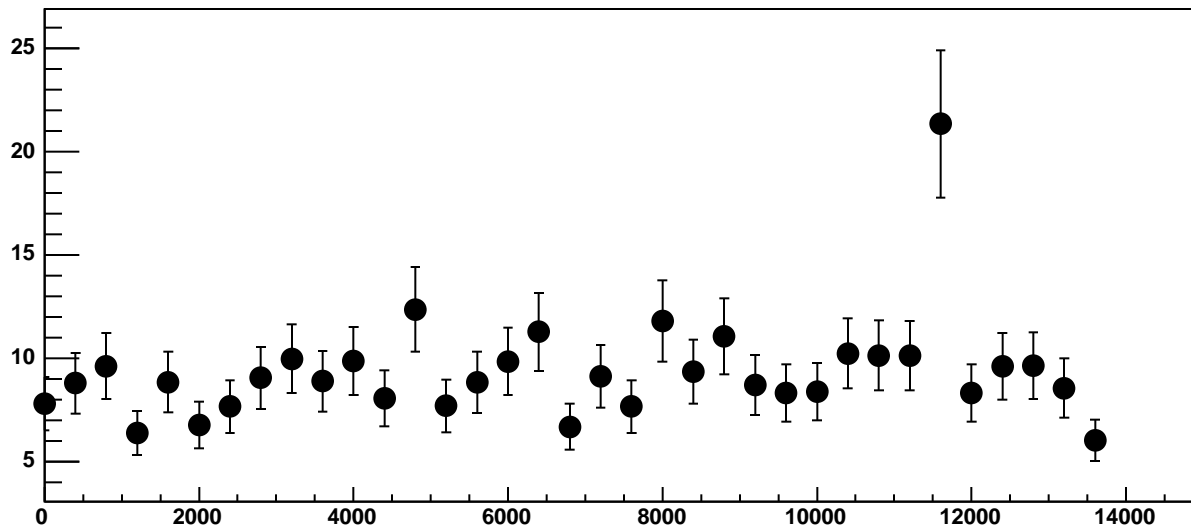


Chip 3, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC

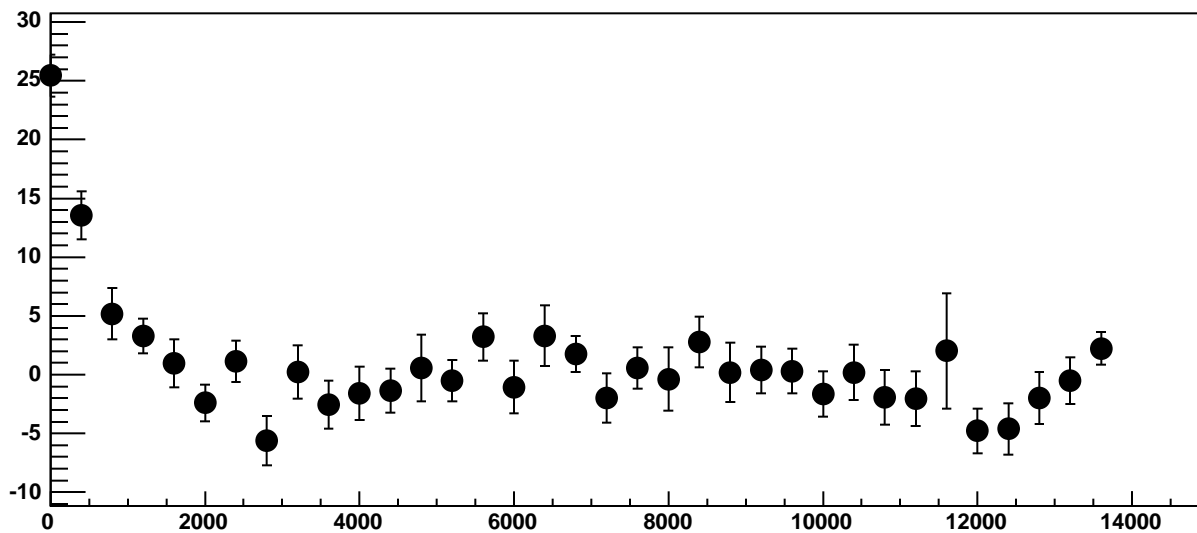


χ^2 / ndf 28.03 / 23
p0 -576.8 ± 0.8546
p1 0.02869 ± 0.0001344

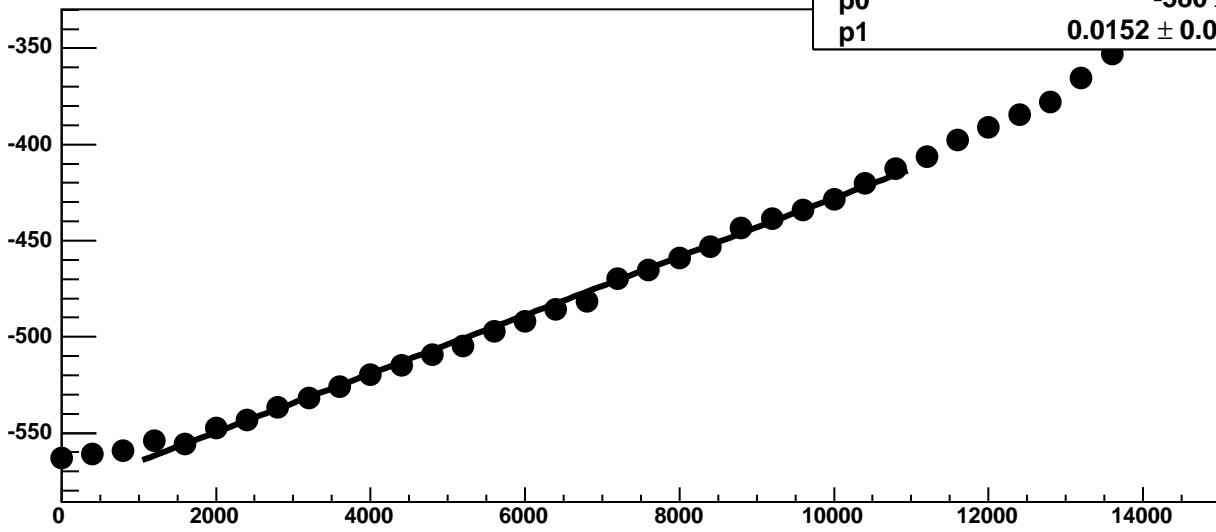
Chip 3, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC

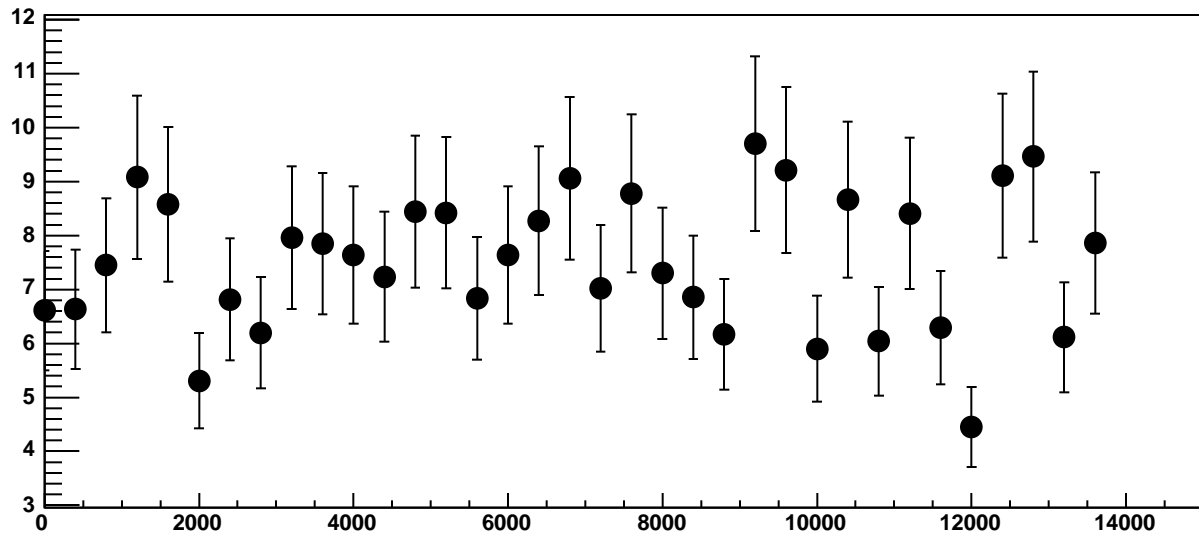


Chip 3, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

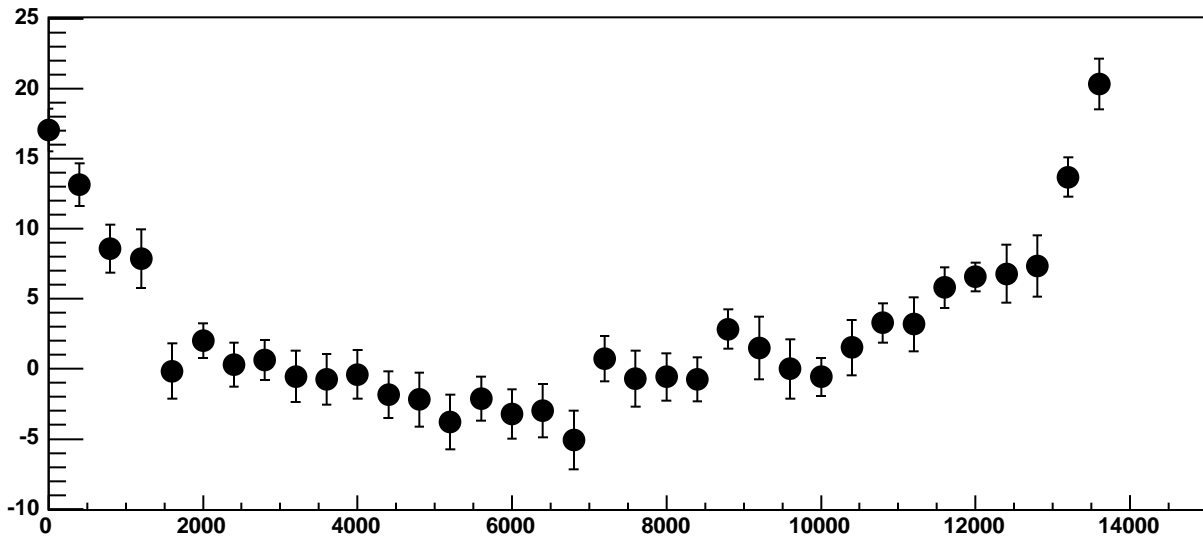


χ^2 / ndf 49.08 / 23
p0 -580 ± 0.758
p1 0.0152 ± 0.0001132

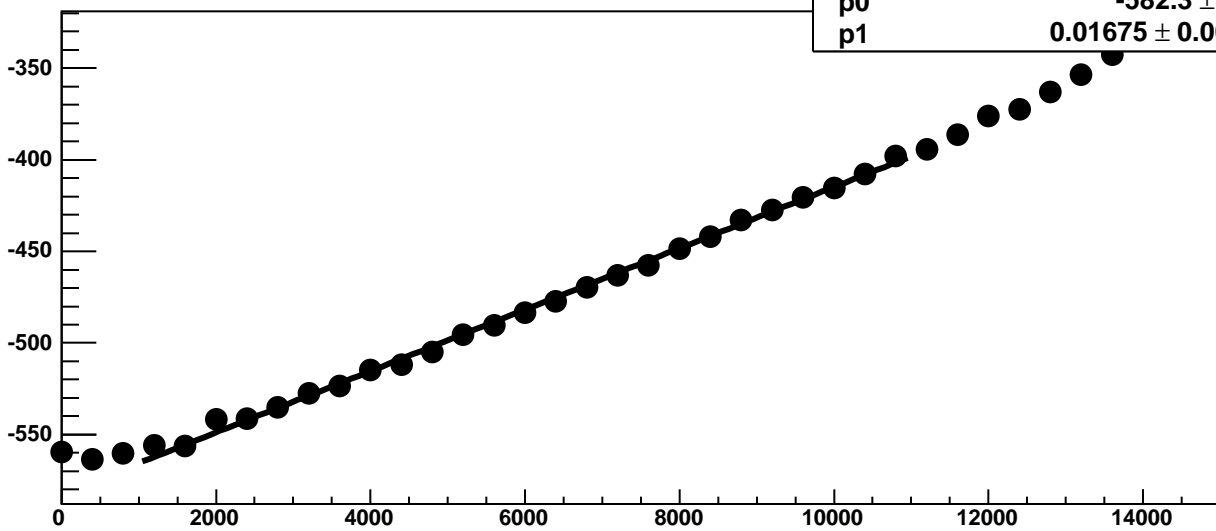
Chip 3, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



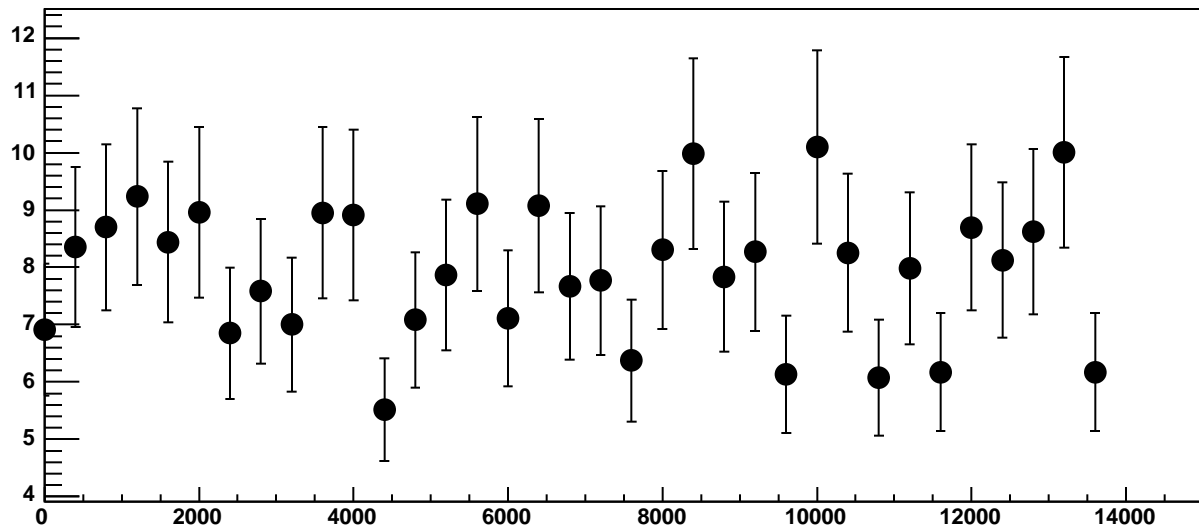
Chip 3, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



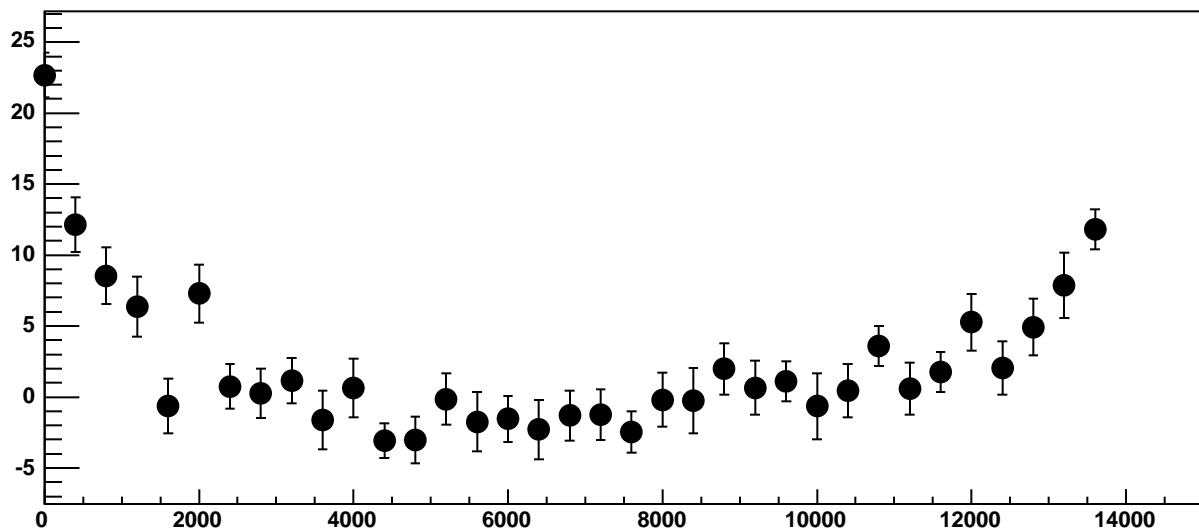
Chip 3, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



Chip 3, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

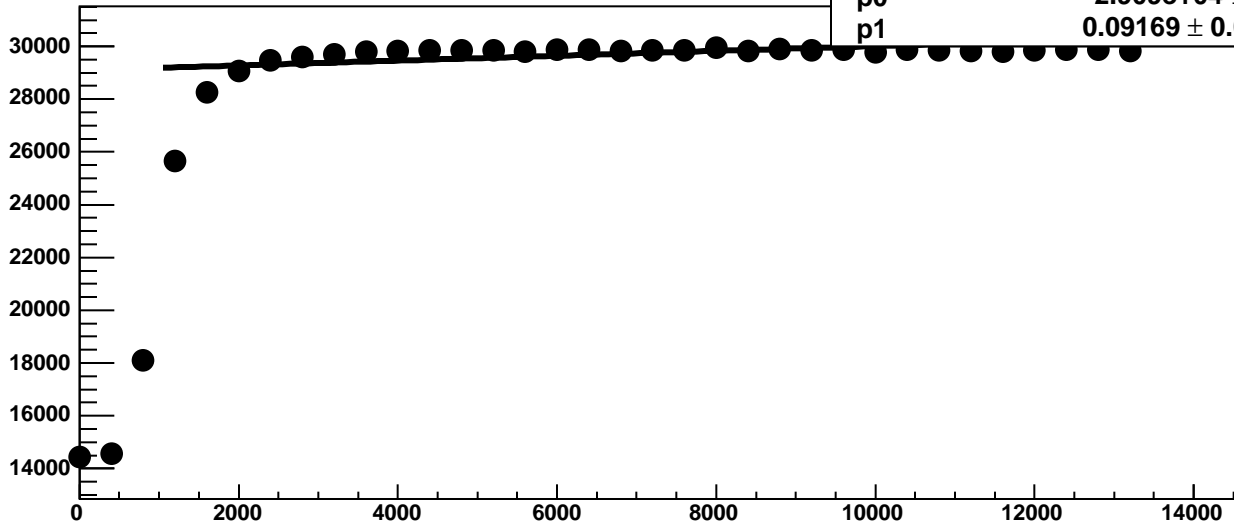
5427 / 23

p0

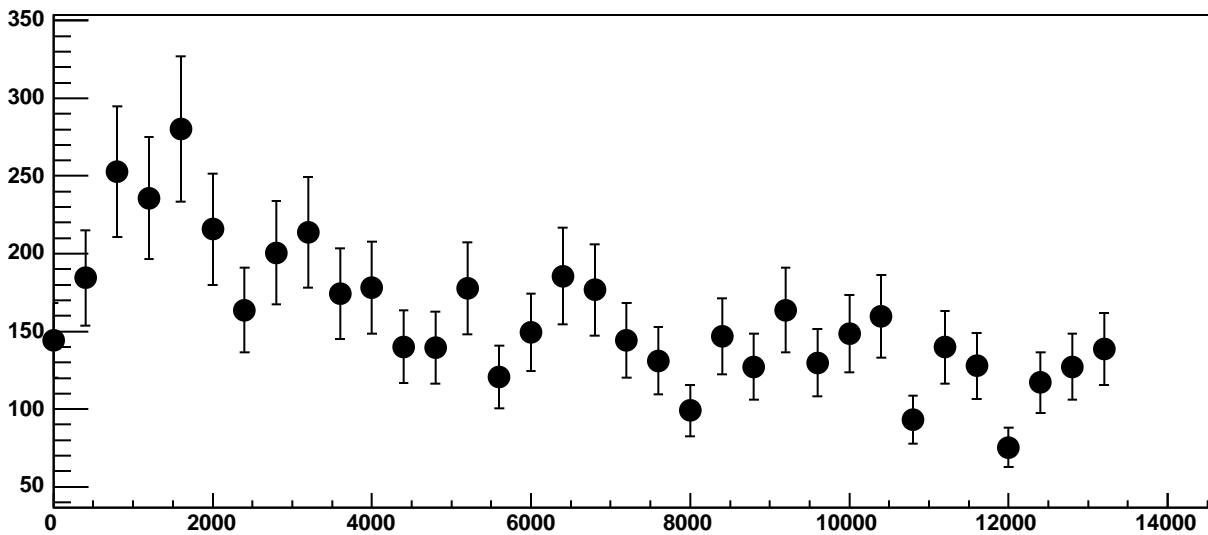
$2.909\text{e}+04 \pm 18.98$

p1

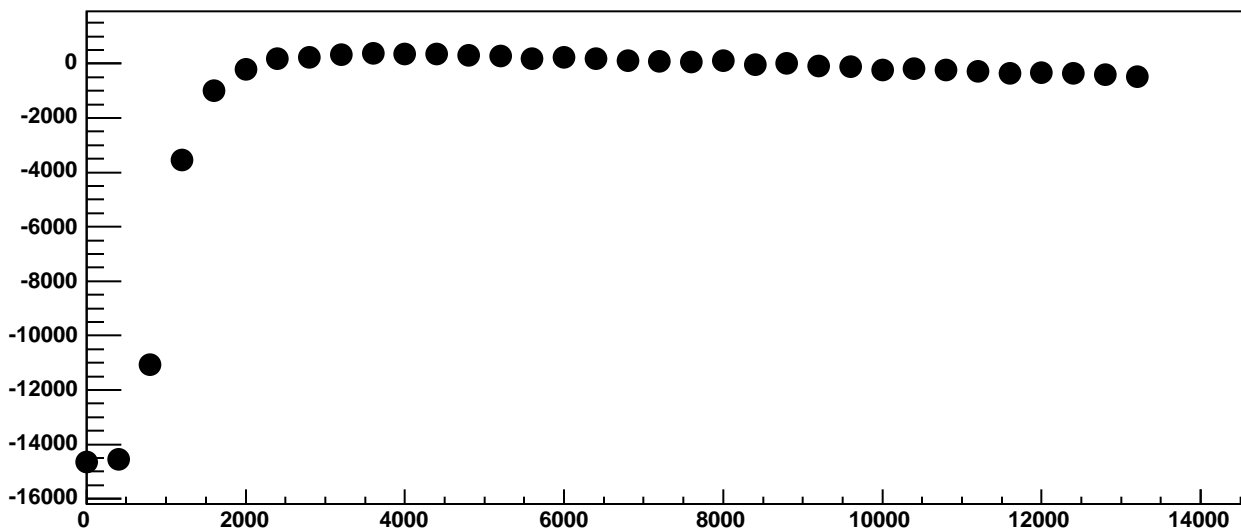
0.09169 ± 0.002545



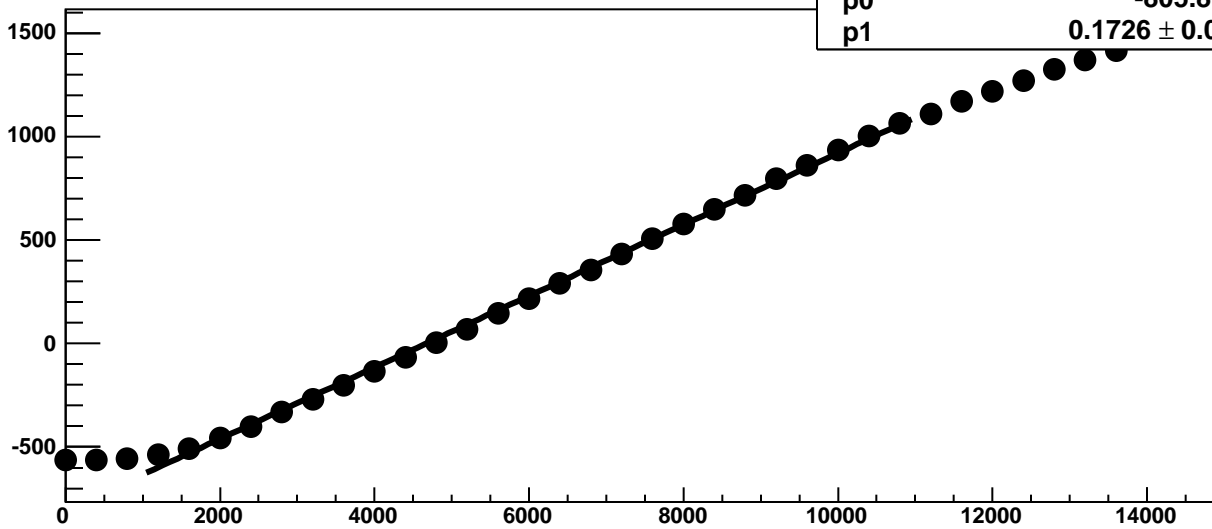
Chip 3, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC



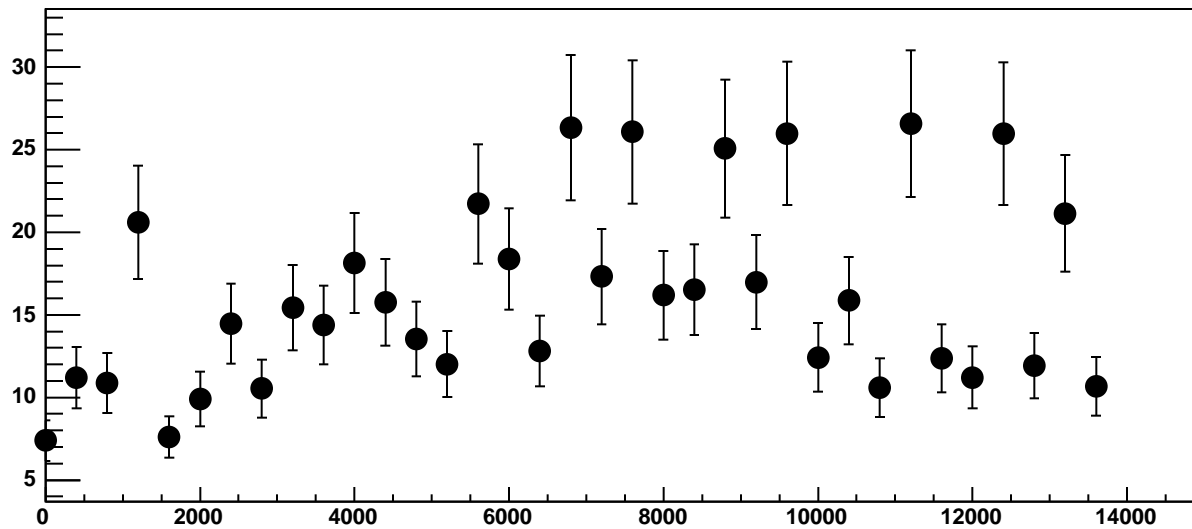
Chip 3, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC



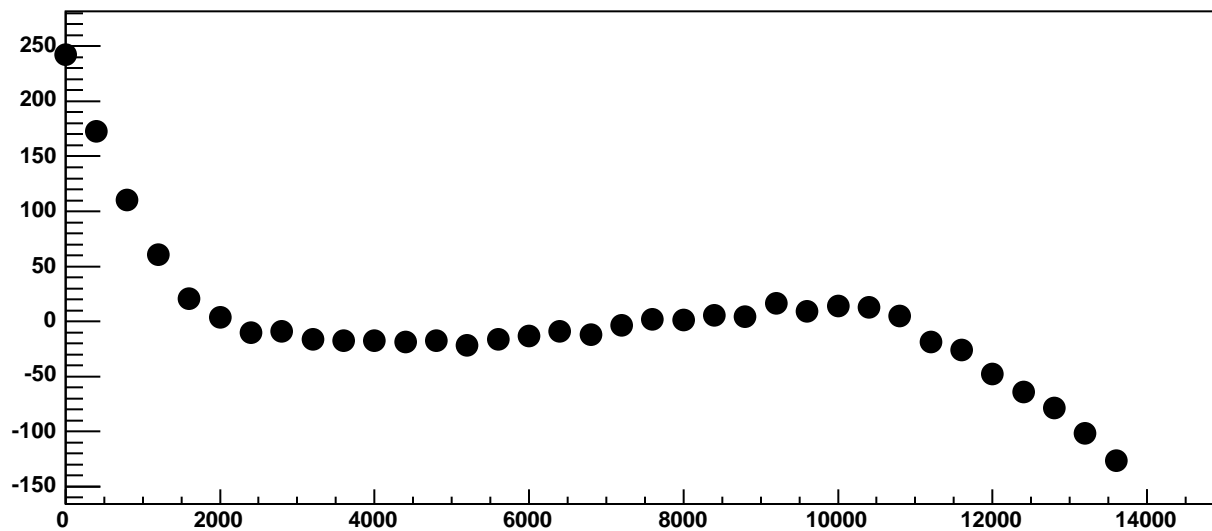
Chip 3, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC



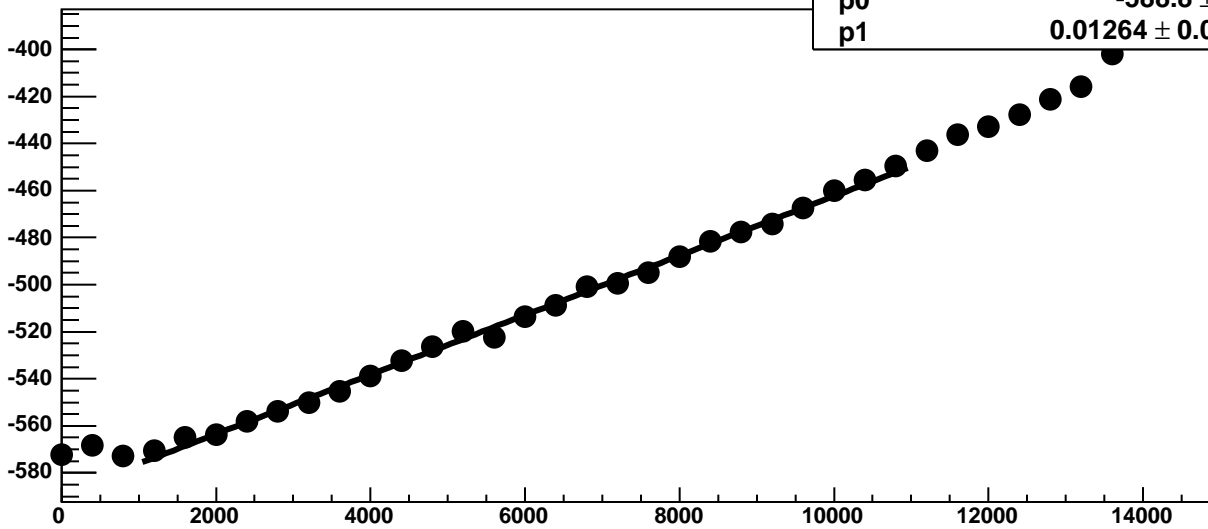
Chip 3, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

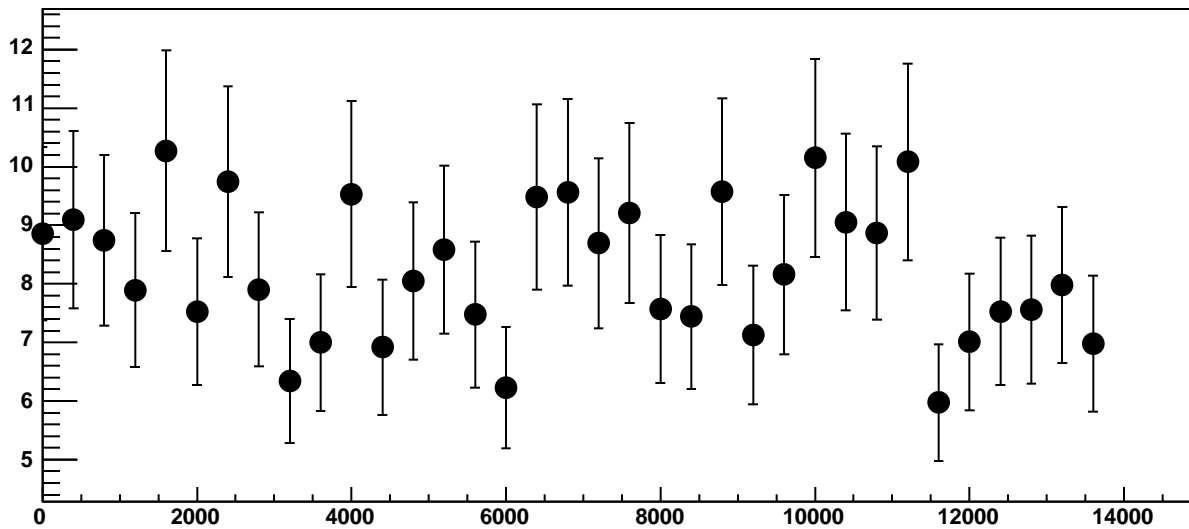


Chip 3, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

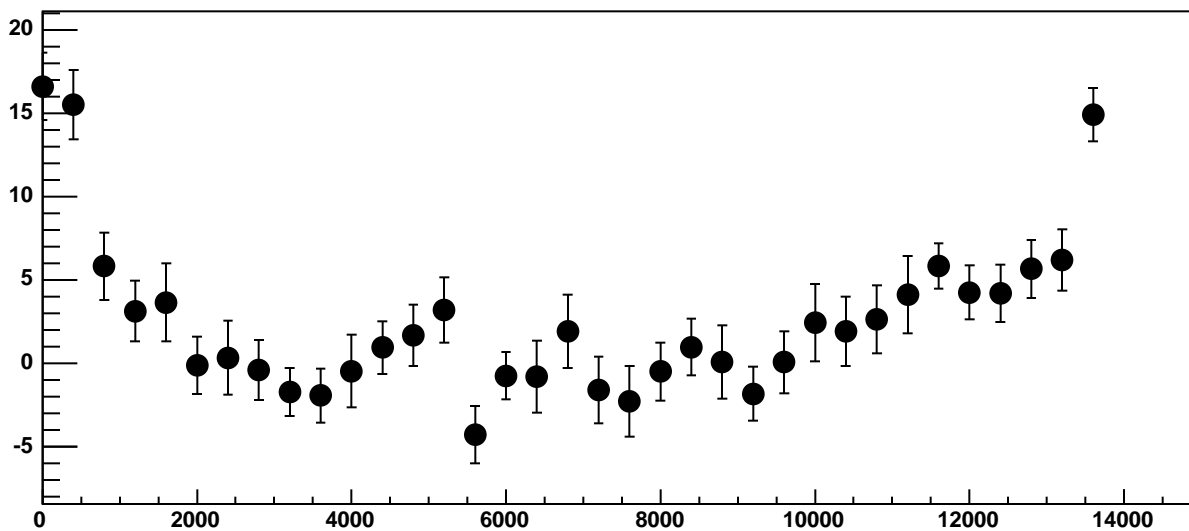


χ^2 / ndf 26.69 / 23
p0 -588.8 ± 0.8614
p1 0.01264 ± 0.0001332

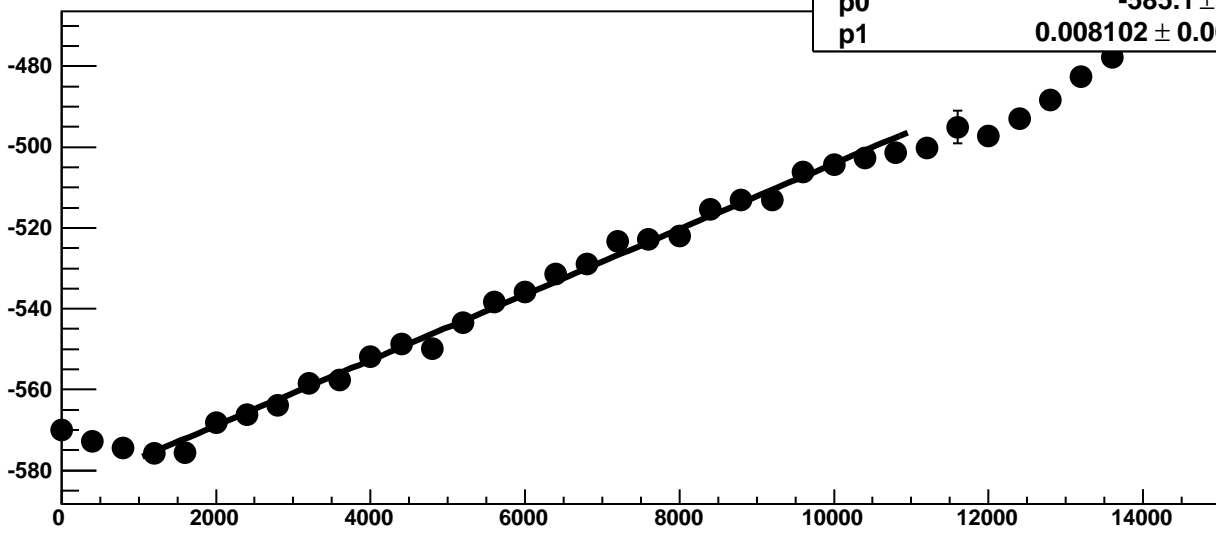
Chip 3, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

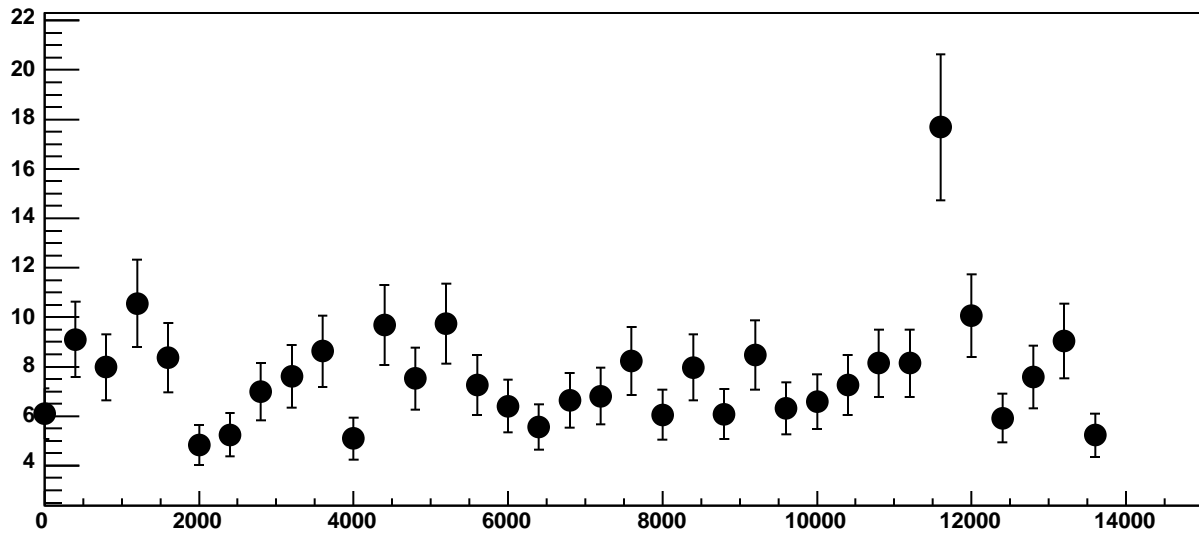


Chip 3, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

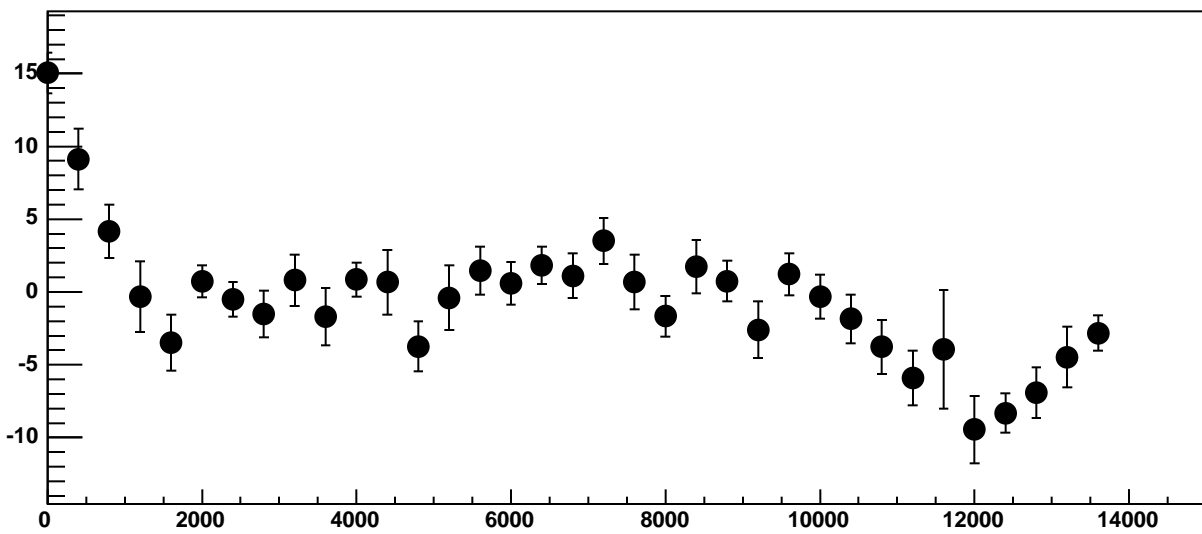


χ^2 / ndf 30.28 / 23
p0 -585.1 ± 0.7227
p1 0.008102 ± 0.0001103

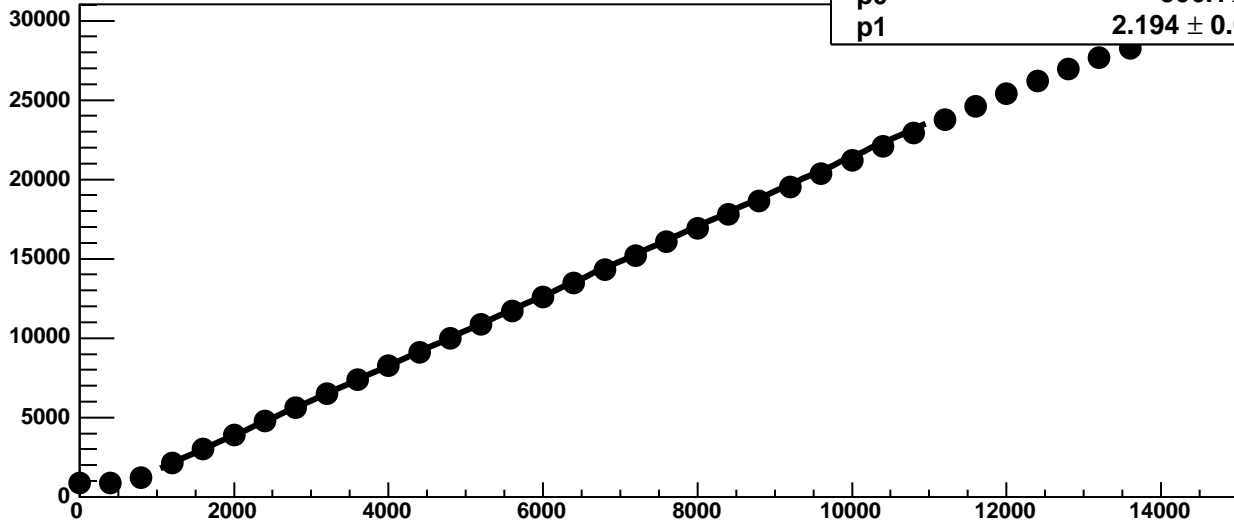
Chip 3, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

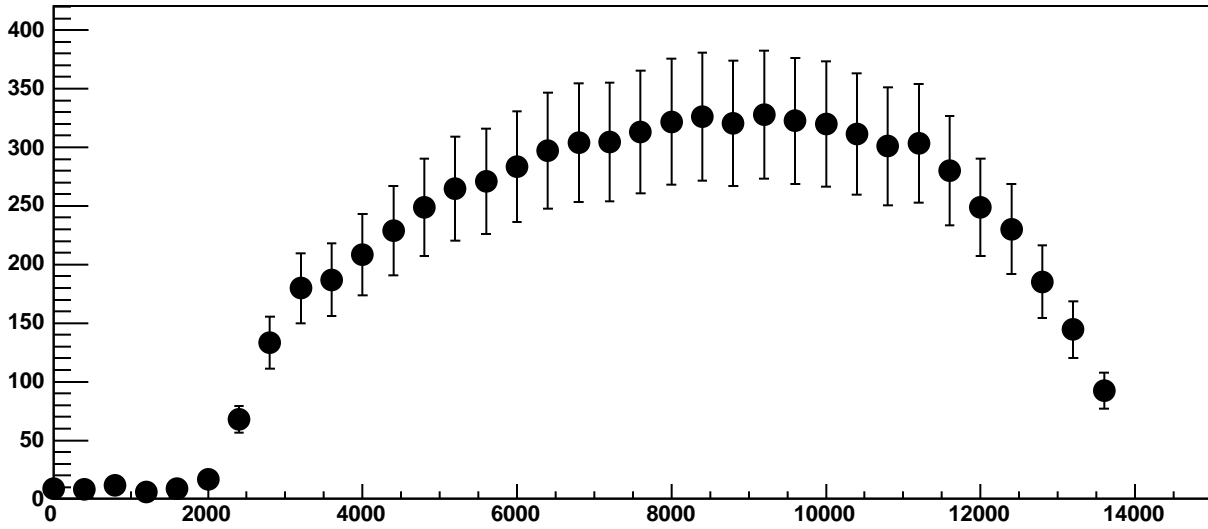


Chip 3, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC

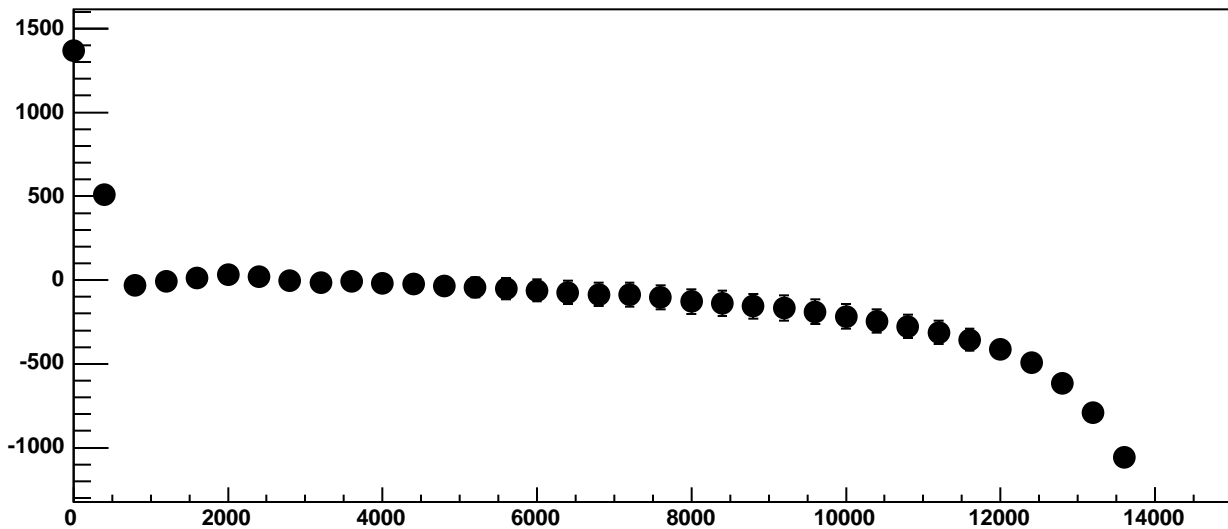


χ^2 / ndf 231.6 / 23
p0 -506.1 ± 3.222
p1 2.194 ± 0.002164

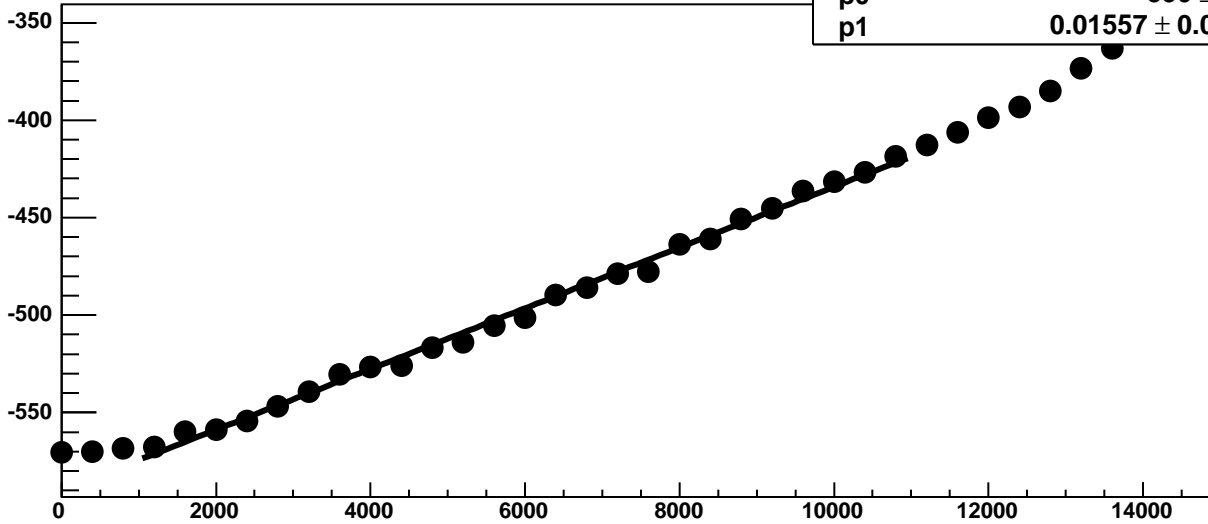
Chip 3, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC

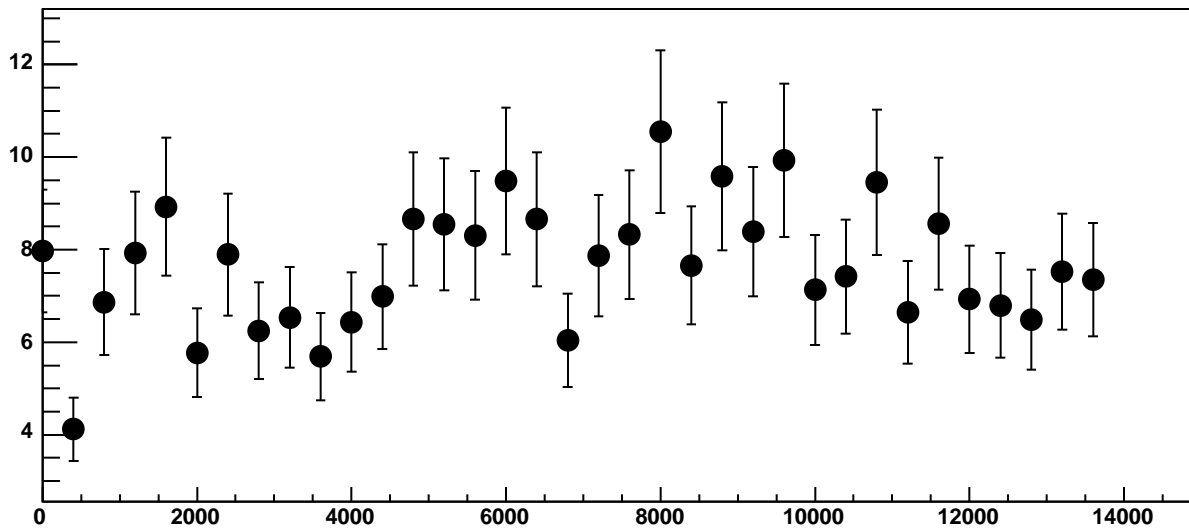


Chip 3, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

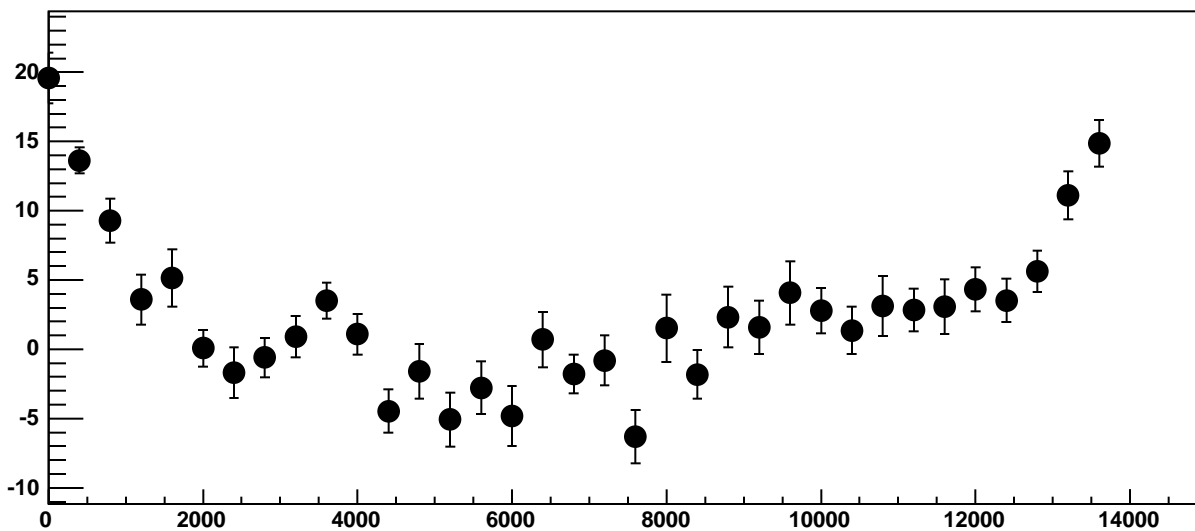


χ^2 / ndf 66.92 / 23
p0 -590 ± 0.7634
p1 0.01557 ± 0.0001223

Chip 3, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

χ^2 / ndf

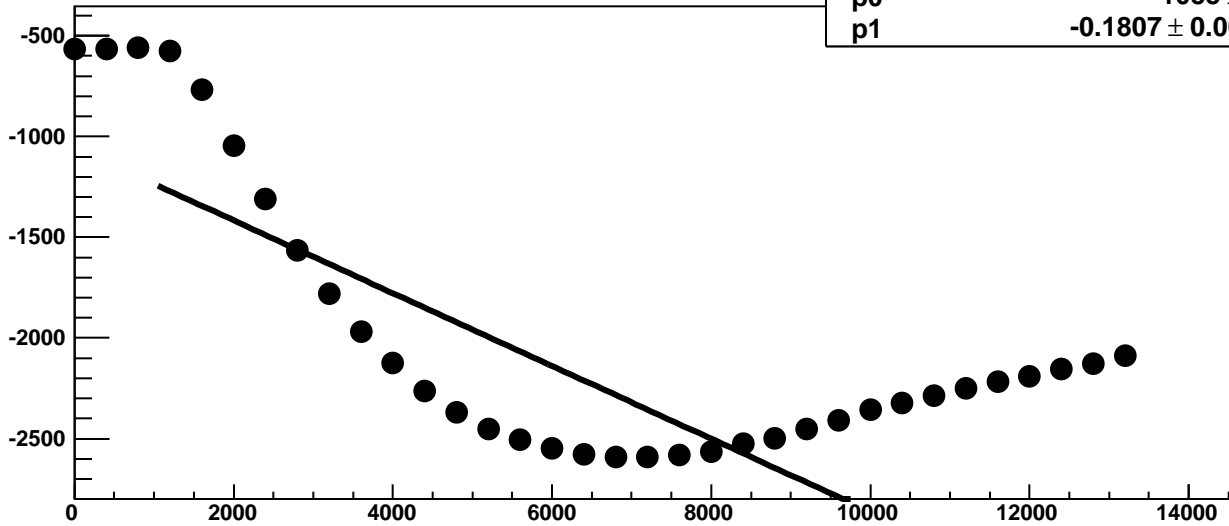
6.413e+05 / 23

p0

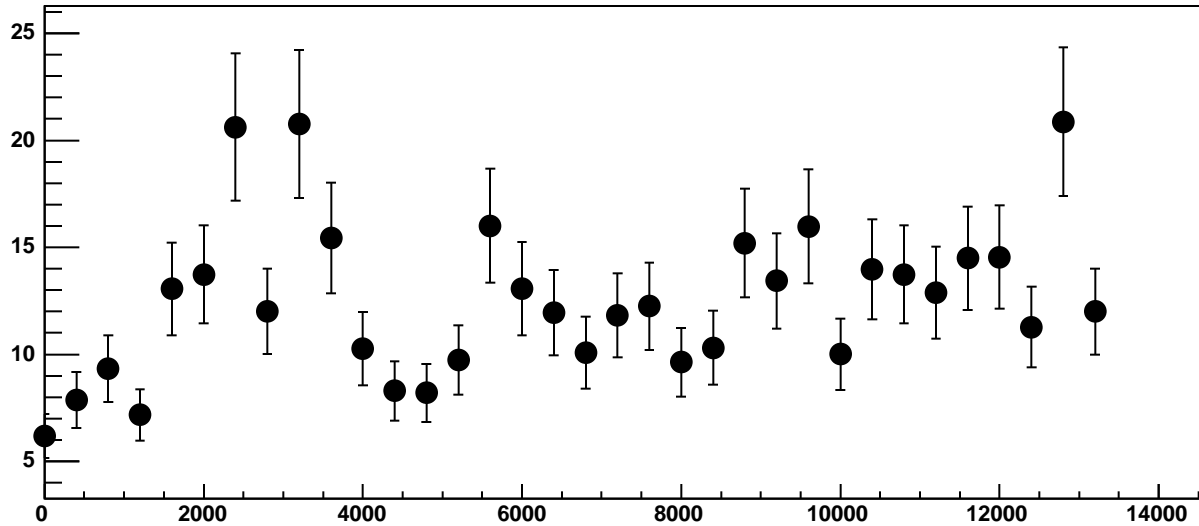
-1055 ± 1.184

p1

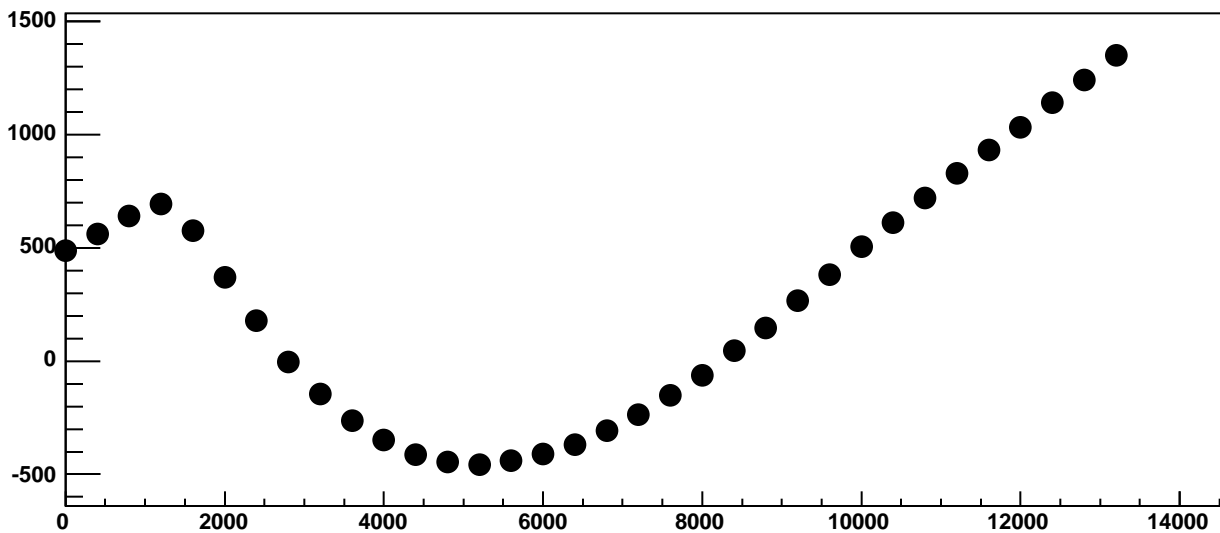
-0.1807 ± 0.0001852



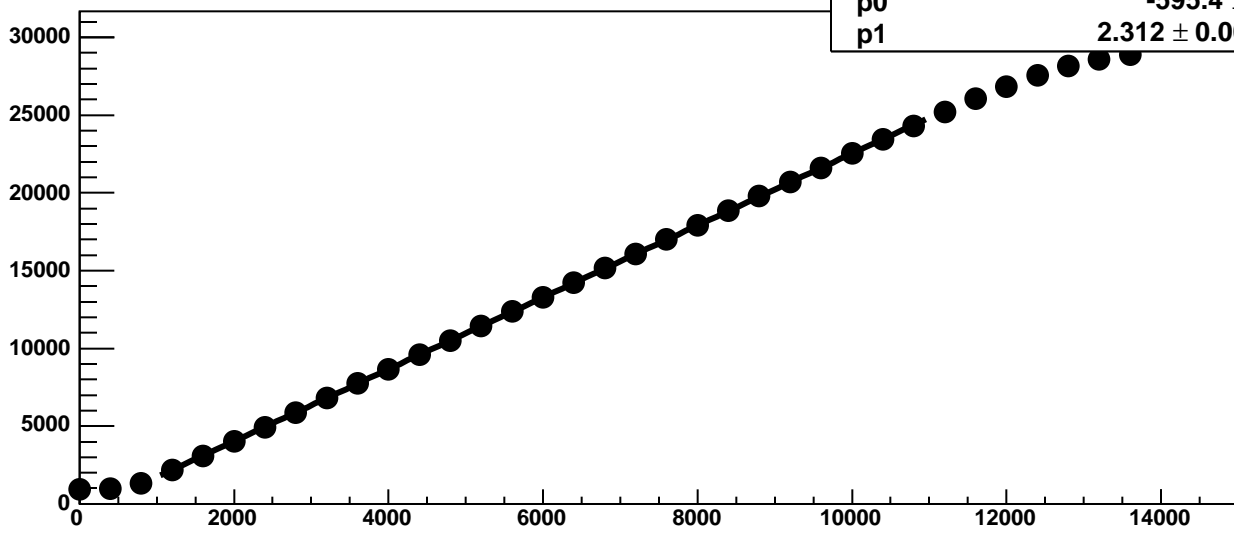
Chip 3, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



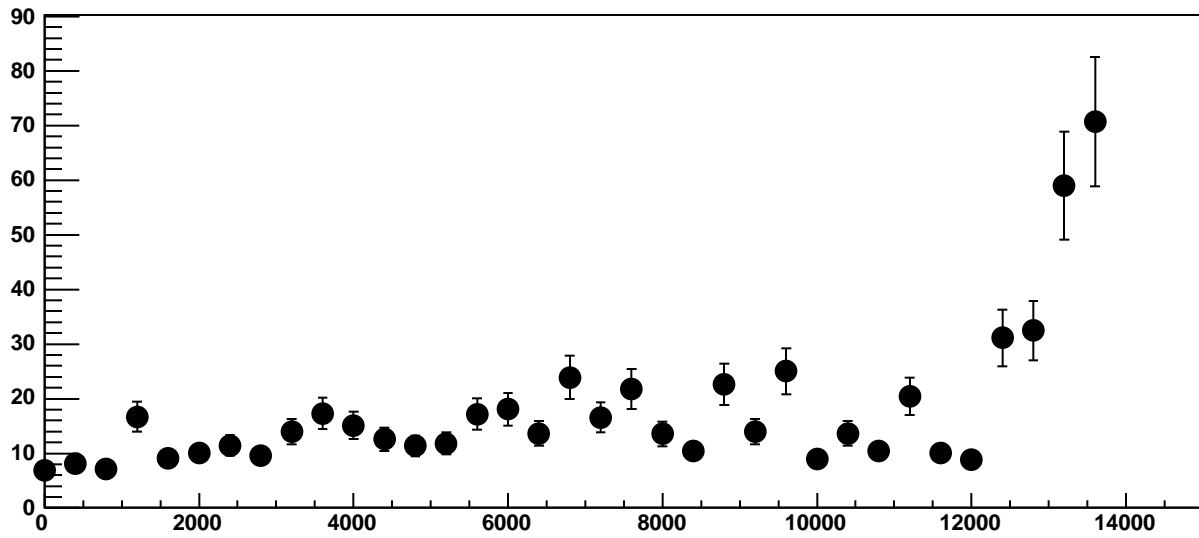
Chip 3, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



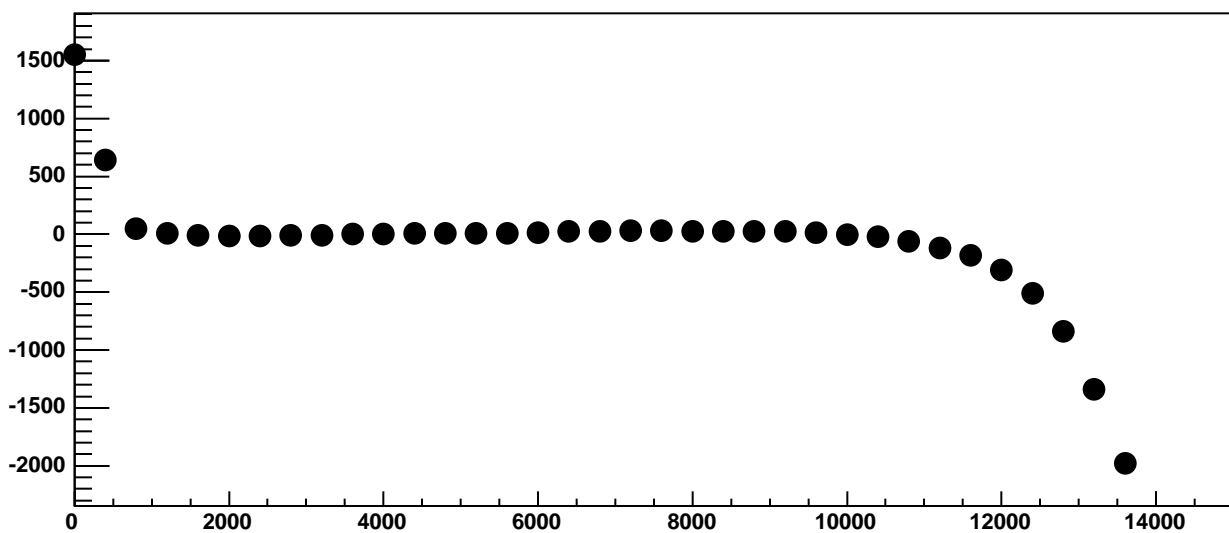
Chip 3, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC



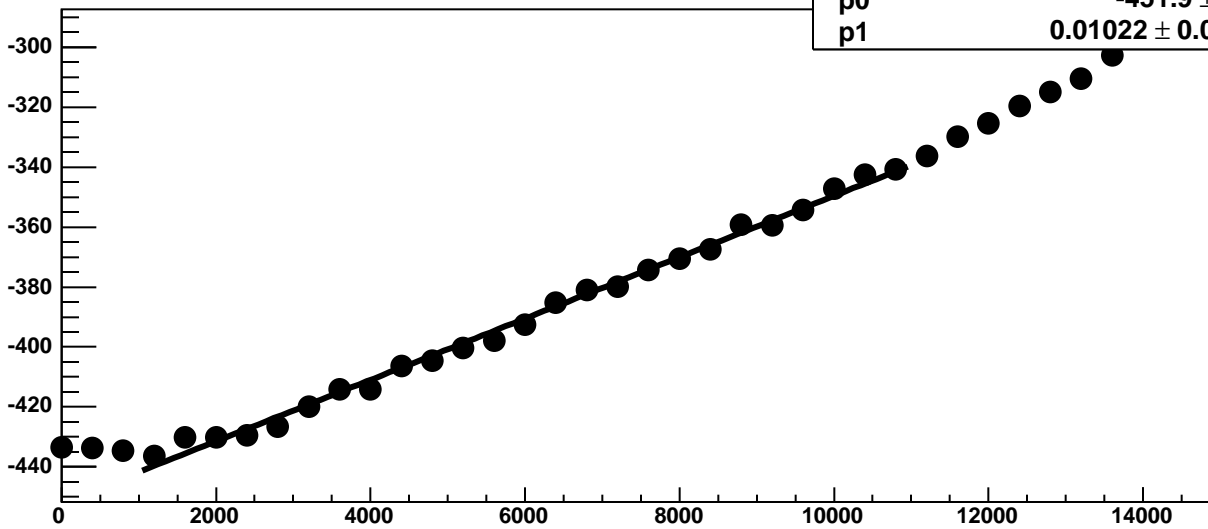
Chip 3, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



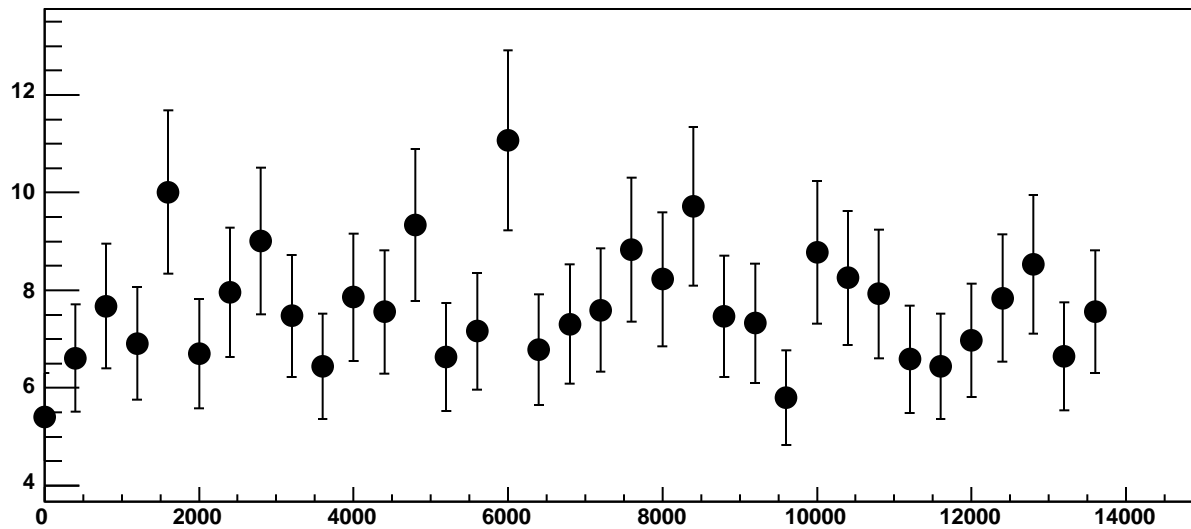
Chip 3, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC



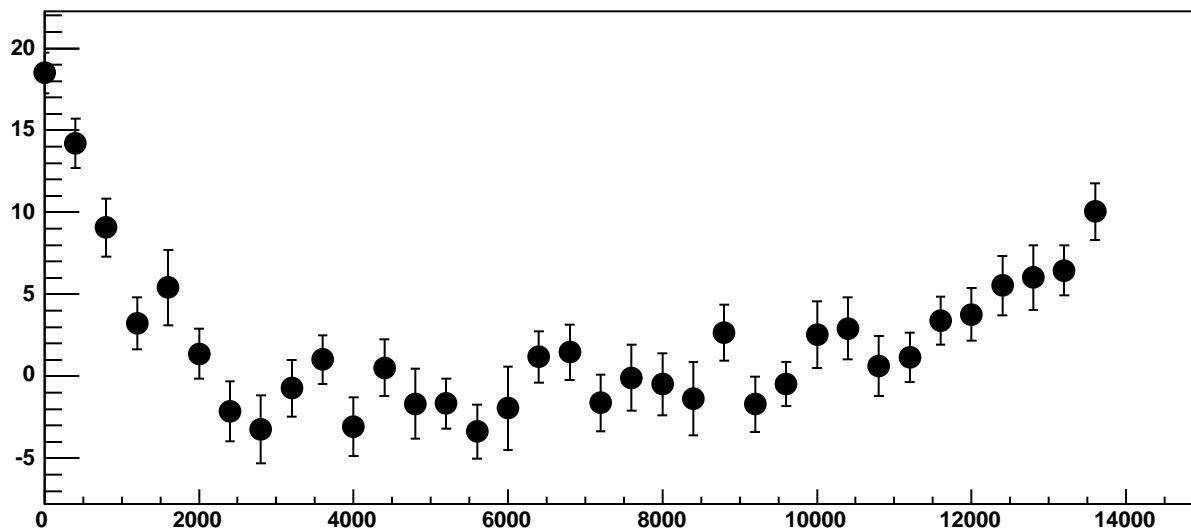
Chip 3, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



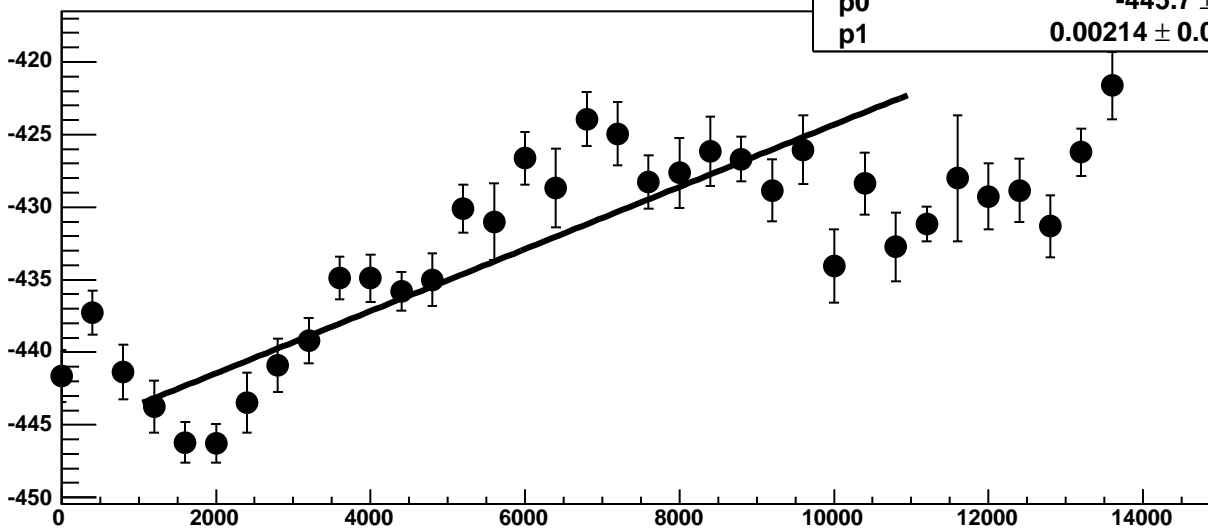
Chip 3, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

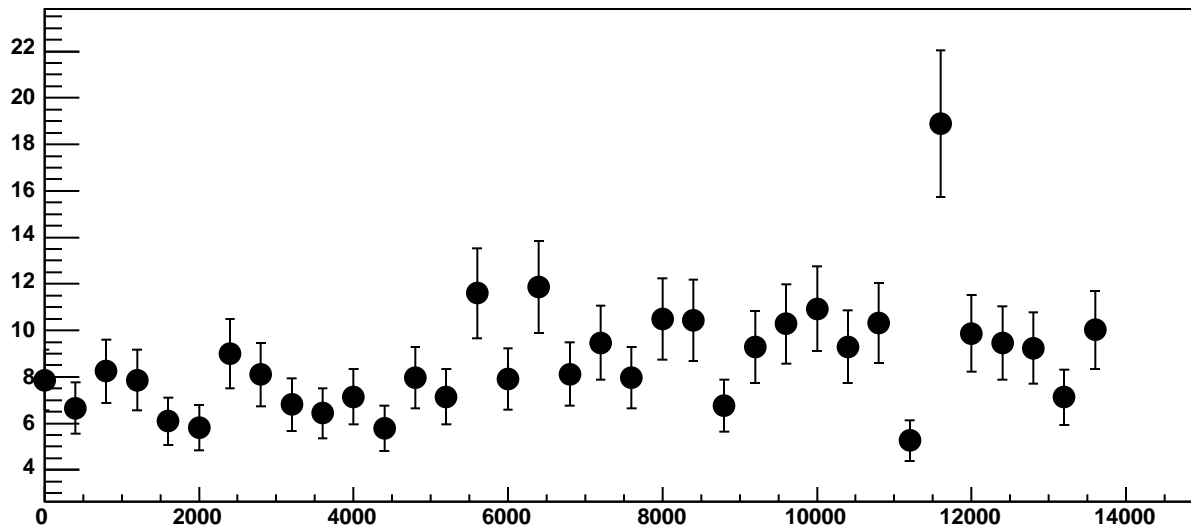


Chip 3, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

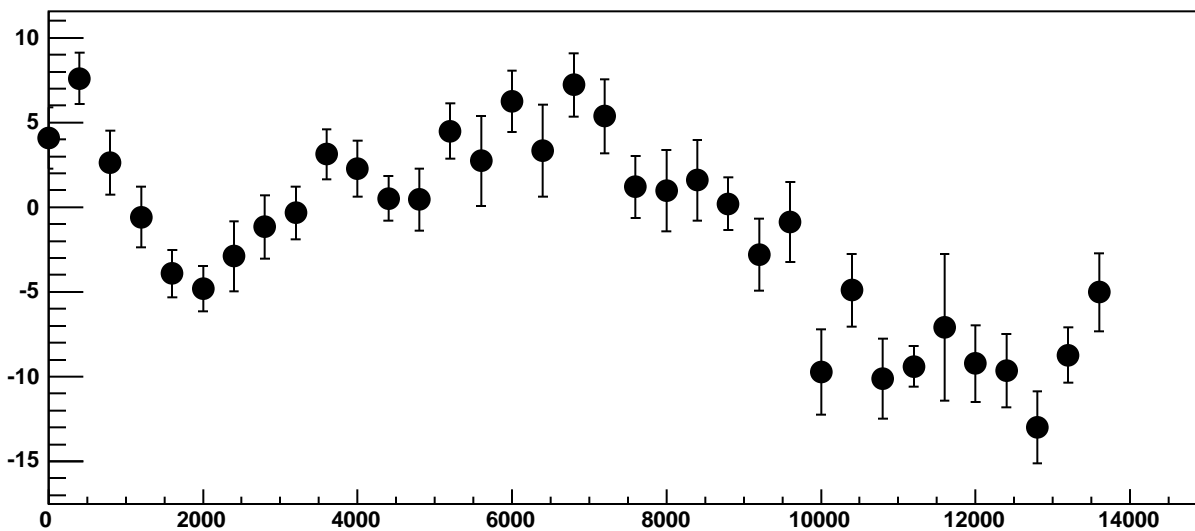


χ^2 / ndf 114.6 / 23
p0 -445.7 ± 0.7752
p1 0.00214 ± 0.0001302

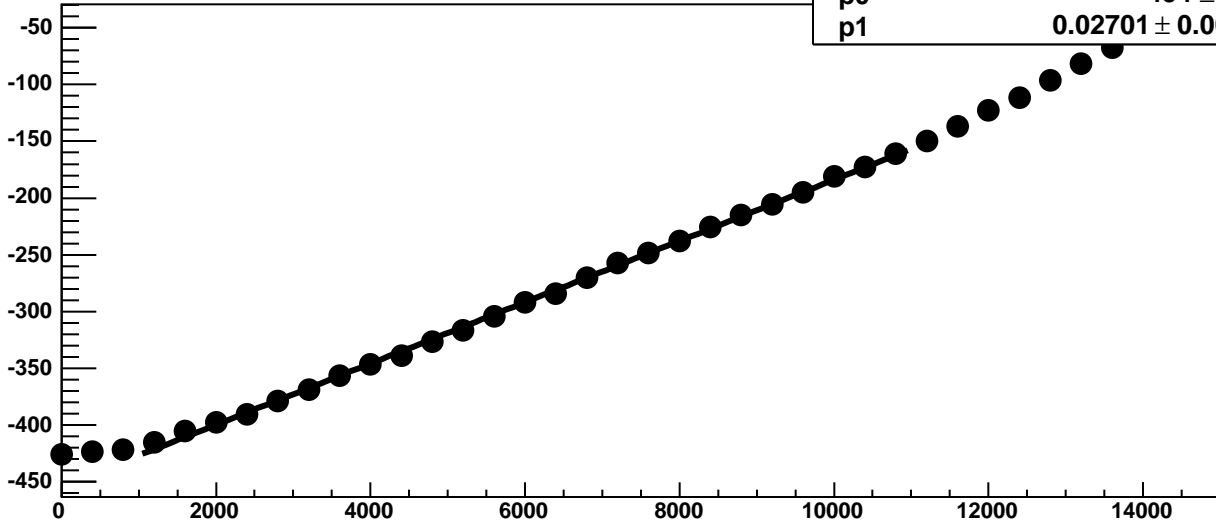
Chip 3, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



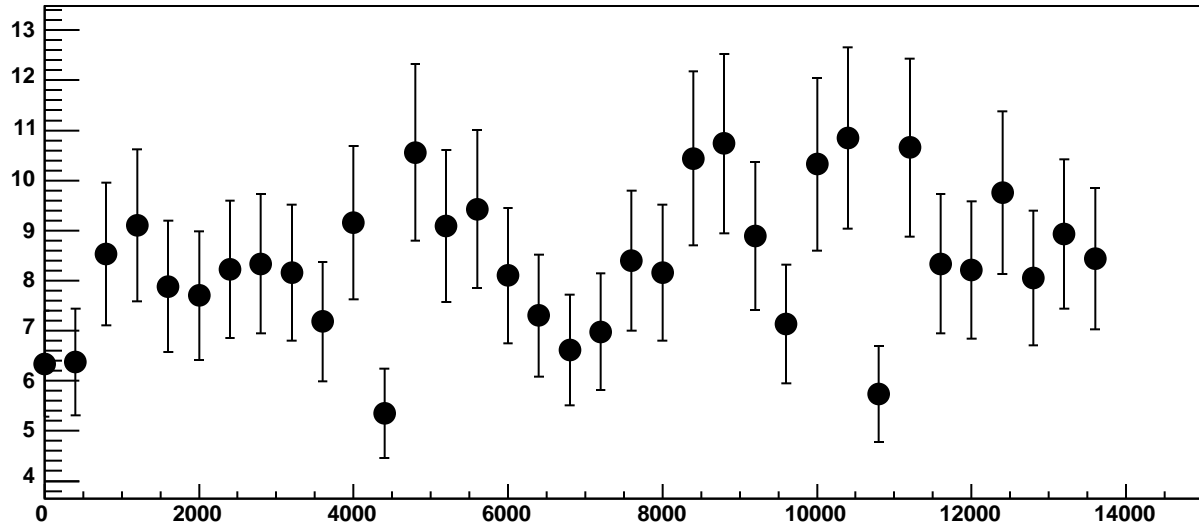
Chip 3, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



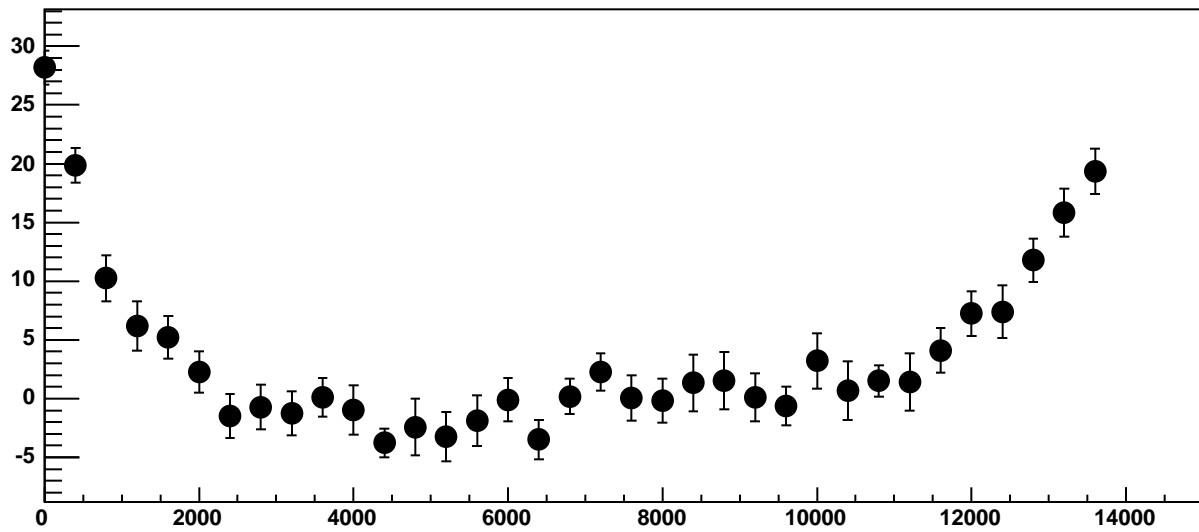
Chip 3, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC



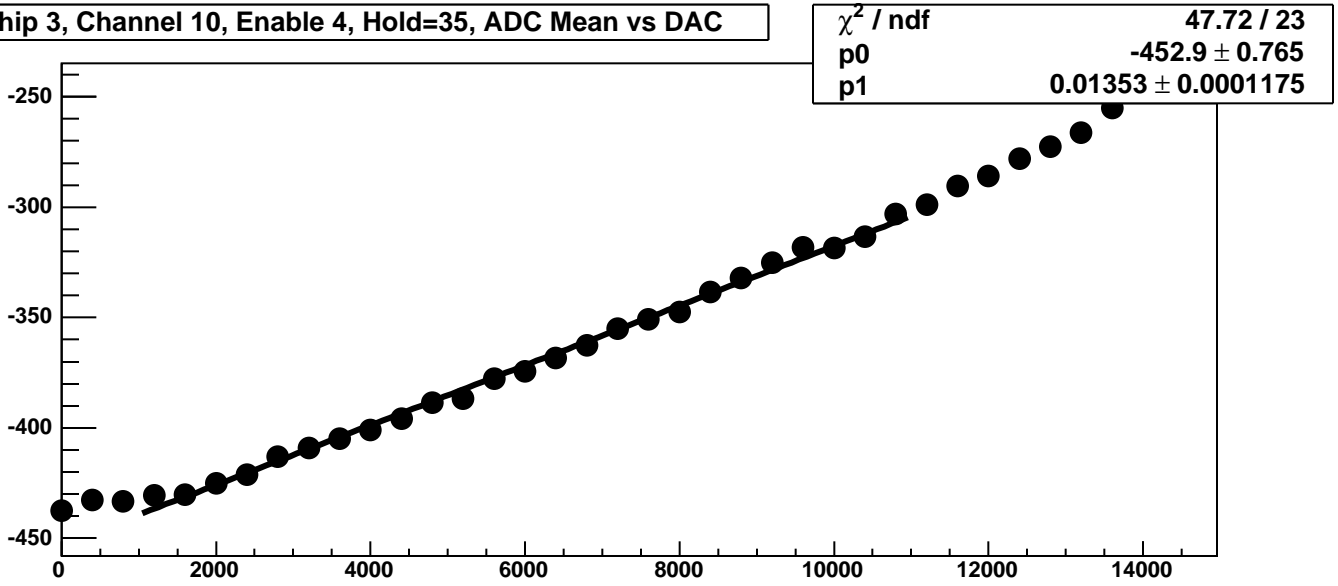
Chip 3, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



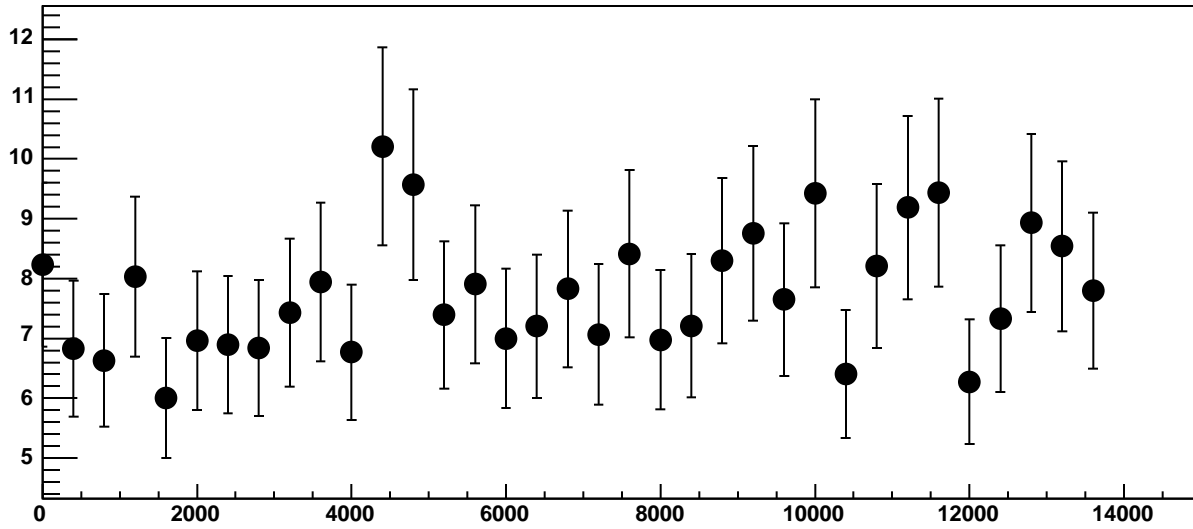
Chip 3, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC



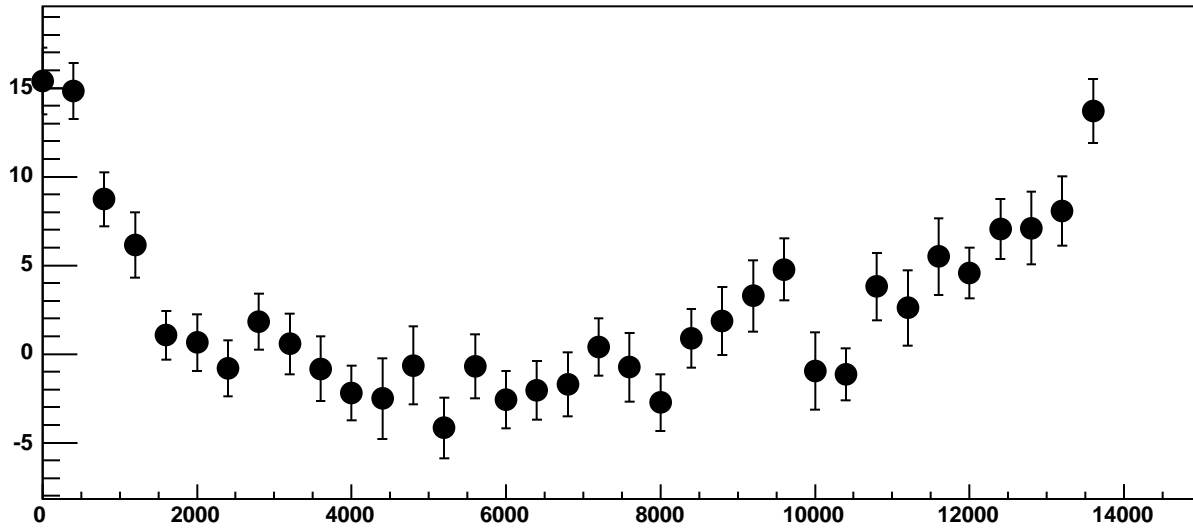
Chip 3, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



Chip 3, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

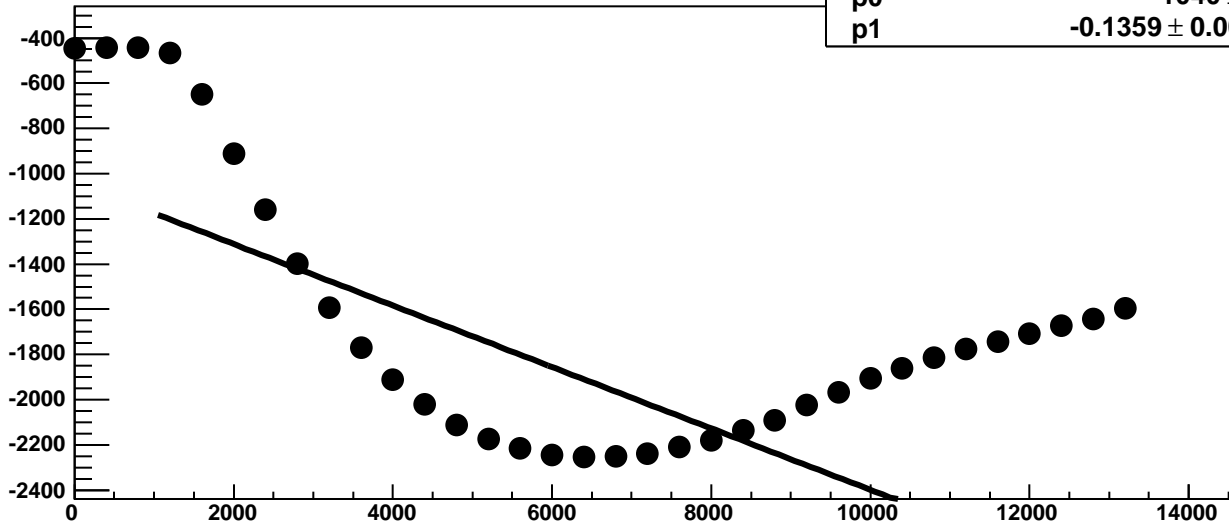


Chip 3, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

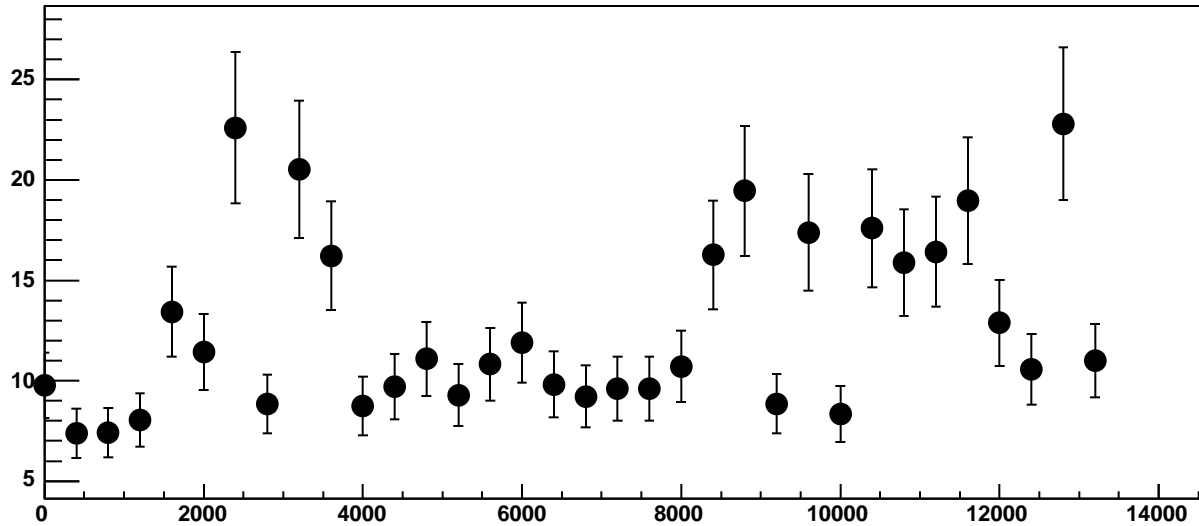


Chip 3, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

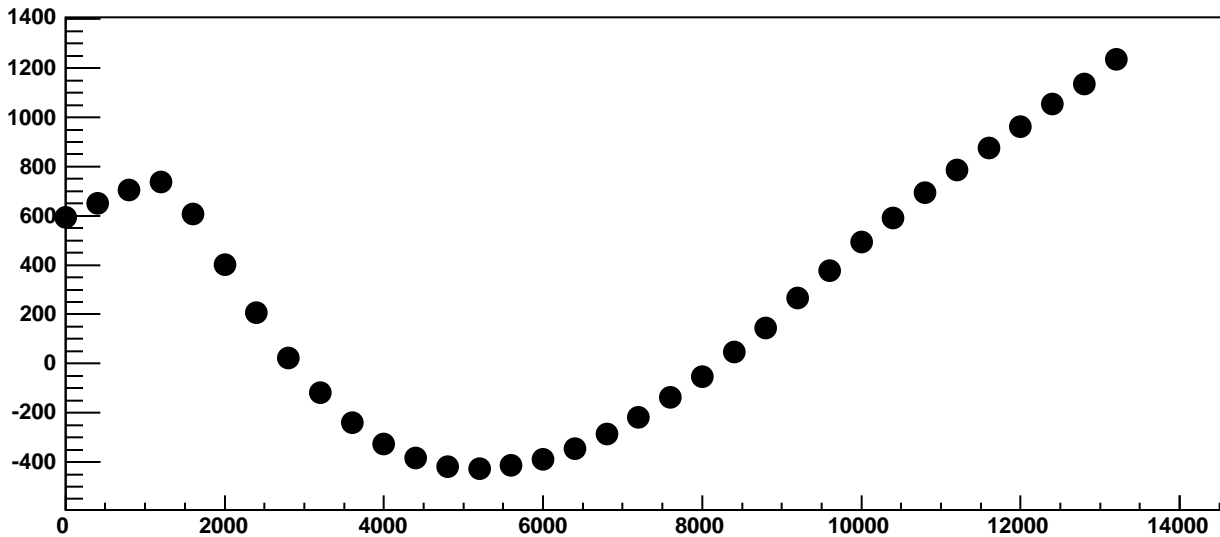
χ^2 / ndf 6.068e+05 / 23
p0 -1040 ± 1.182
p1 -0.1359 ± 0.0001835



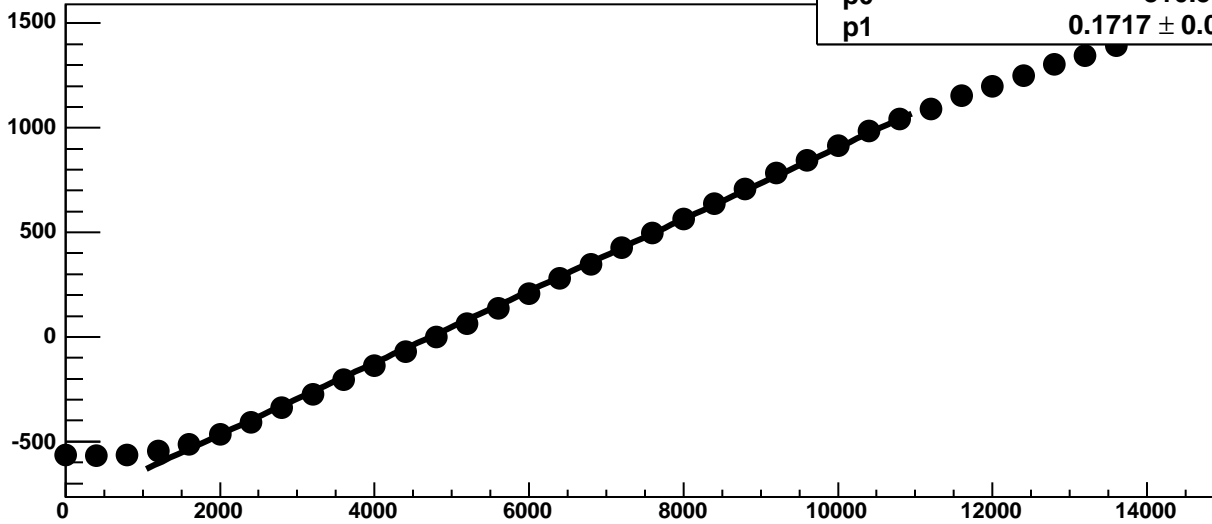
Chip 3, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



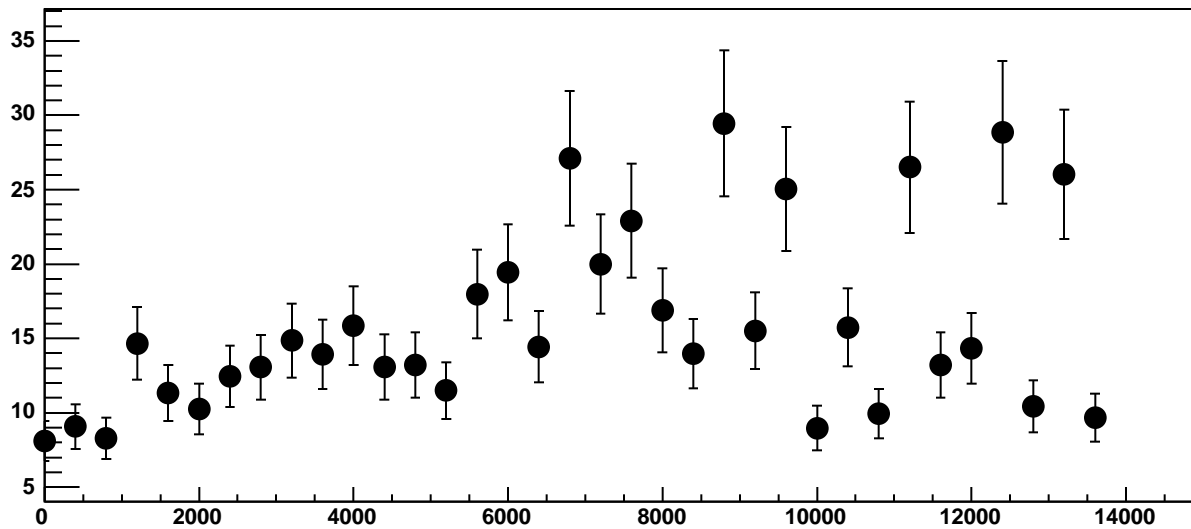
Chip 3, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



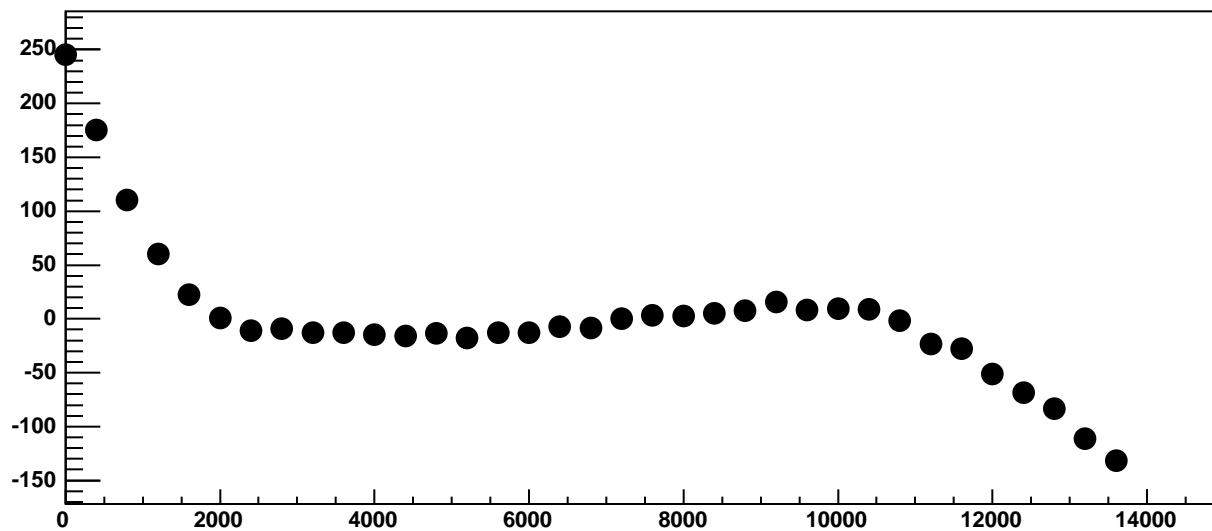
Chip 3, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC



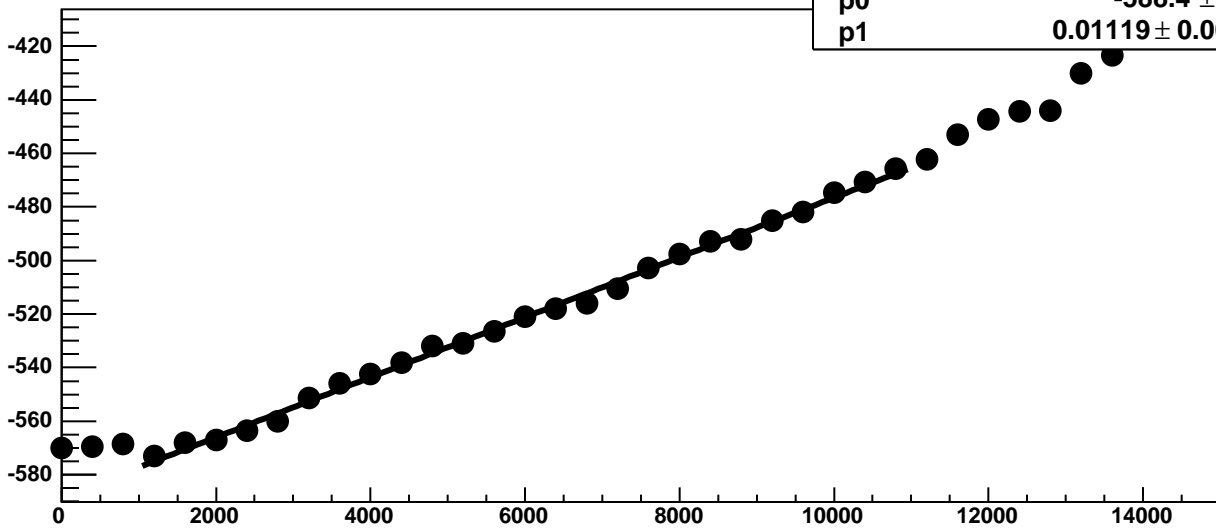
Chip 3, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



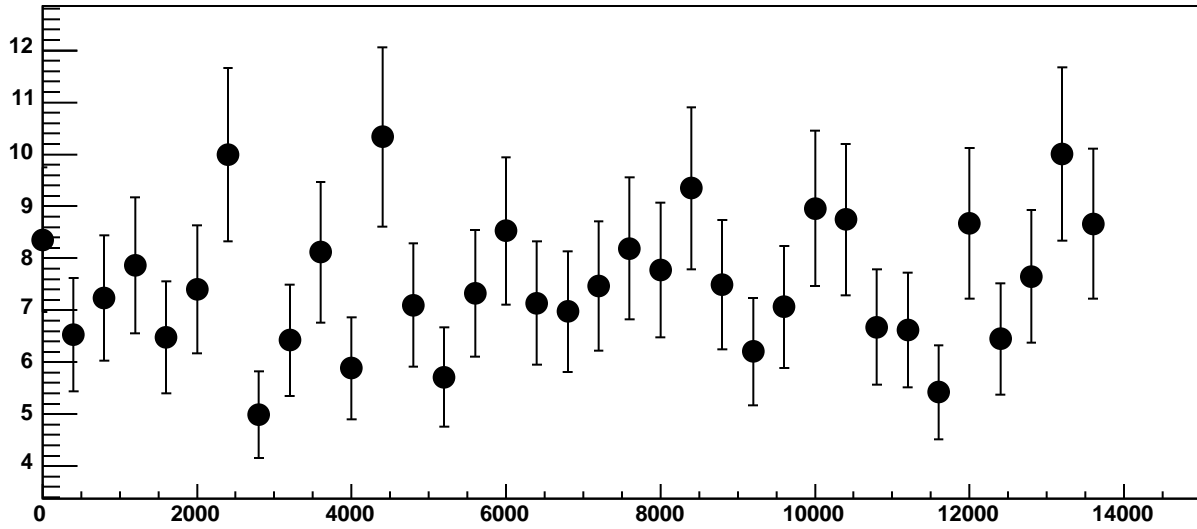
Chip 3, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC



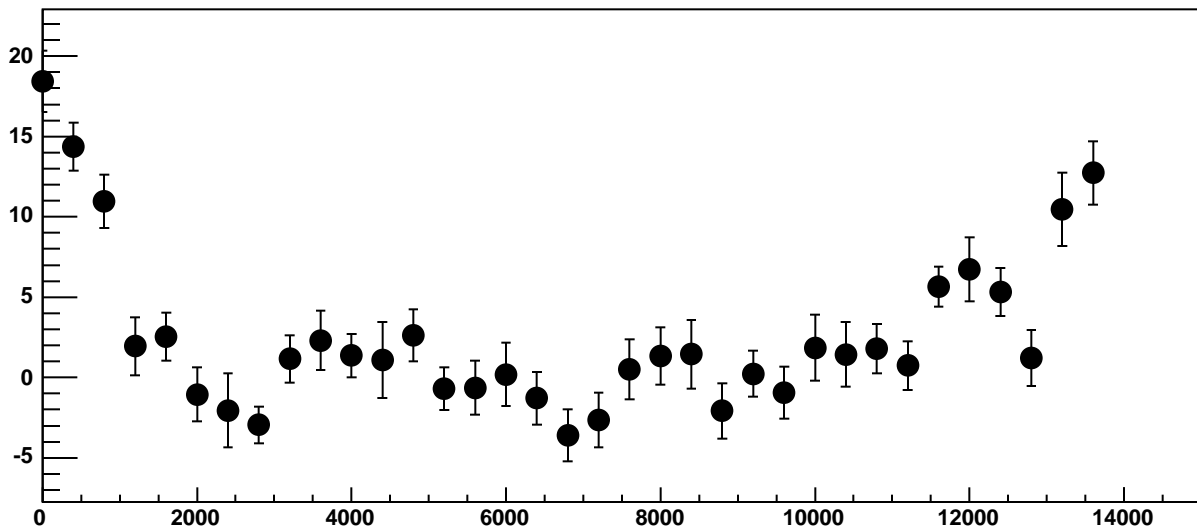
Chip 3, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



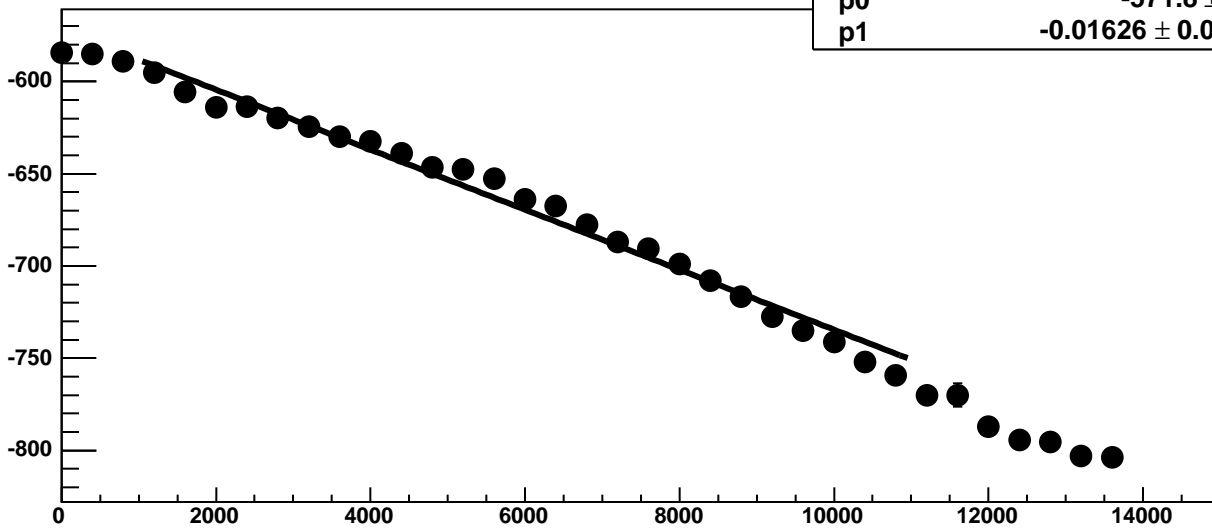
Chip 3, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

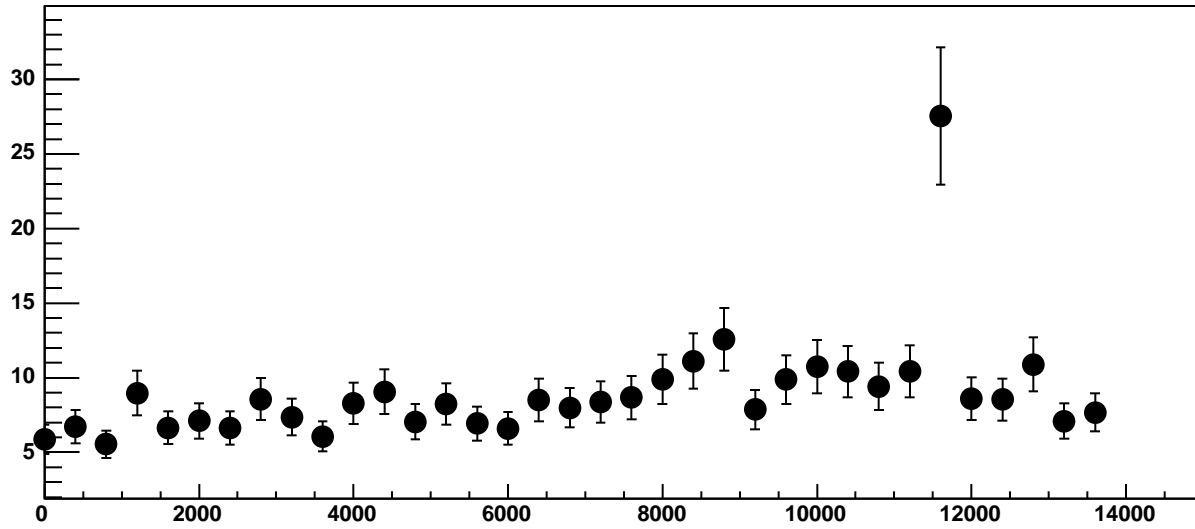


Chip 3, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

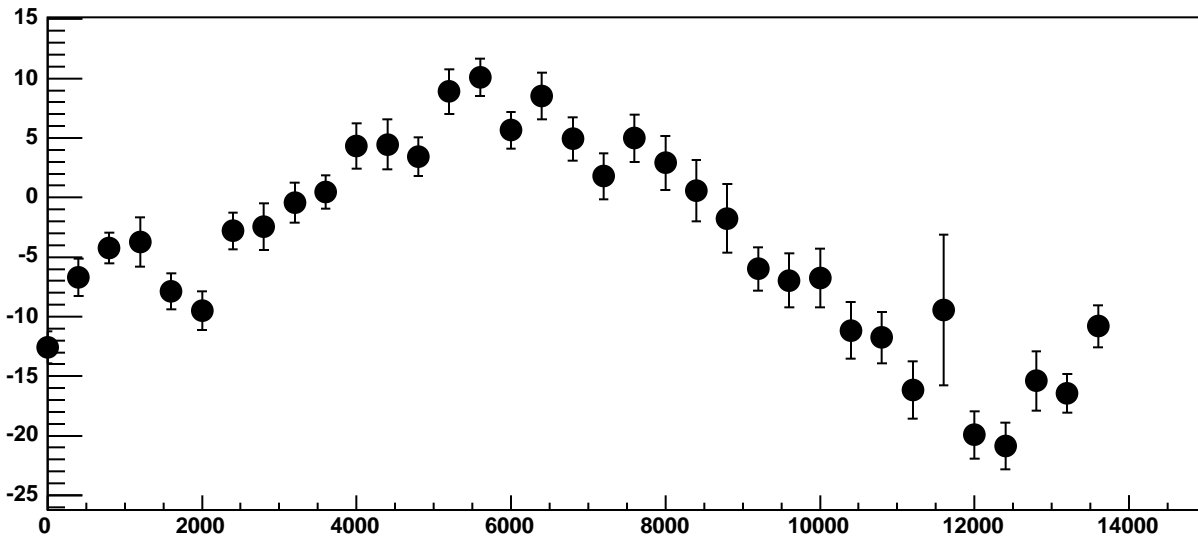


χ^2 / ndf 274.8 / 23
p0 -571.8 ± 0.8201
p1 -0.01626 ± 0.0001363

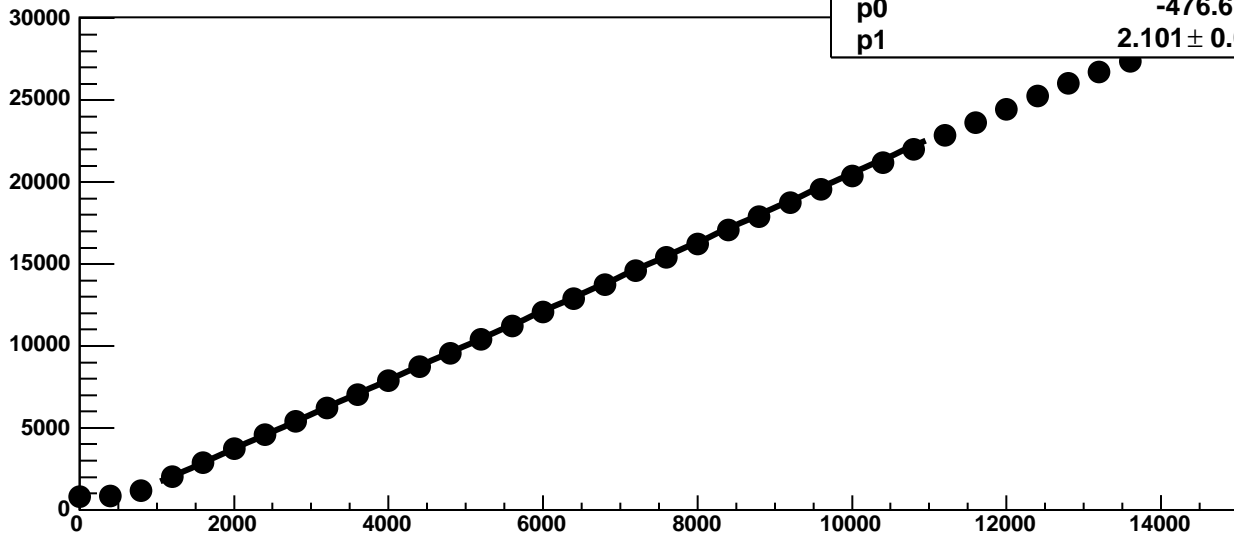
Chip 3, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



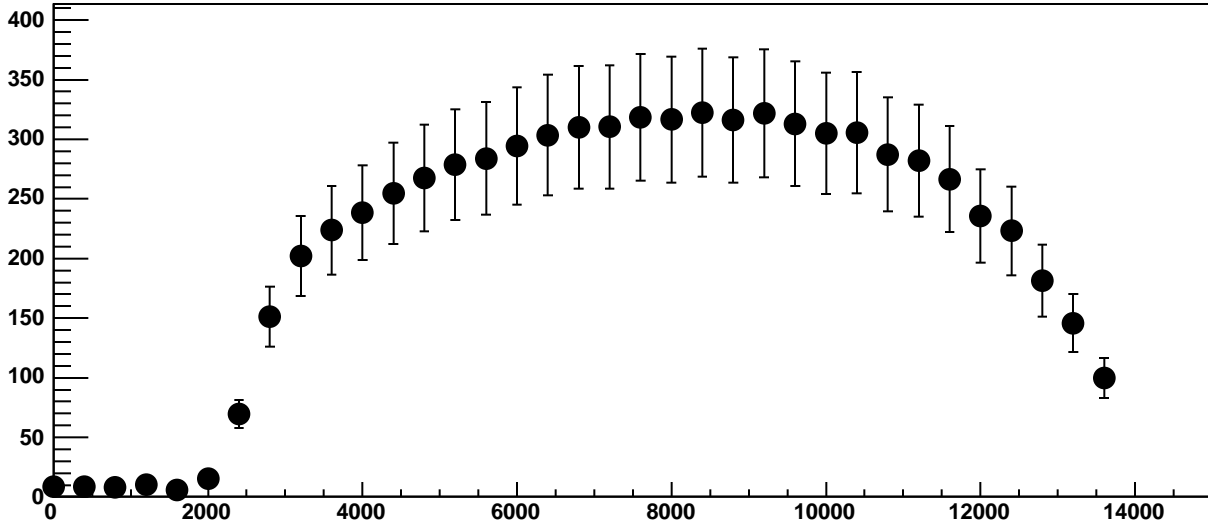
Chip 3, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



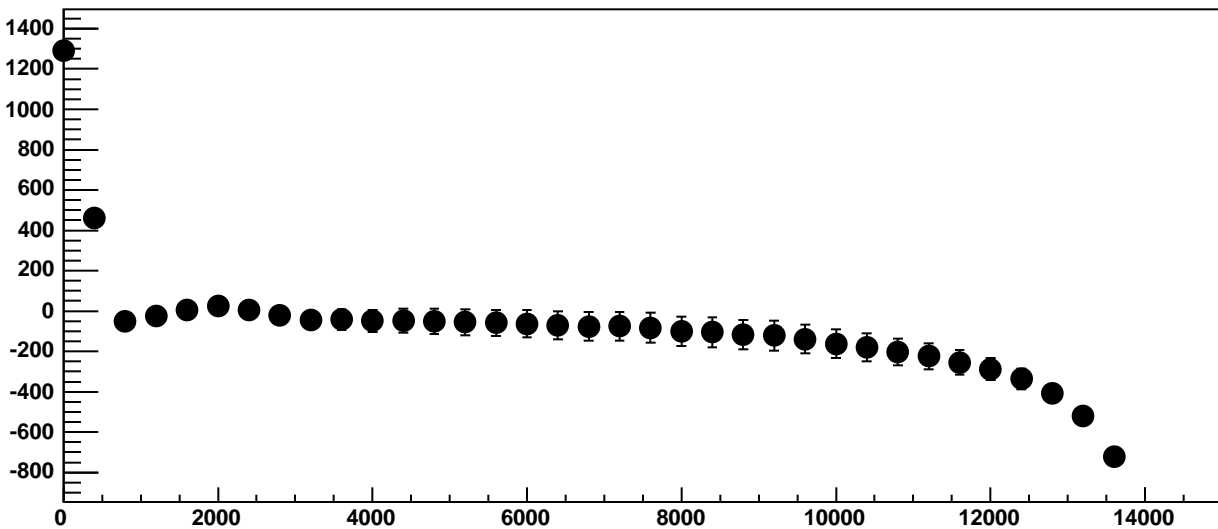
Chip 3, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC



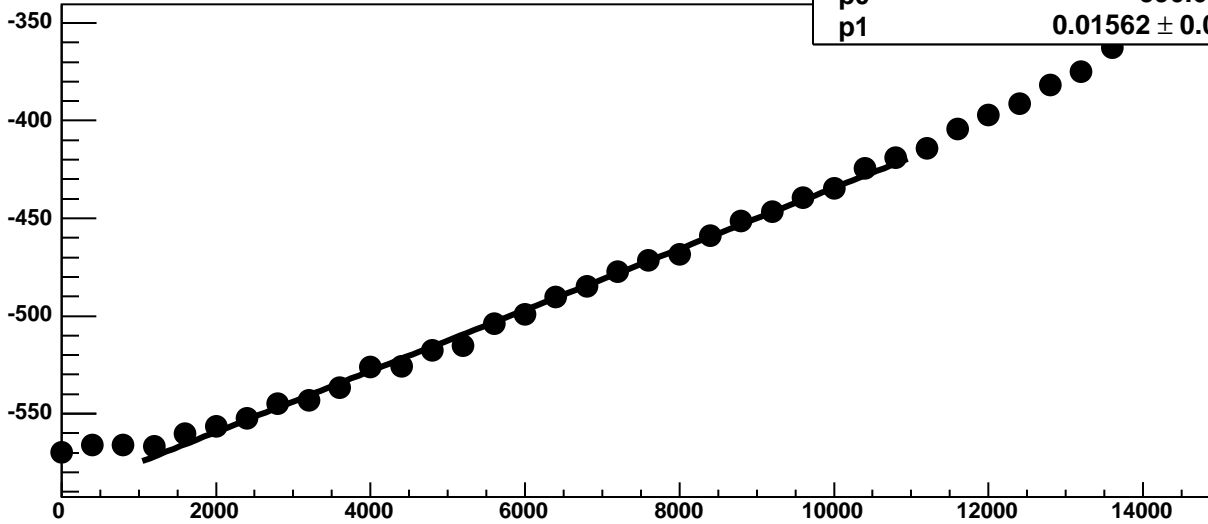
Chip 3, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC

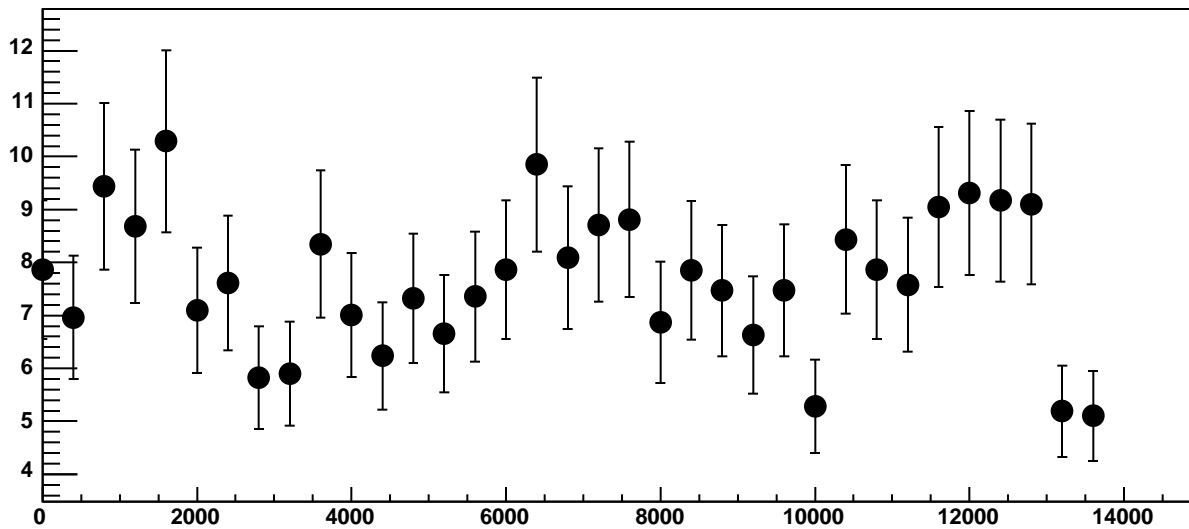


Chip 3, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

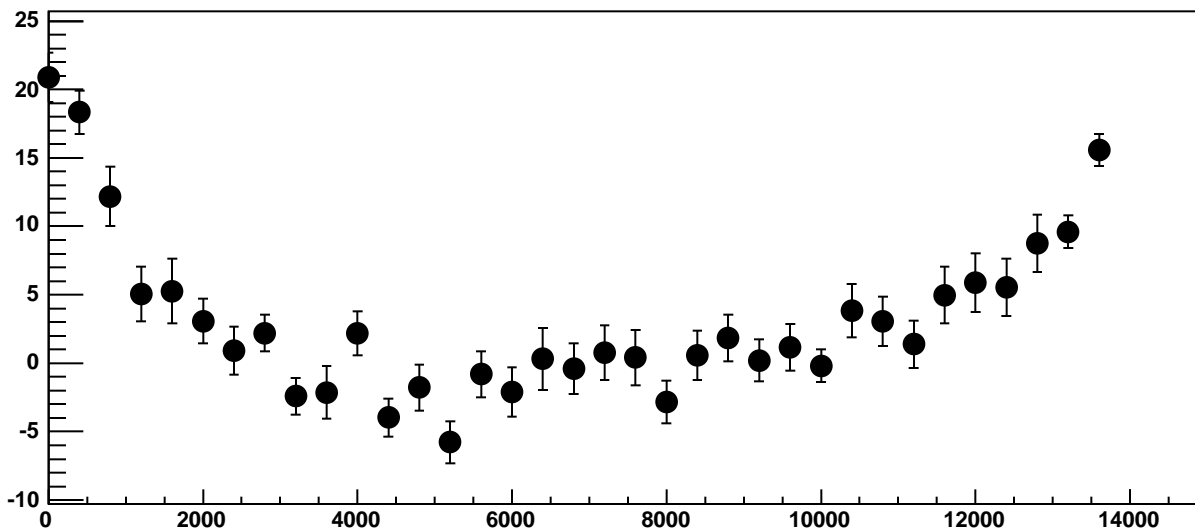


χ^2 / ndf 61.32 / 23
p0 -590.6 ± 0.777
p1 0.01562 ± 0.0001162

Chip 3, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

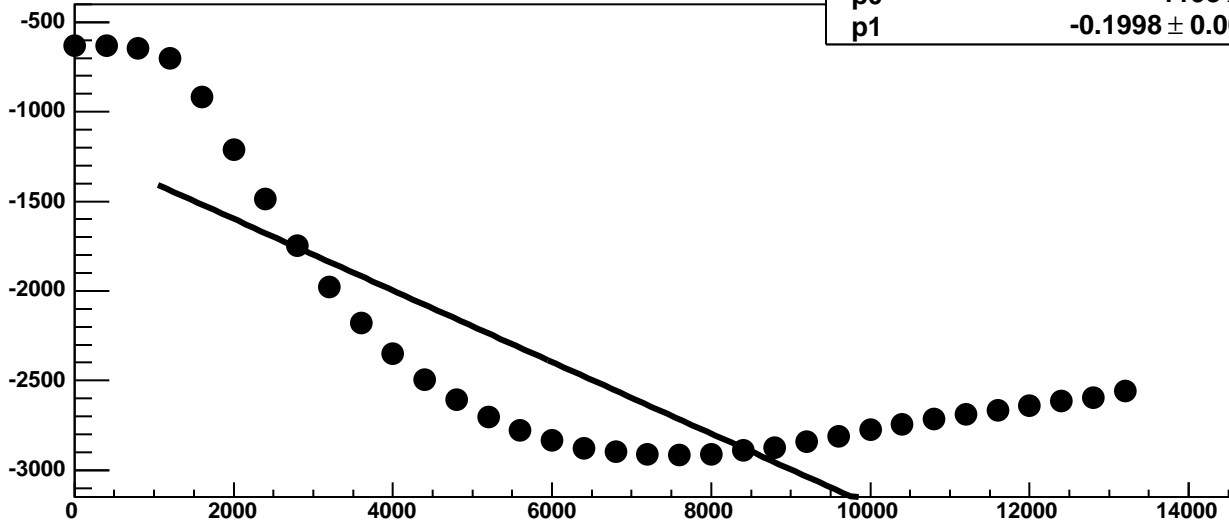


Chip 3, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

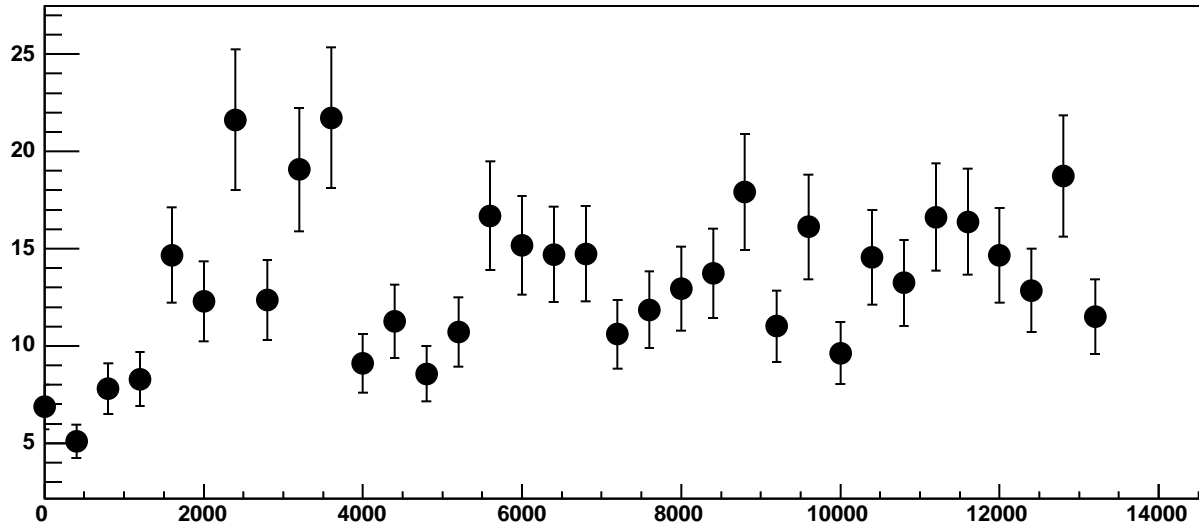


Chip 3, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

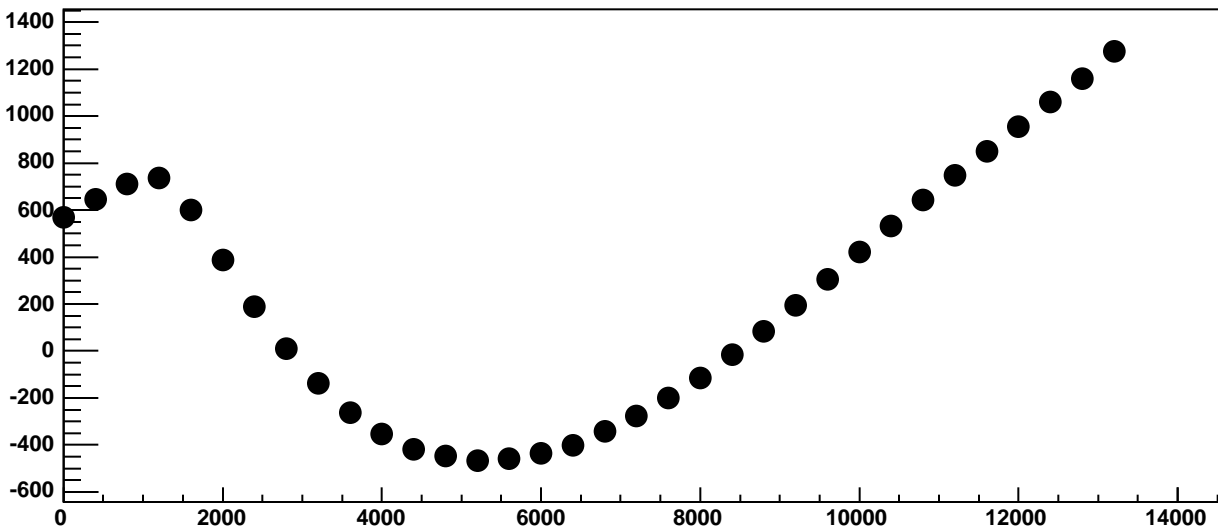
χ^2 / ndf 5.428e+05 / 23
p0 -1198 ± 1.269
p1 -0.1998 ± 0.0001945



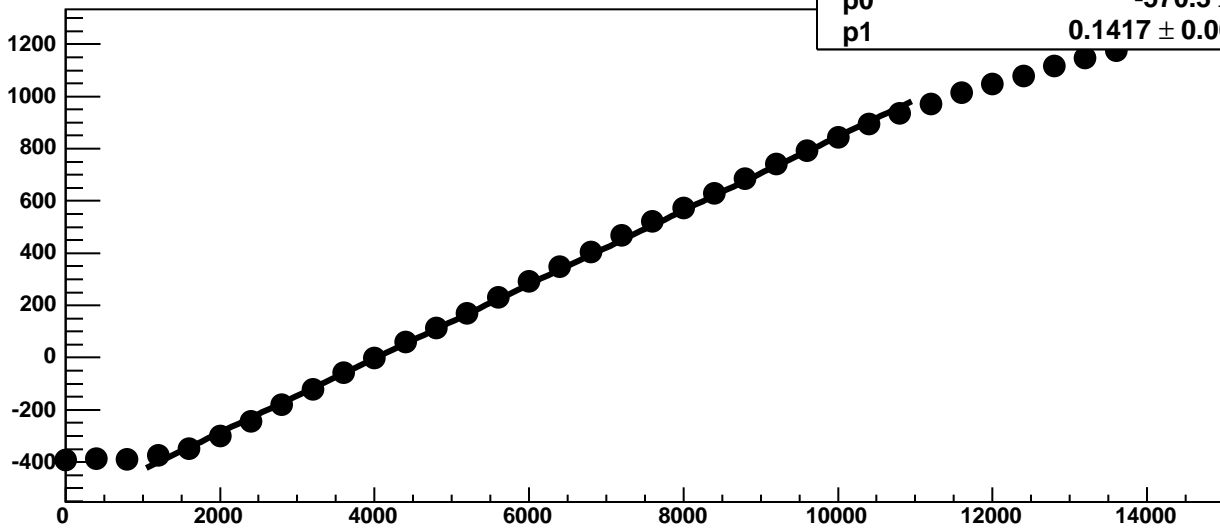
Chip 3, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

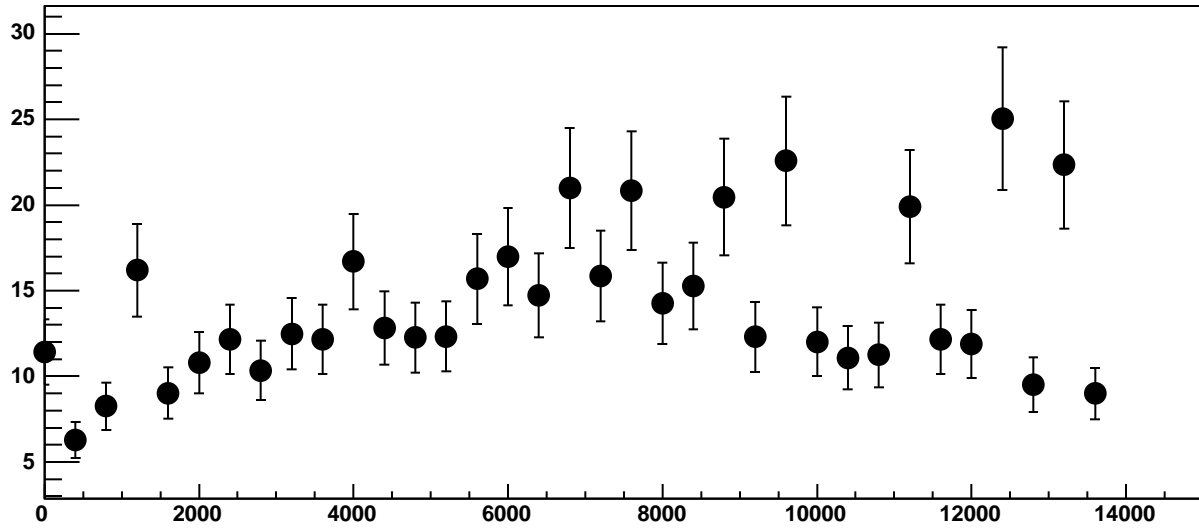


Chip 3, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

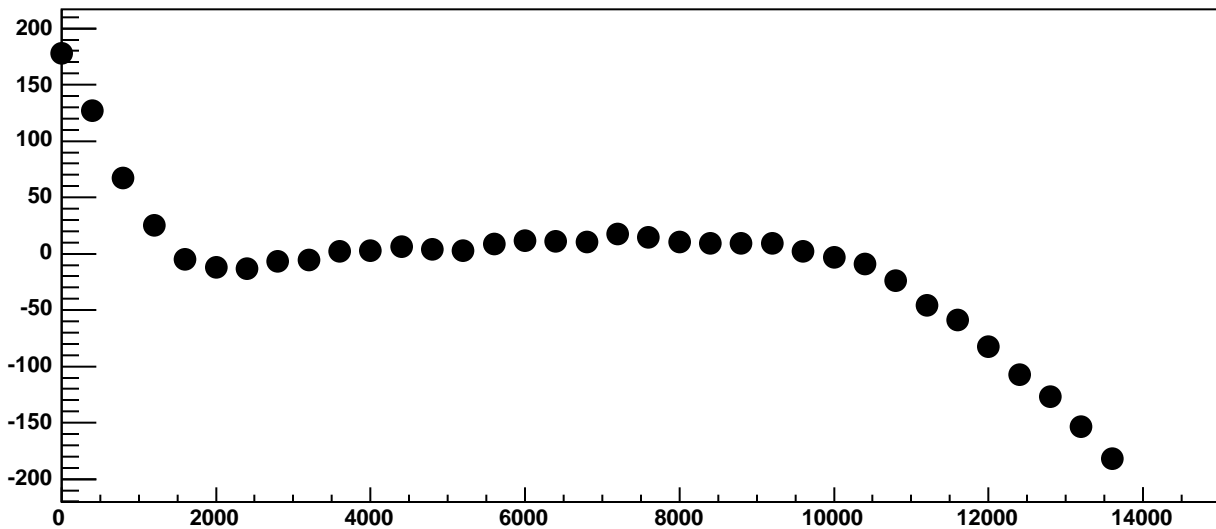


χ^2 / ndf 308.5 / 23
p0 -570.3 ± 1.252
p1 0.1417 ± 0.0001955

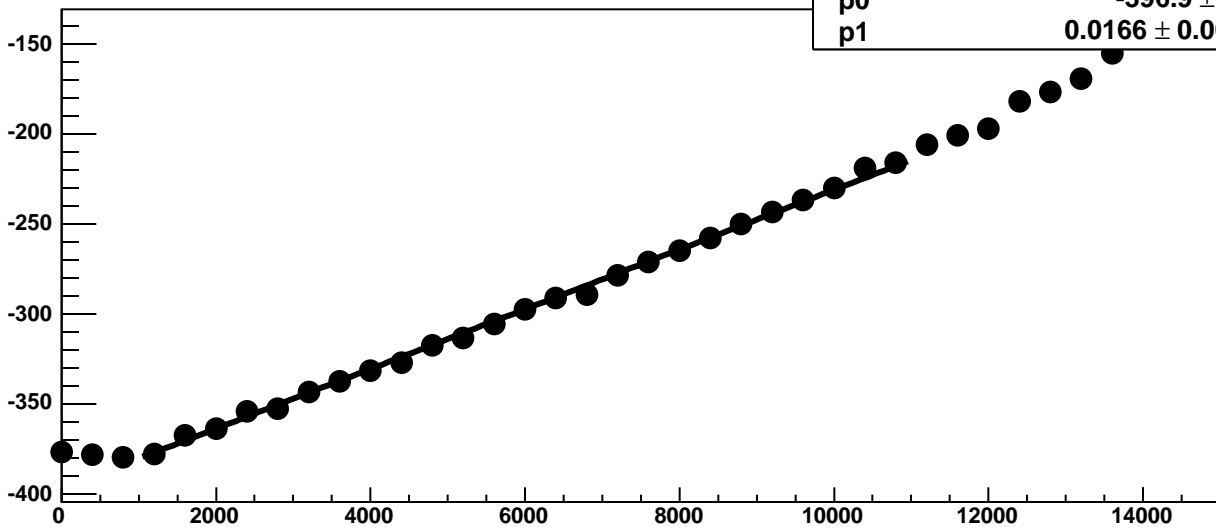
Chip 3, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



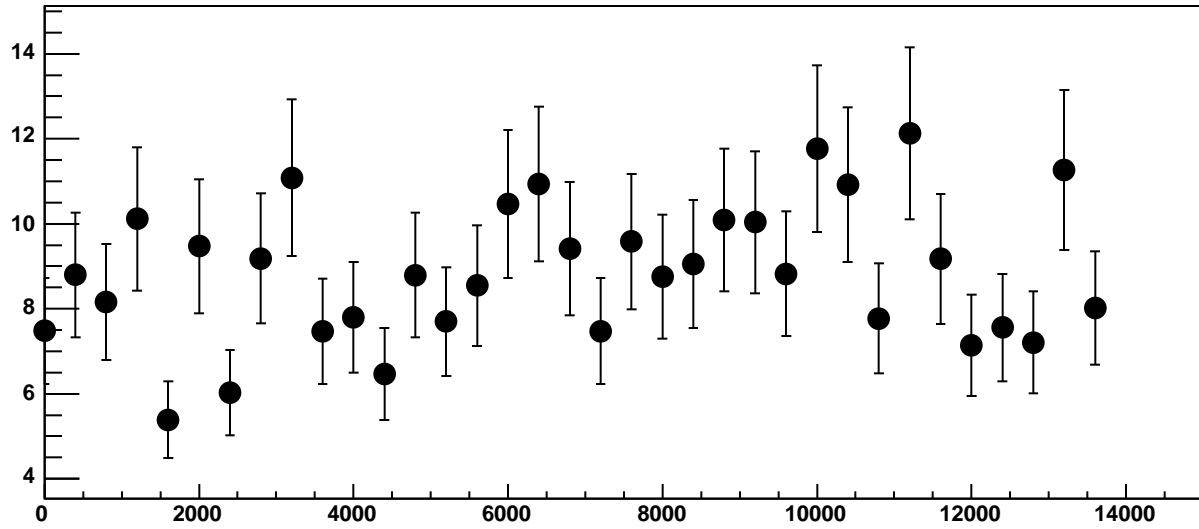
Chip 3, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



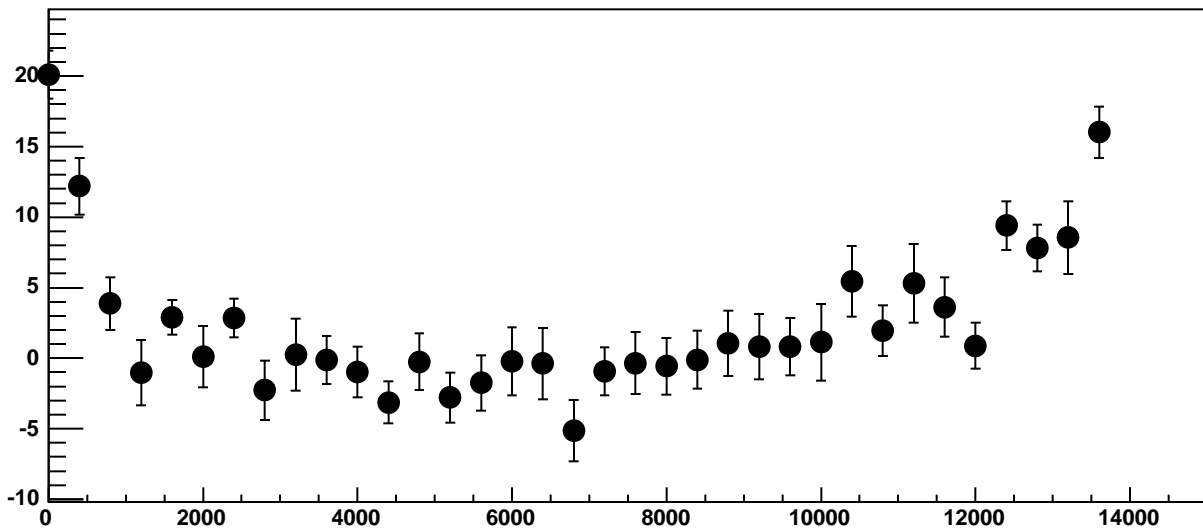
Chip 3, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC



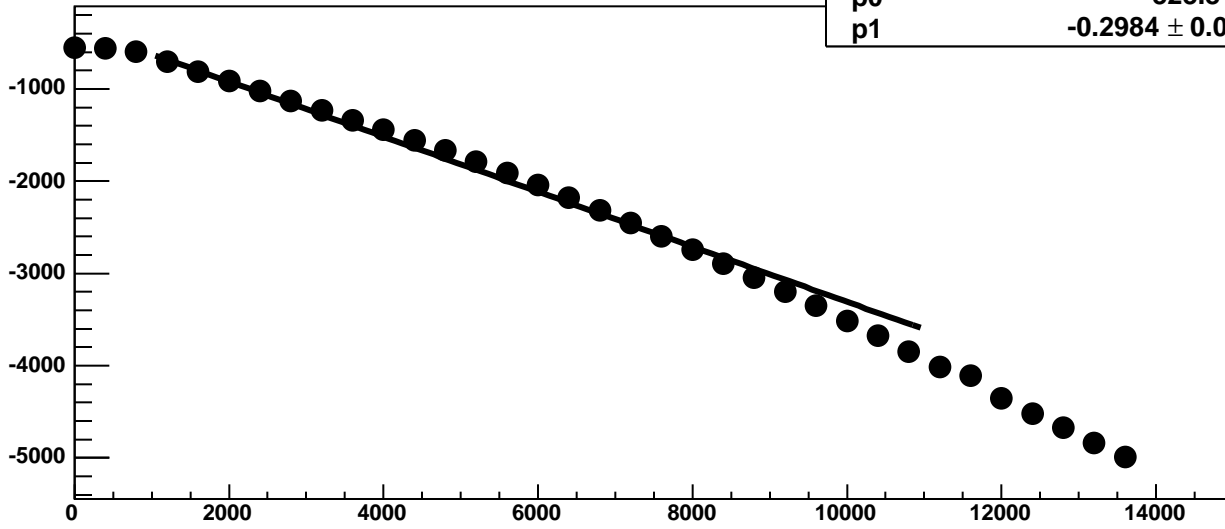
Chip 3, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC

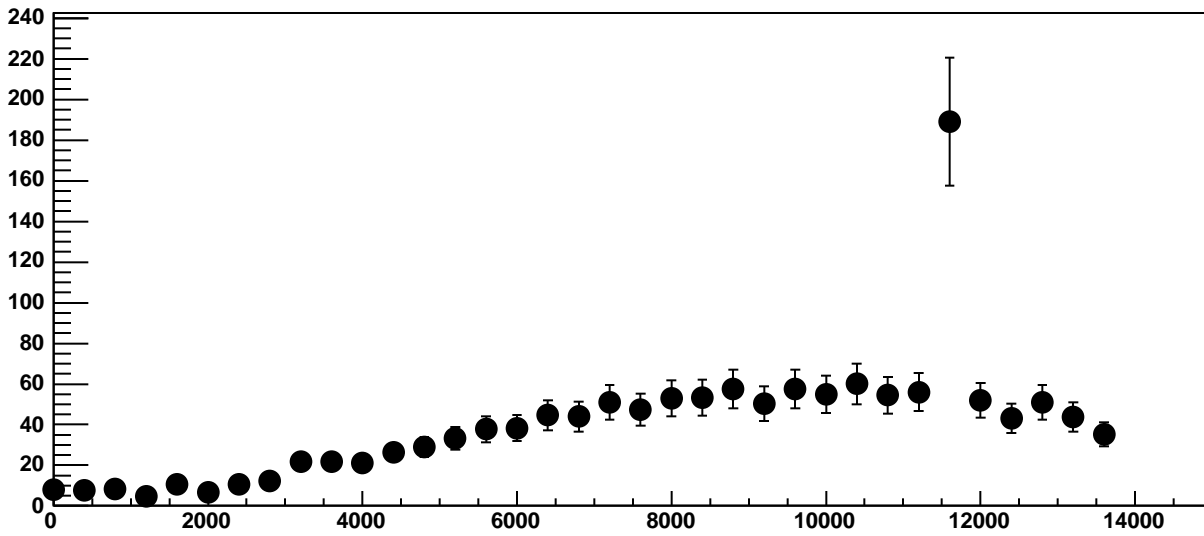


Chip 3, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

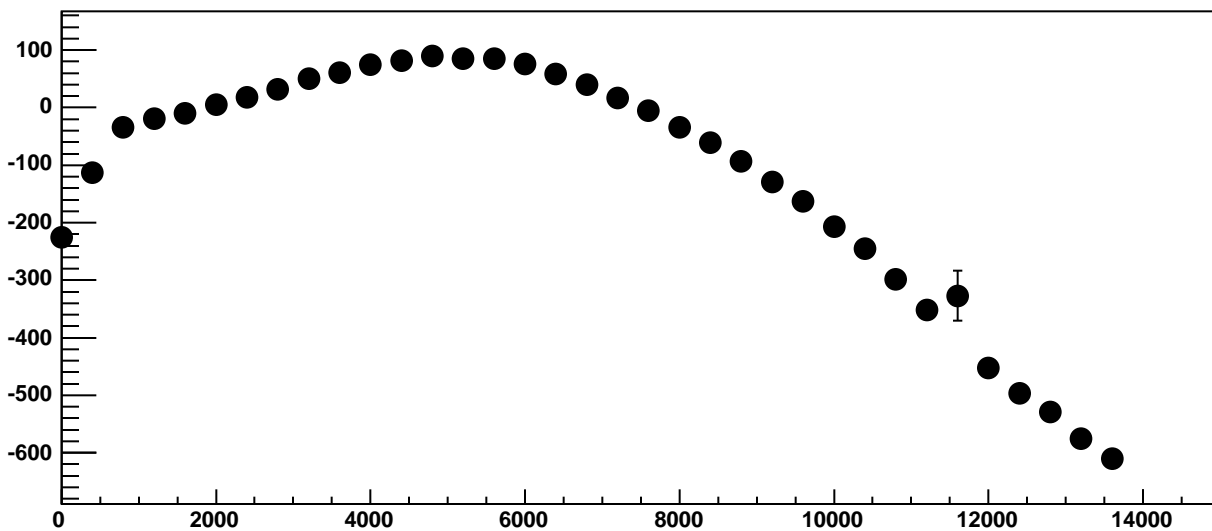


χ^2 / ndf 3253 / 23
p0 -323.8 ± 1.175
p1 -0.2984 ± 0.0004277

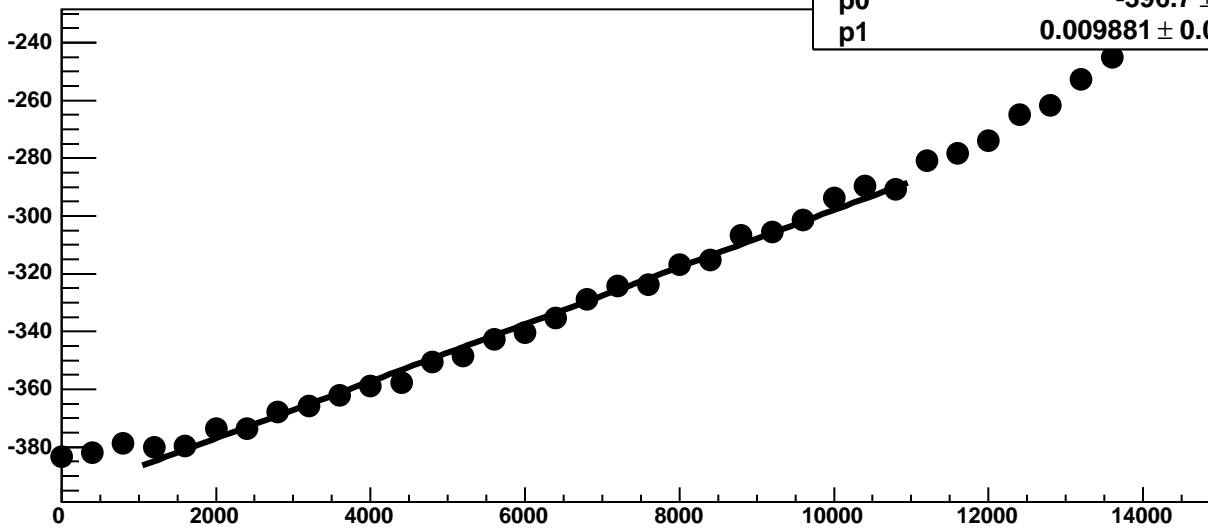
Chip 3, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



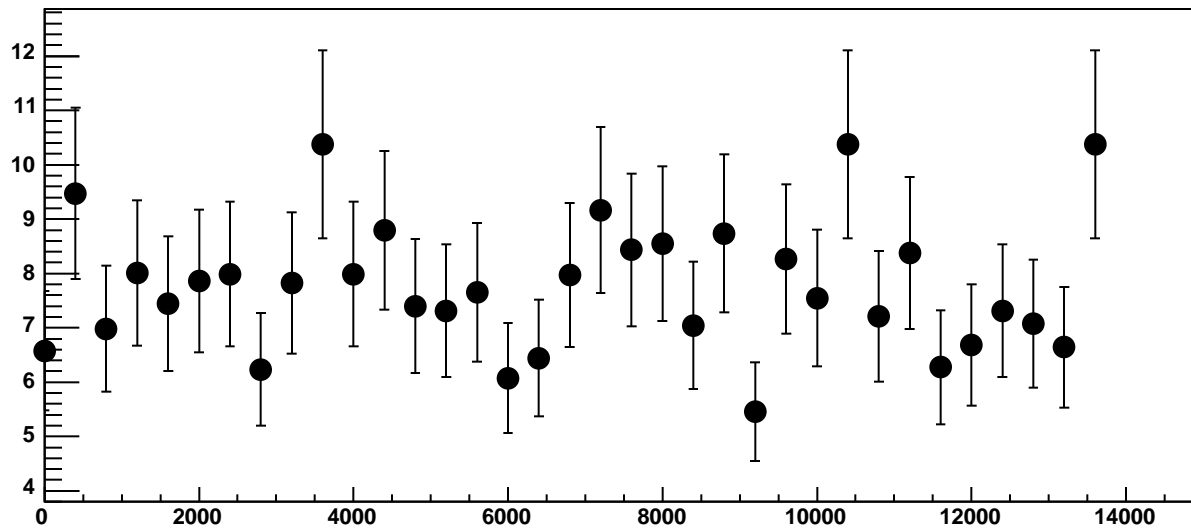
Chip 3, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



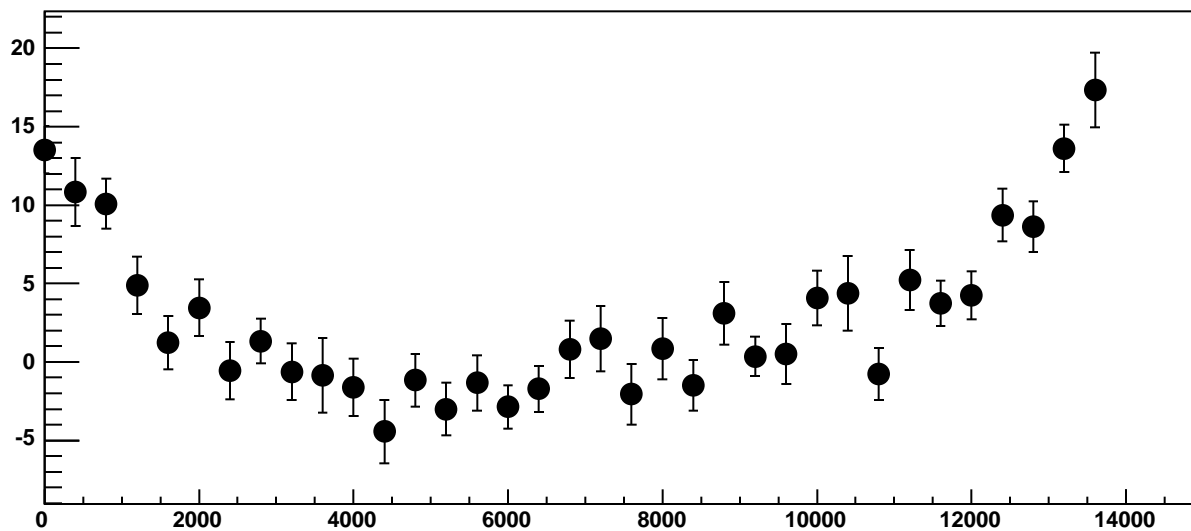
Chip 3, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



Chip 3, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC

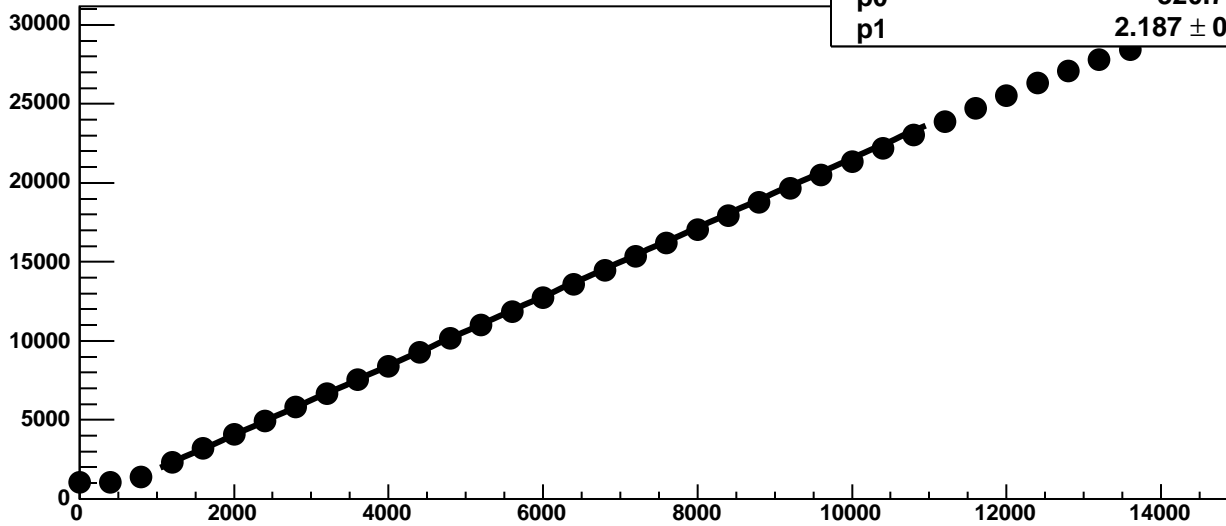


Chip 3, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

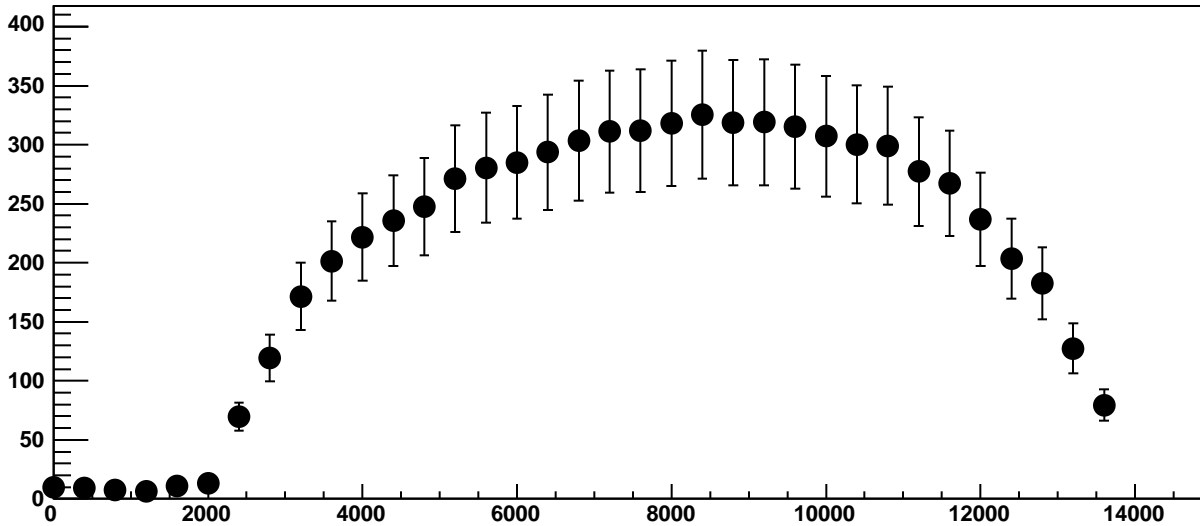


Chip 3, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC

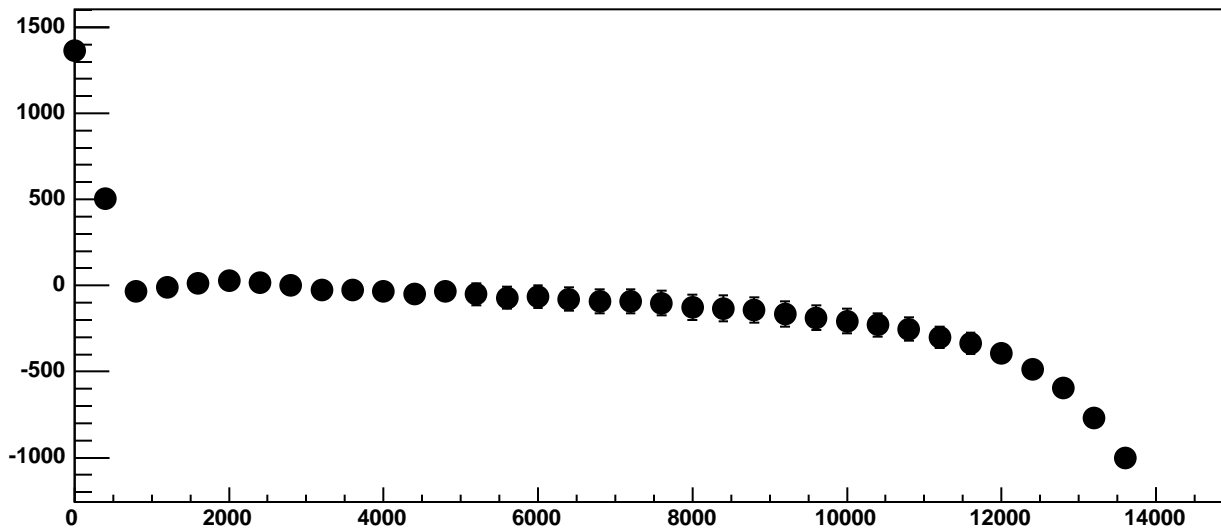
χ^2 / ndf 217.7 / 23
p0 -320.7 ± 3.243
p1 2.187 ± 0.002113



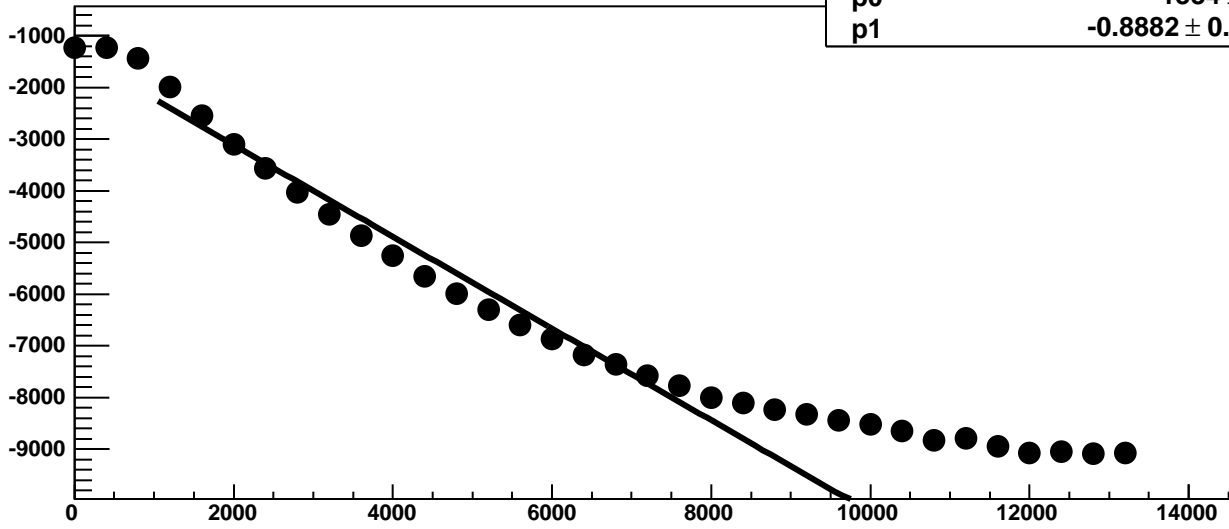
Chip 3, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC



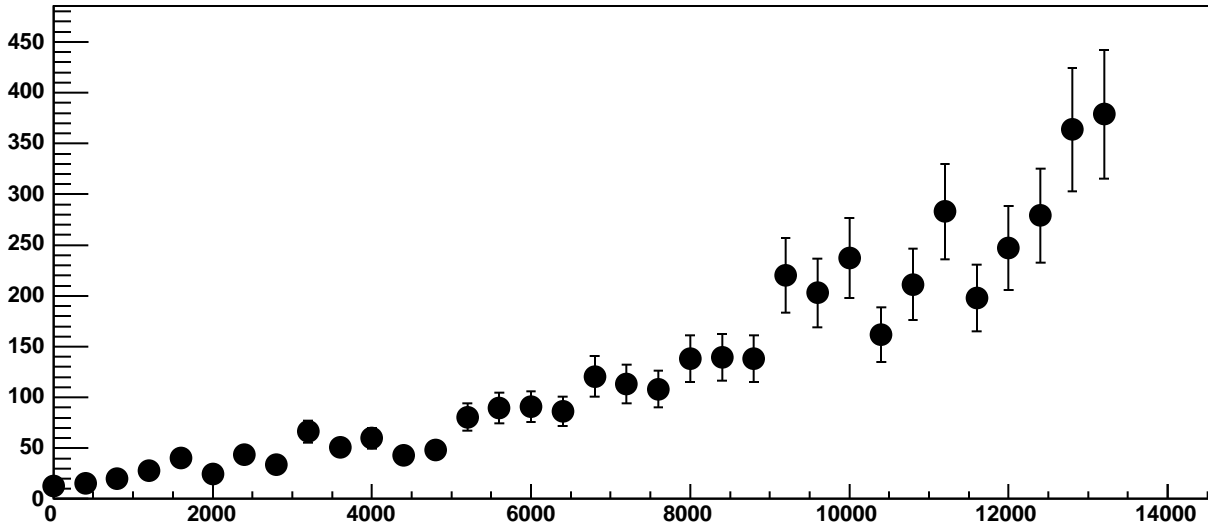
Chip 3, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC



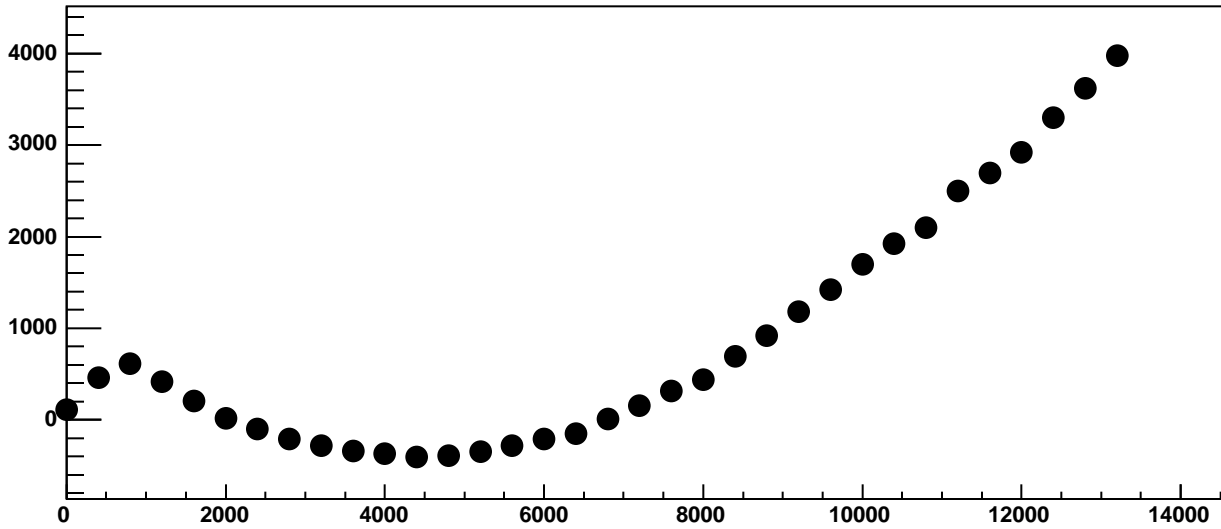
Chip 3, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC



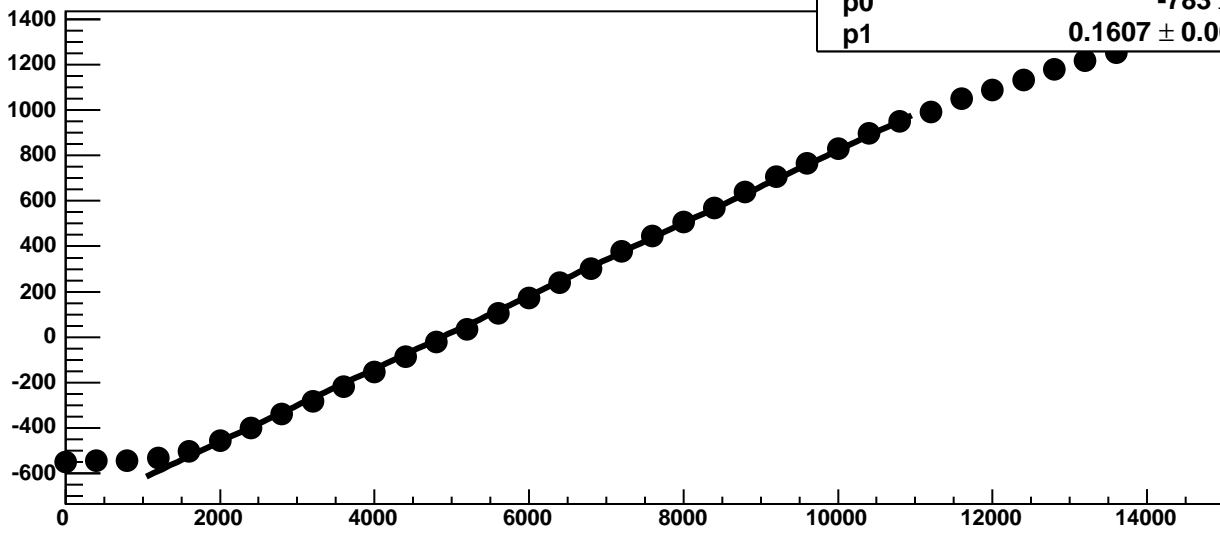
Chip 3, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

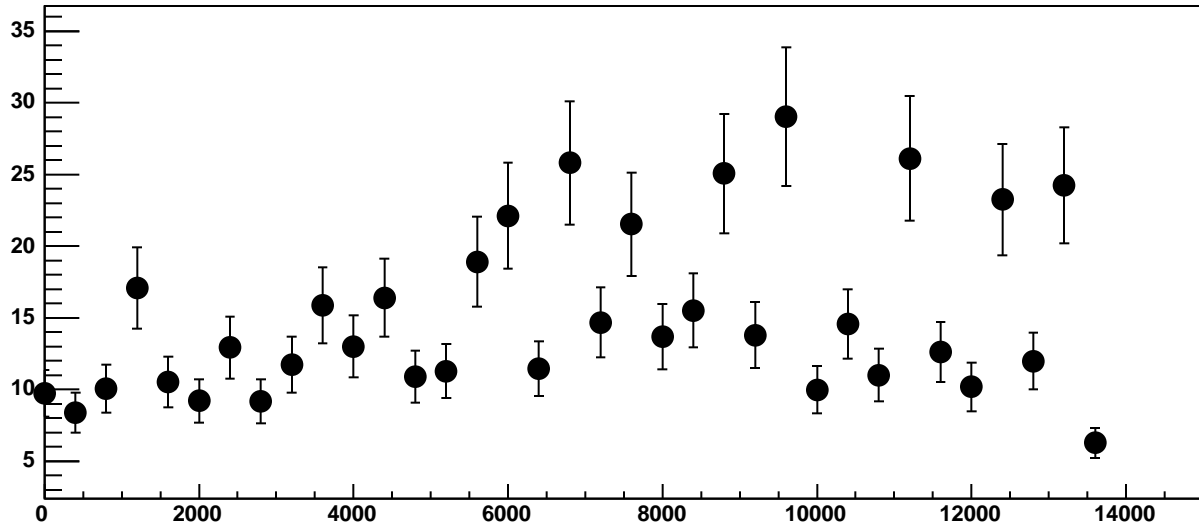


Chip 3, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

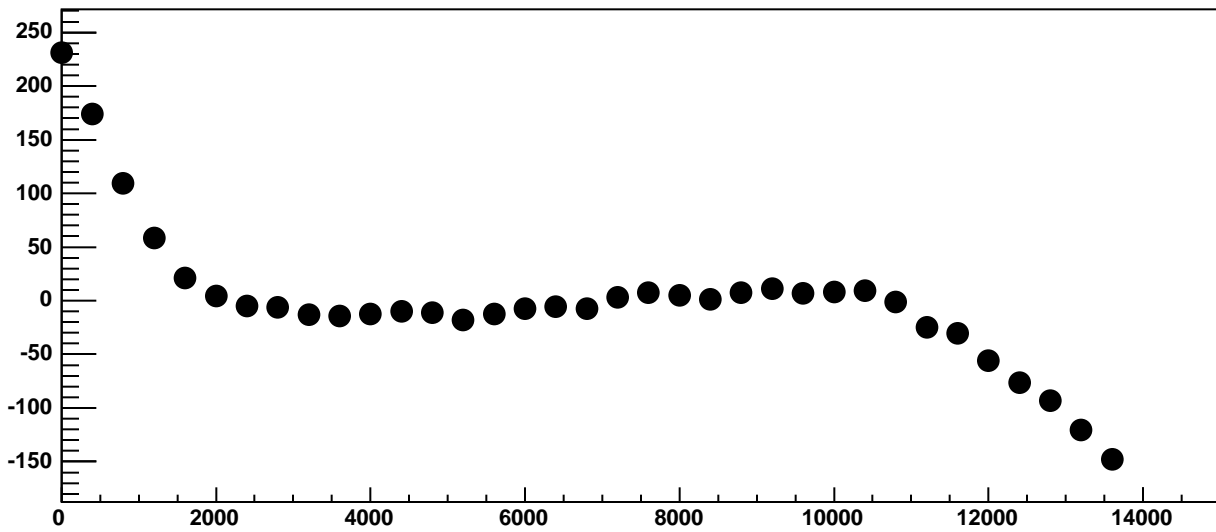


χ^2 / ndf 503.9 / 23
p0 -783 ± 1.258
p1 0.1607 ± 0.0001993

Chip 3, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

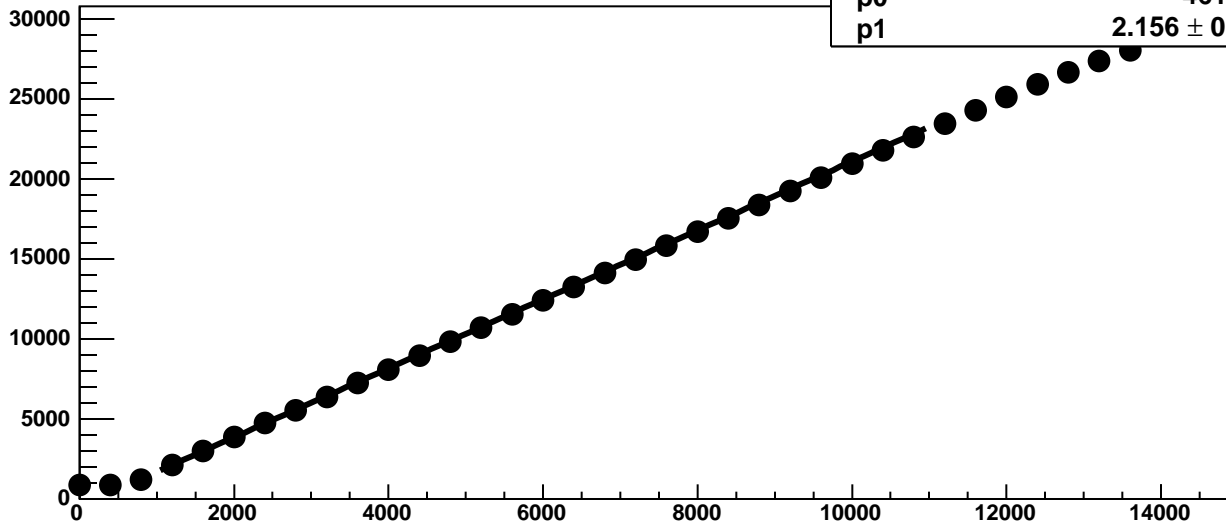


Chip 3, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

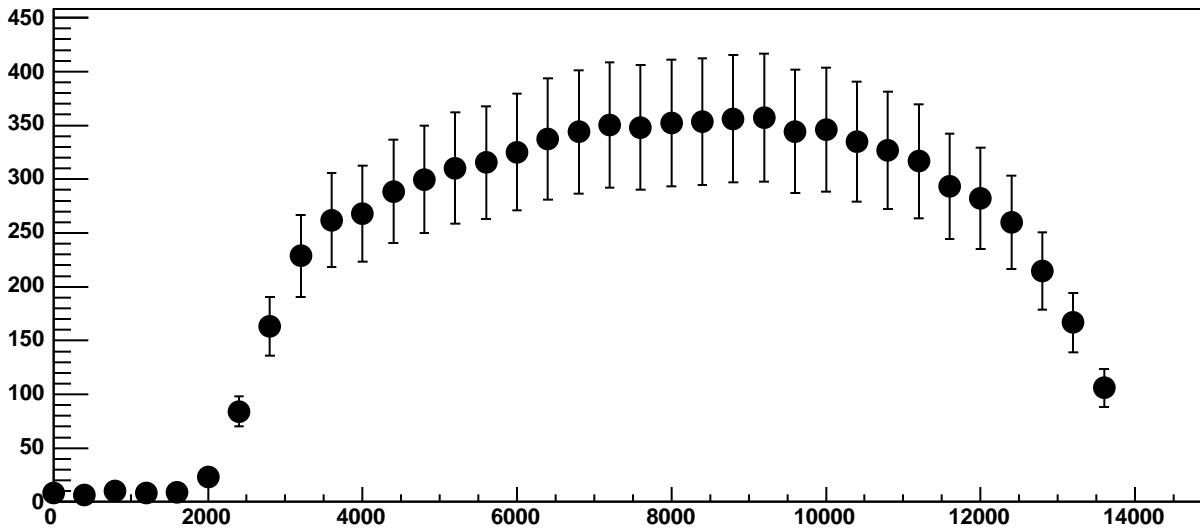


Chip 3, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC

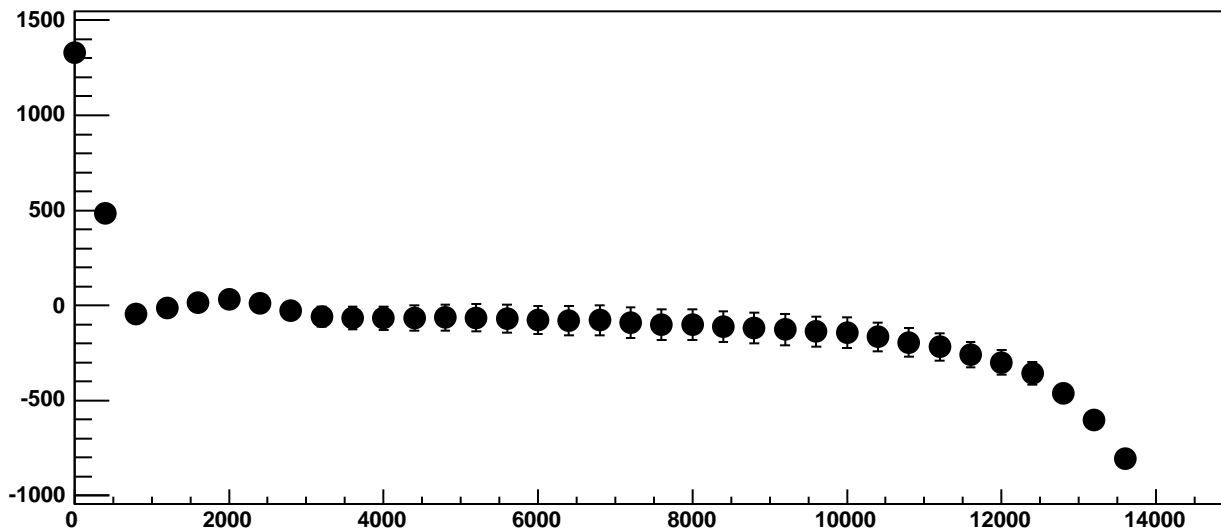
χ^2 / ndf 185.8 / 23
p0 -461 ± 3.954
p1 2.156 ± 0.002529



Chip 3, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC

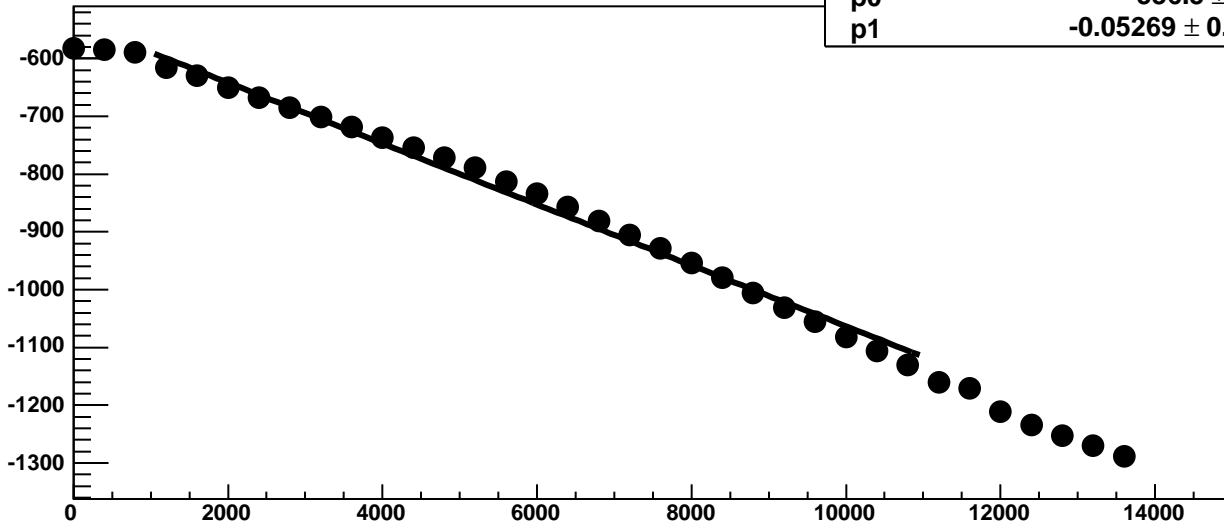


Chip 3, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC

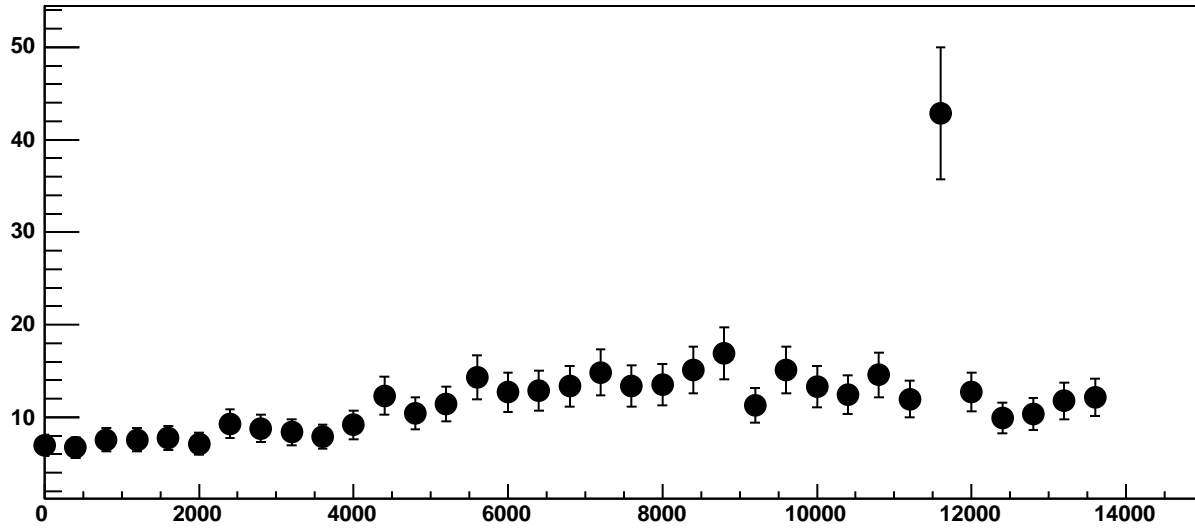


Chip 3, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

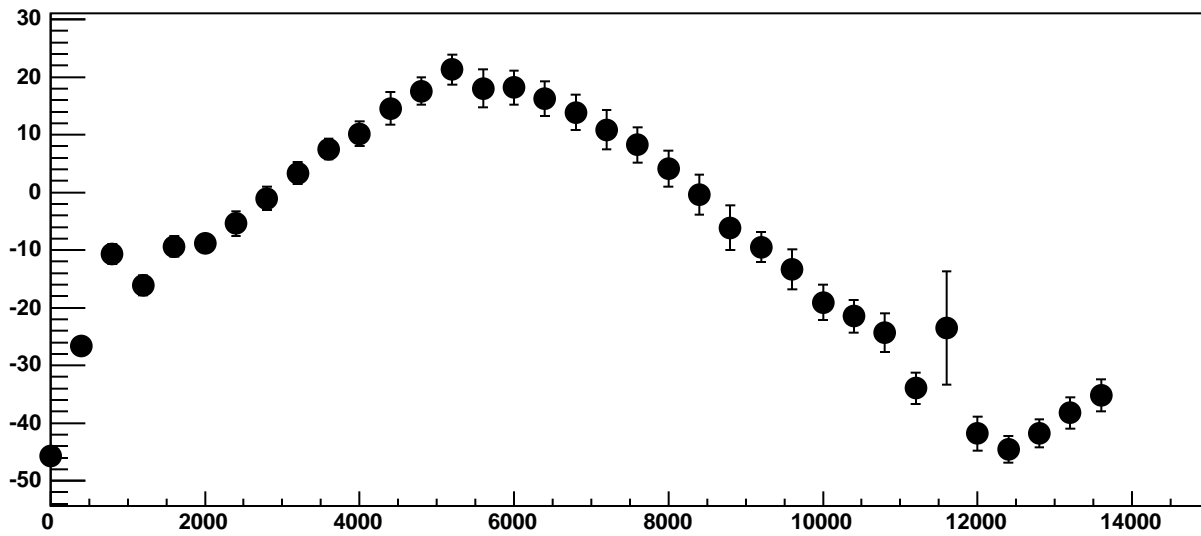
χ^2 / ndf 657.4 / 23
p0 -536.5 ± 0.9429
p1 -0.05269 ± 0.000171



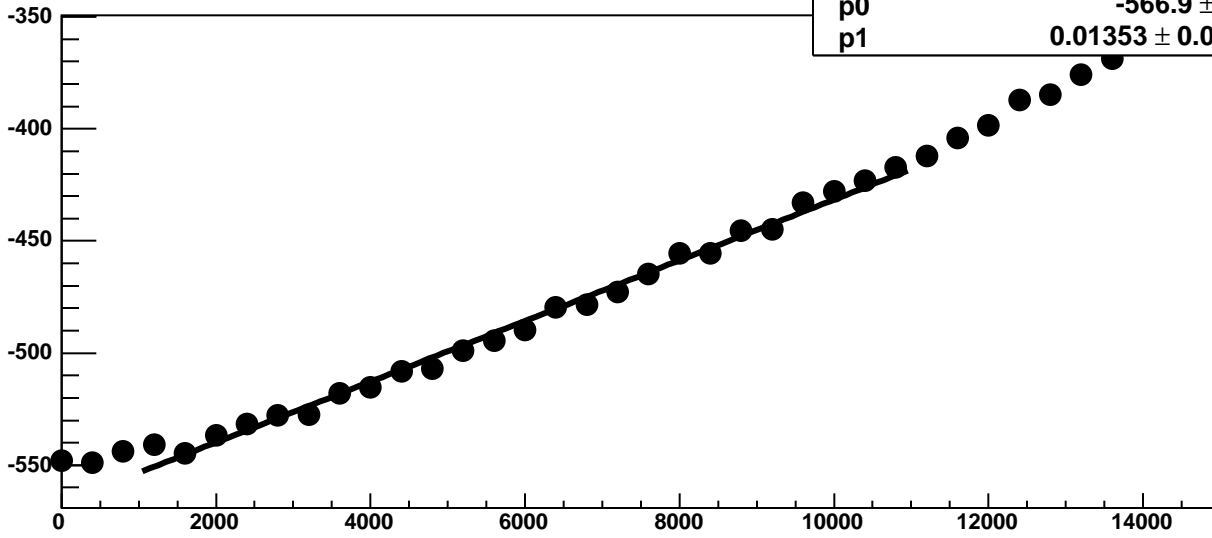
Chip 3, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



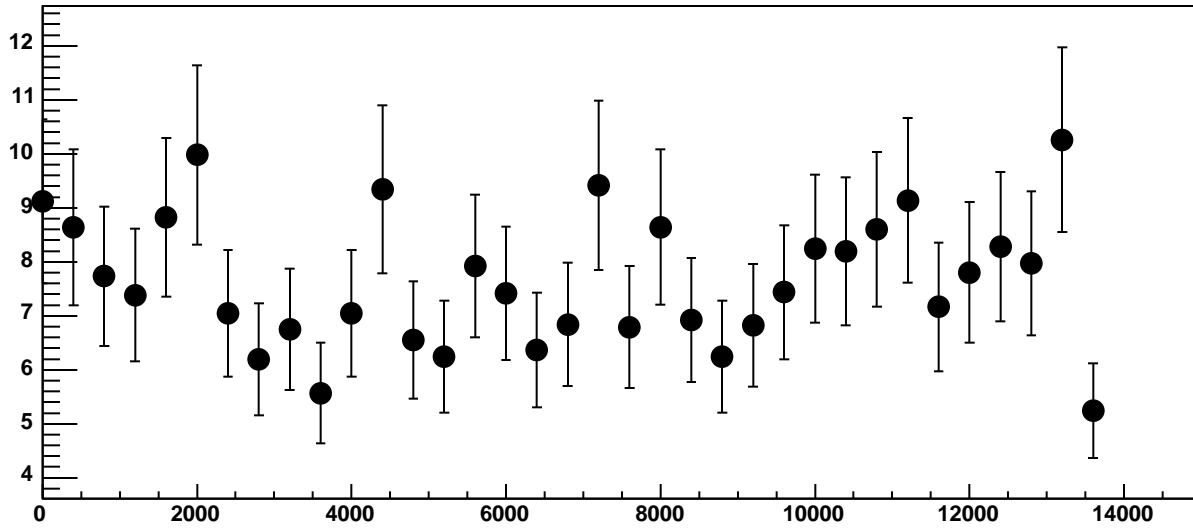
Chip 3, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



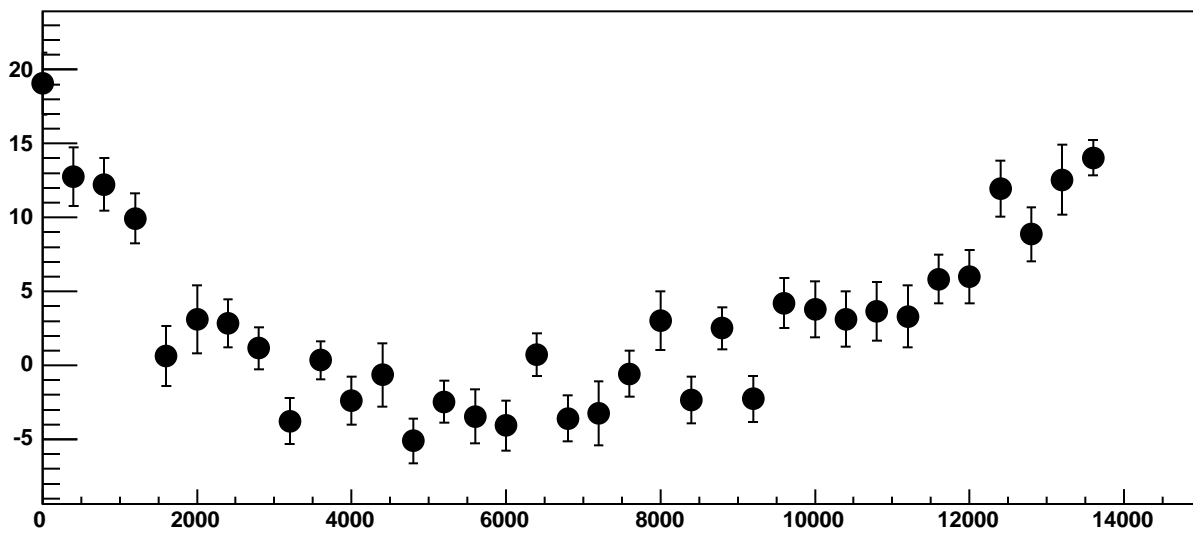
Chip 3, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC



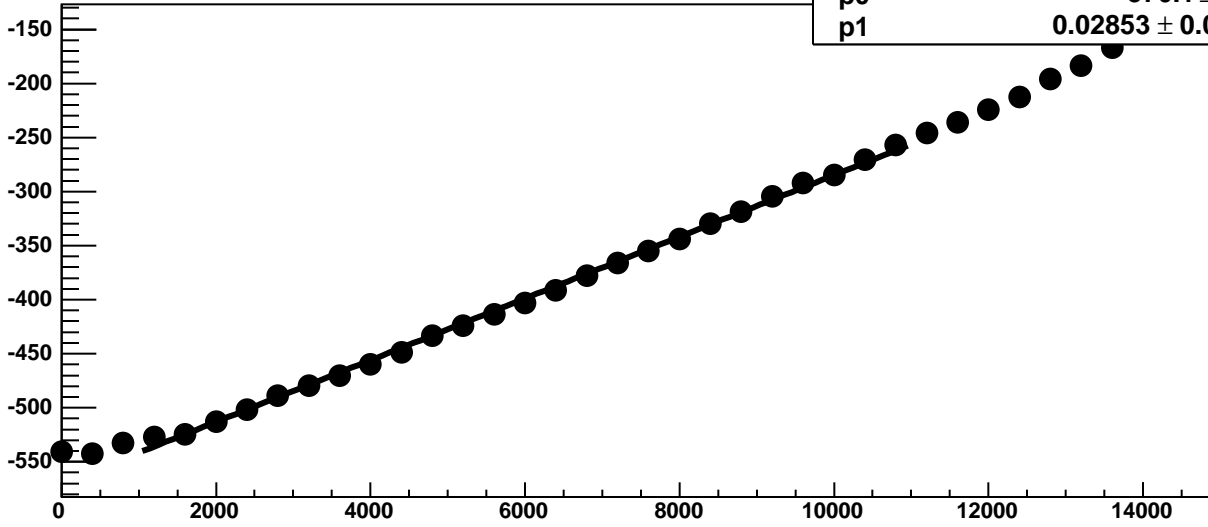
Chip 3, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



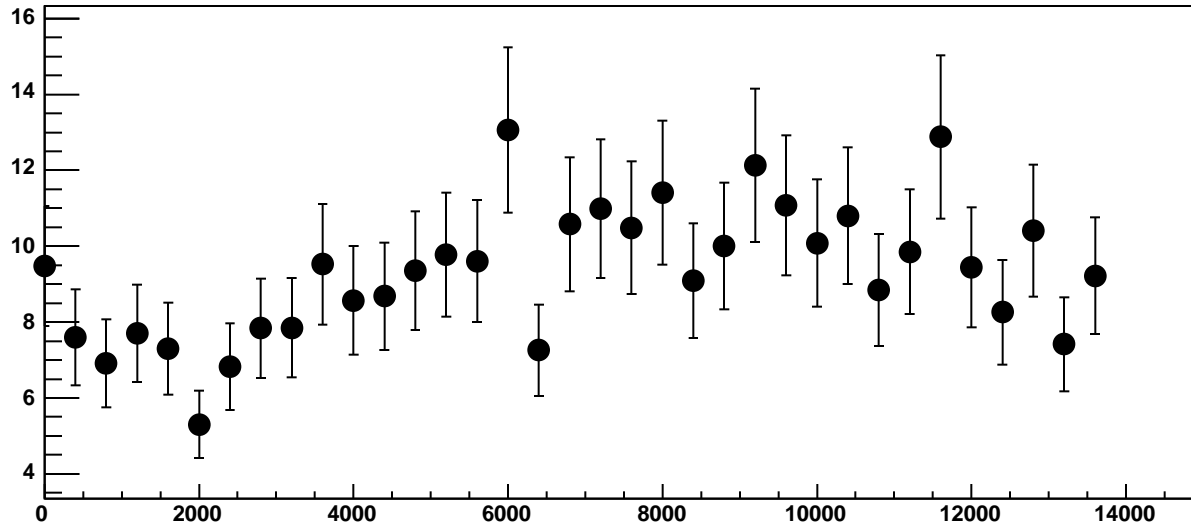
Chip 3, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



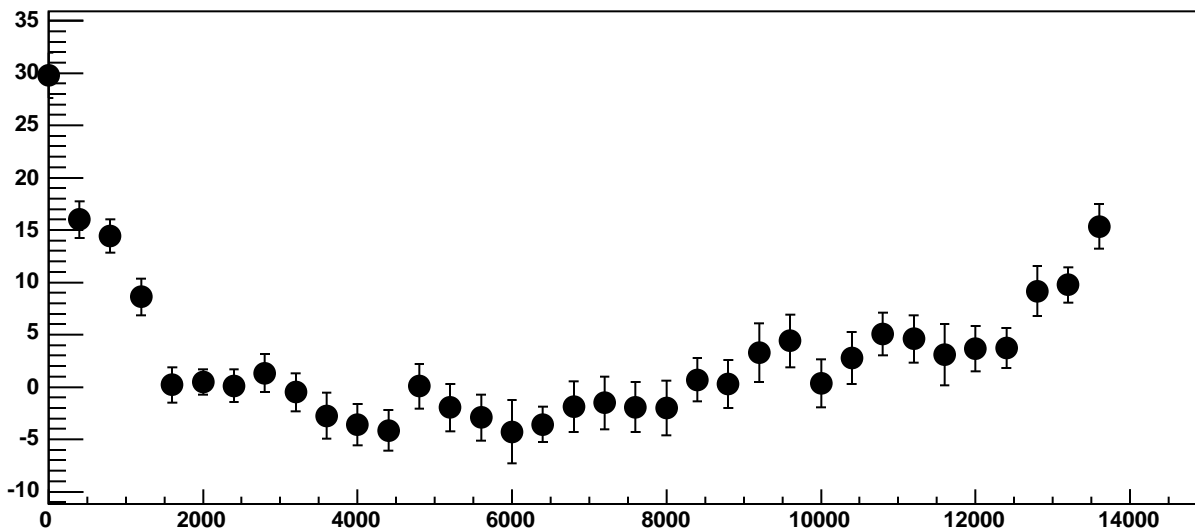
Chip 3, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC



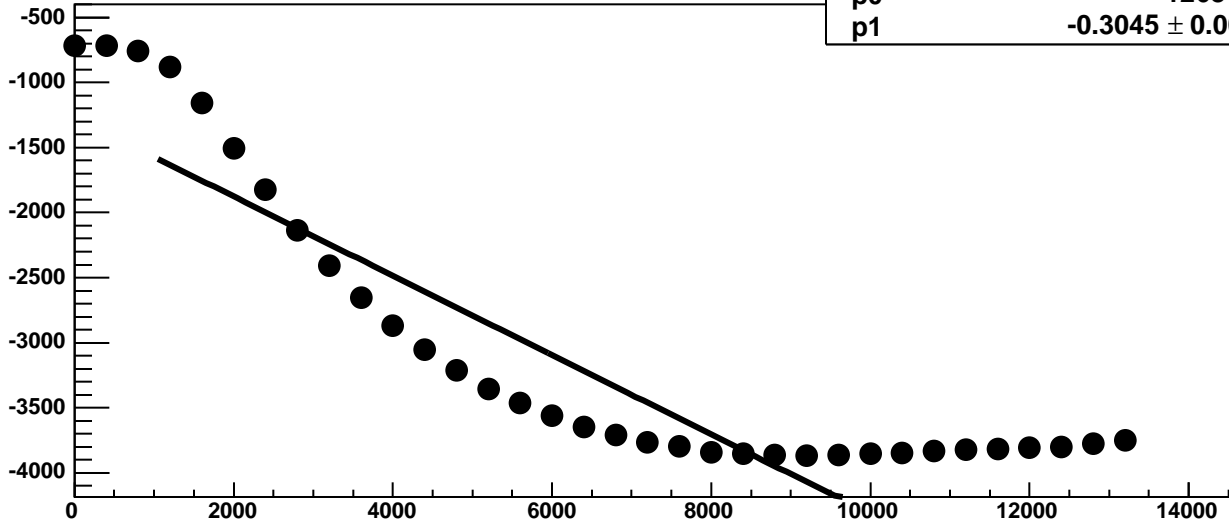
Chip 3, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



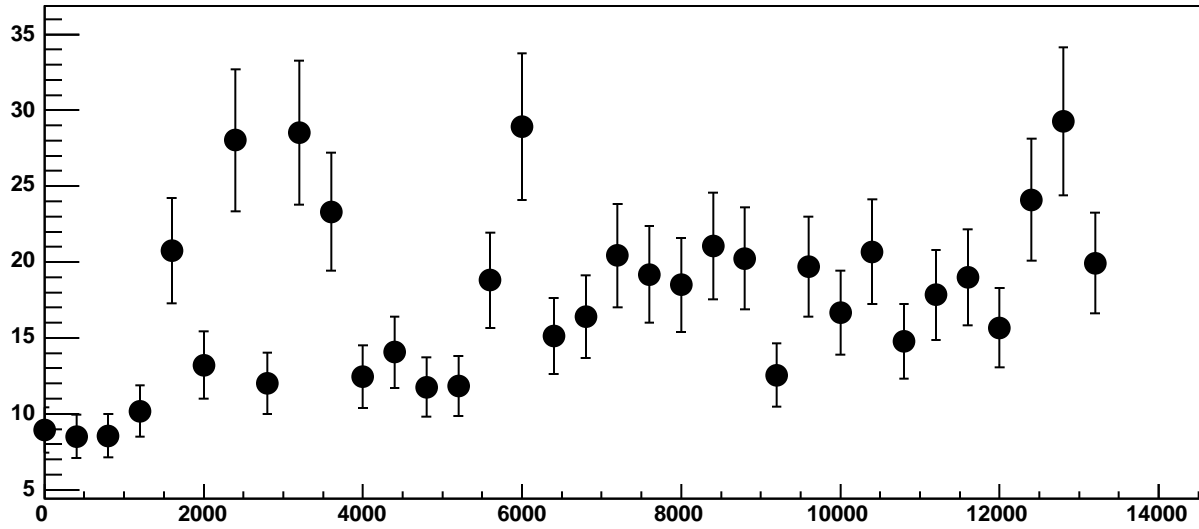
Chip 3, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC



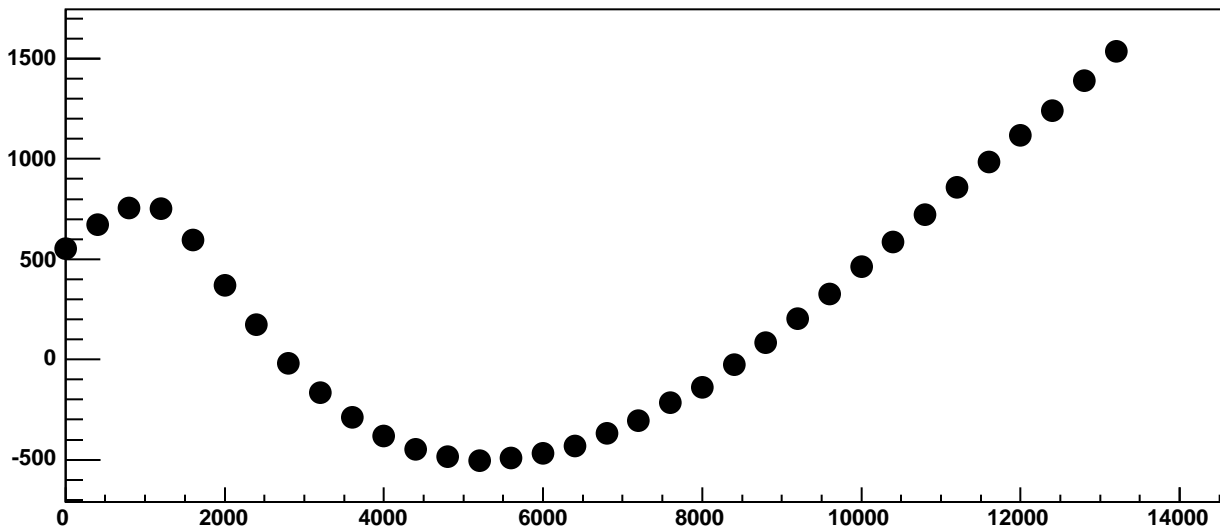
Chip 3, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



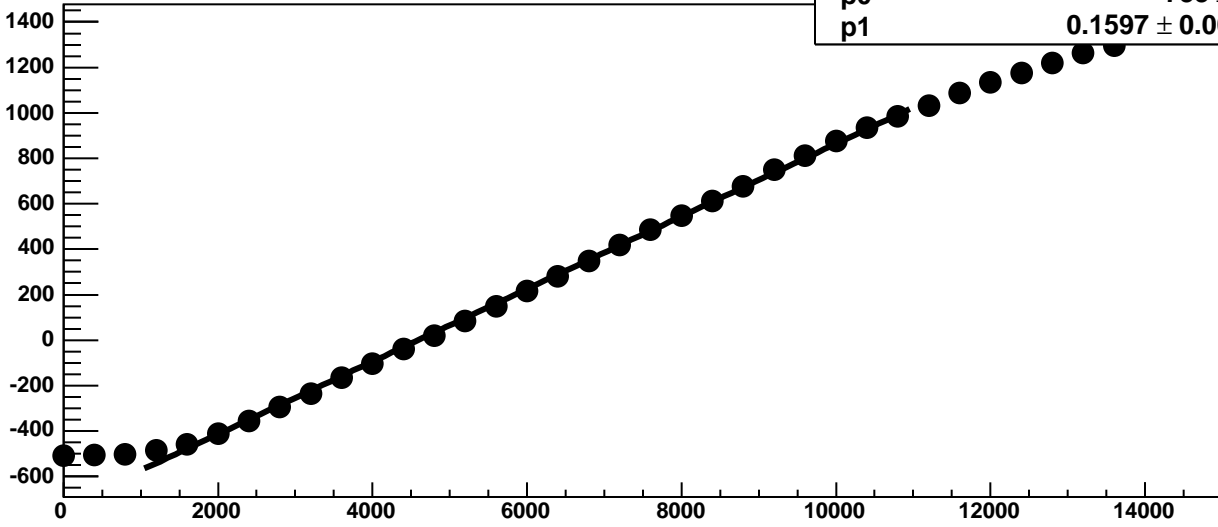
Chip 3, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



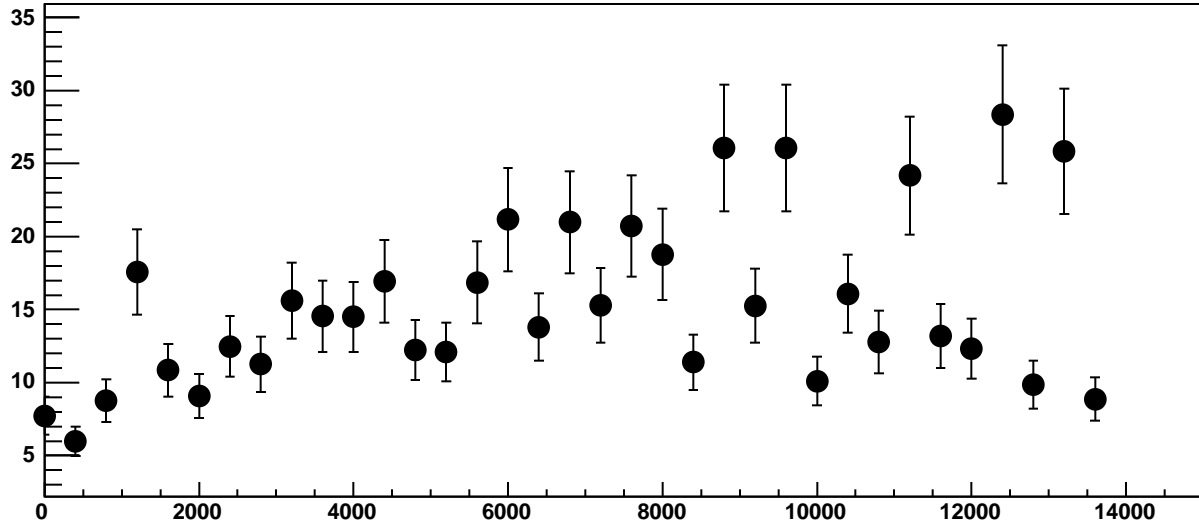
Chip 3, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



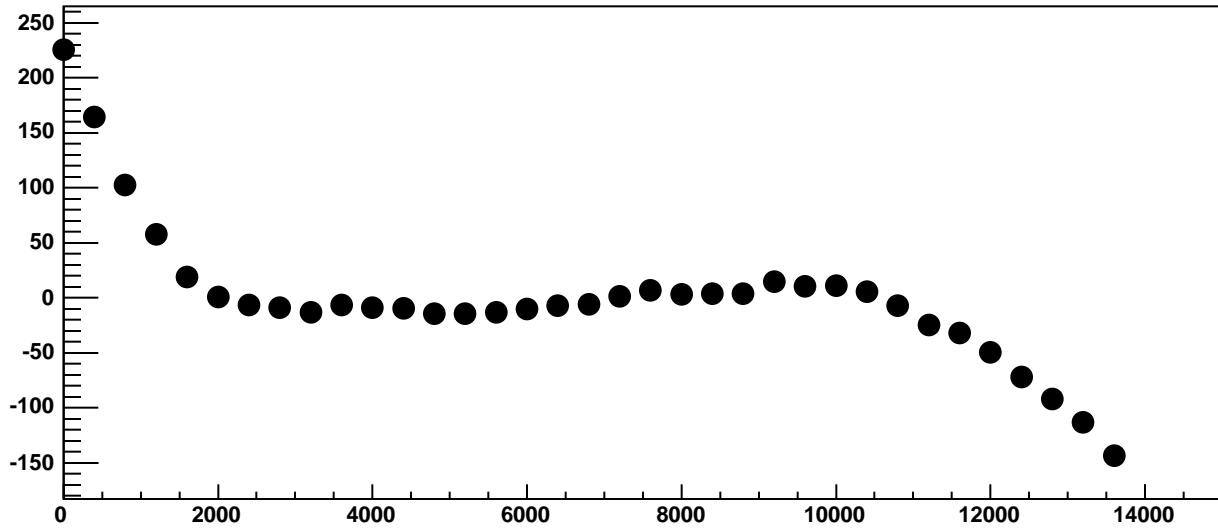
Chip 3, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



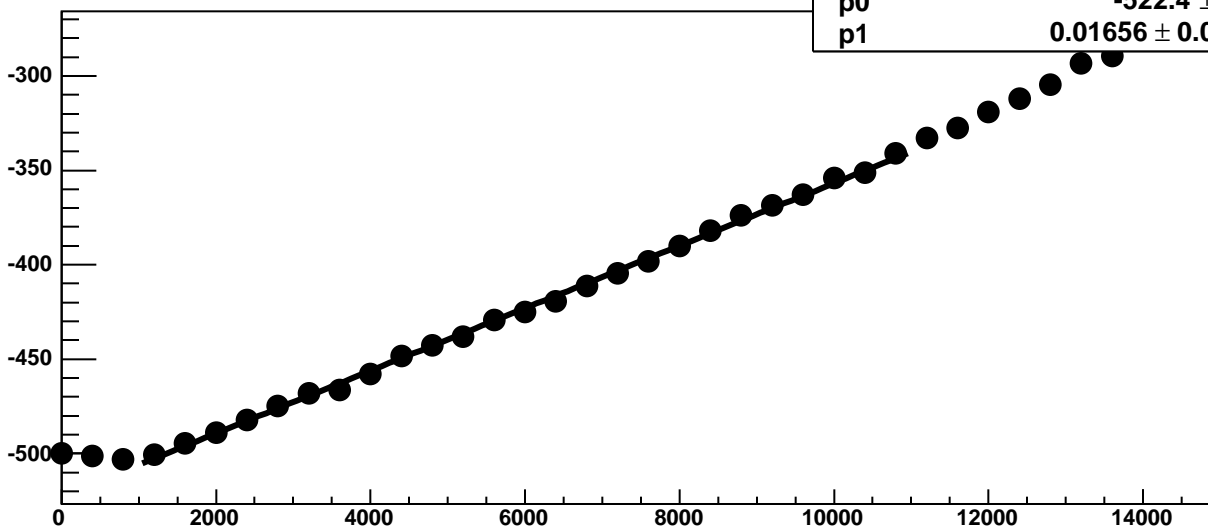
Chip 3, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

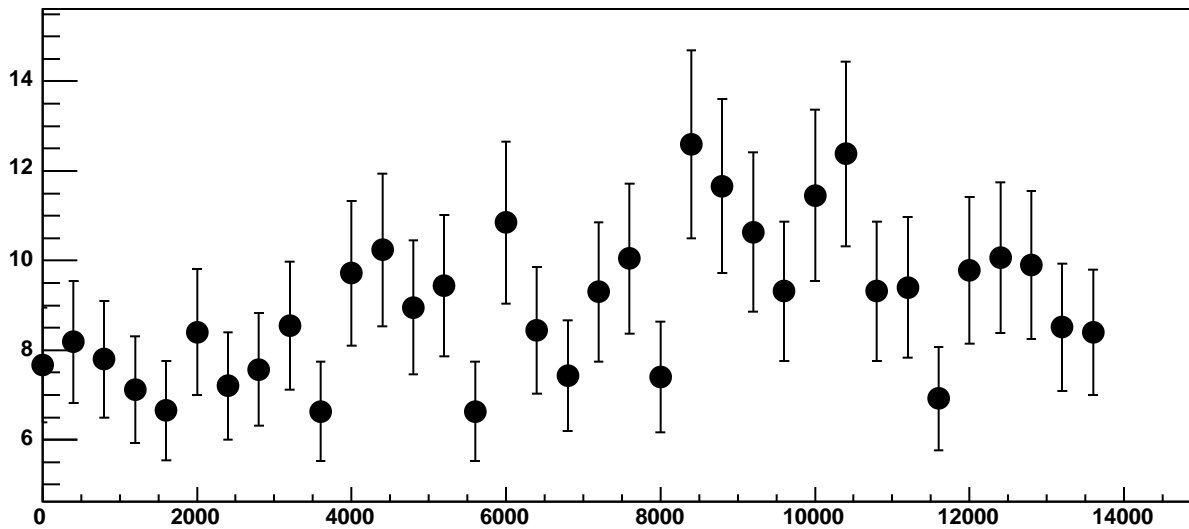


Chip 3, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC

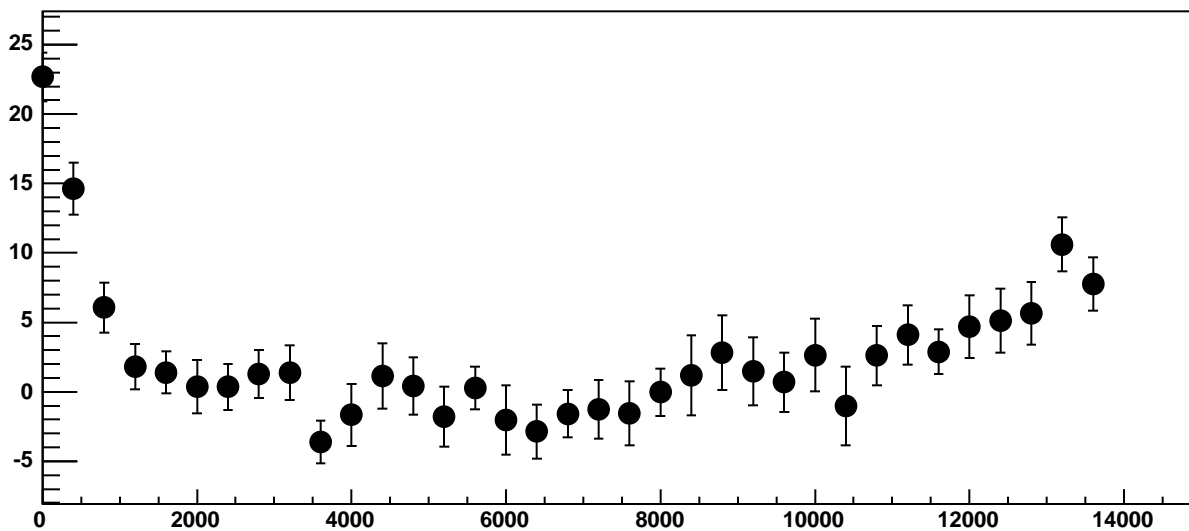


χ^2 / ndf 19.25 / 23
p0 -522.4 ± 0.8407
p1 0.01656 ± 0.0001397

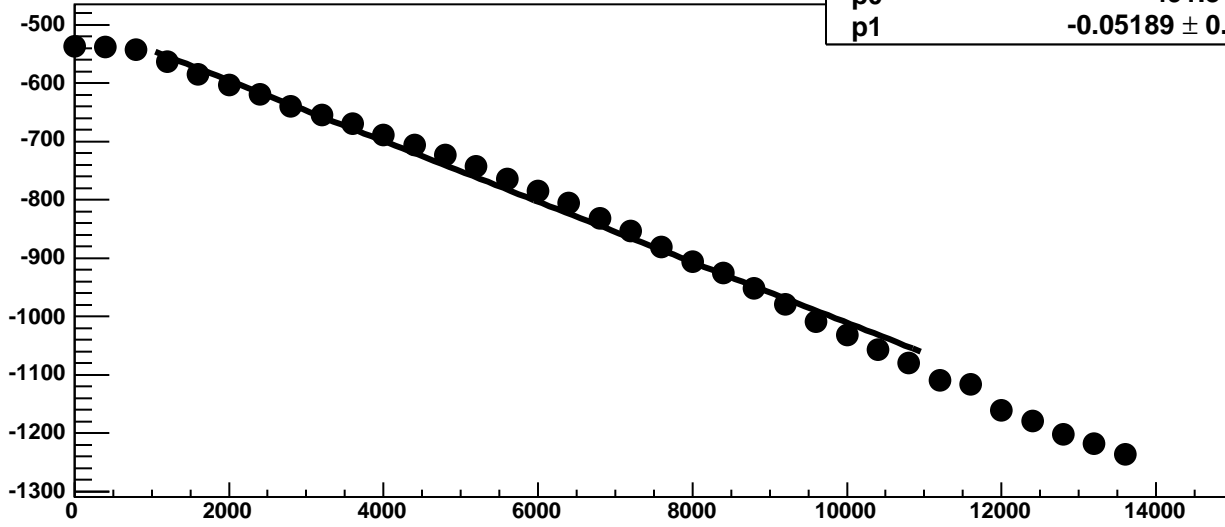
Chip 3, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

684.9 / 23

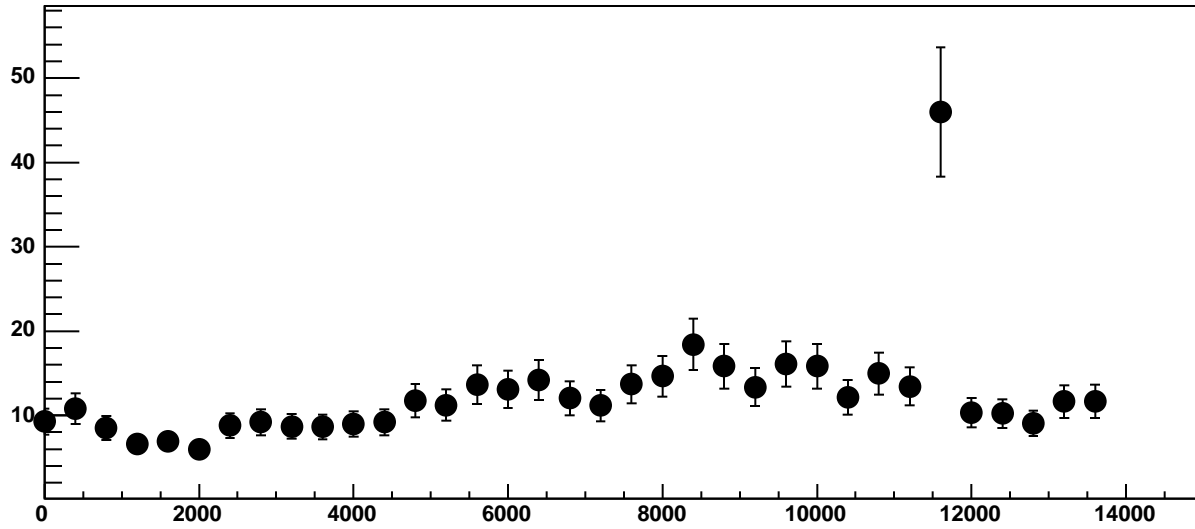
p0

-491.8 ± 0.882

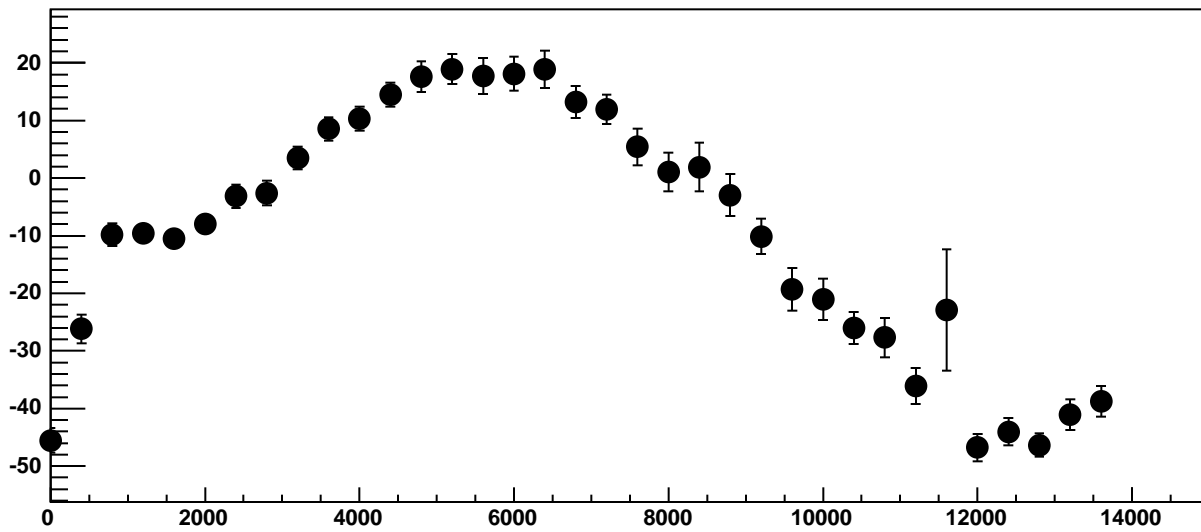
p1

-0.05189 ± 0.000169

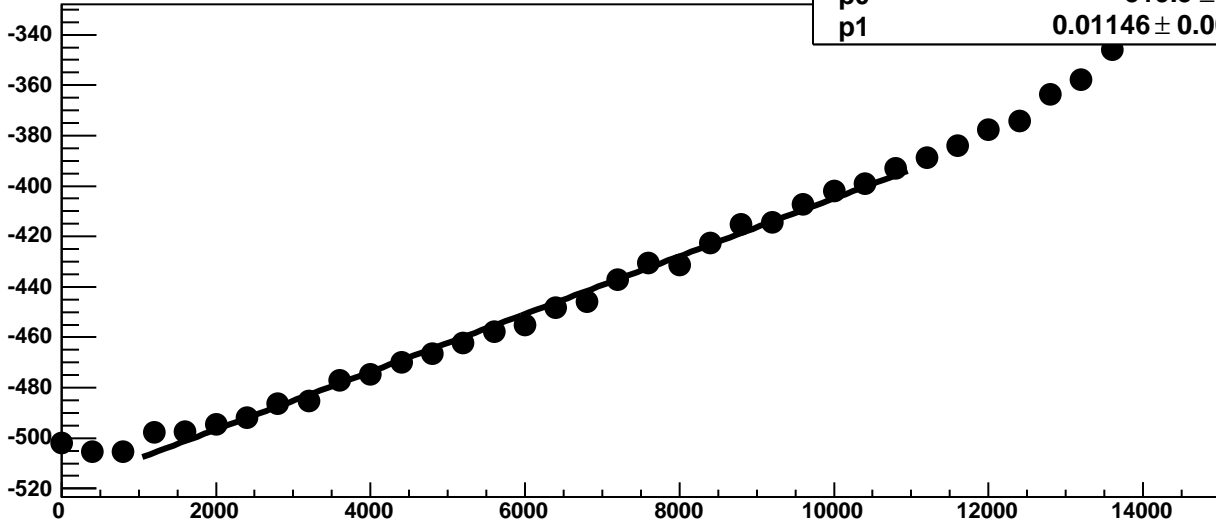
Chip 3, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

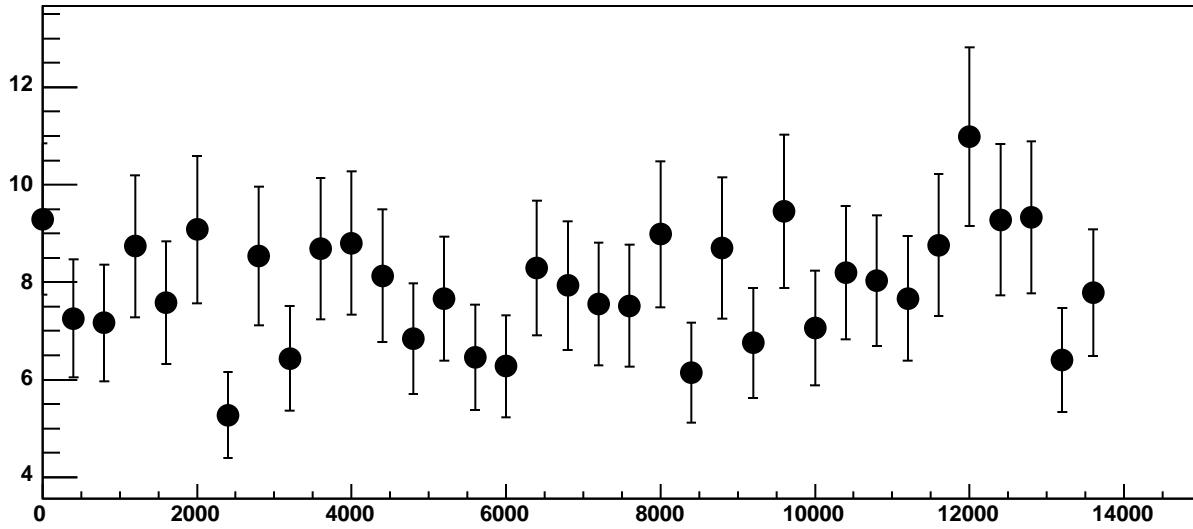


Chip 3, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

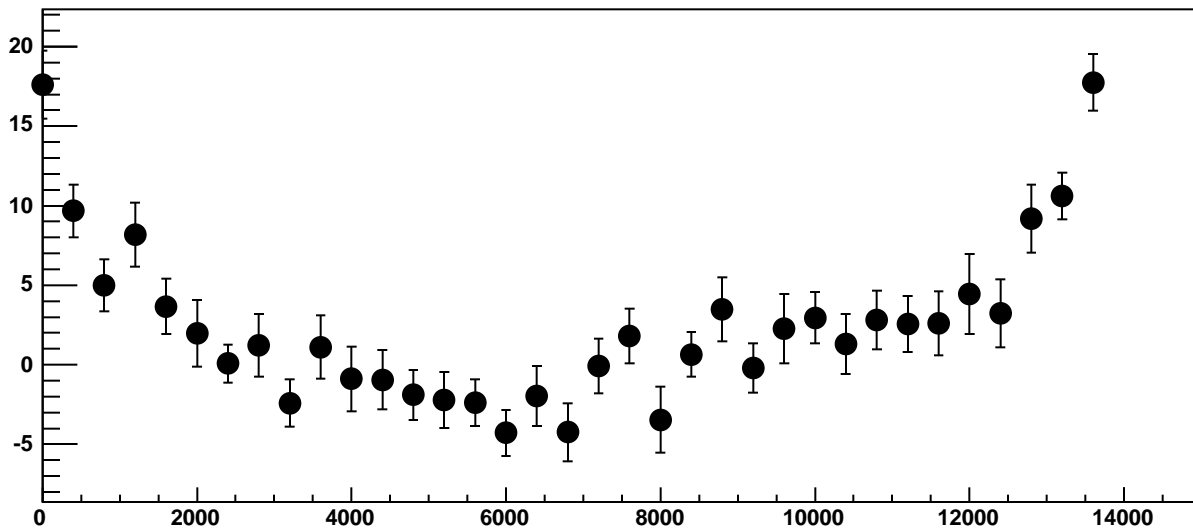


χ^2 / ndf 61.21 / 23
p0 -519.5 ± 0.7974
p1 0.01146 ± 0.0001216

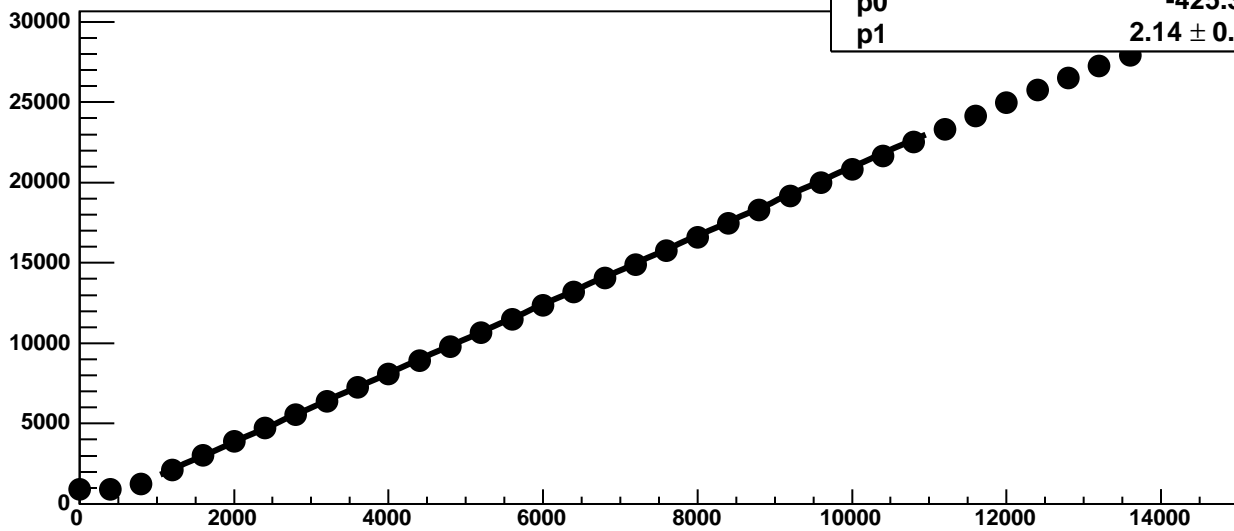
Chip 3, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



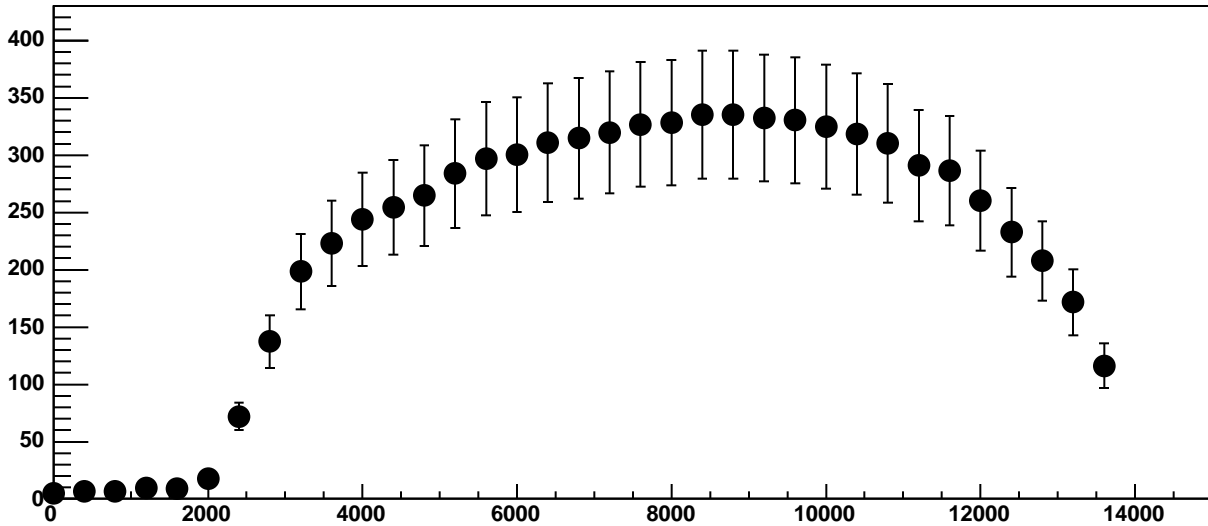
Chip 3, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



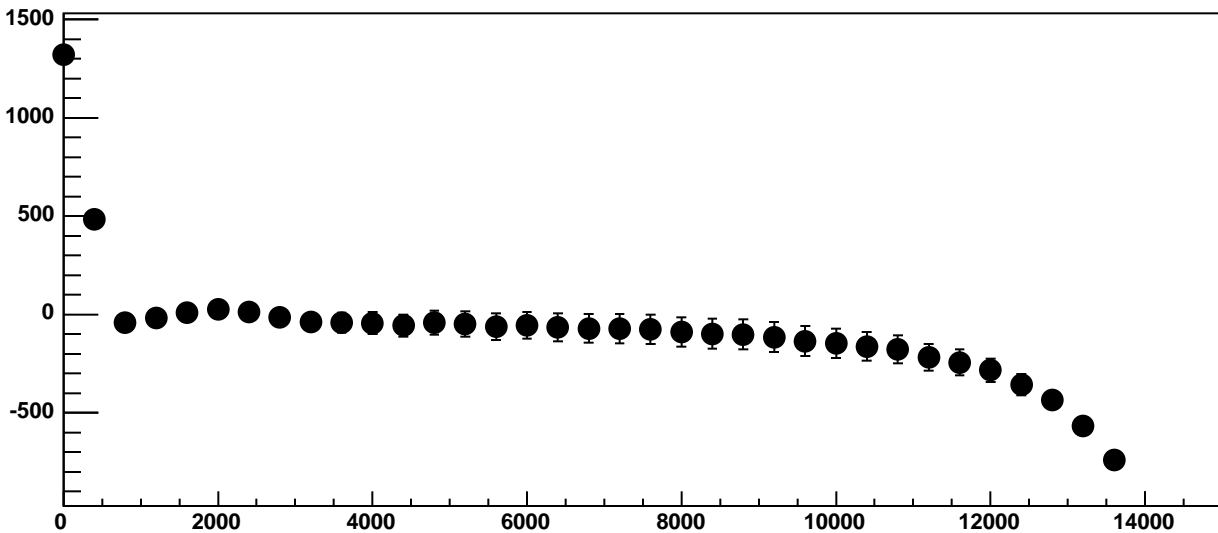
Chip 3, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC



Chip 3, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC

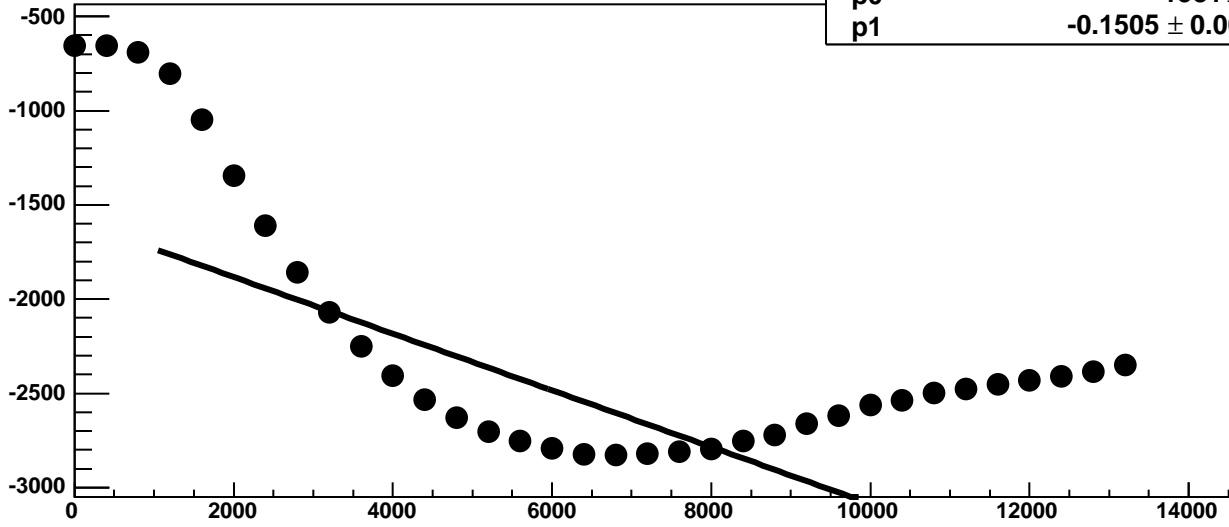


Chip 3, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC

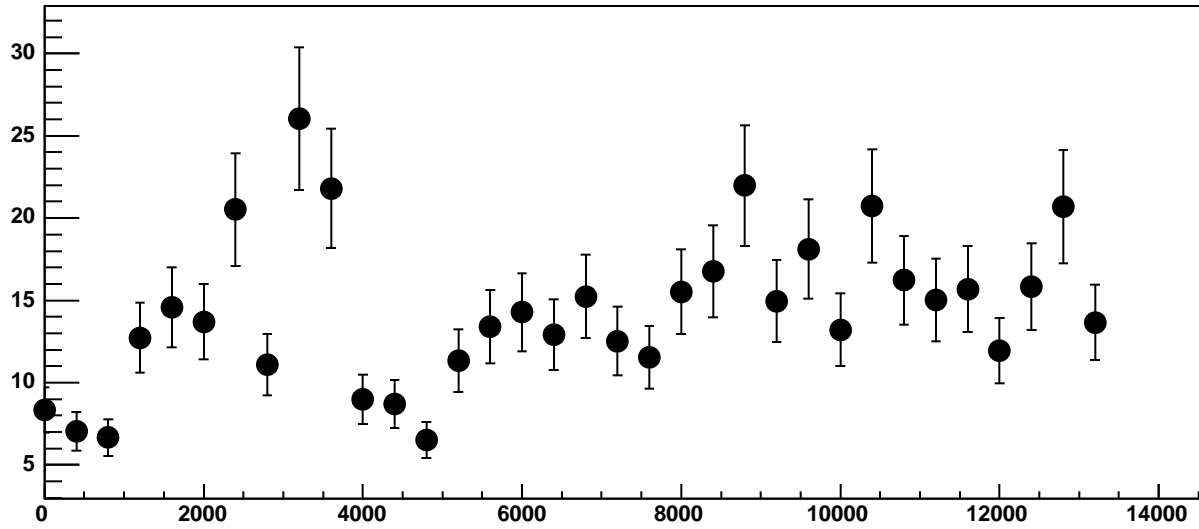


Chip 3, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

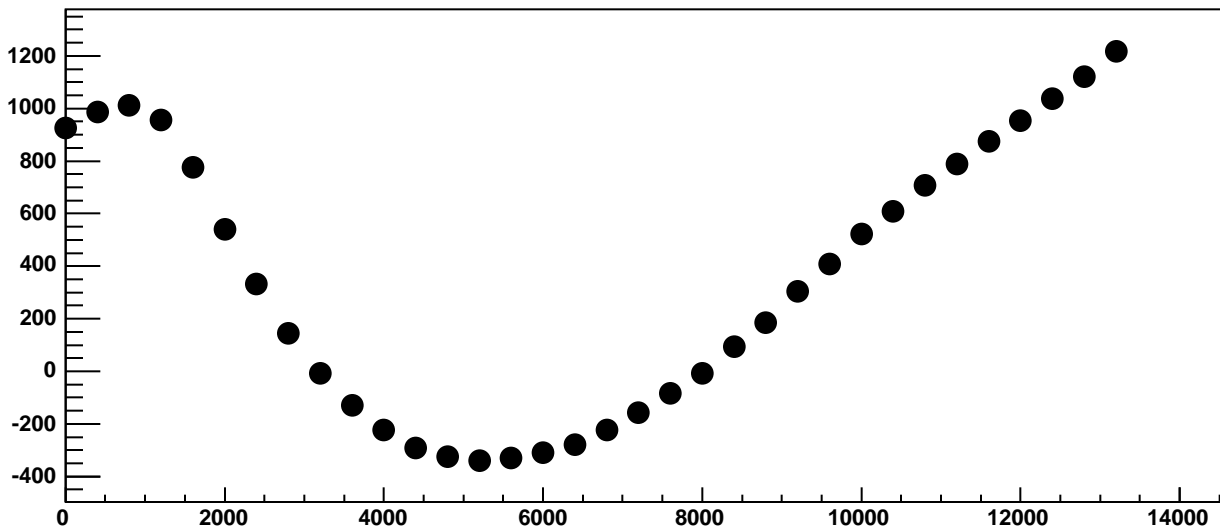
χ^2 / ndf 4.356e+05 / 23
p0 -1581 ± 1.433
p1 -0.1505 ± 0.0002368



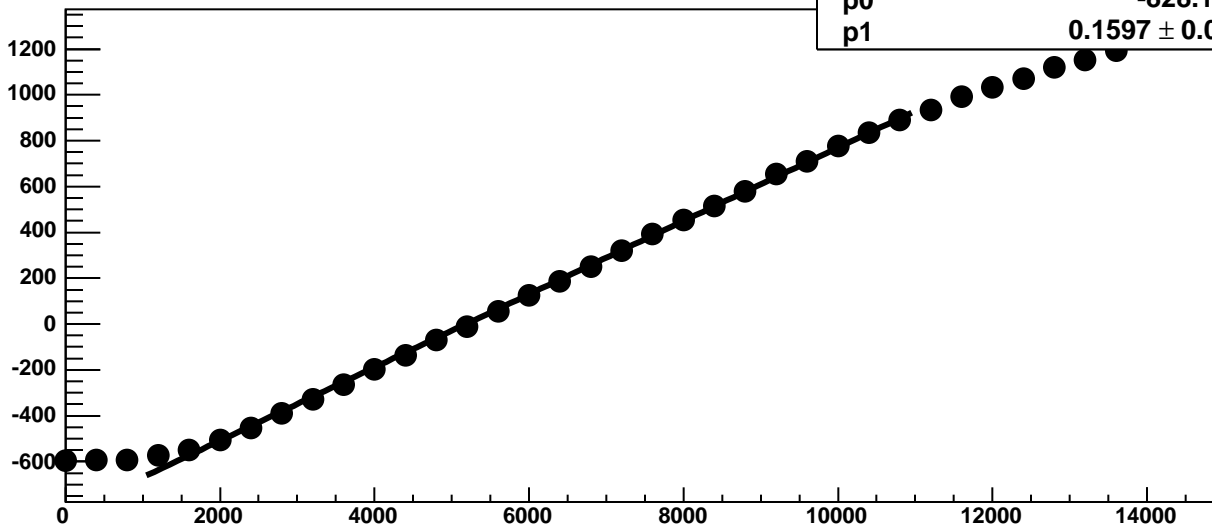
Chip 3, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



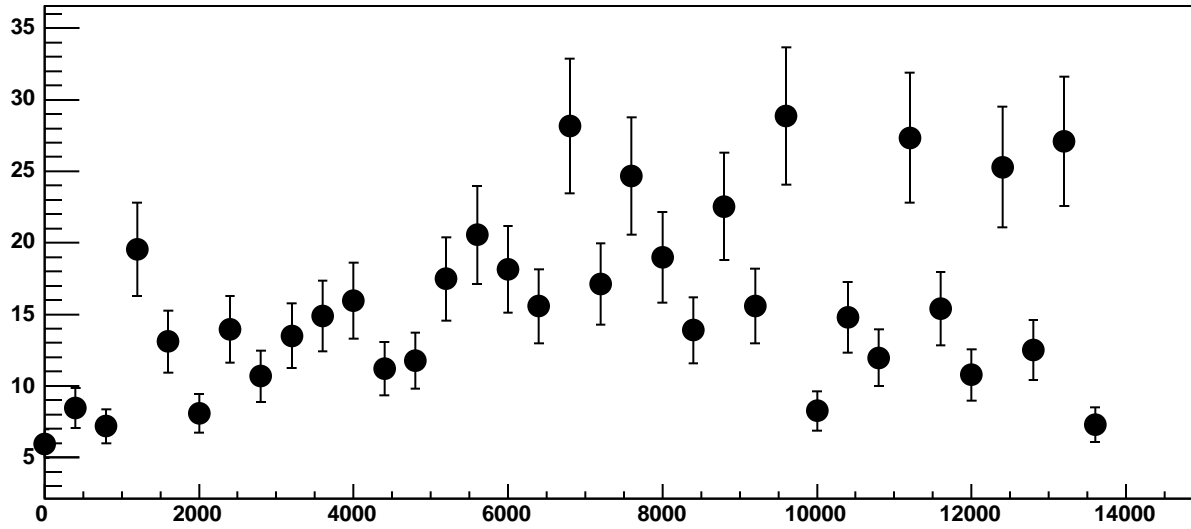
Chip 3, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



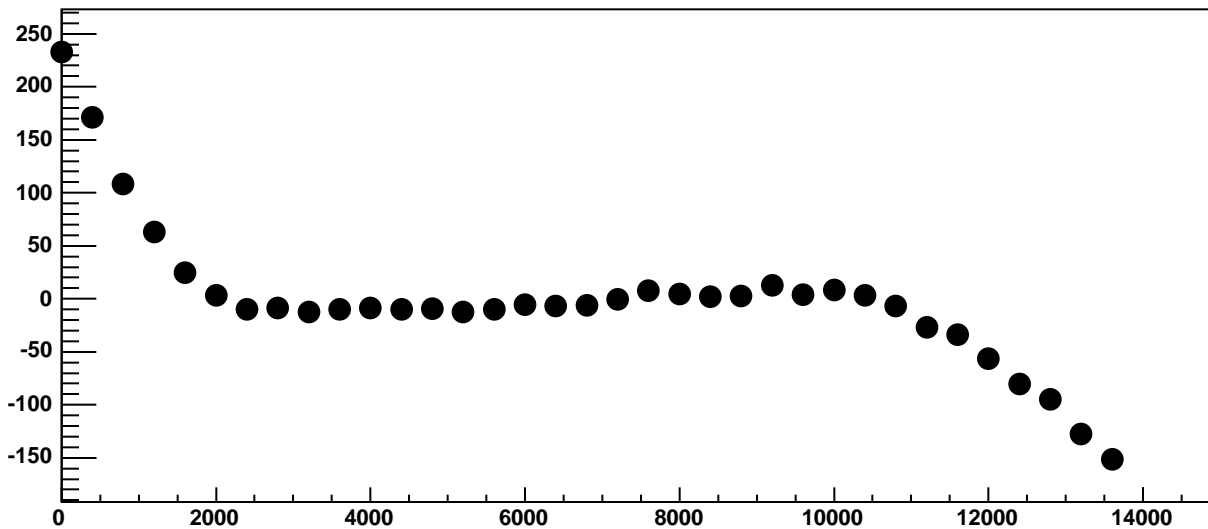
Chip 3, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



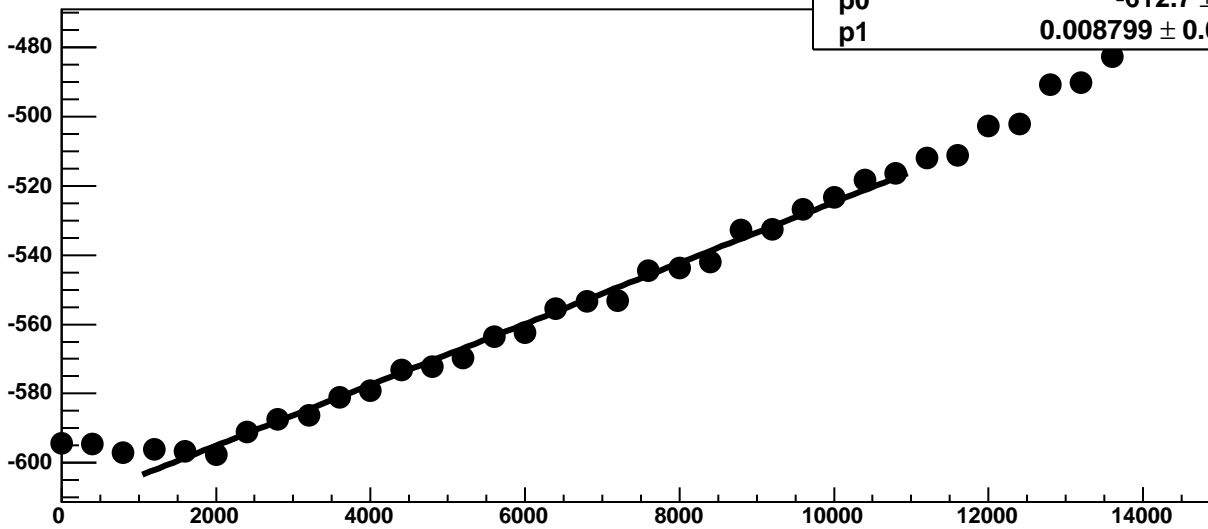
Chip 3, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



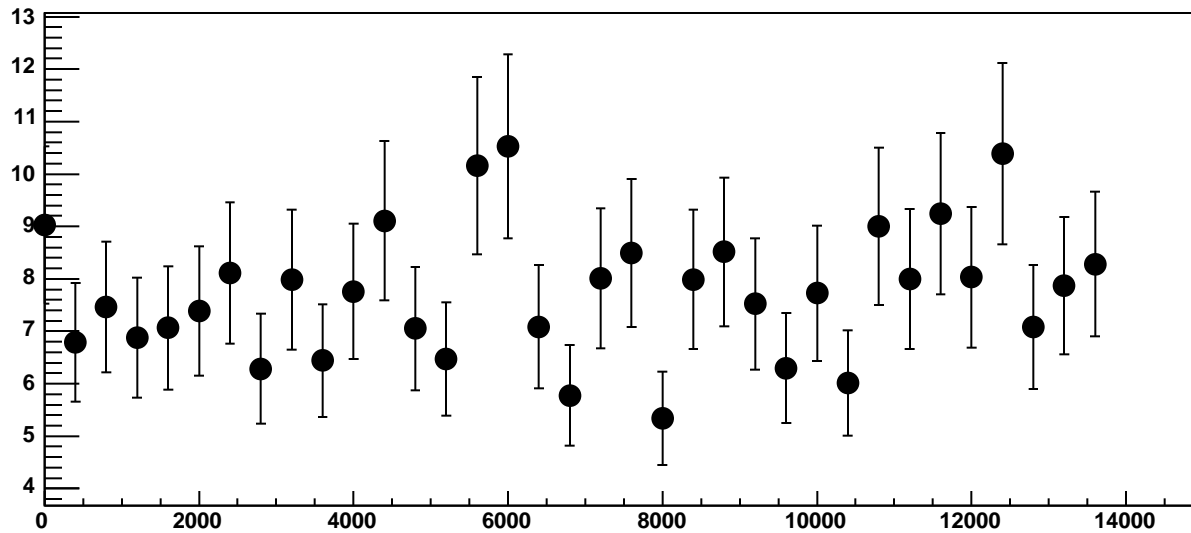
Chip 3, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



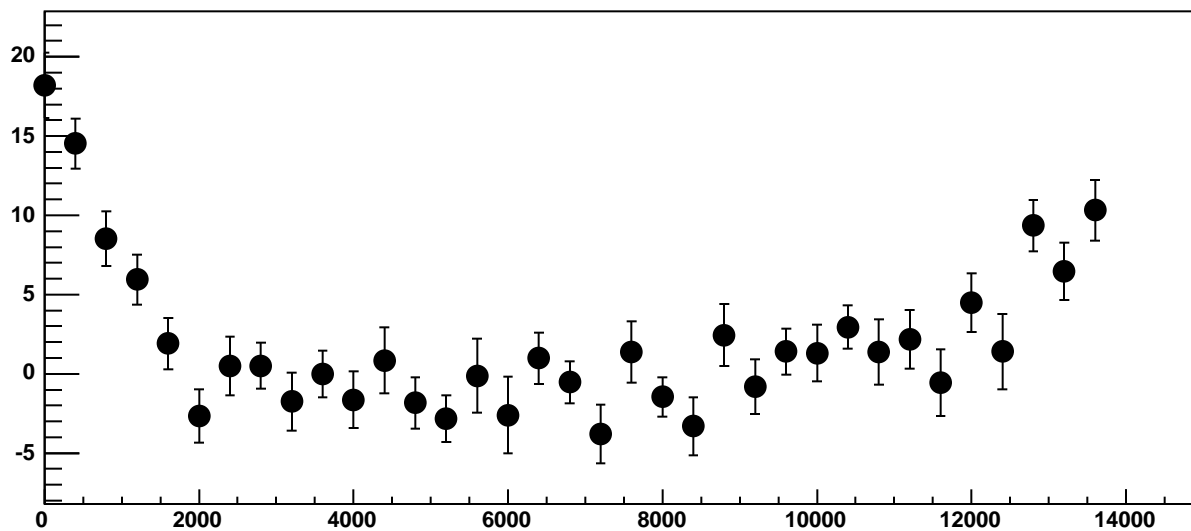
Chip 3, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



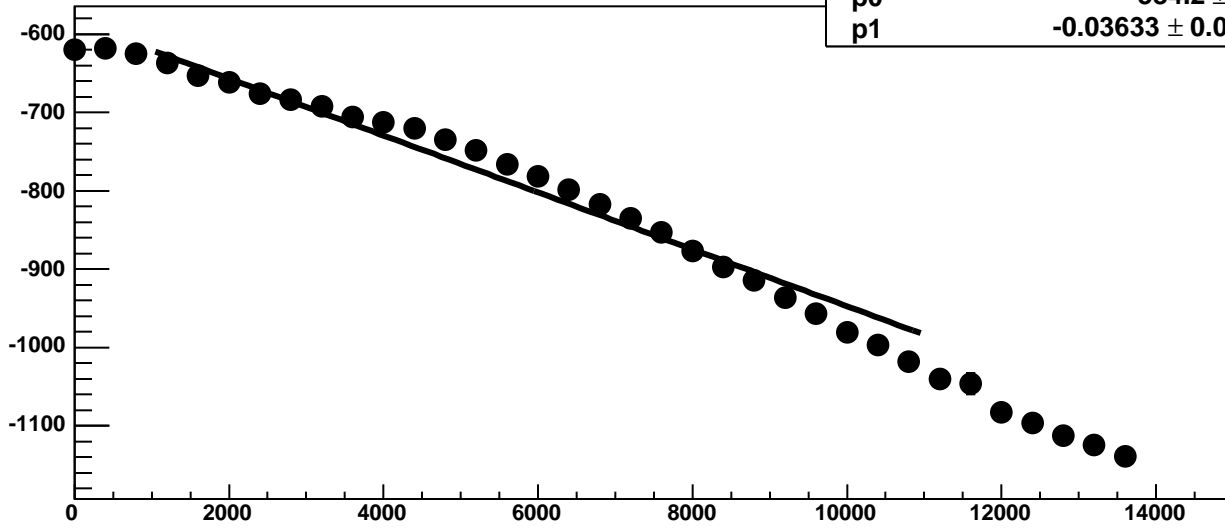
Chip 3, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

799.5 / 23

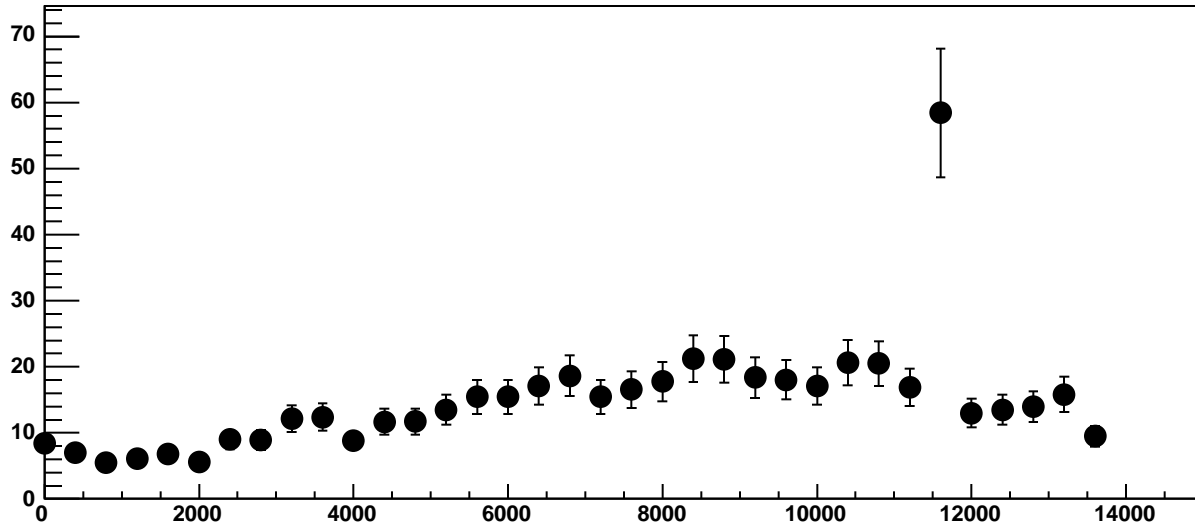
p0

-584.2 ± 0.9104

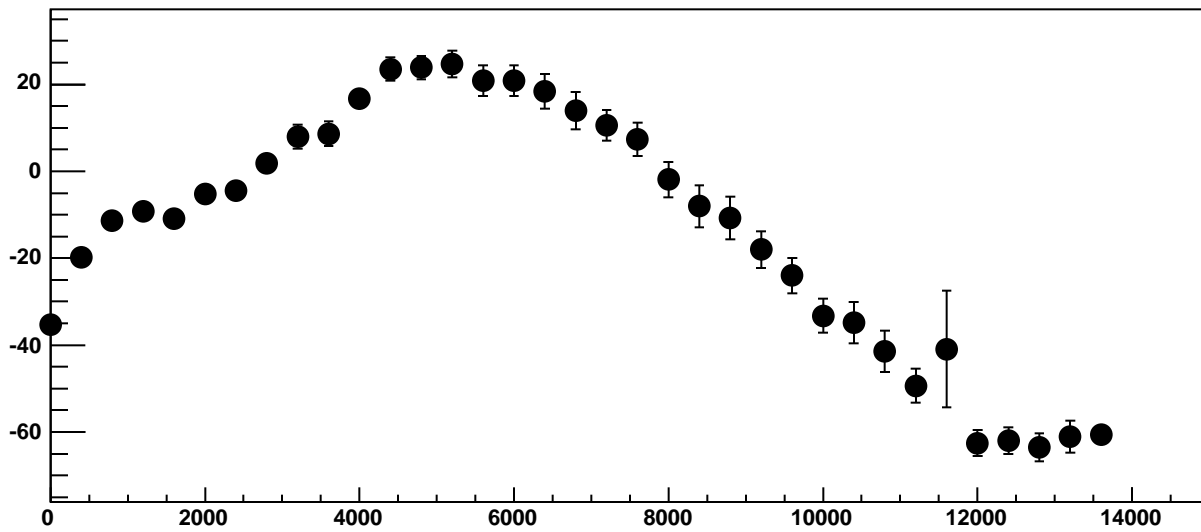
p1

-0.03633 ± 0.0001984

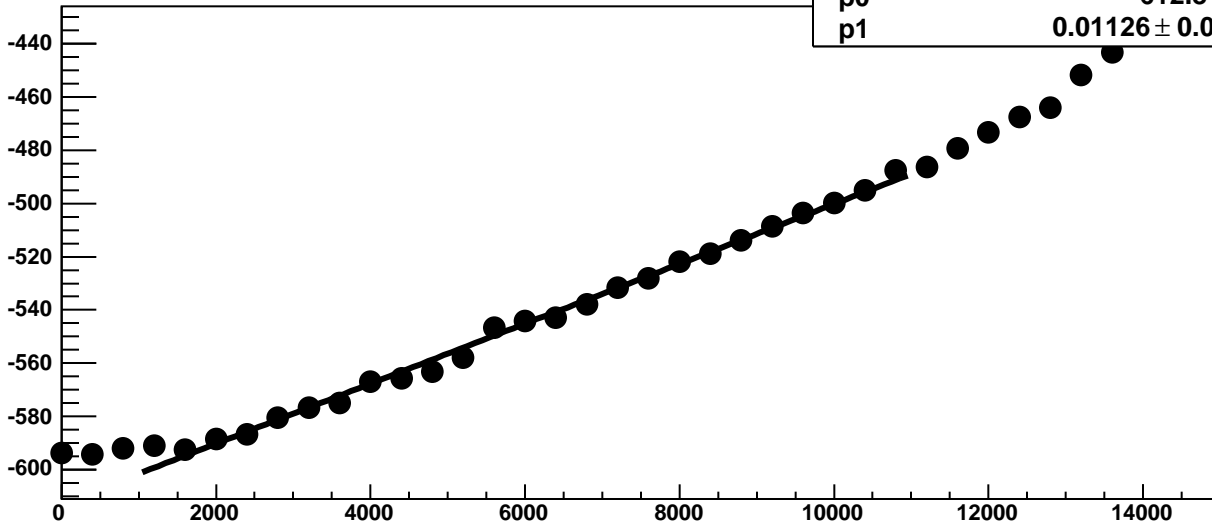
Chip 3, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



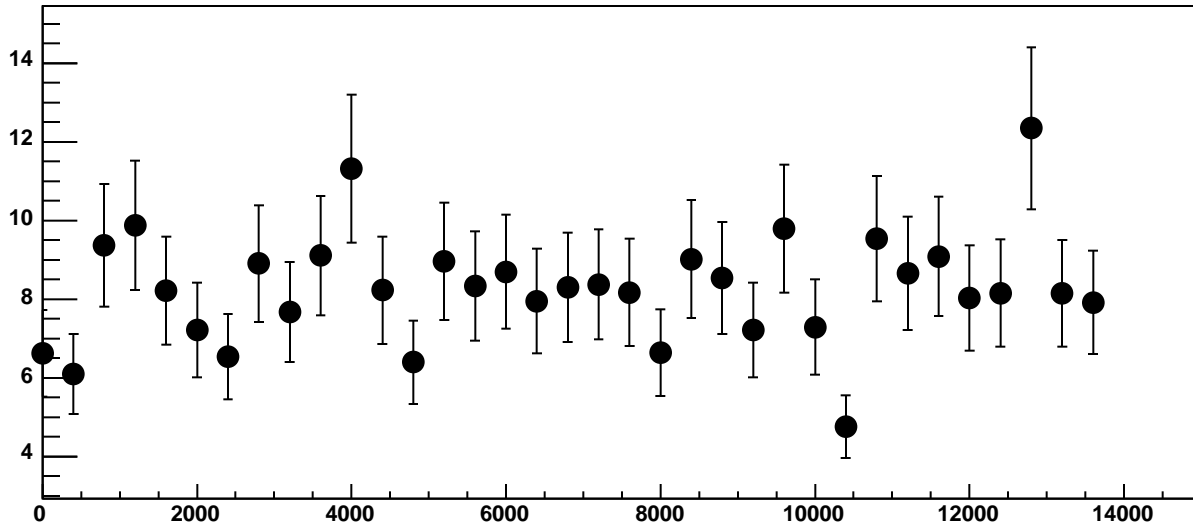
Chip 3, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC



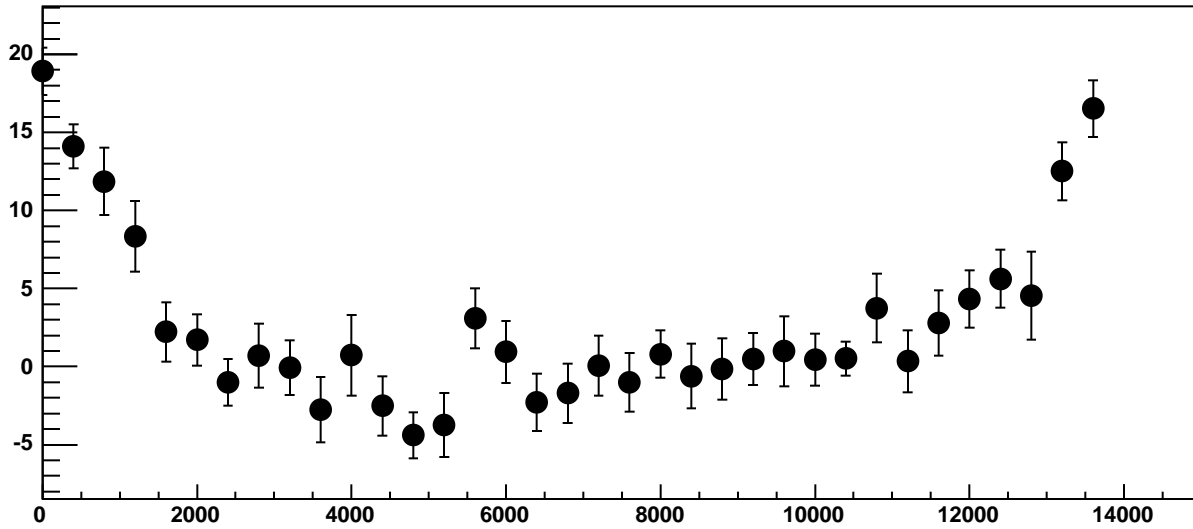
Chip 3, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



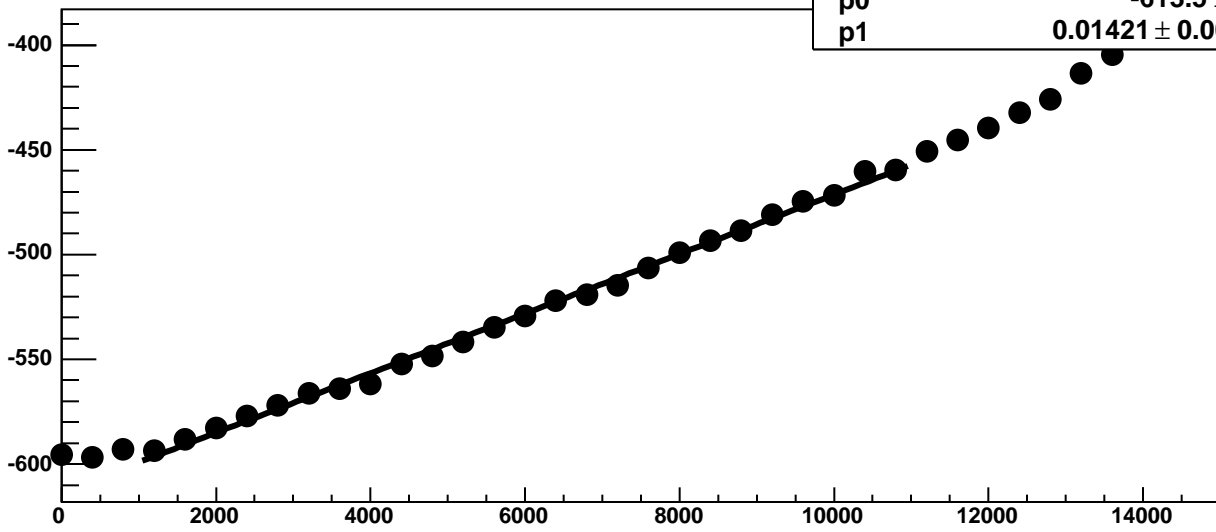
Chip 3, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

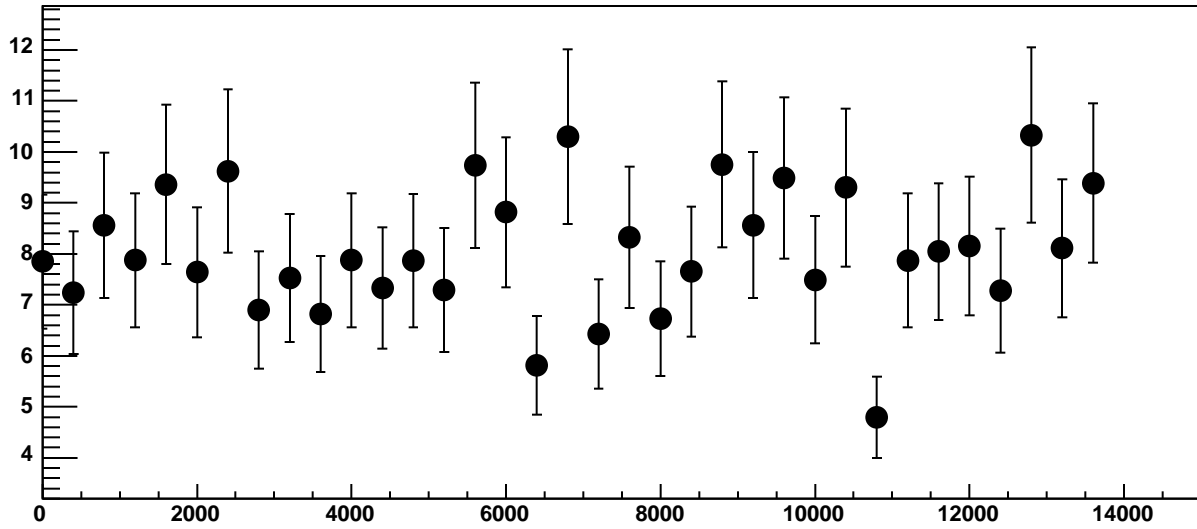


Chip 3, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

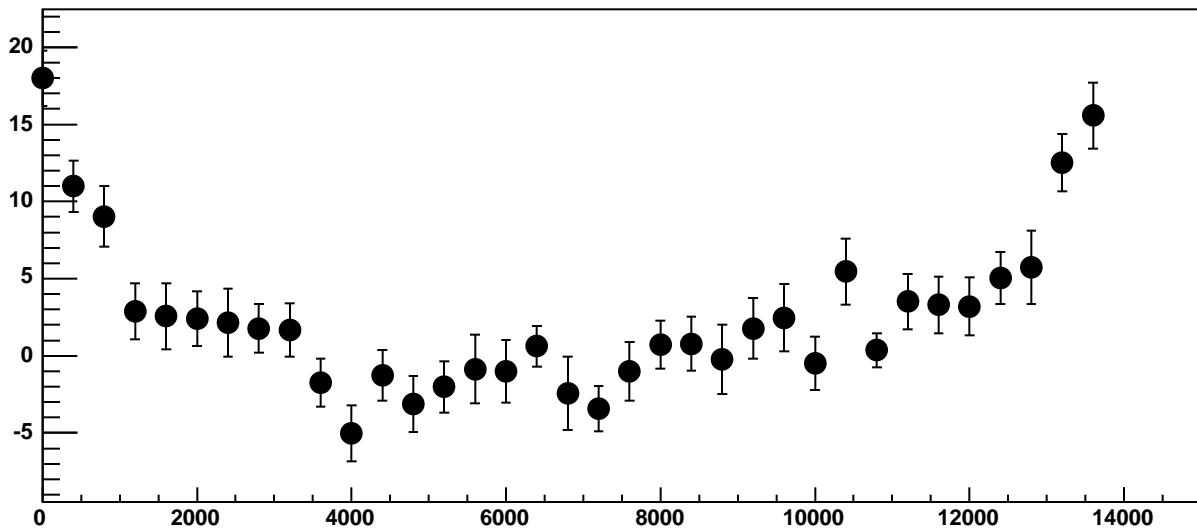


χ^2 / ndf 39.6 / 23
p0 -613.5 ± 0.814
p1 0.01421 ± 0.0001183

Chip 3, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

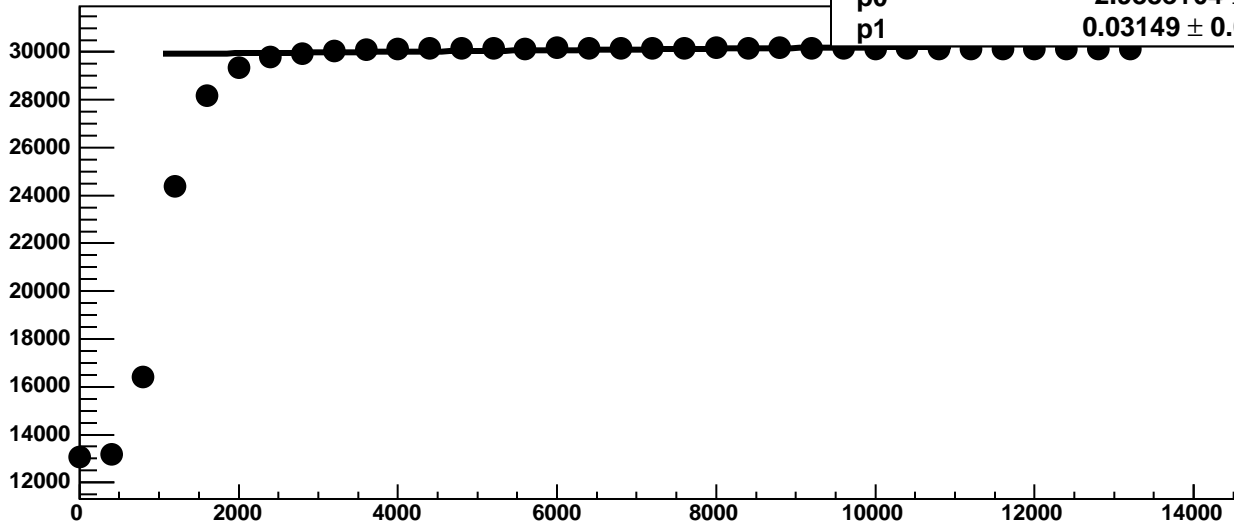
9093 / 23

p0

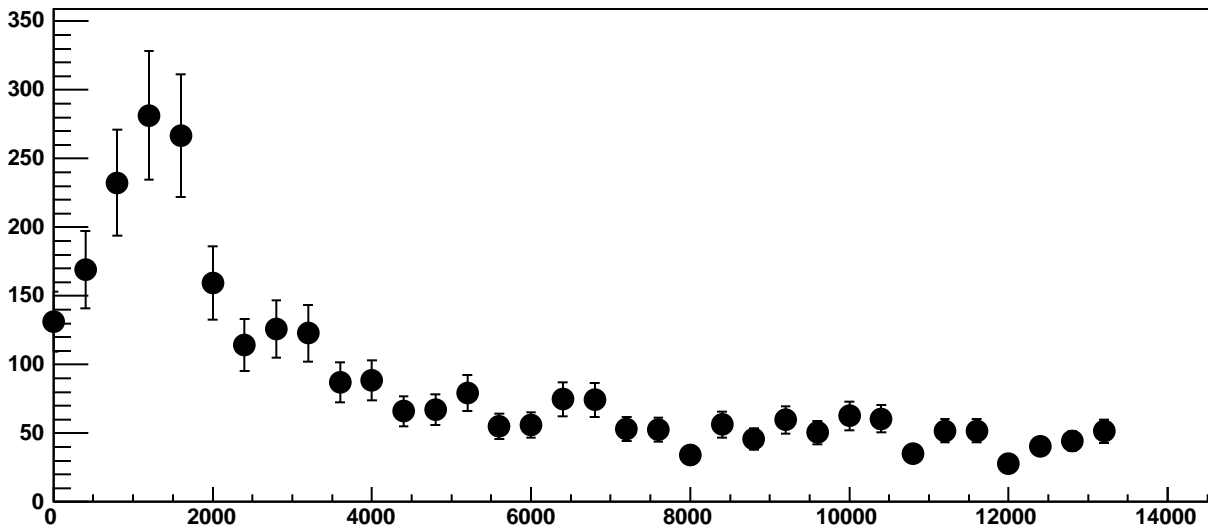
$2.988\text{e}+04 \pm 10.05$

p1

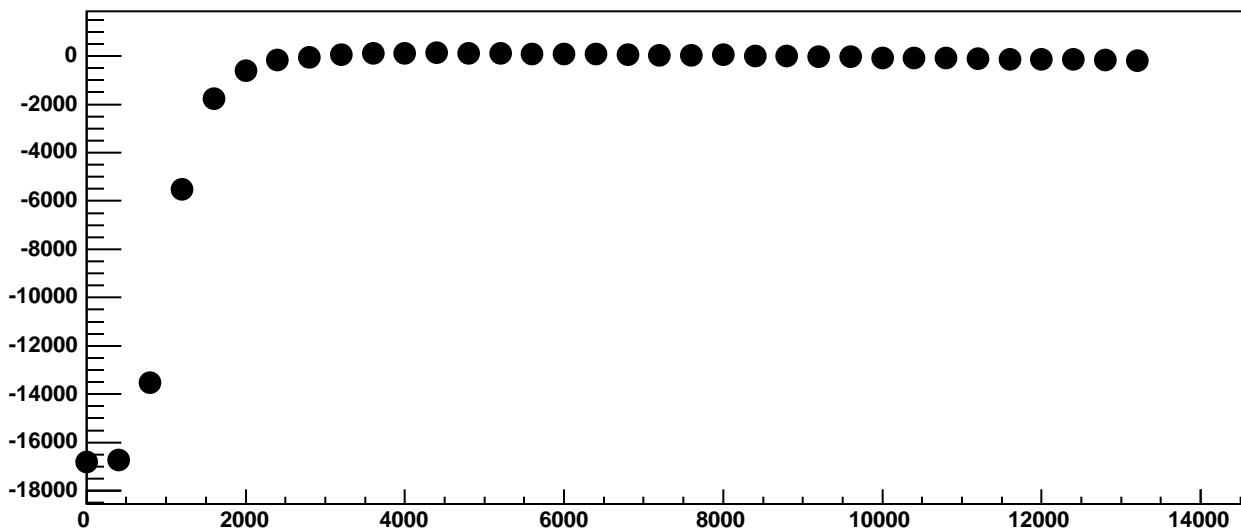
0.03149 ± 0.001254



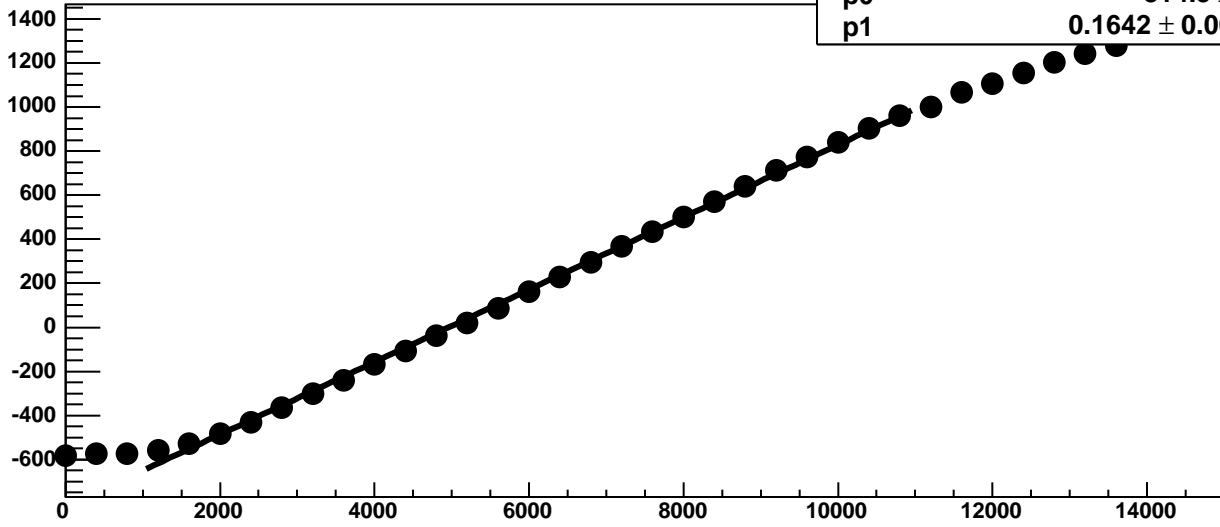
Chip 3, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC

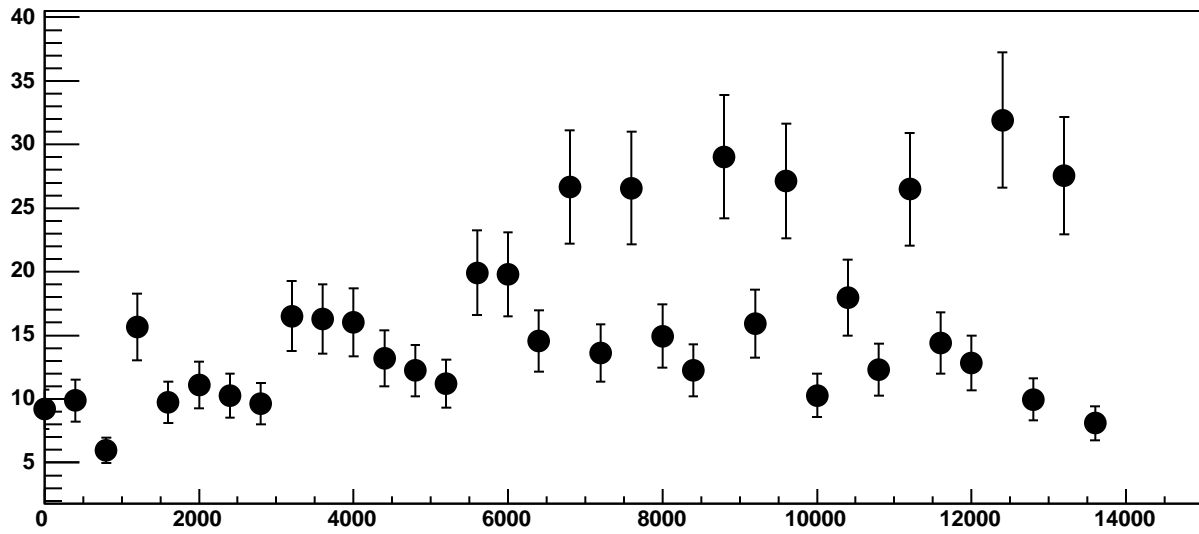


Chip 3, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

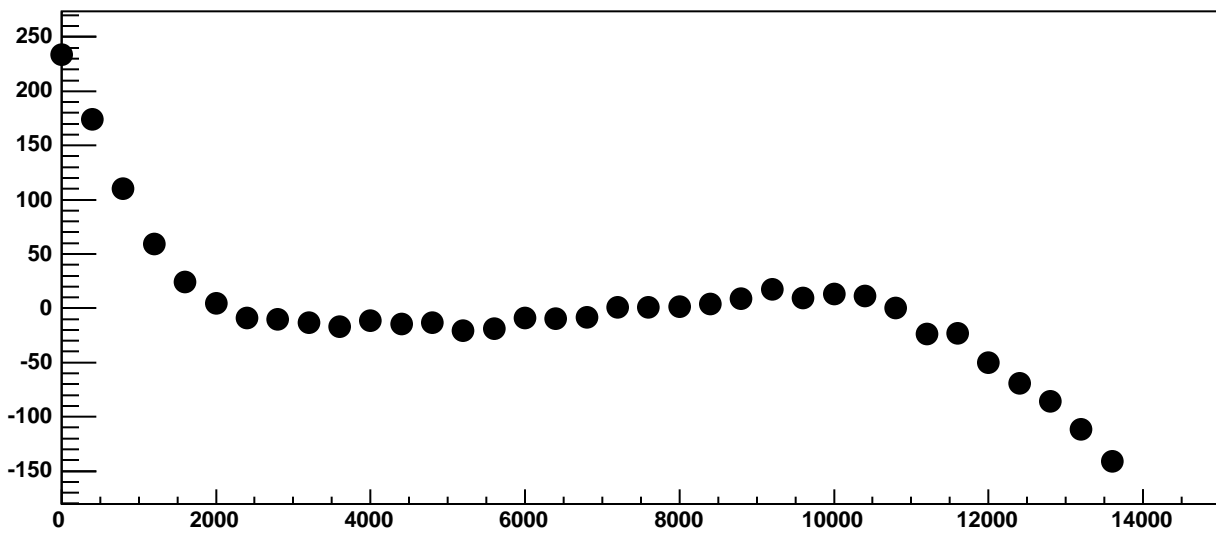


χ^2 / ndf 681.1 / 23
p0 -814.5 ± 1.293
p1 0.1642 ± 0.0002084

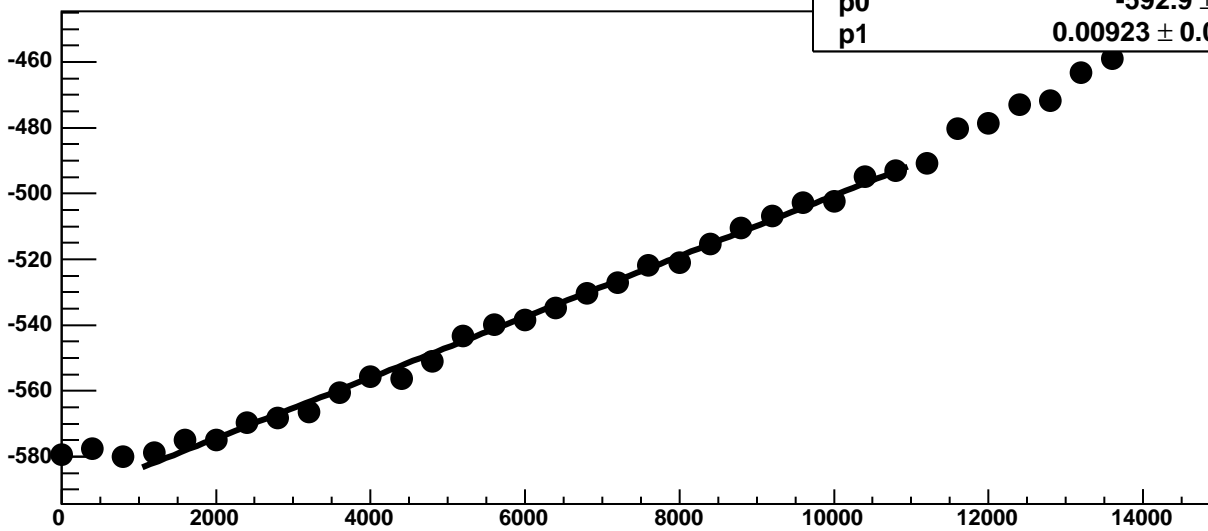
Chip 3, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

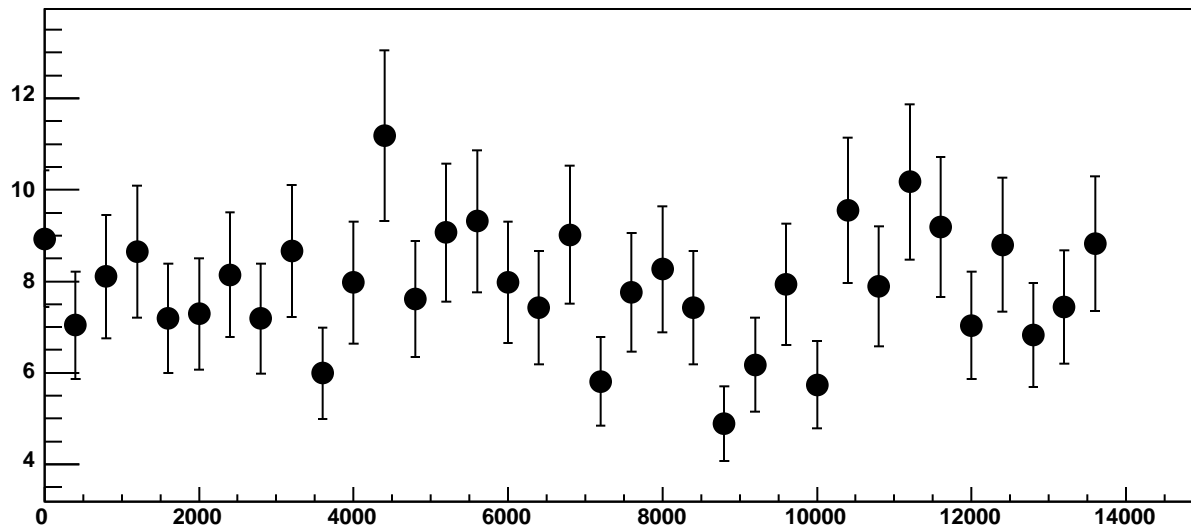


Chip 3, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

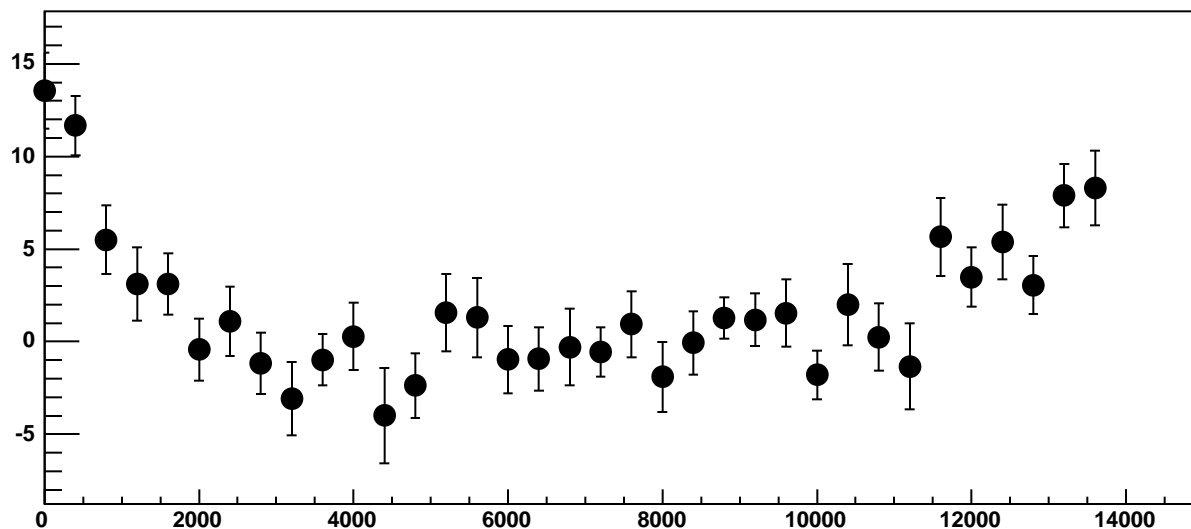


χ^2 / ndf 22.57 / 23
p0 -592.9 ± 0.8096
p1 0.00923 ± 0.0001169

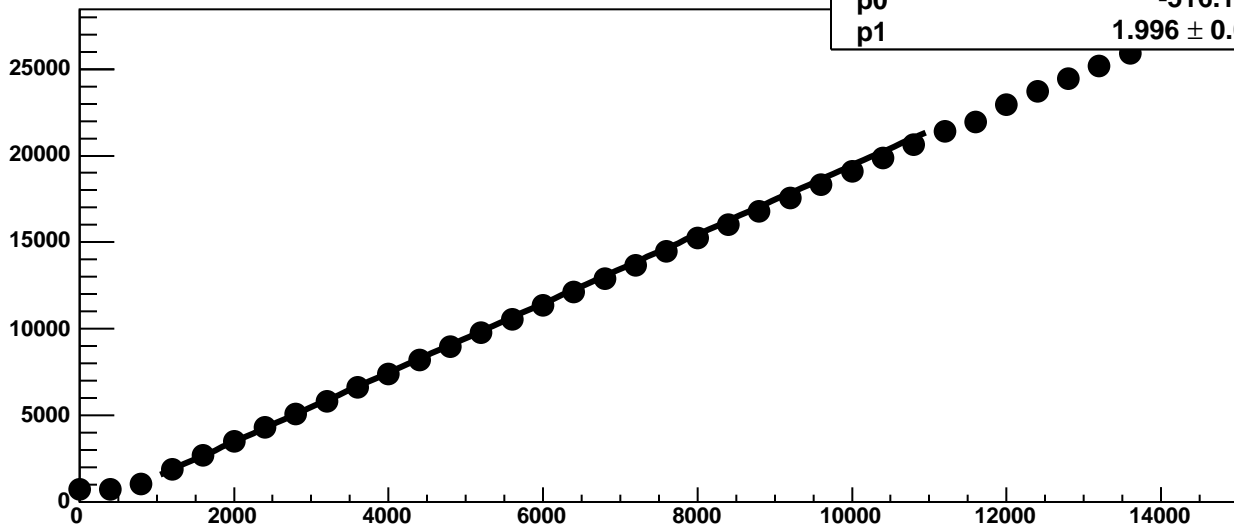
Chip 3, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



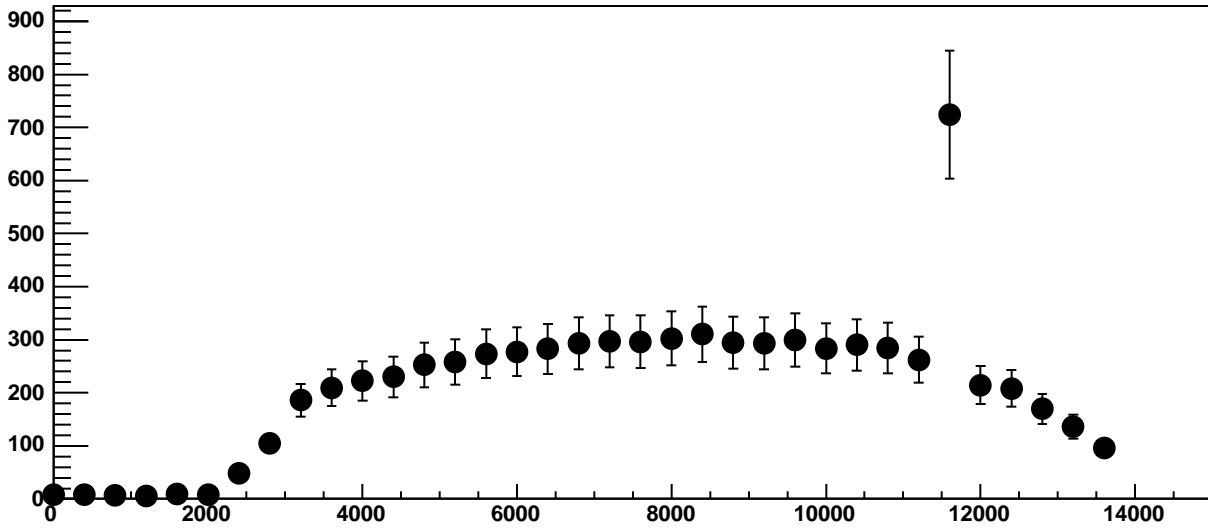
Chip 3, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



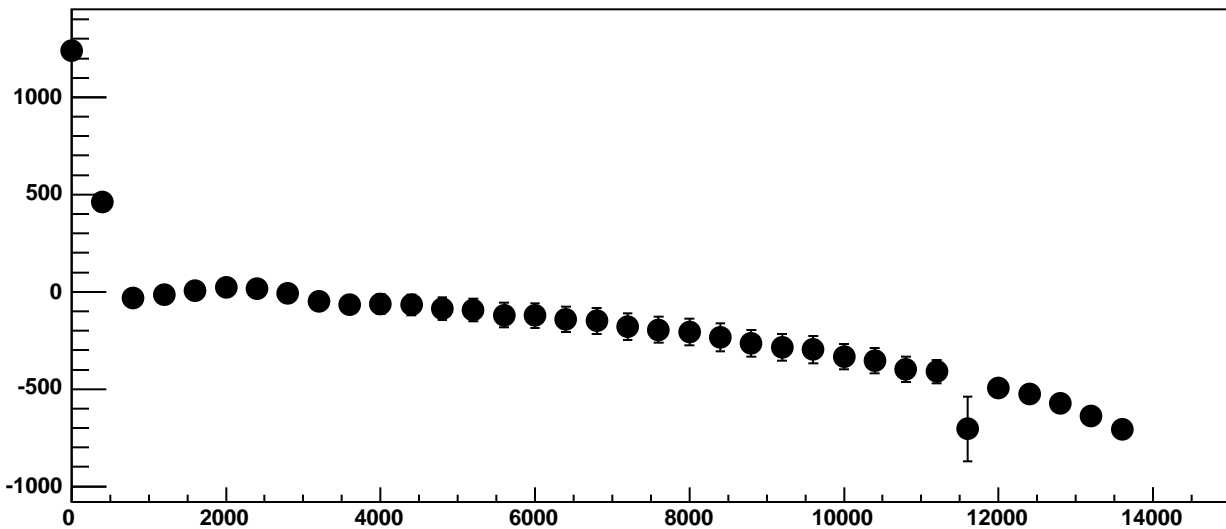
Chip 3, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC



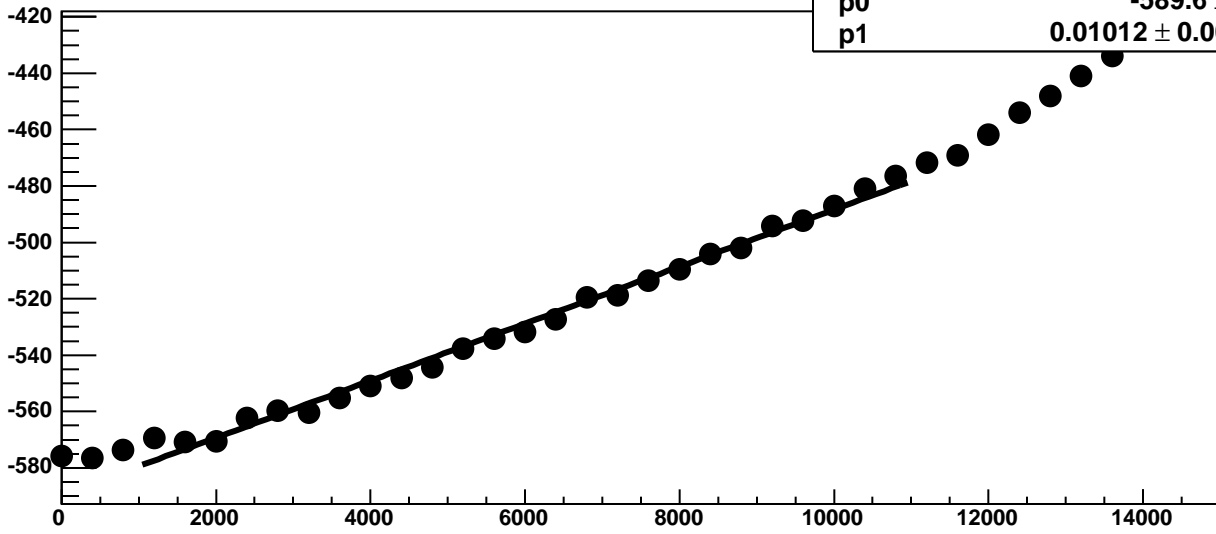
Chip 3, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC



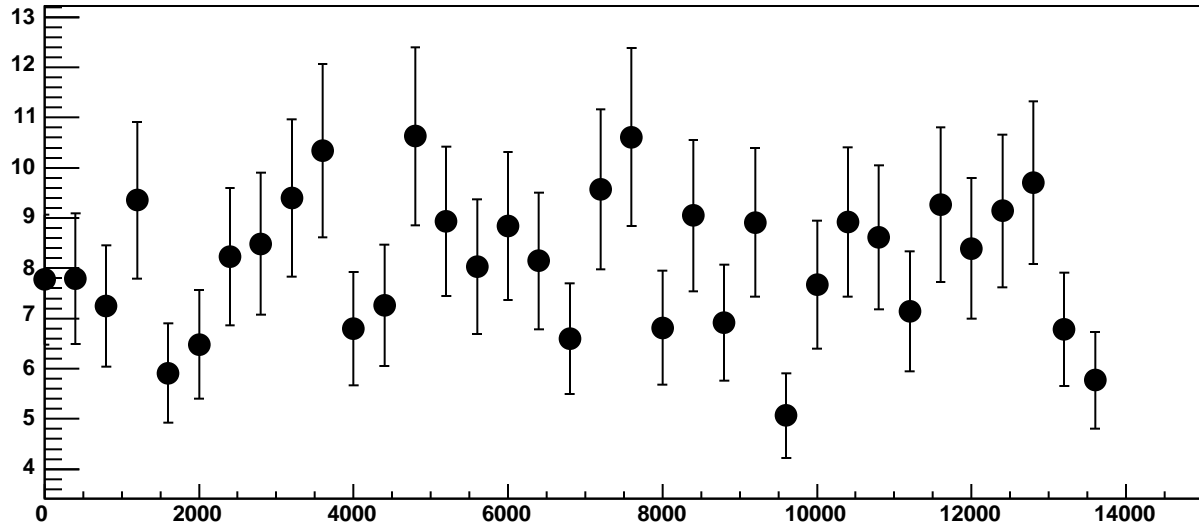
Chip 3, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC



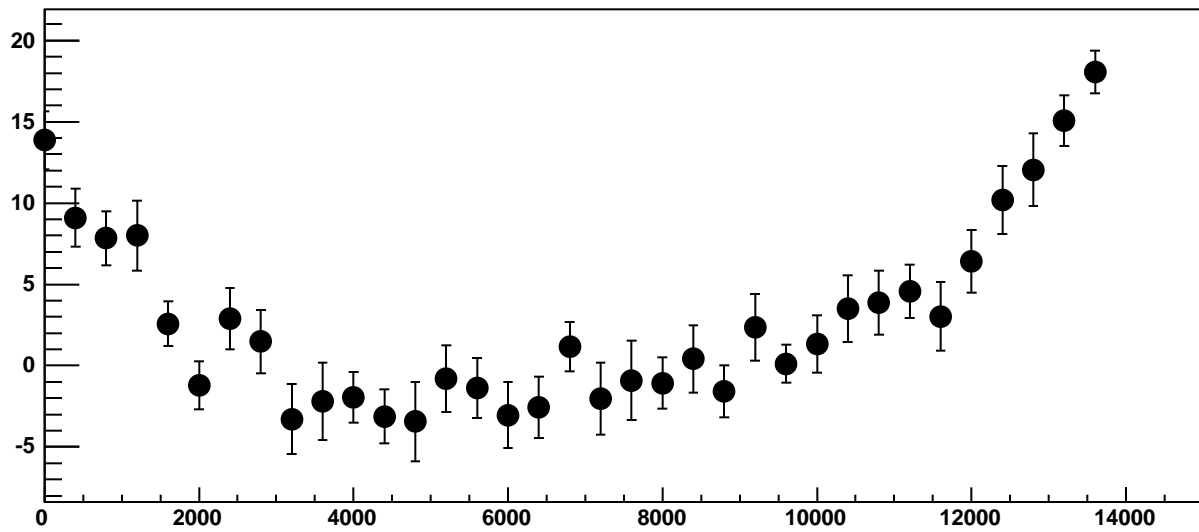
Chip 3, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



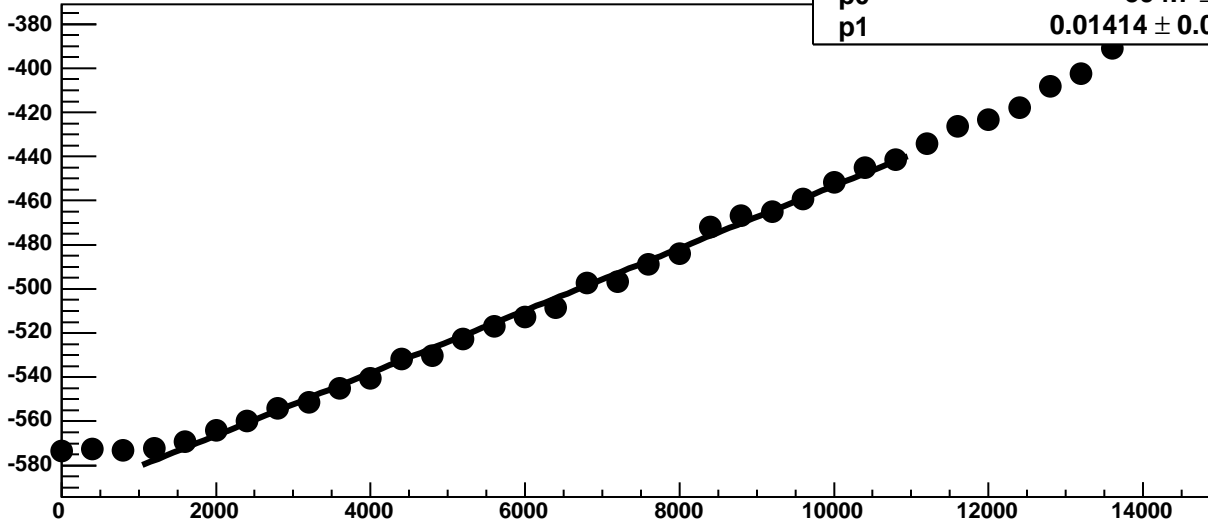
Chip 3, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



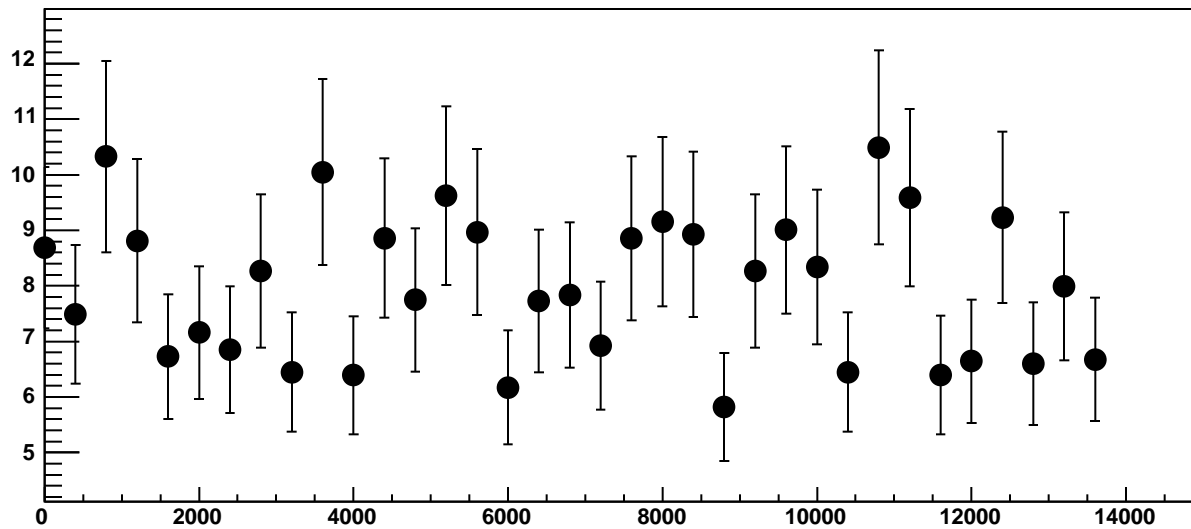
Chip 3, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



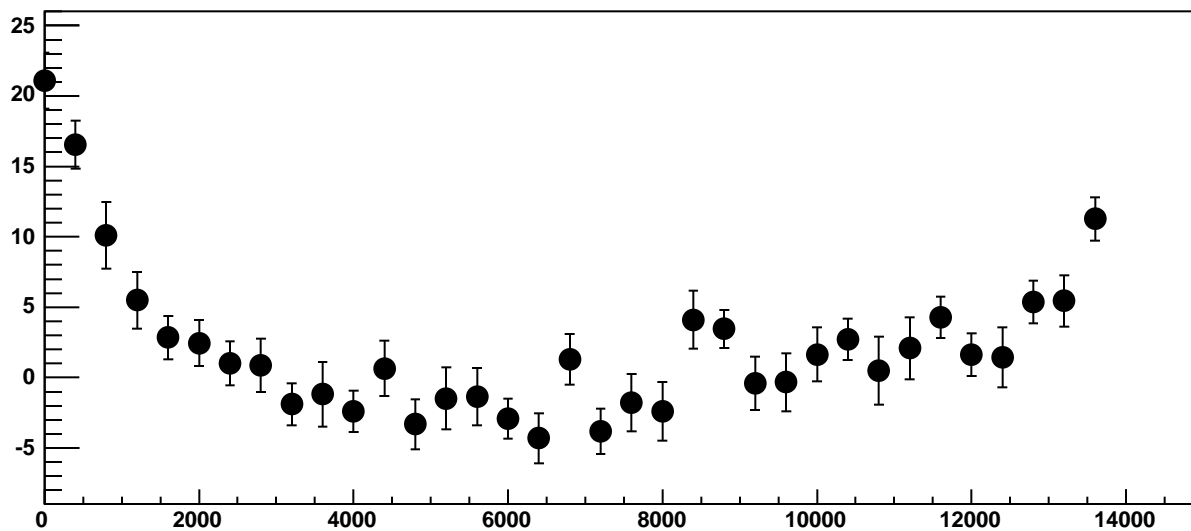
Chip 3, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



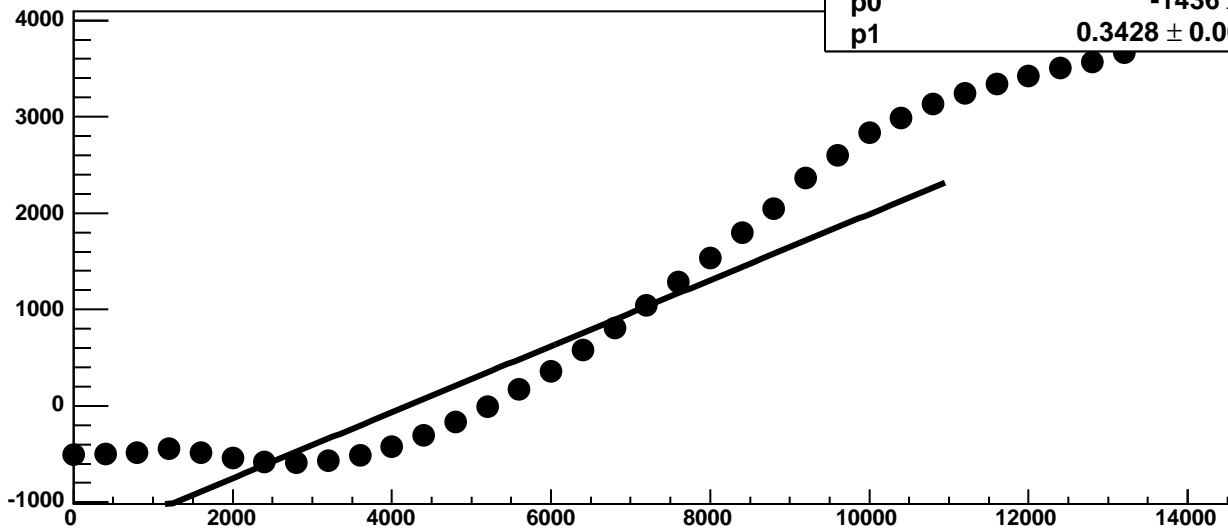
Chip 3, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



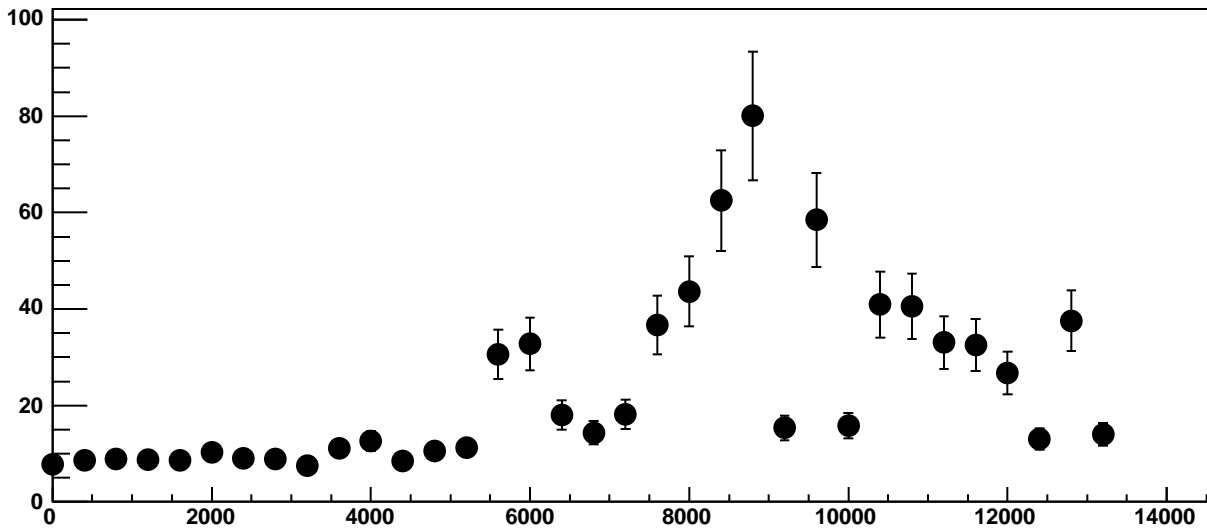
Chip 3, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



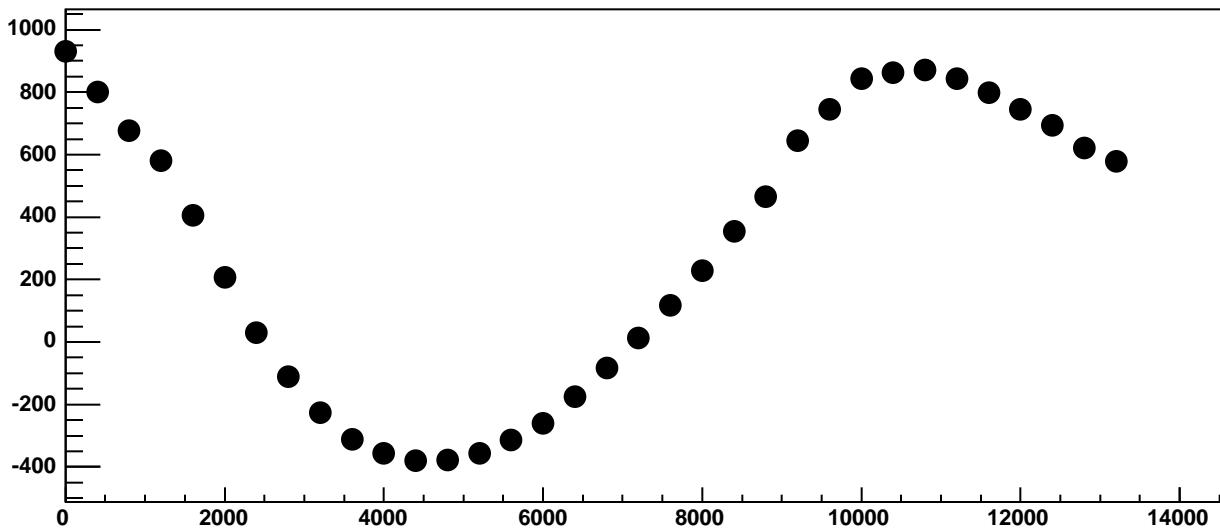
Chip 3, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC



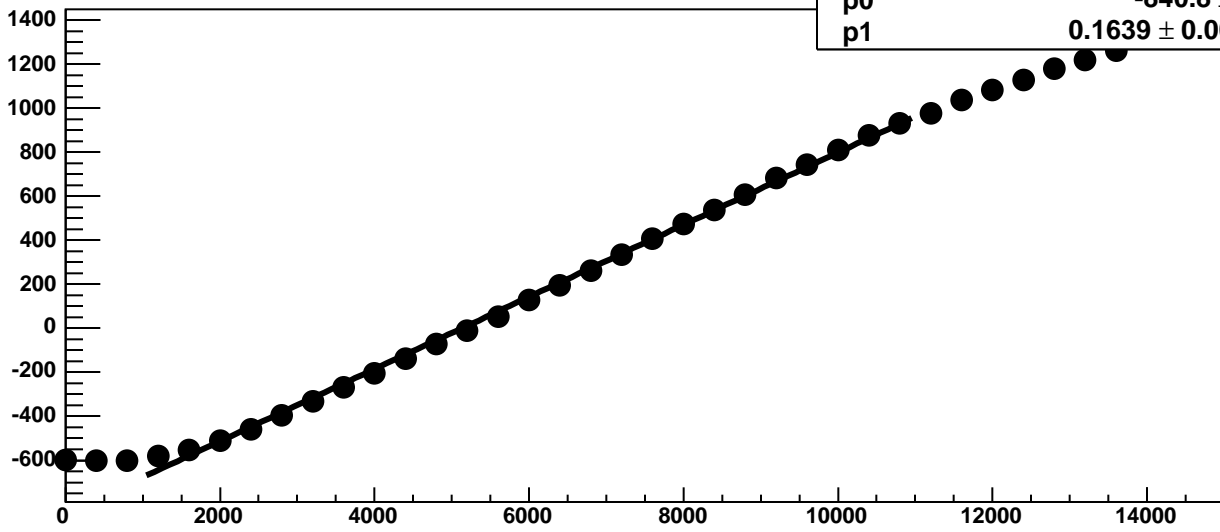
Chip 3, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



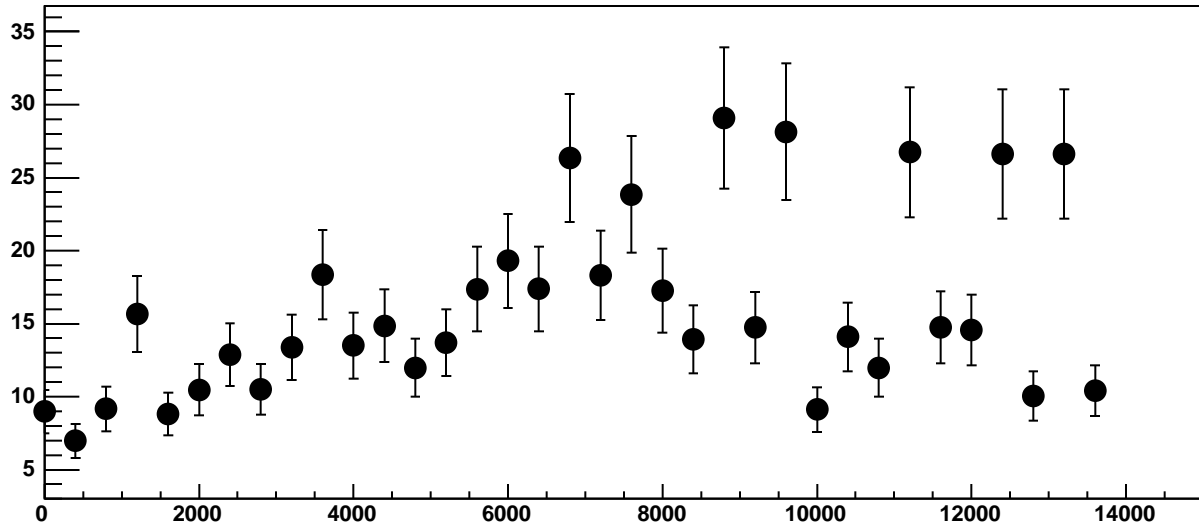
Chip 3, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC



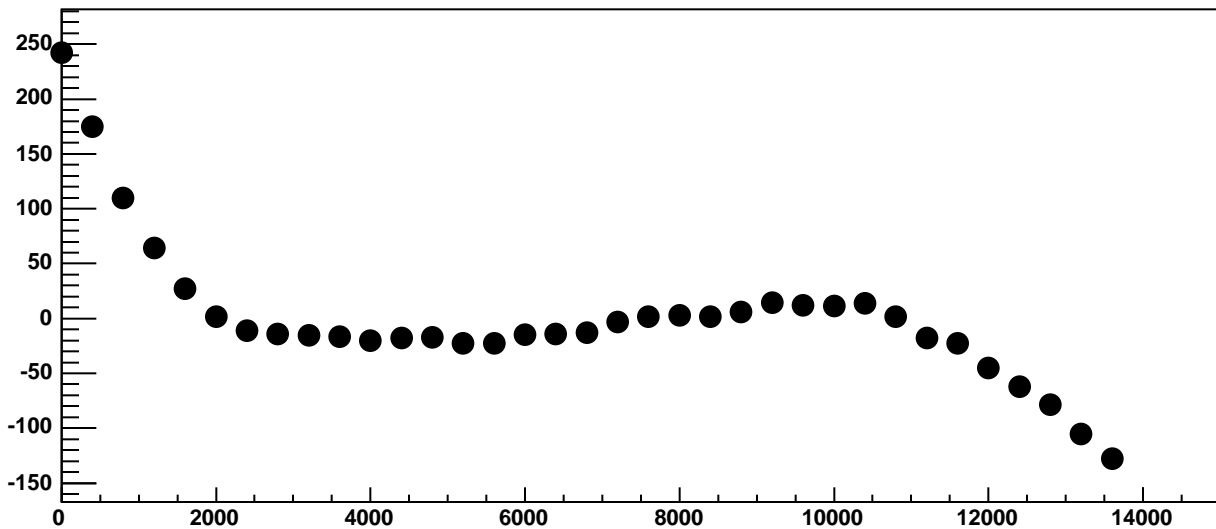
Chip 3, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



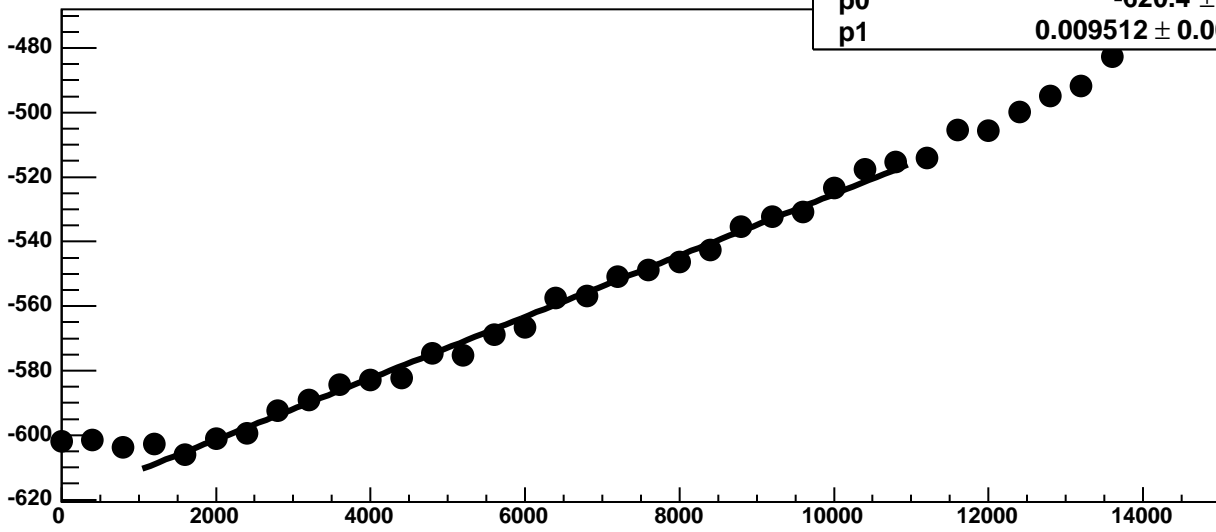
Chip 3, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



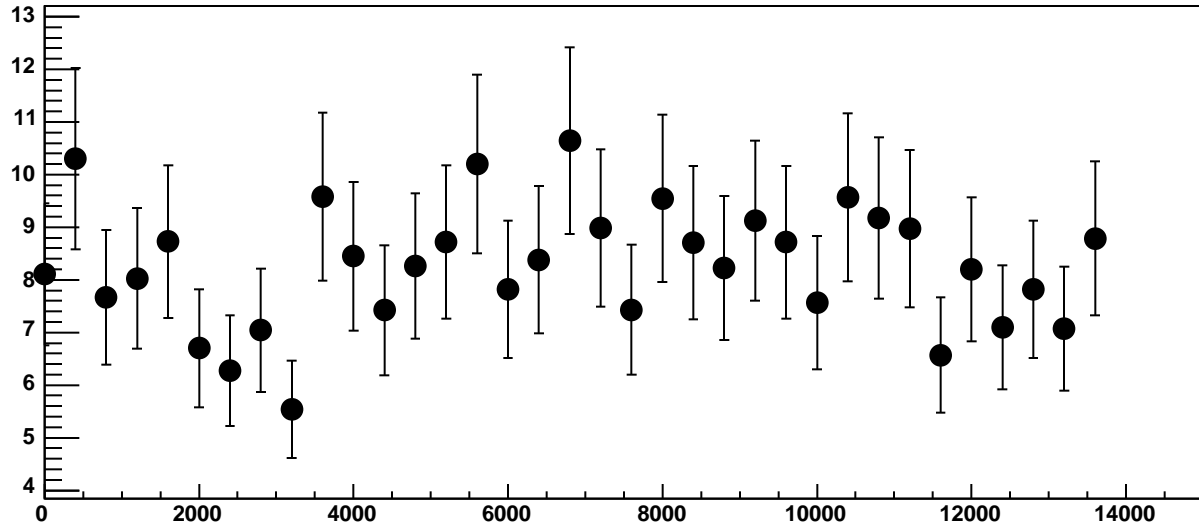
Chip 3, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



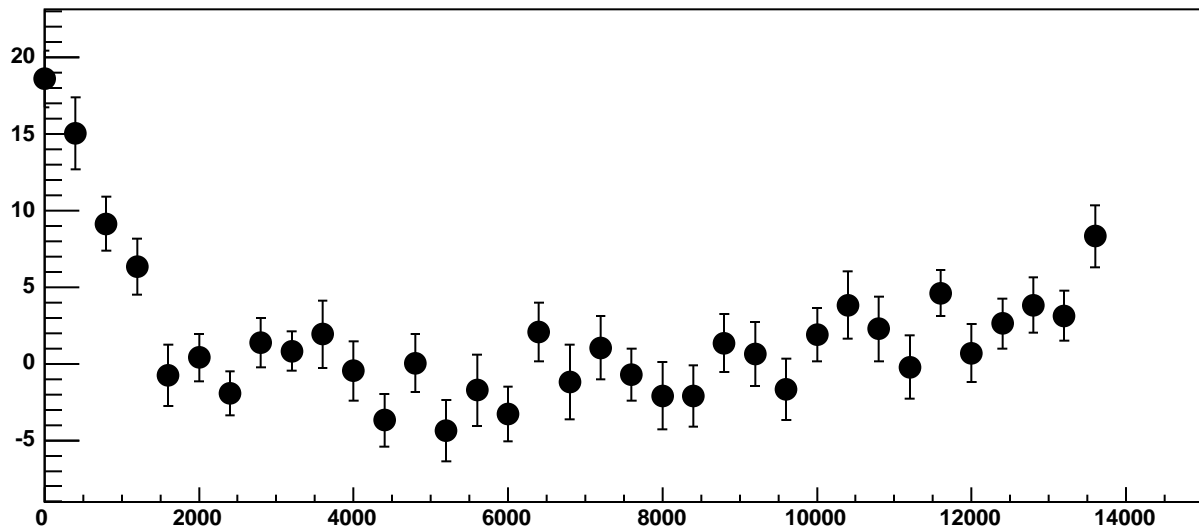
Chip 3, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



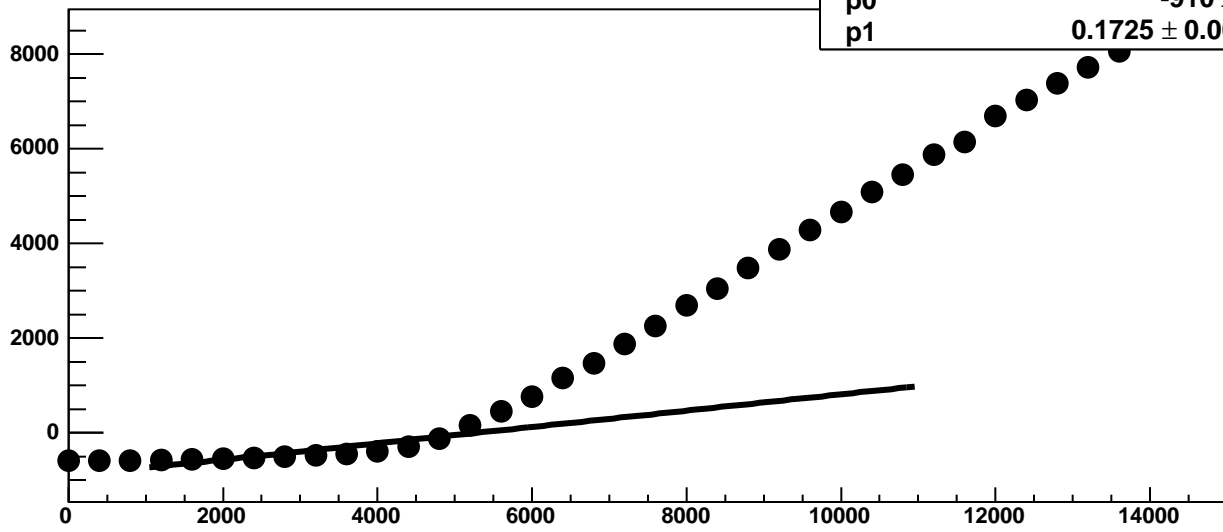
Chip 3, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



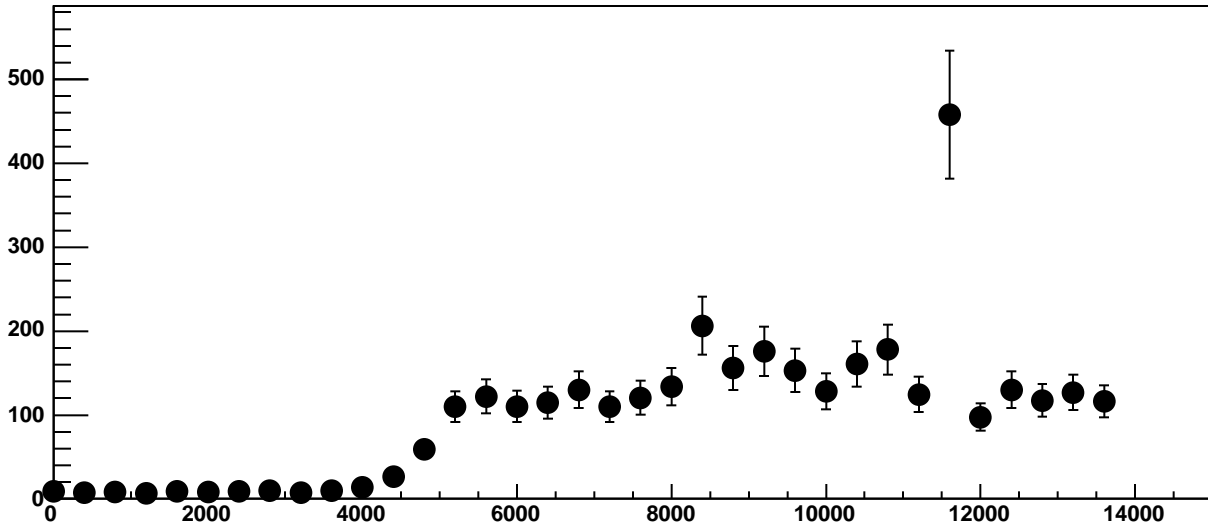
Chip 3, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



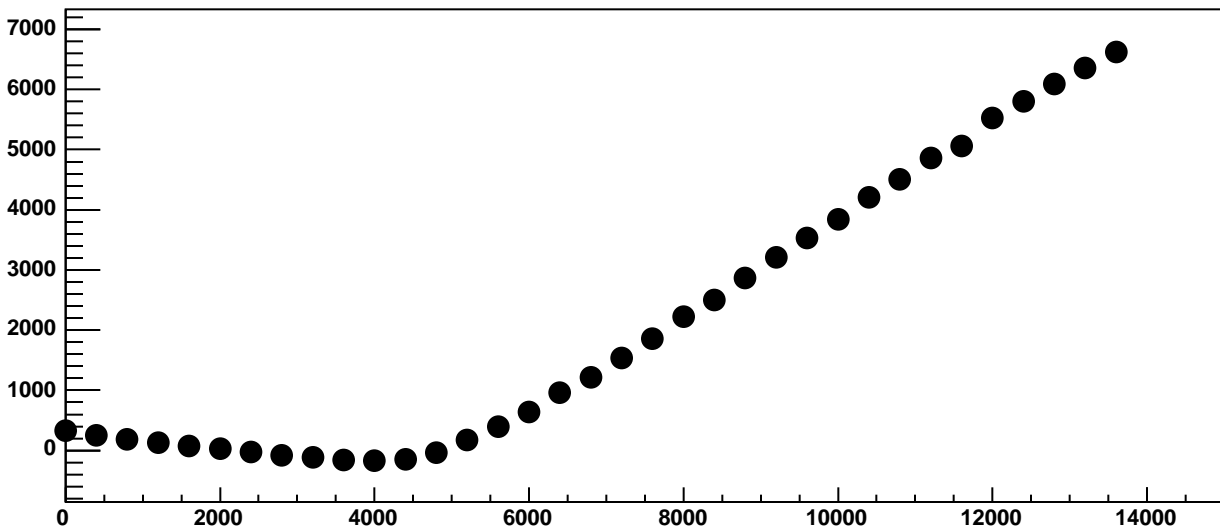
Chip 3, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC



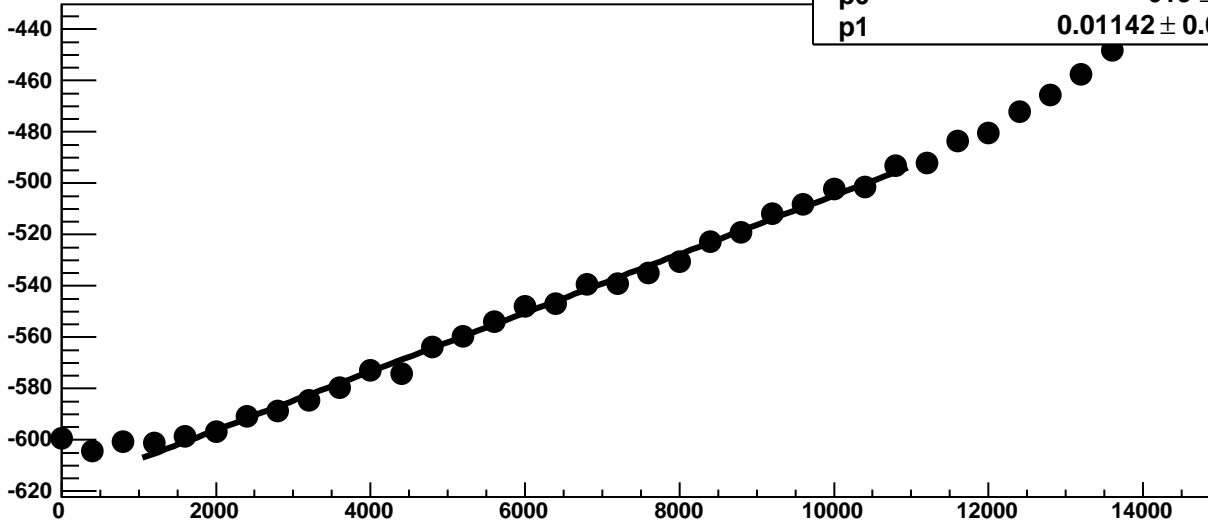
Chip 3, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC

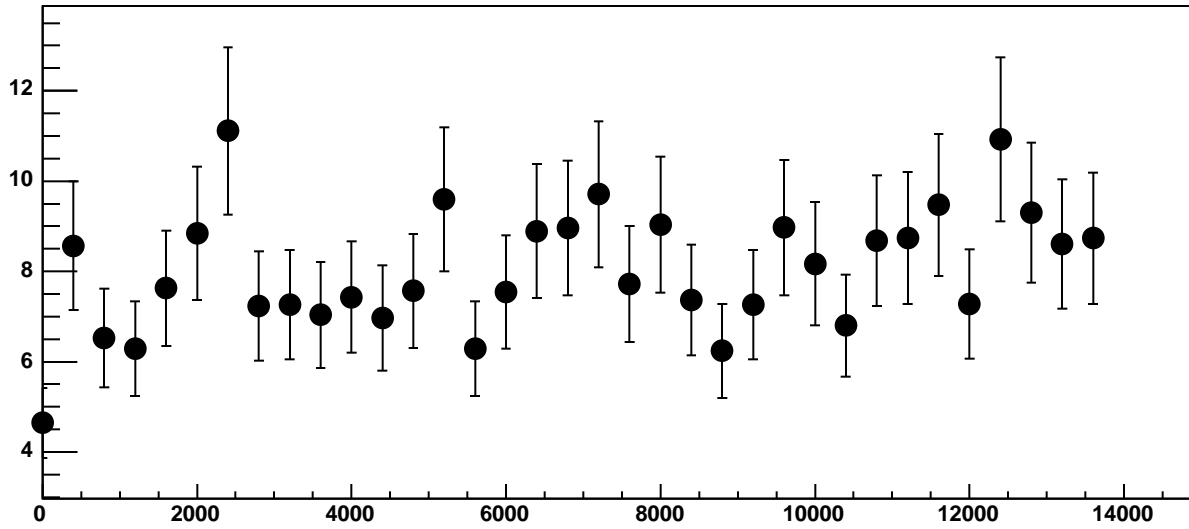


Chip 3, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

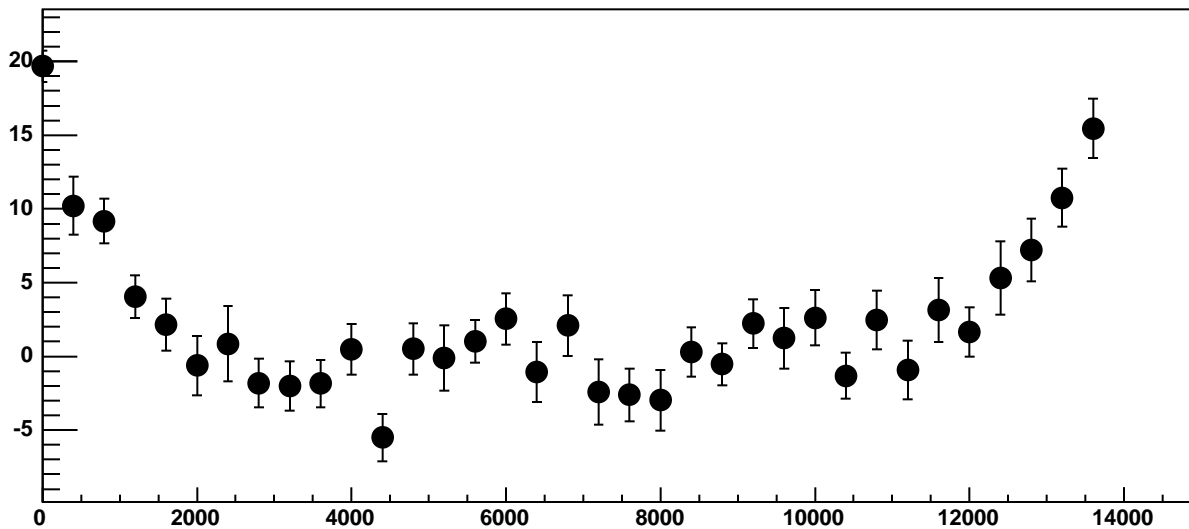


χ^2 / ndf 41.42 / 23
p0 -619 ± 0.8003
p1 0.01142 ± 0.0001211

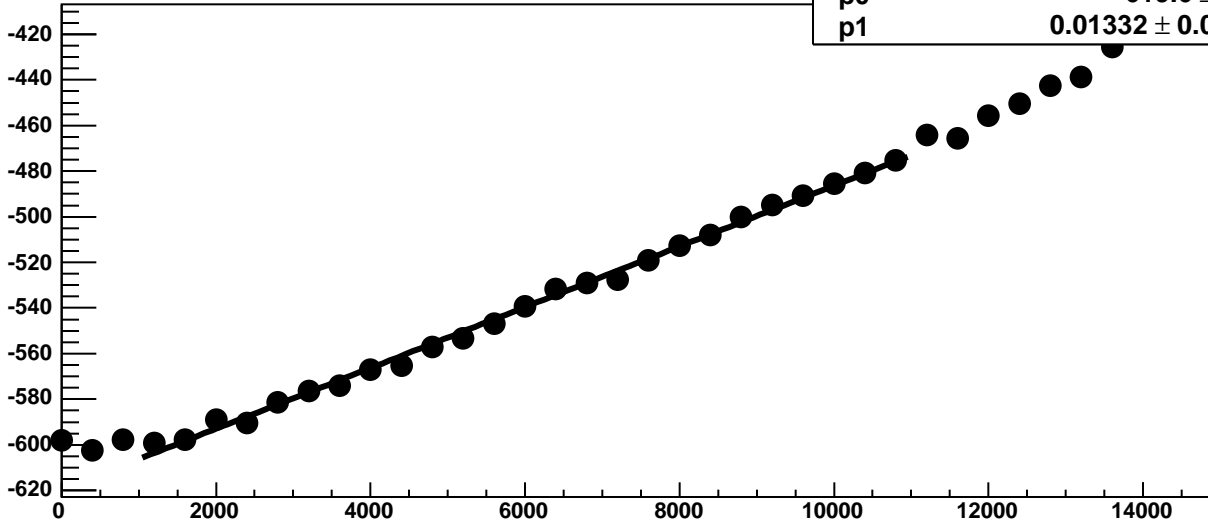
Chip 3, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

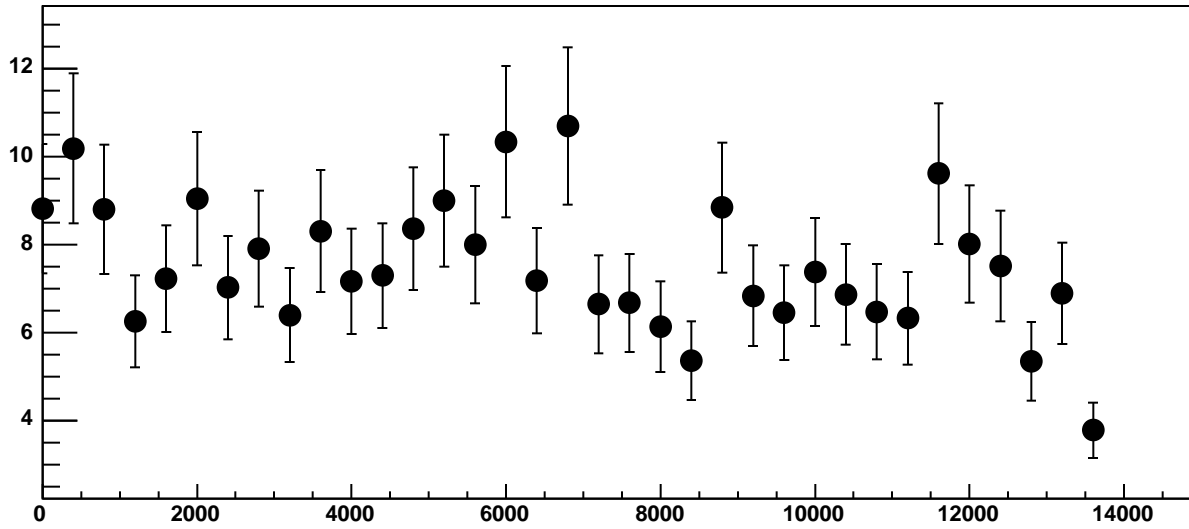


Chip 3, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

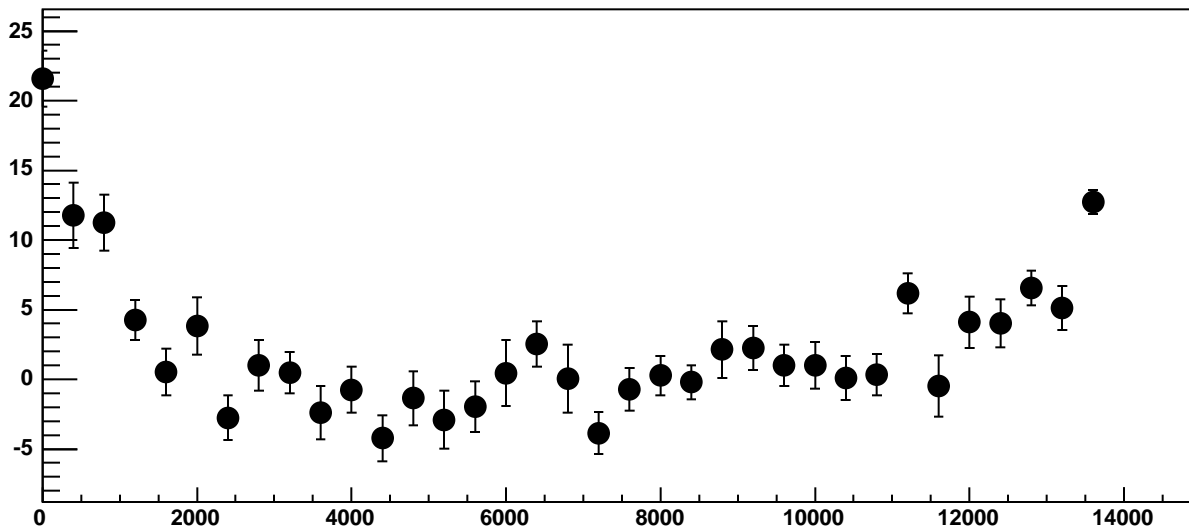


χ^2 / ndf 40.98 / 23
p0 -619.6 ± 0.7637
p1 0.01332 ± 0.0001108

Chip 3, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

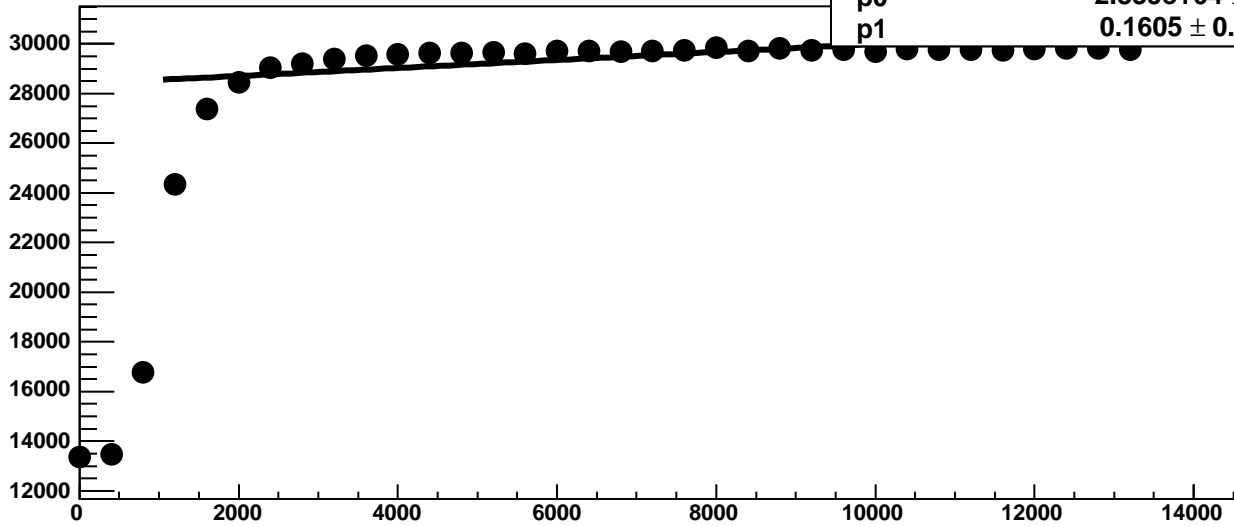
7274 / 23

p0

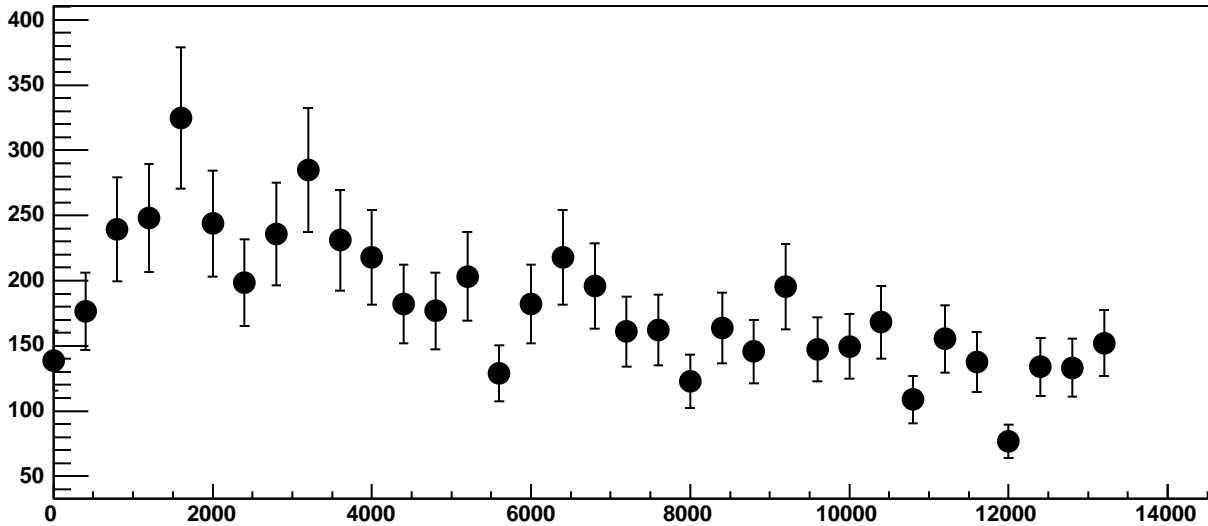
$2.839\text{e}+04 \pm 22.36$

p1

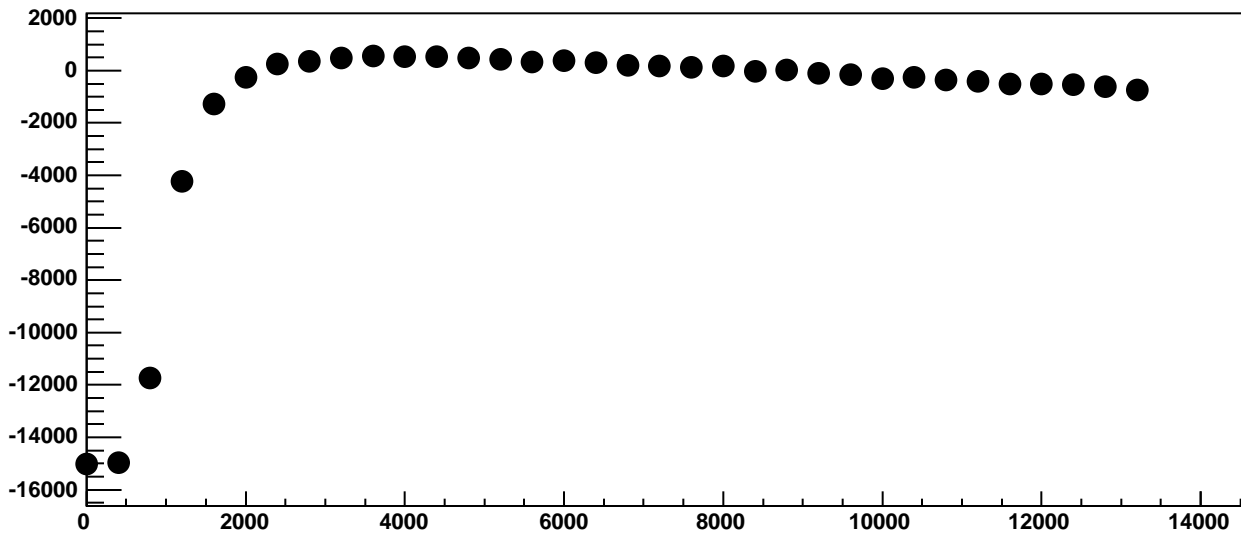
0.1605 ± 0.002951



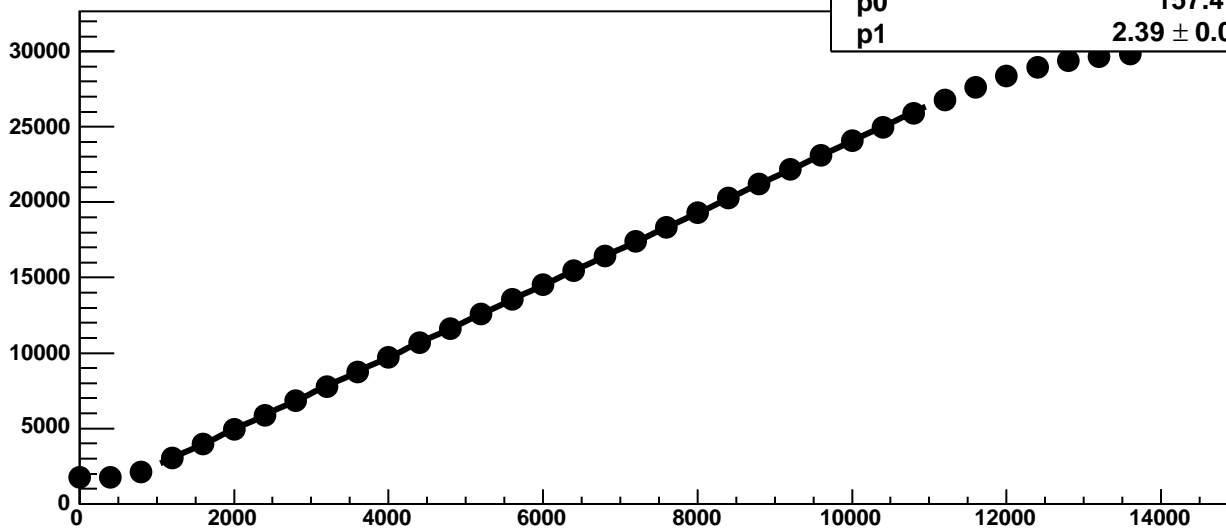
Chip 3, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC



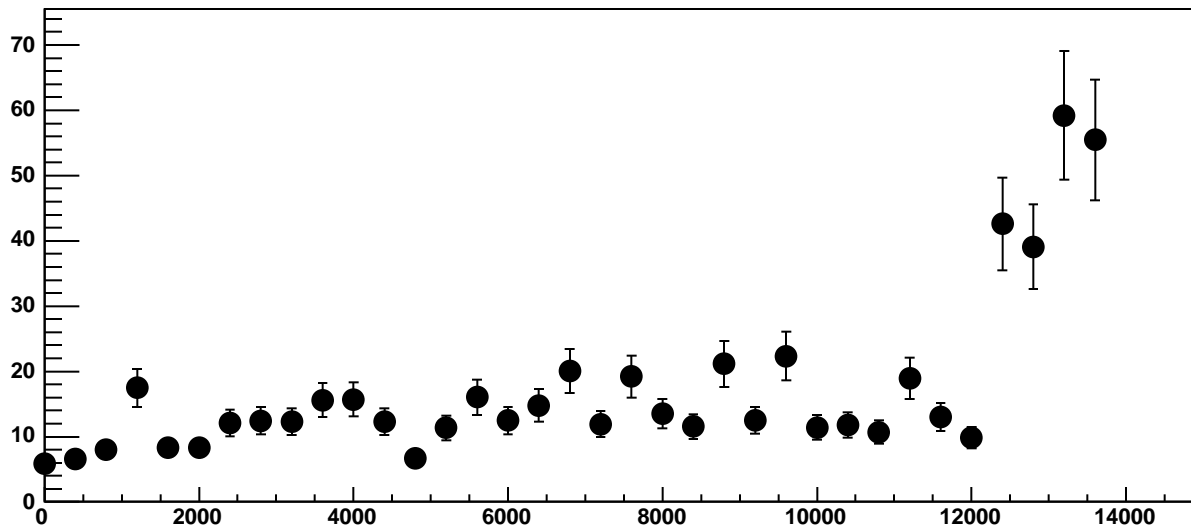
Chip 3, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC



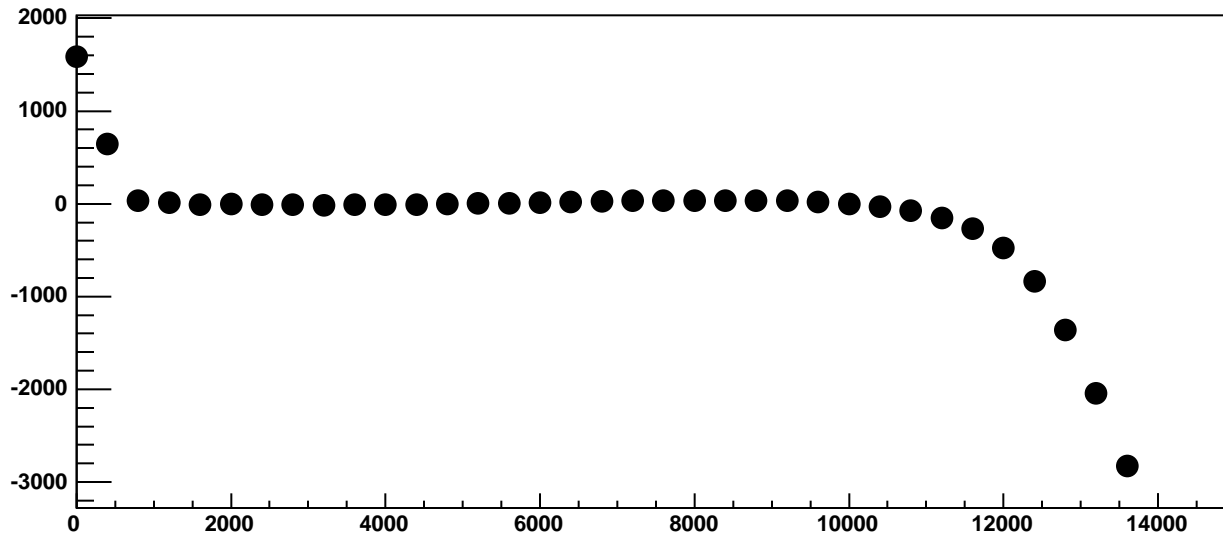
Chip 4, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



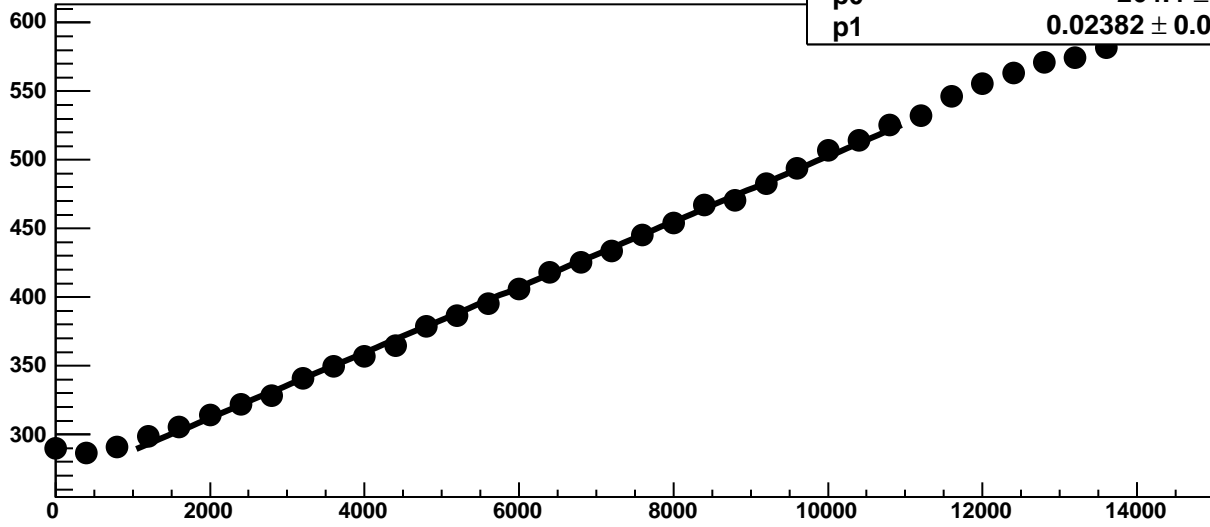
Chip 4, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC

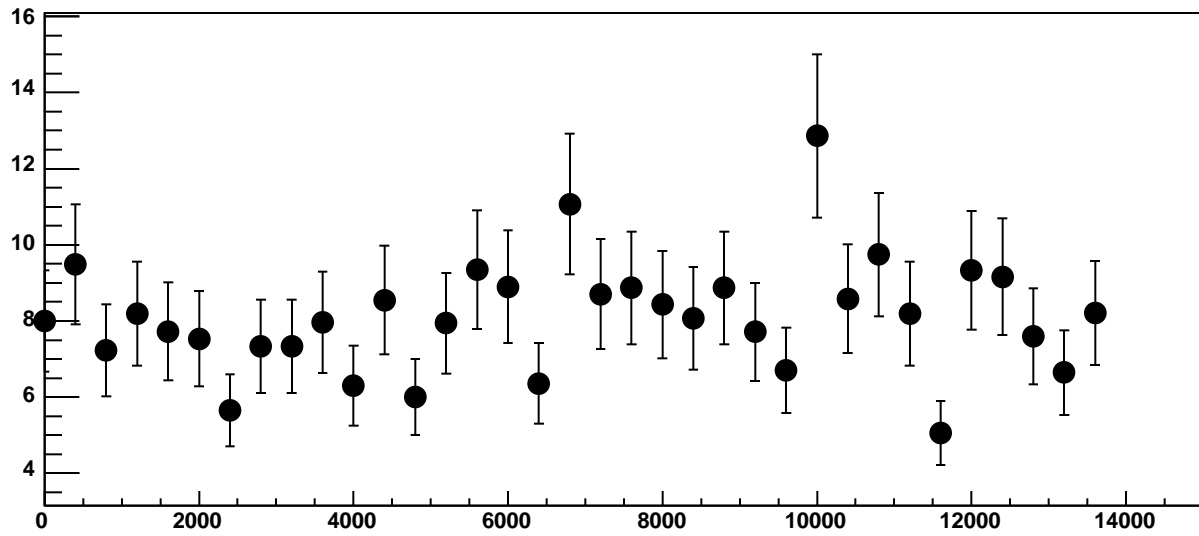


Chip 4, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

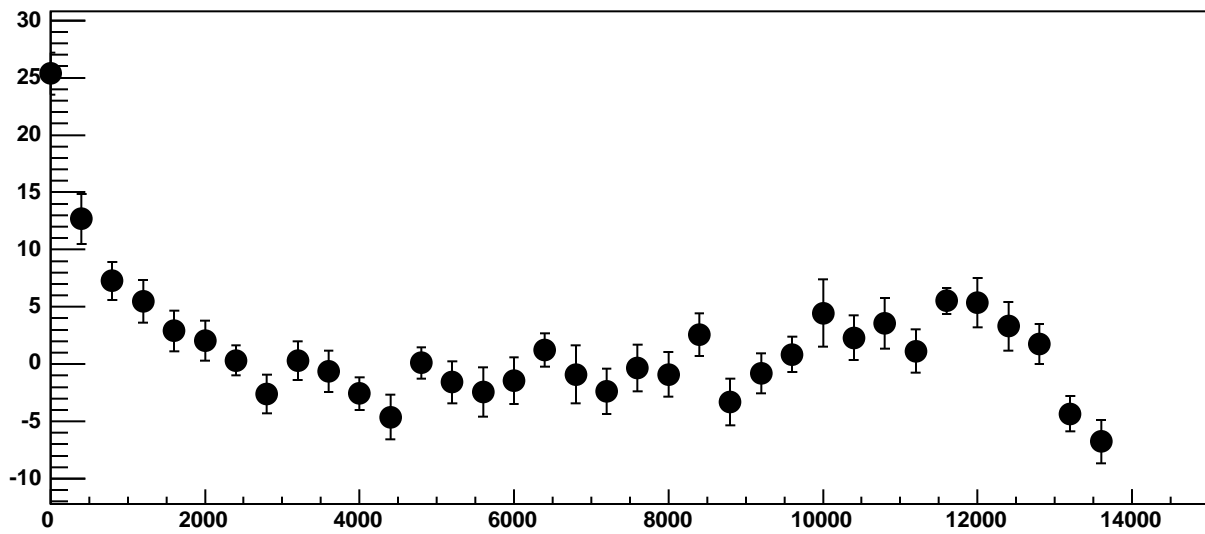


χ^2 / ndf 40.37 / 23
p0 264.4 ± 0.7926
p1 0.02382 ± 0.0001271

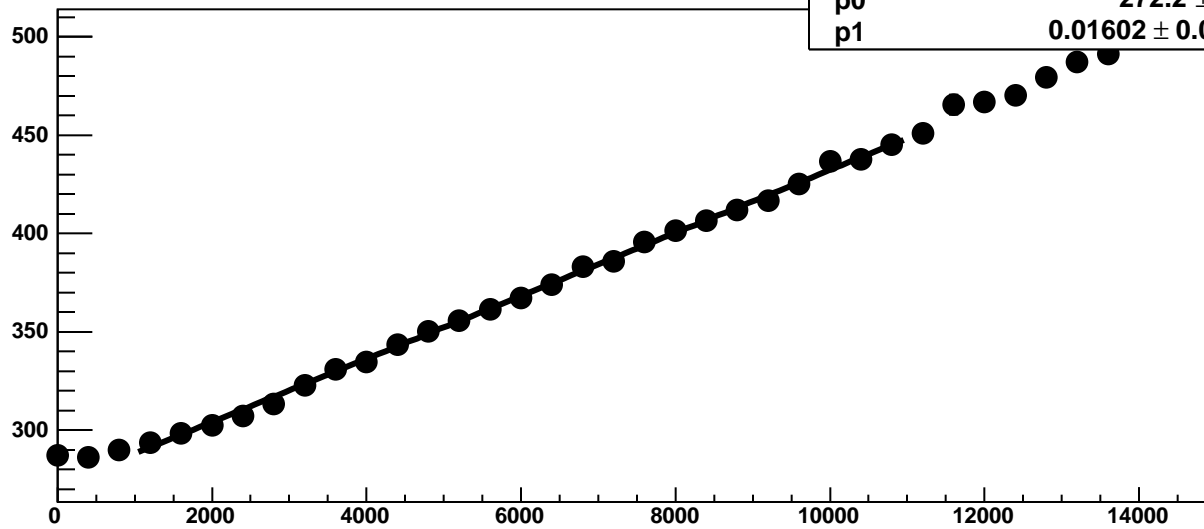
Chip 4, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

28.18 / 23

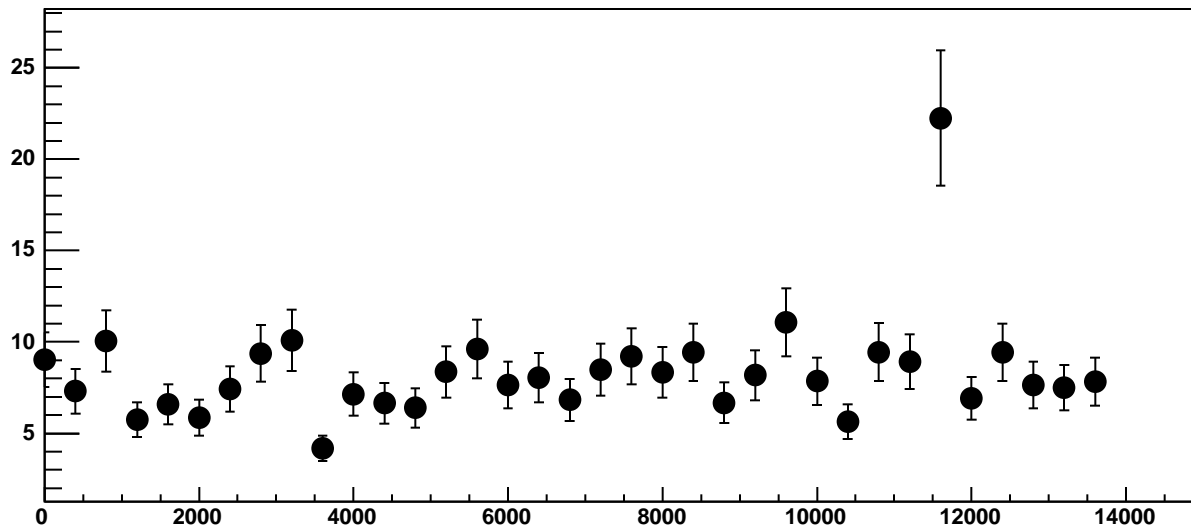
p0

272.2 ± 0.7006

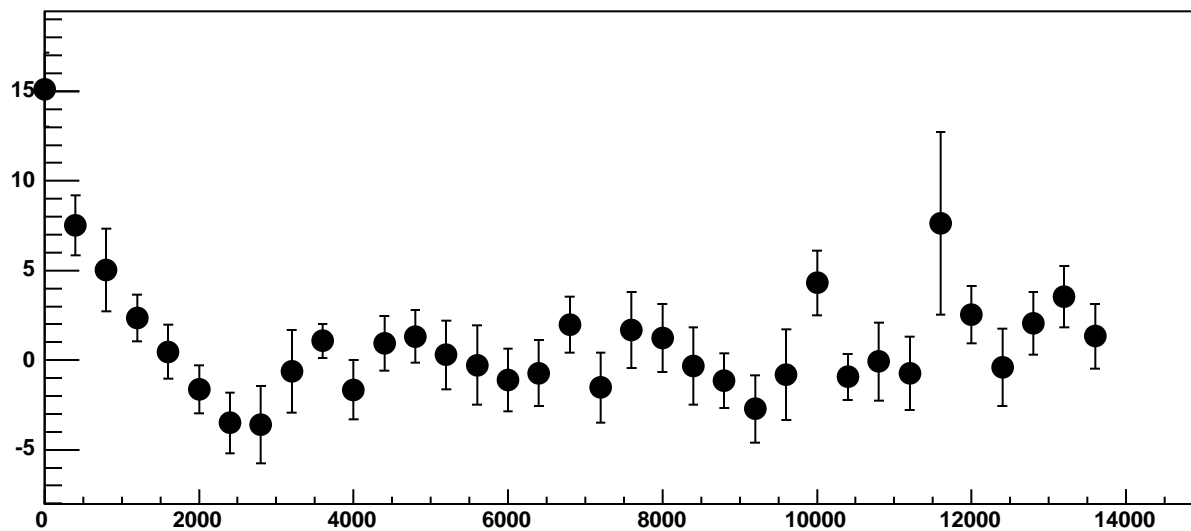
p1

0.01602 ± 0.0001122

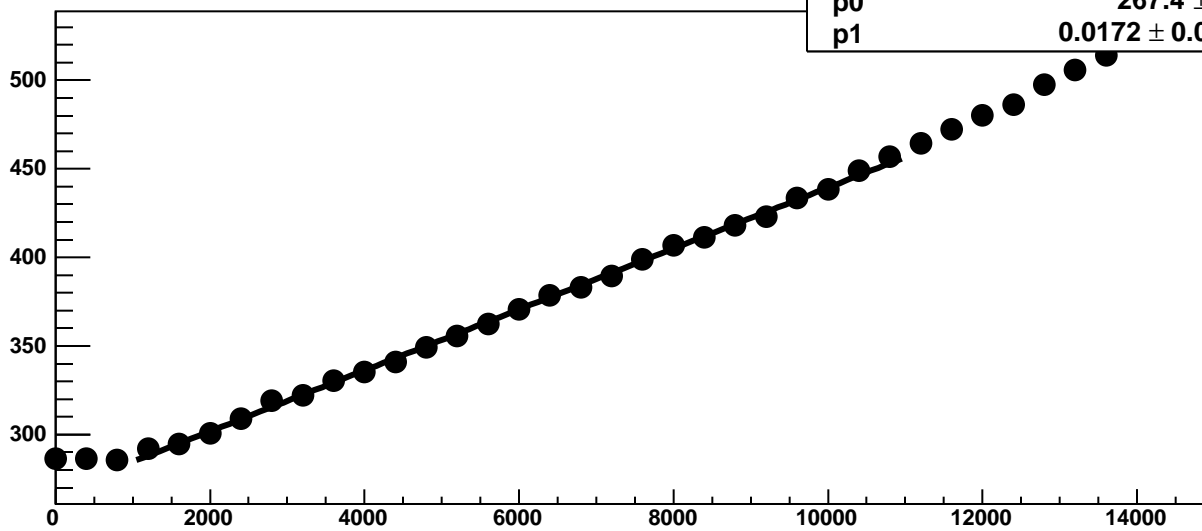
Chip 4, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

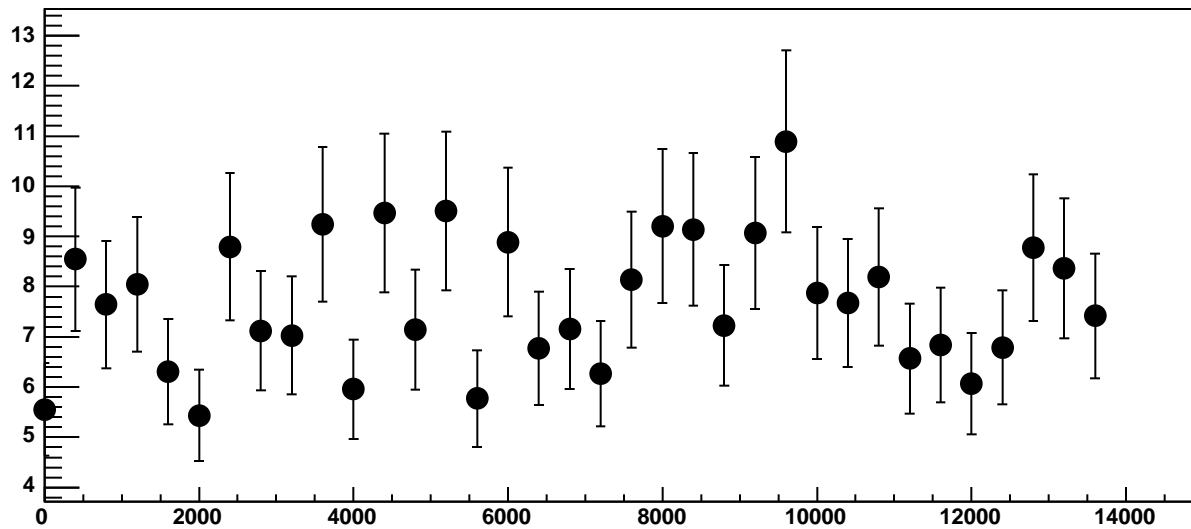


Chip 4, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

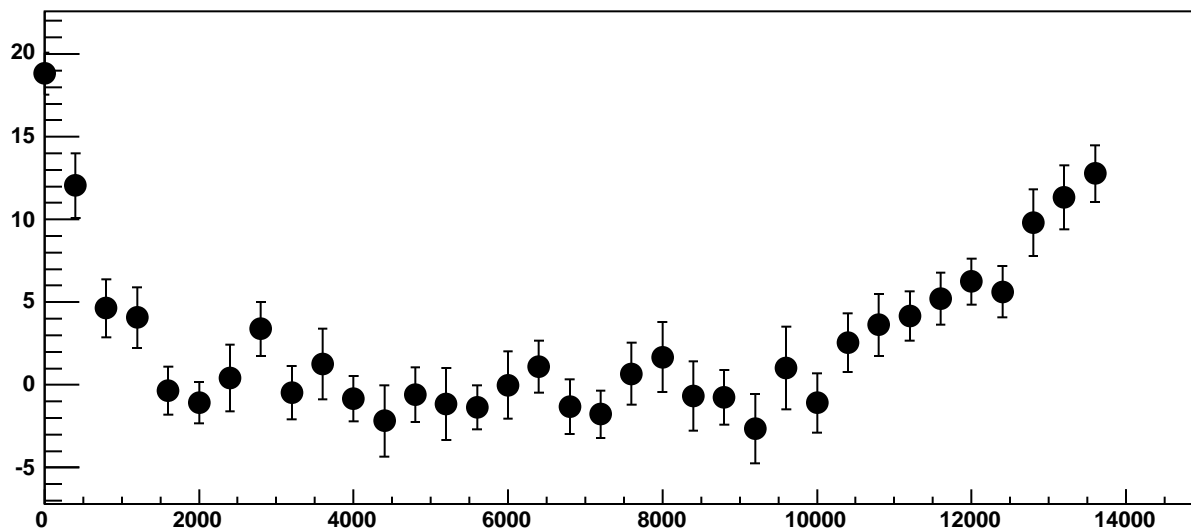


χ^2 / ndf 24.97 / 23
p0 267.4 ± 0.7602
p1 0.0172 ± 0.0001206

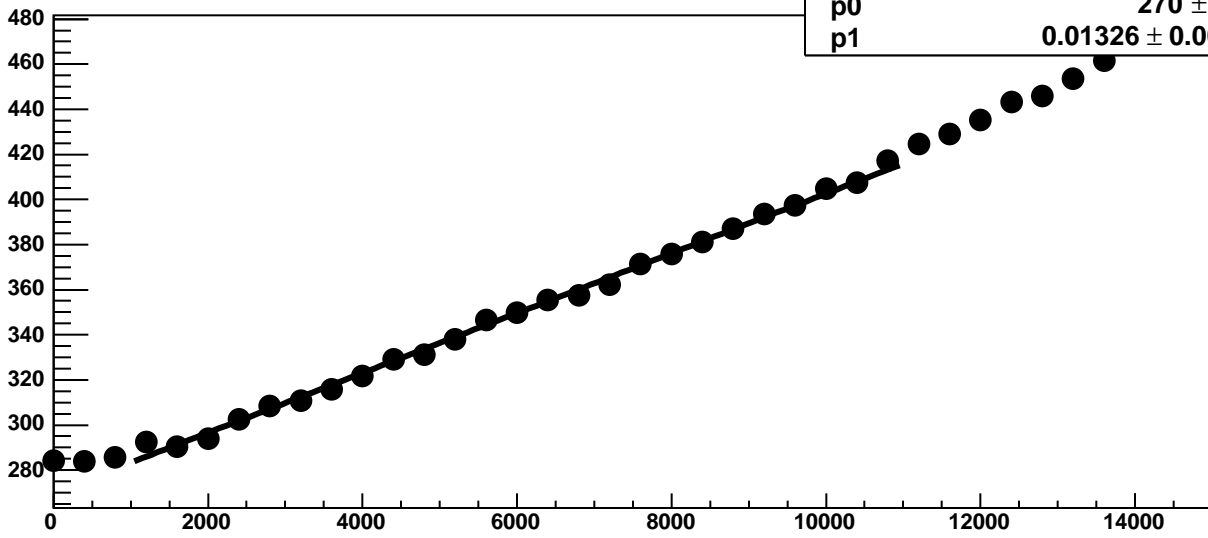
Chip 4, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC

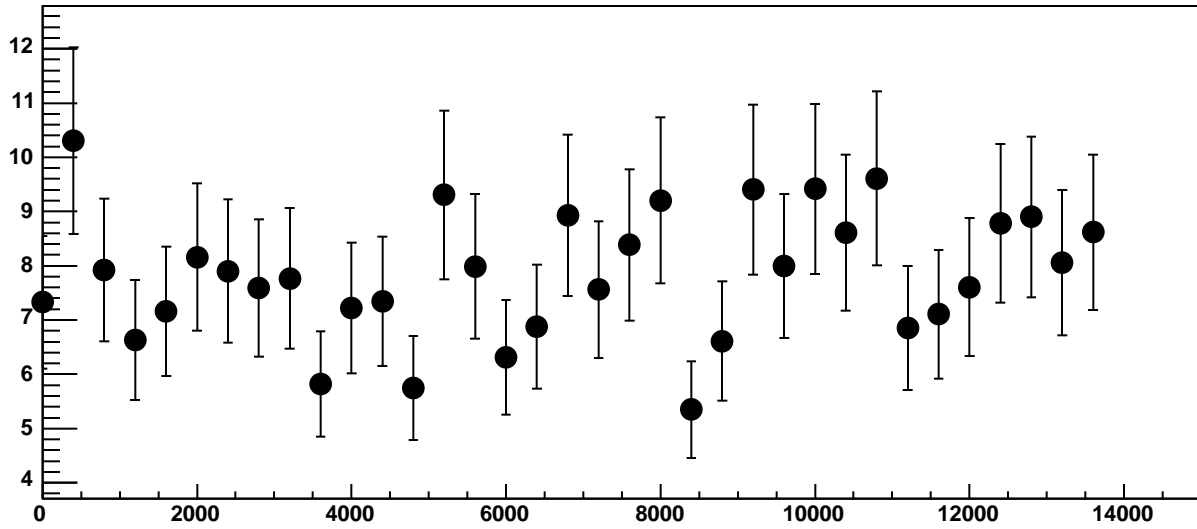


Chip 4, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

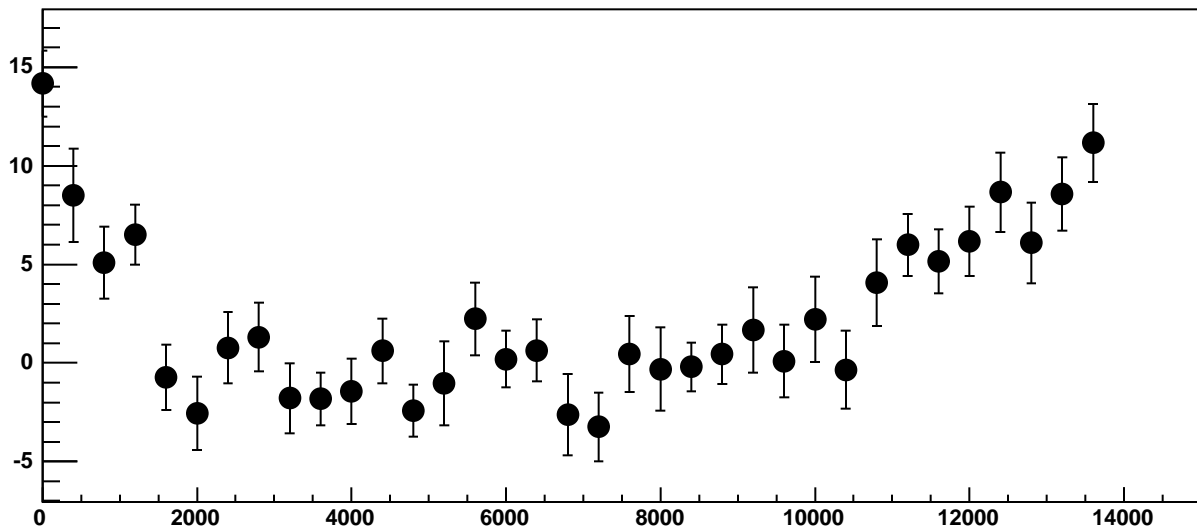


χ^2 / ndf 40.68 / 23
p0 270 ± 0.7817
p1 0.01326 ± 0.0001229

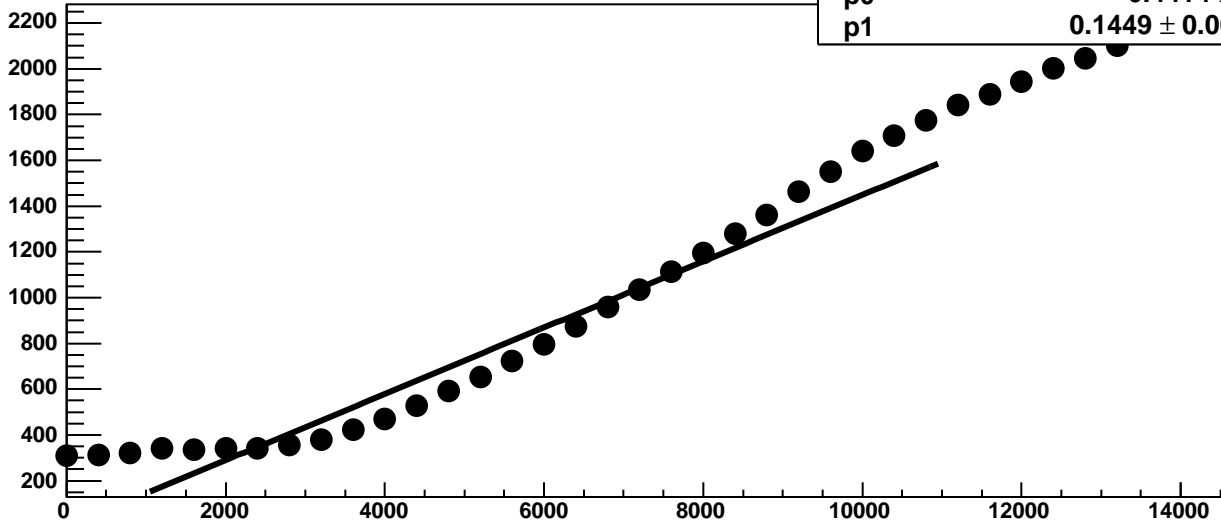
Chip 4, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



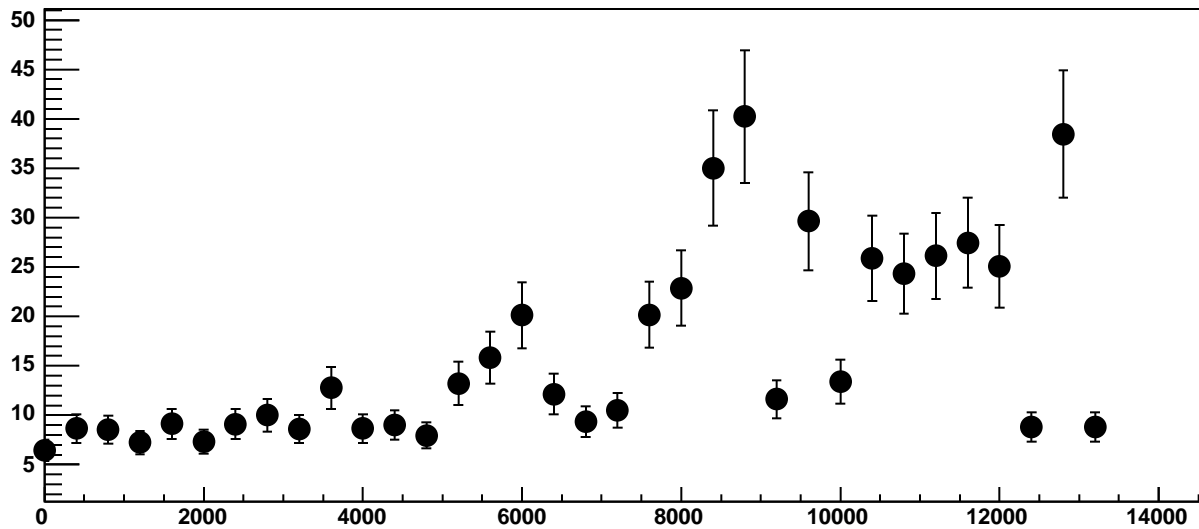
Chip 4, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



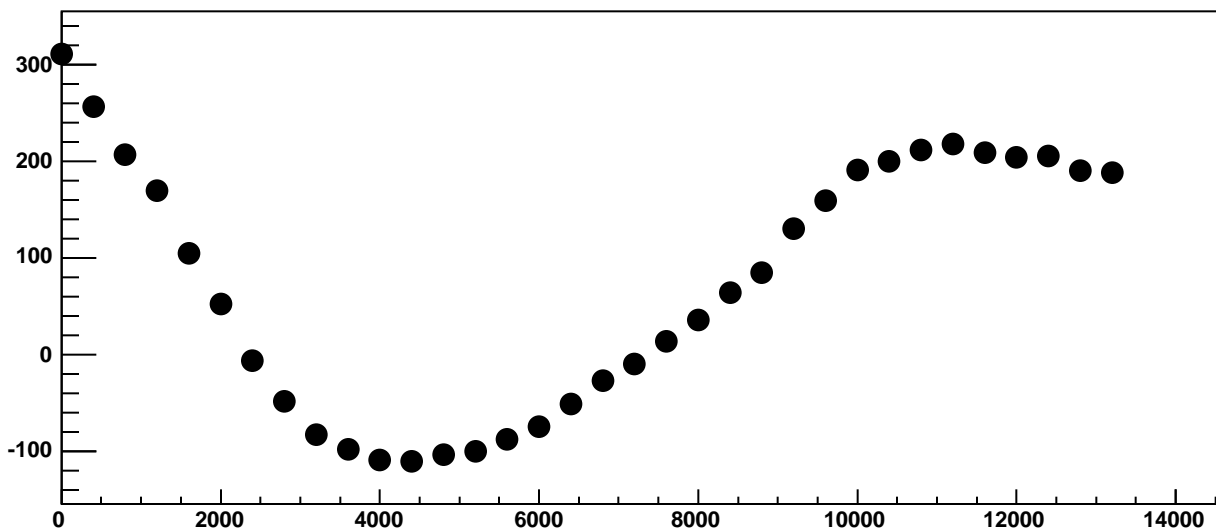
Chip 4, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



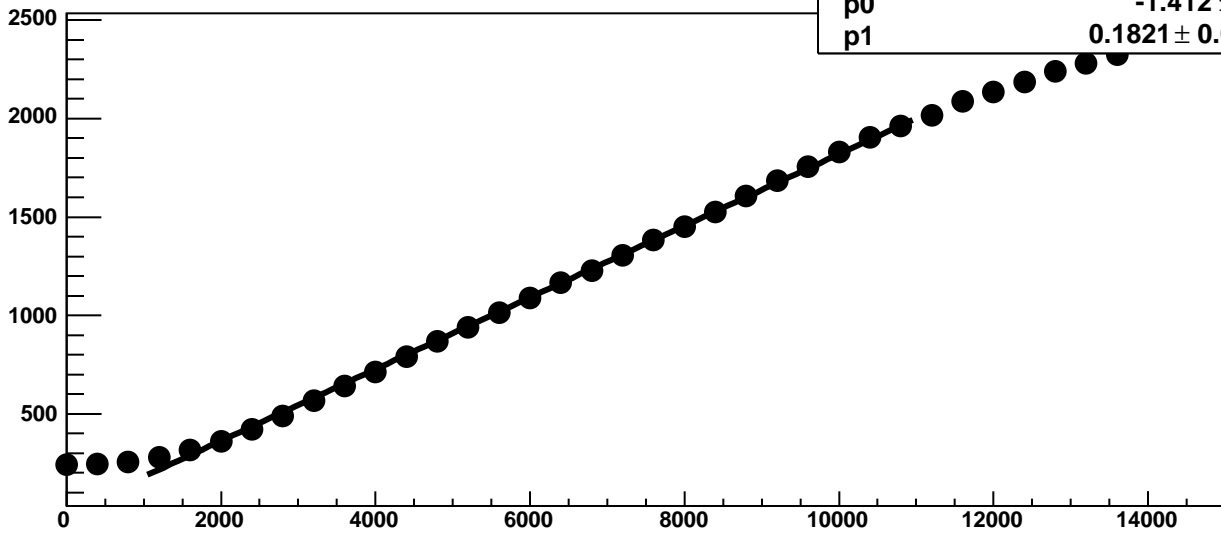
Chip 4, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

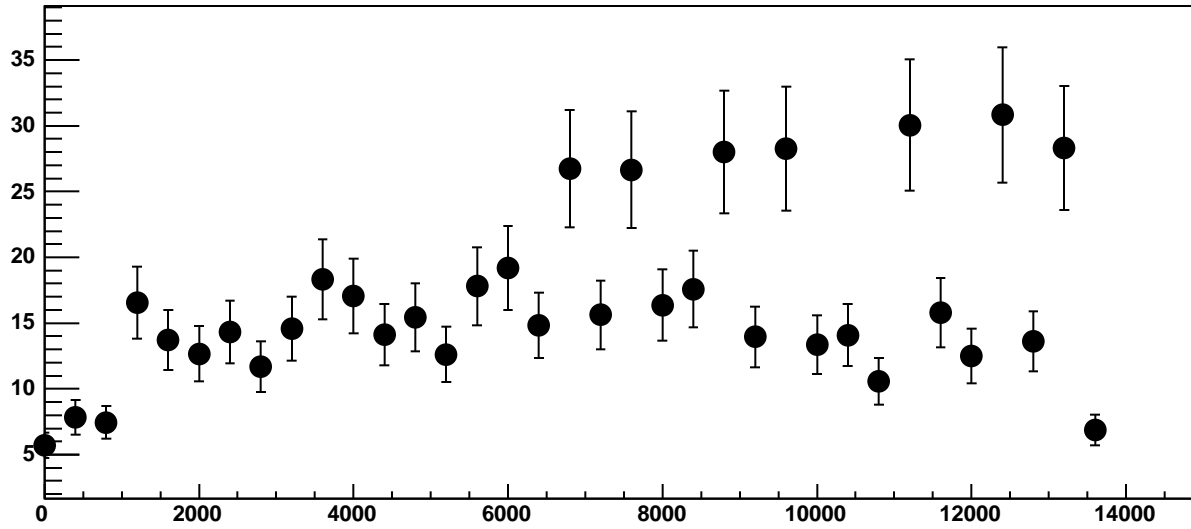


Chip 4, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC

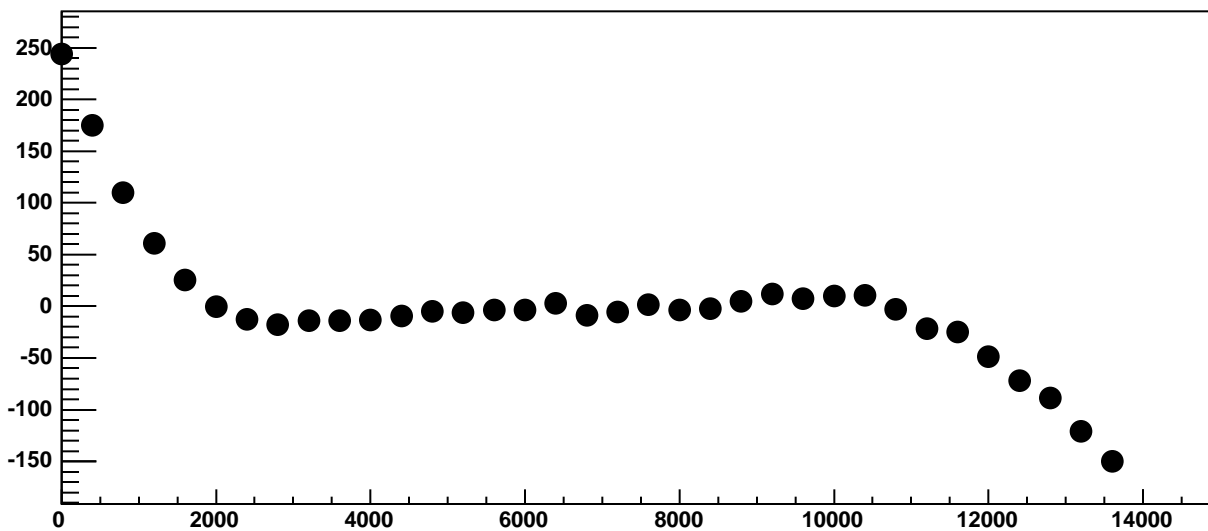


χ^2 / ndf 485.1 / 23
p0 -1.412 ± 1.498
p1 0.1821 ± 0.000226

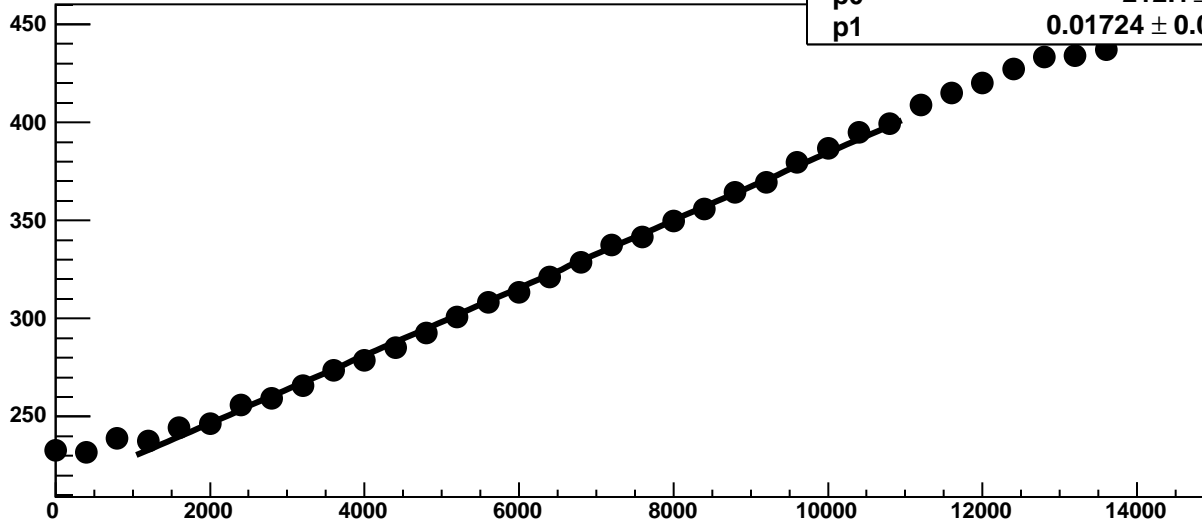
Chip 4, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC

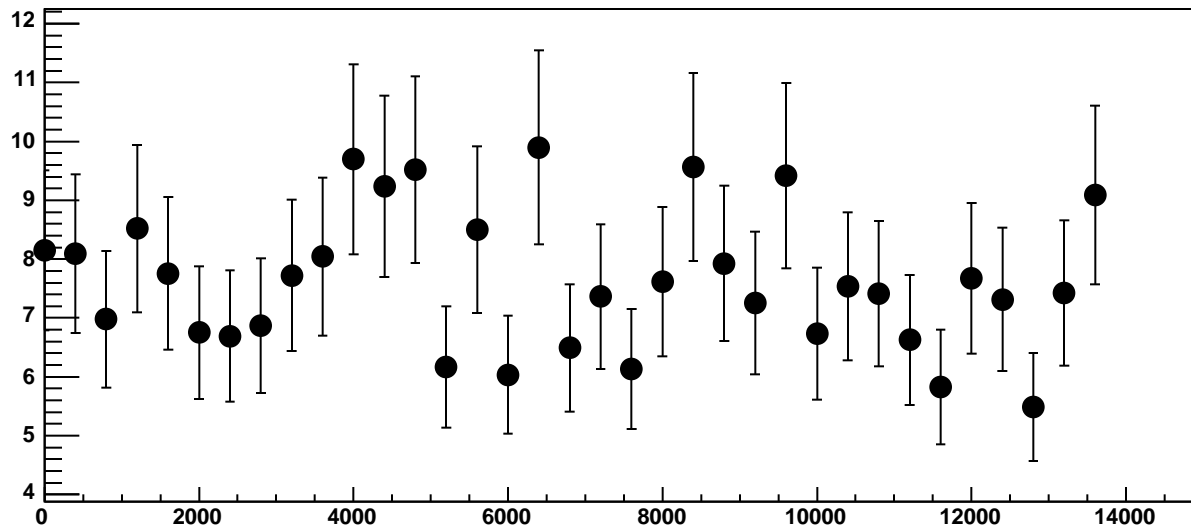


Chip 4, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

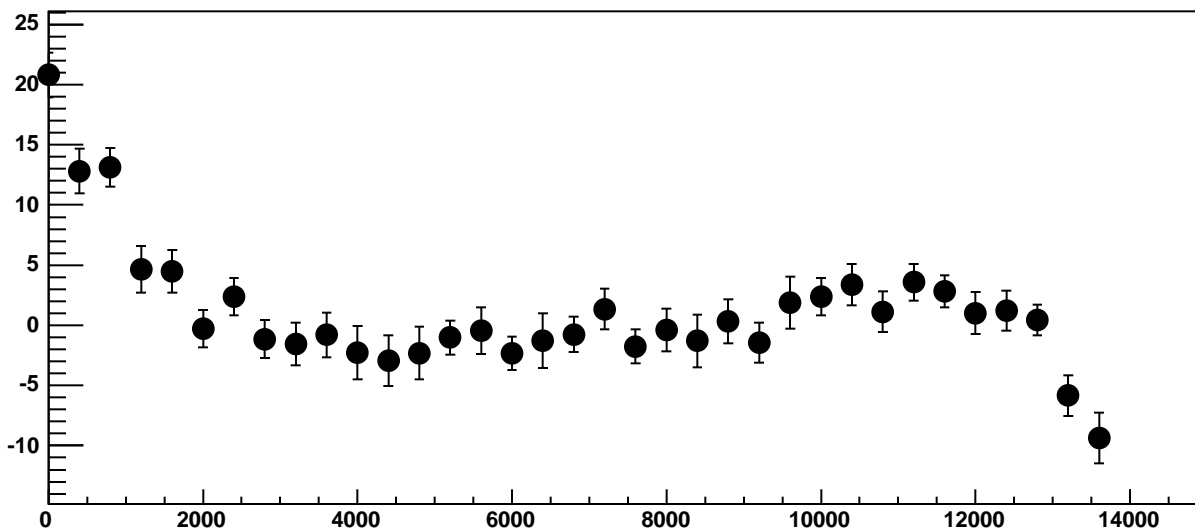


χ^2 / ndf 34.78 / 23
p0 212.1 ± 0.8015
p1 0.01724 ± 0.0001201

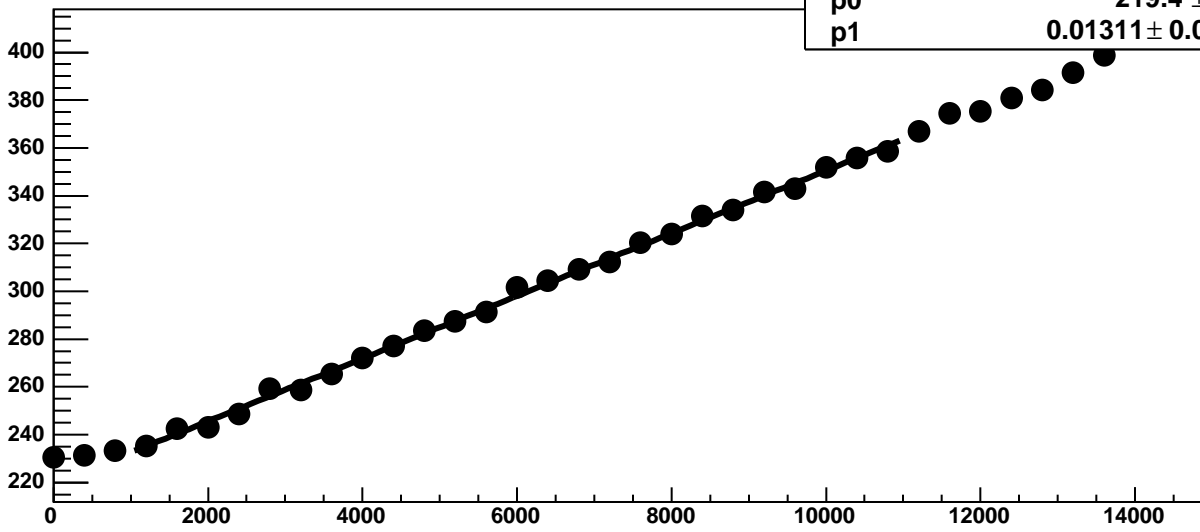
Chip 4, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

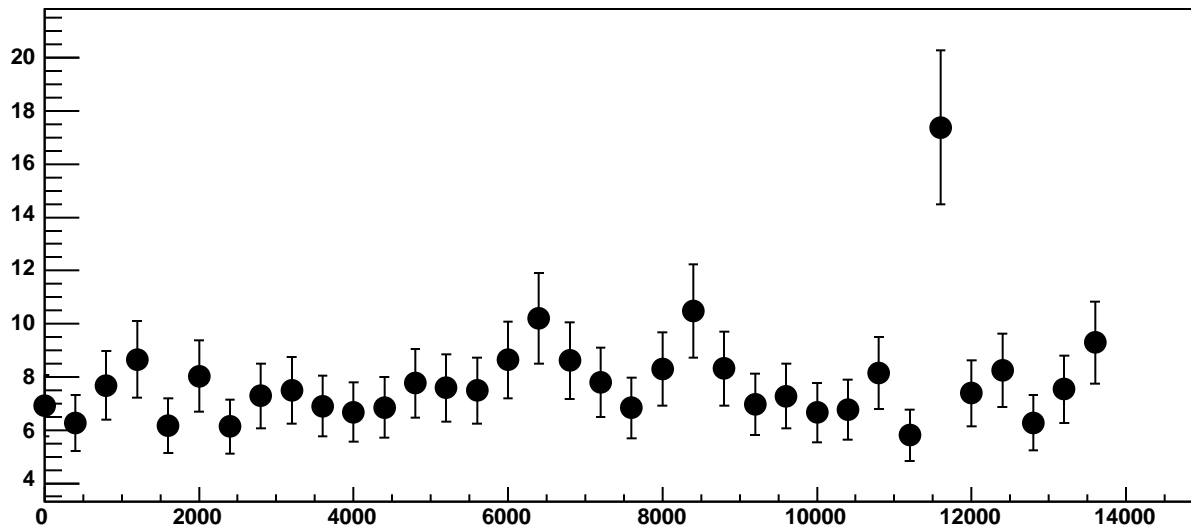


Chip 4, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

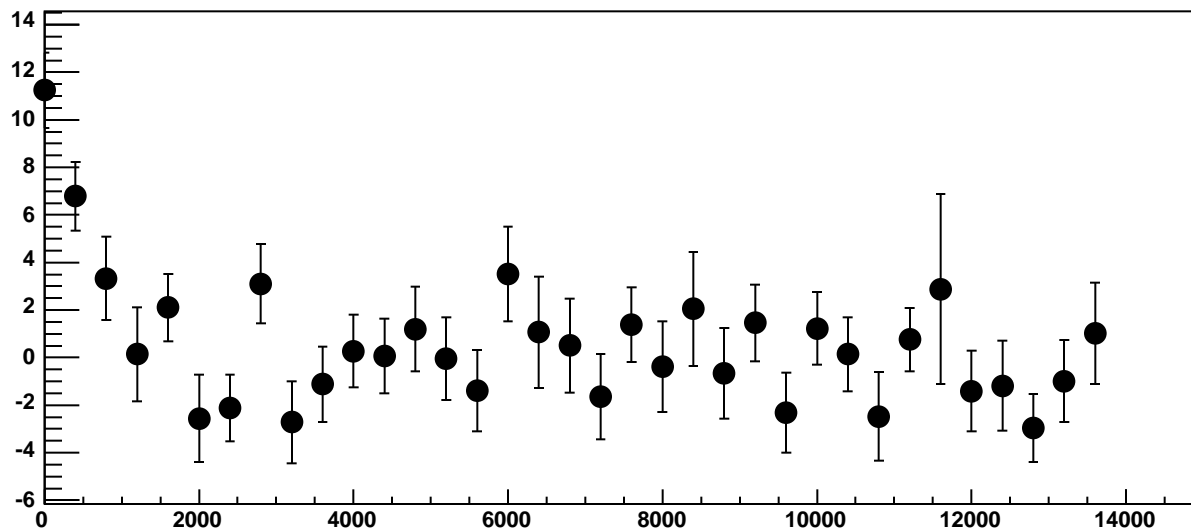


χ^2 / ndf 25.09 / 23
p0 219.4 ± 0.7595
p1 0.01311 ± 0.0001157

Chip 4, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

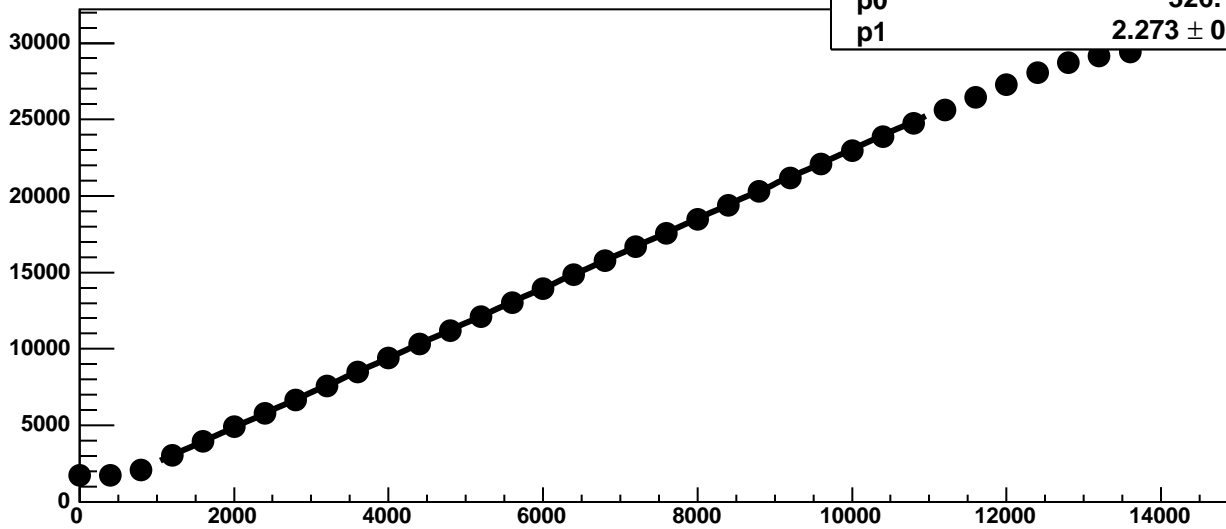


Chip 4, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

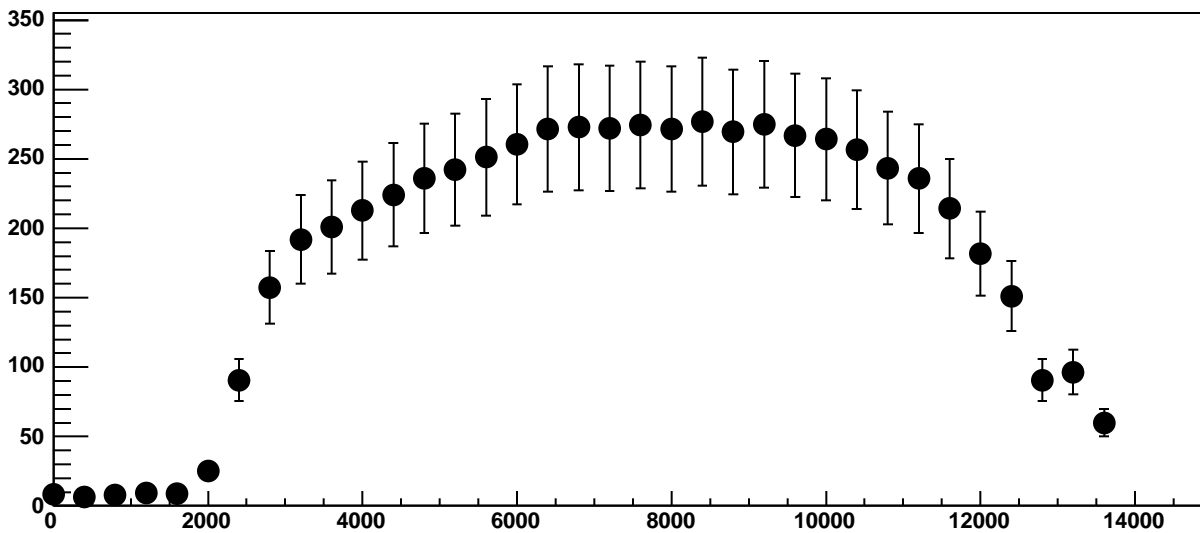


Chip 4, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC

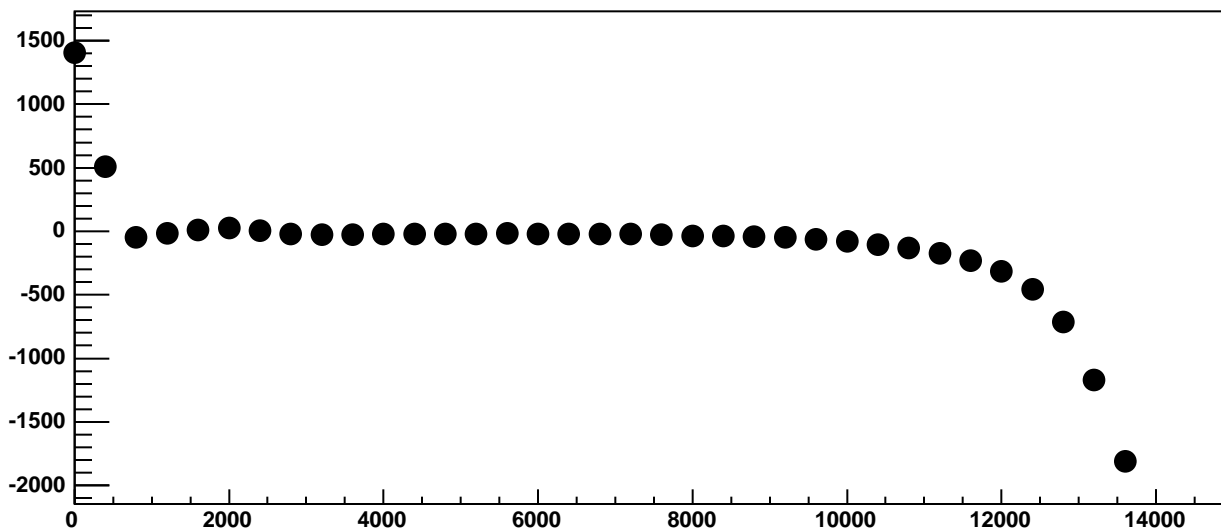
χ^2 / ndf 113.6 / 23
p0 326.1 ± 3.48
p1 2.273 ± 0.002085



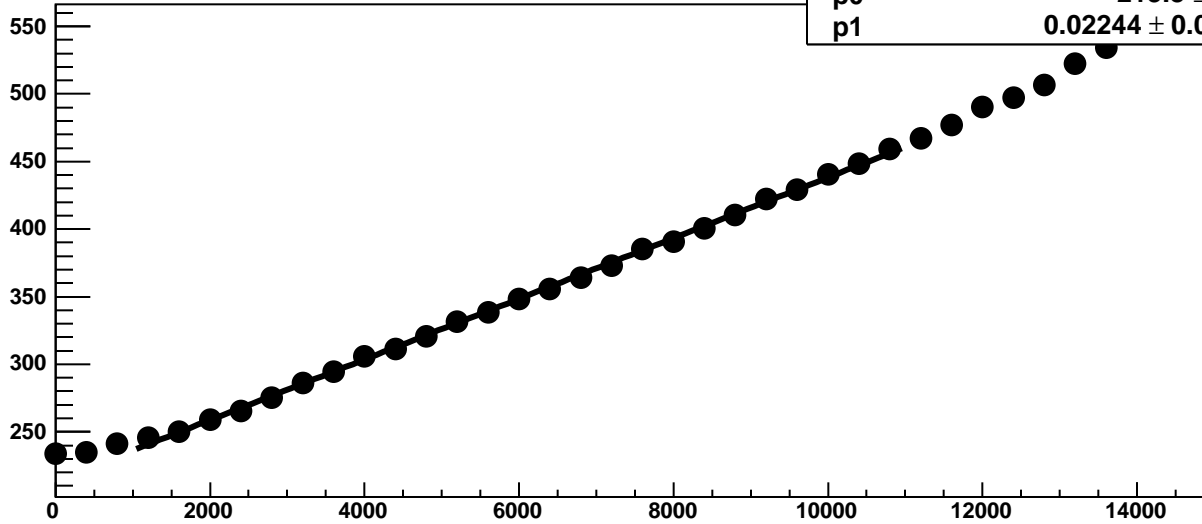
Chip 4, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC

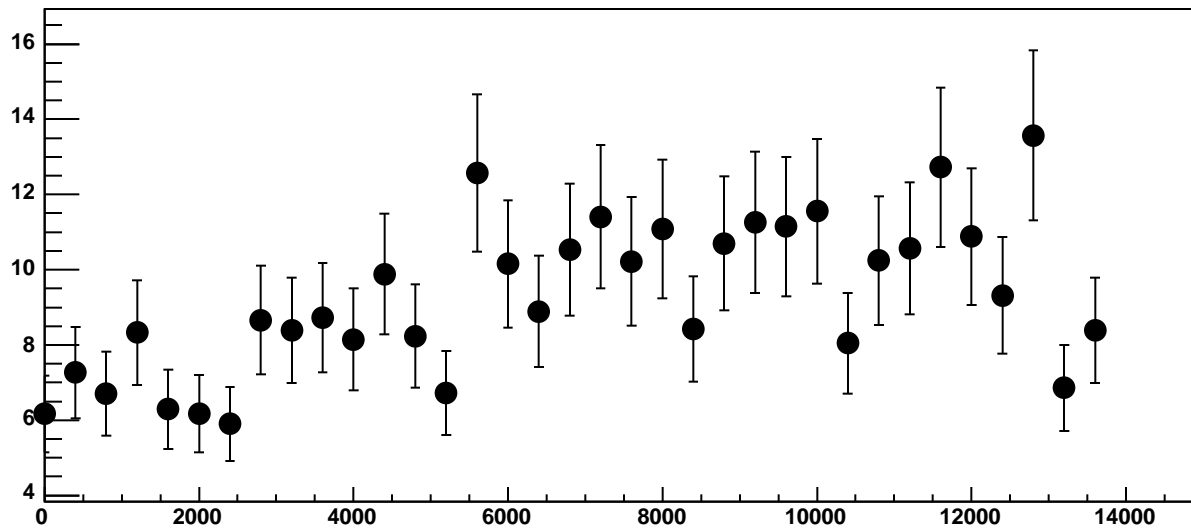


Chip 4, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

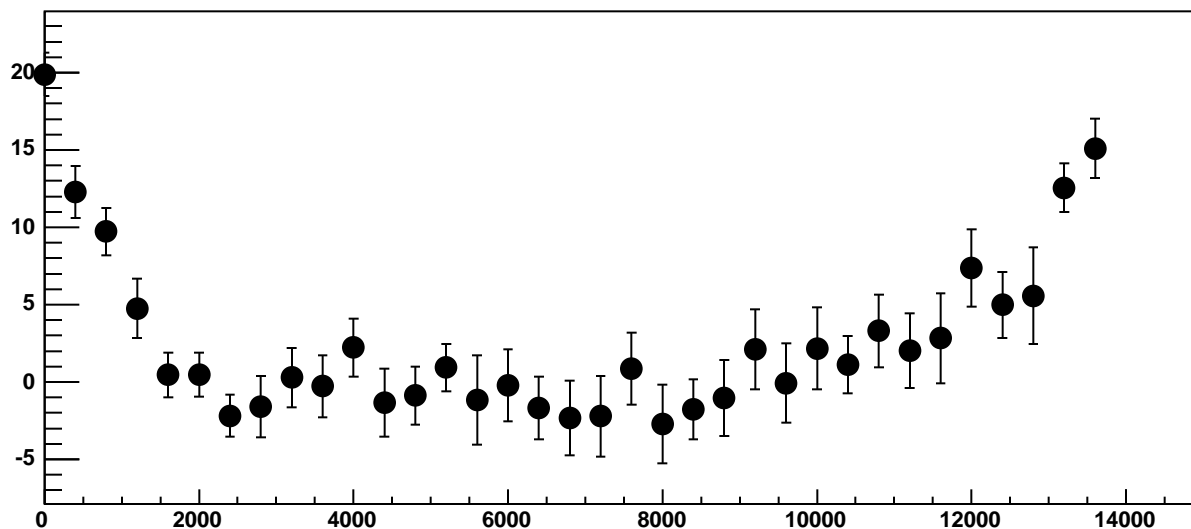


χ^2 / ndf 20.51 / 23
p0 213.8 ± 0.8034
p1 0.02244 ± 0.0001353

Chip 4, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

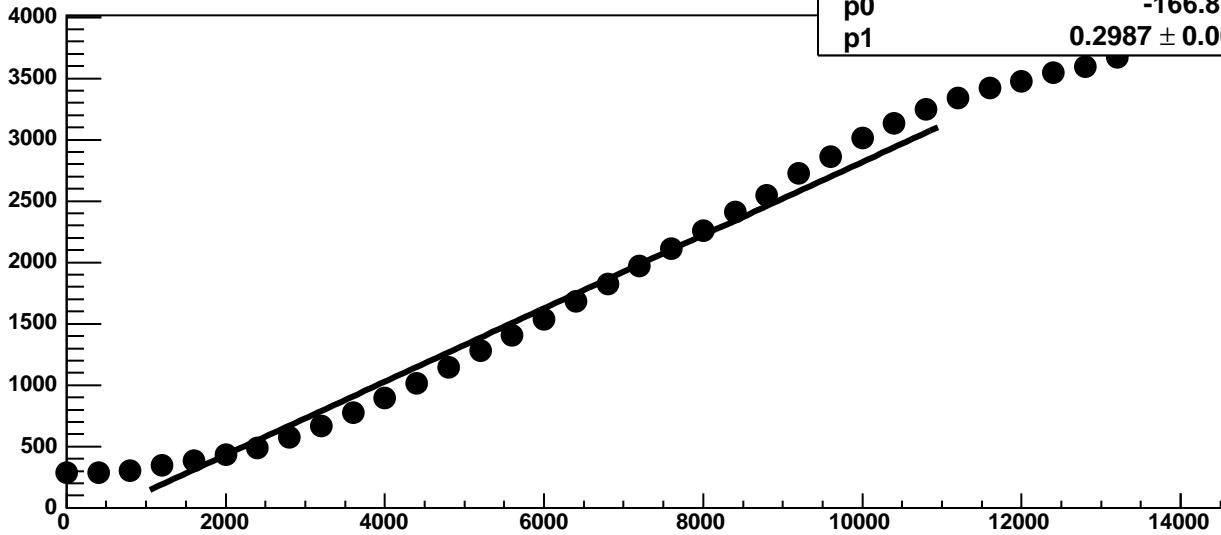


Chip 4, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

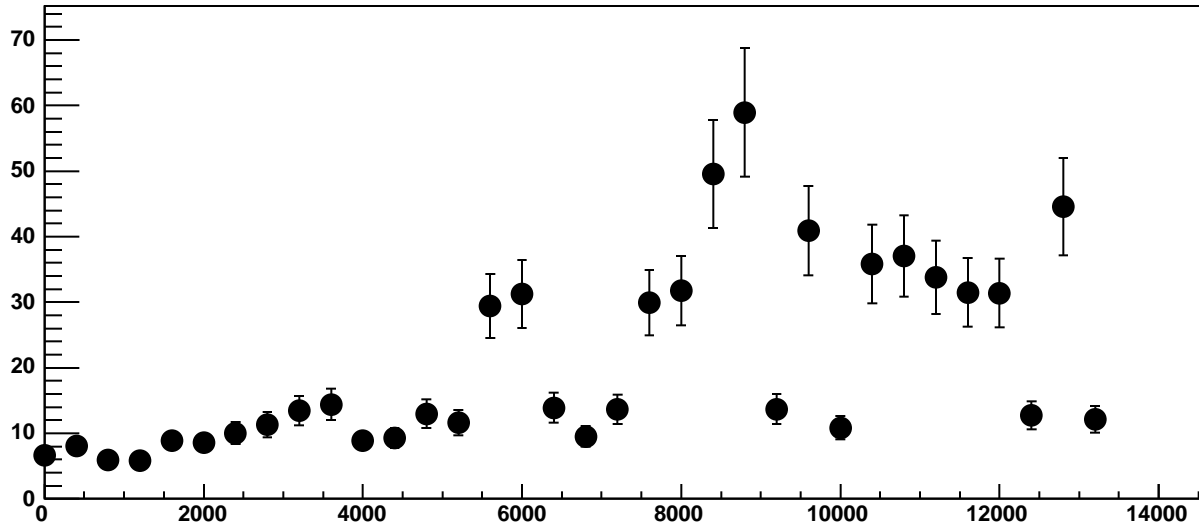


Chip 4, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

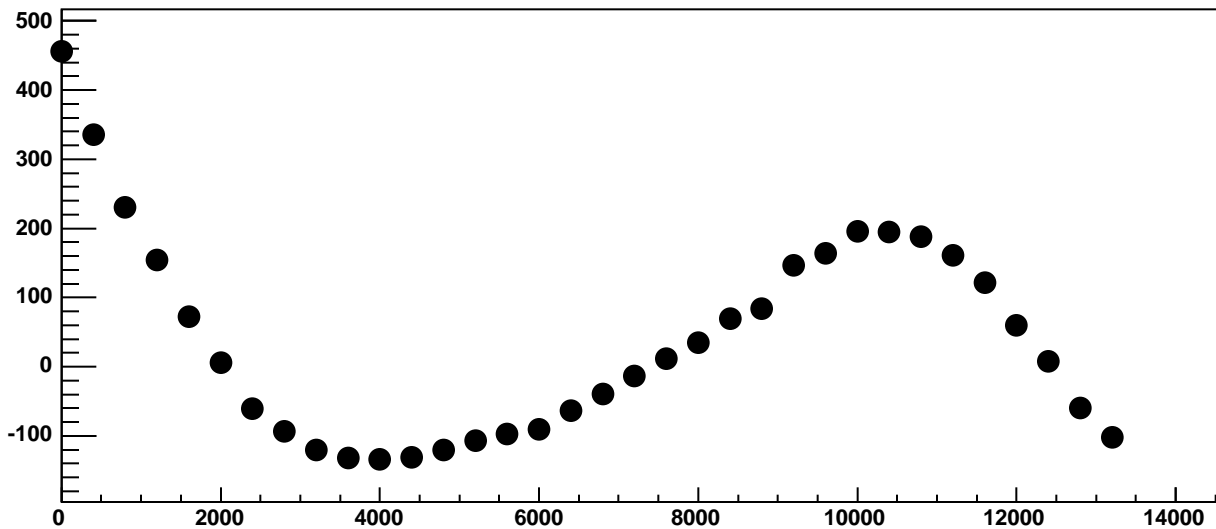
χ^2 / ndf 4.164e+04 / 23
p0 -166.8 ± 1.04
p1 0.2987 ± 0.0002126



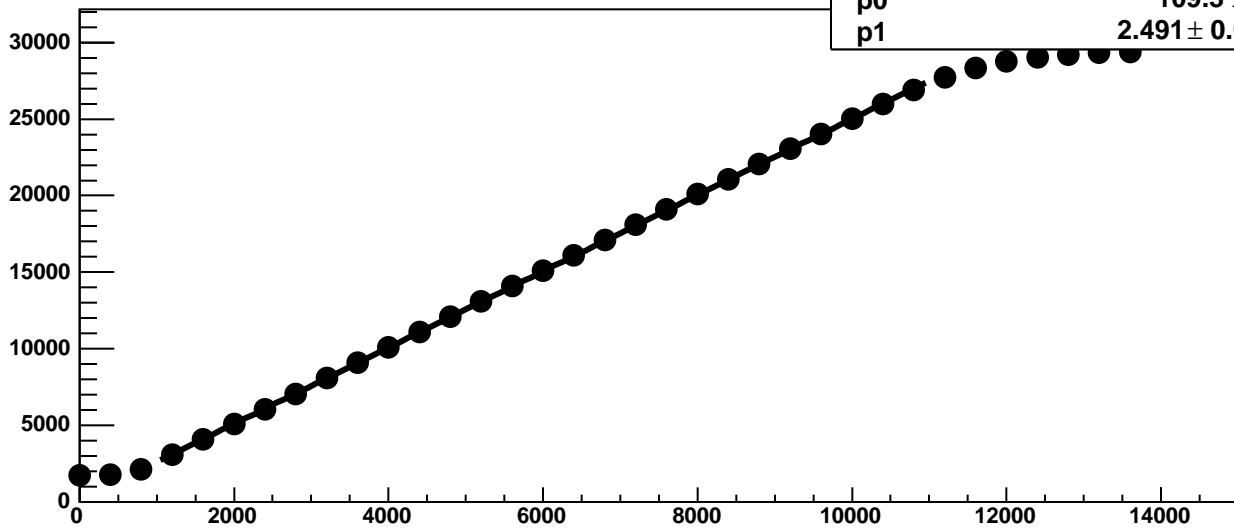
Chip 4, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

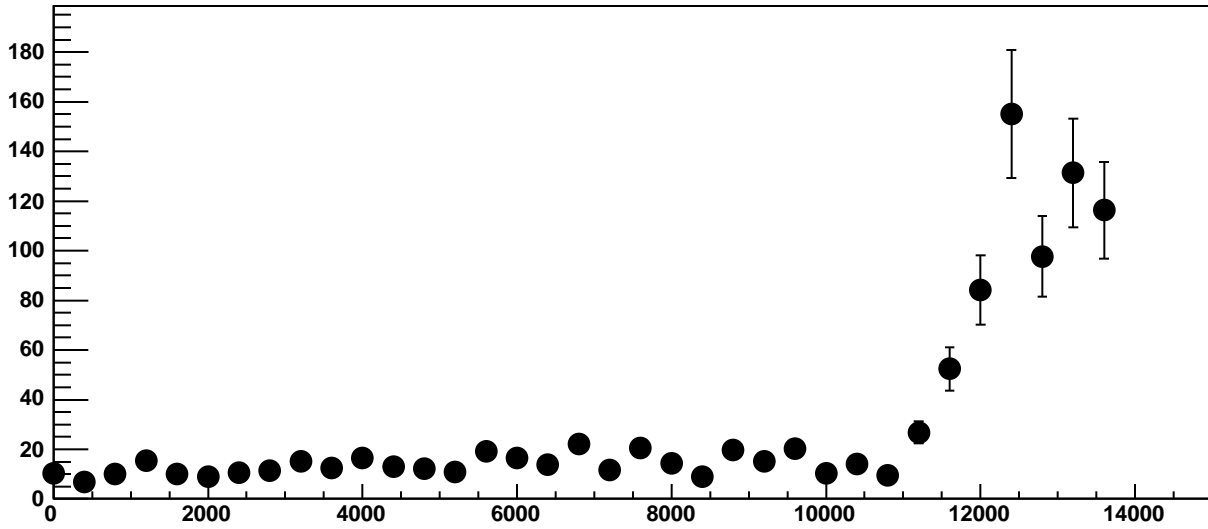


Chip 4, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC

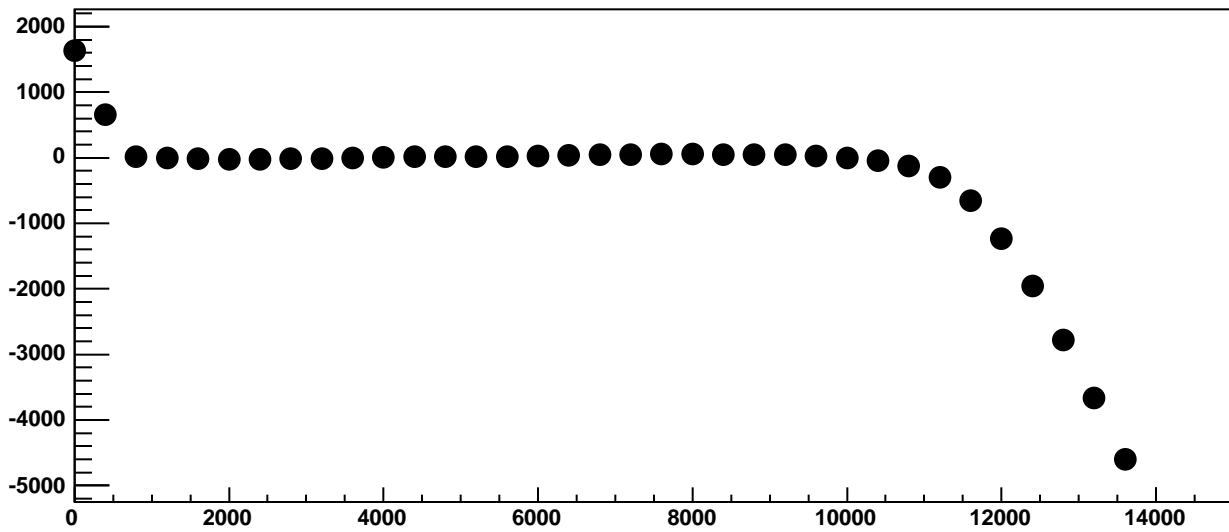


χ^2 / ndf 5711 / 23
p0 109.3 ± 1.228
p1 2.491 ± 0.000187

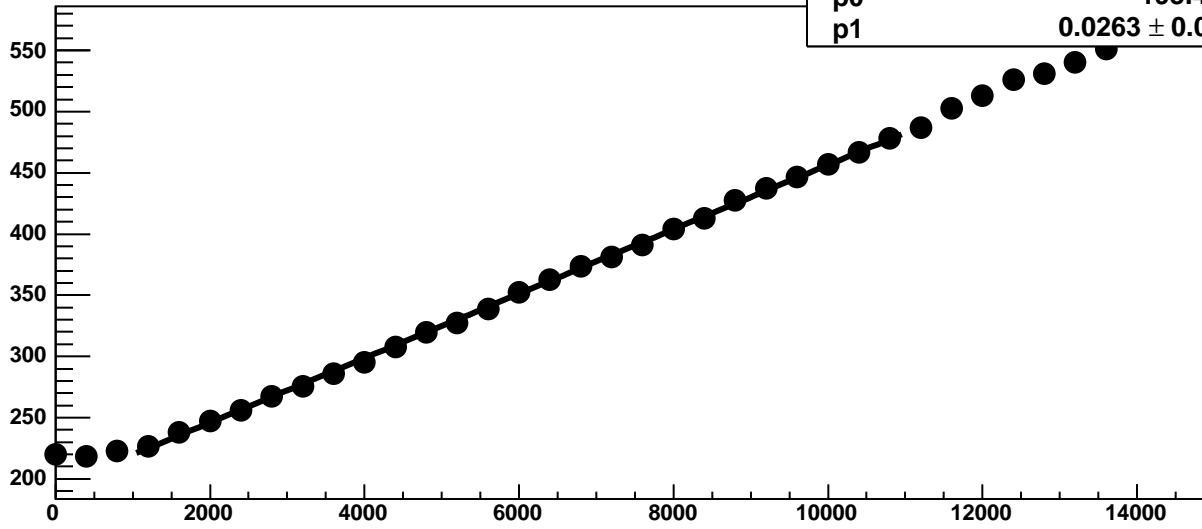
Chip 4, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC

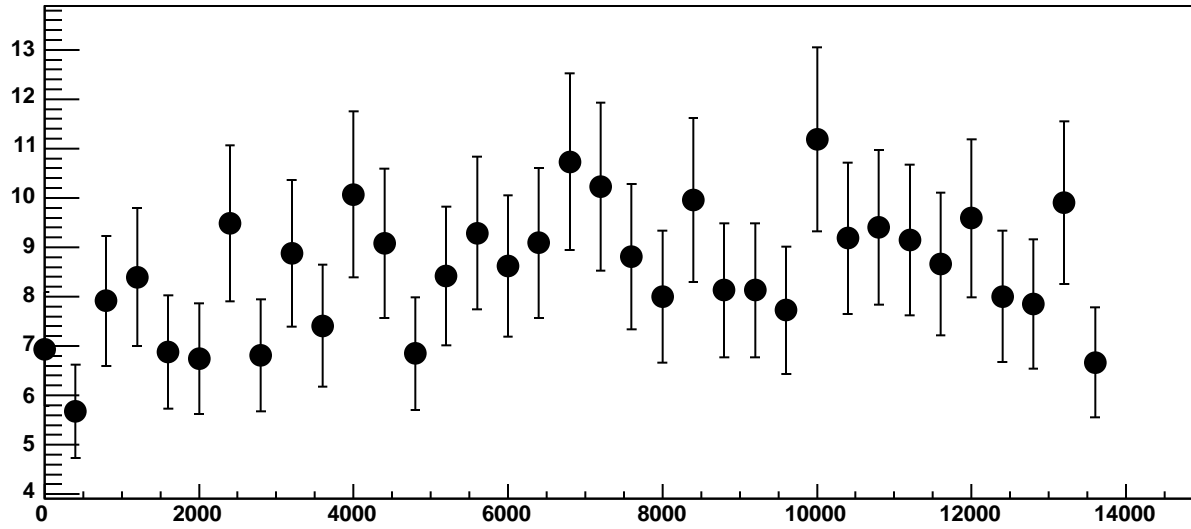


Chip 4, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

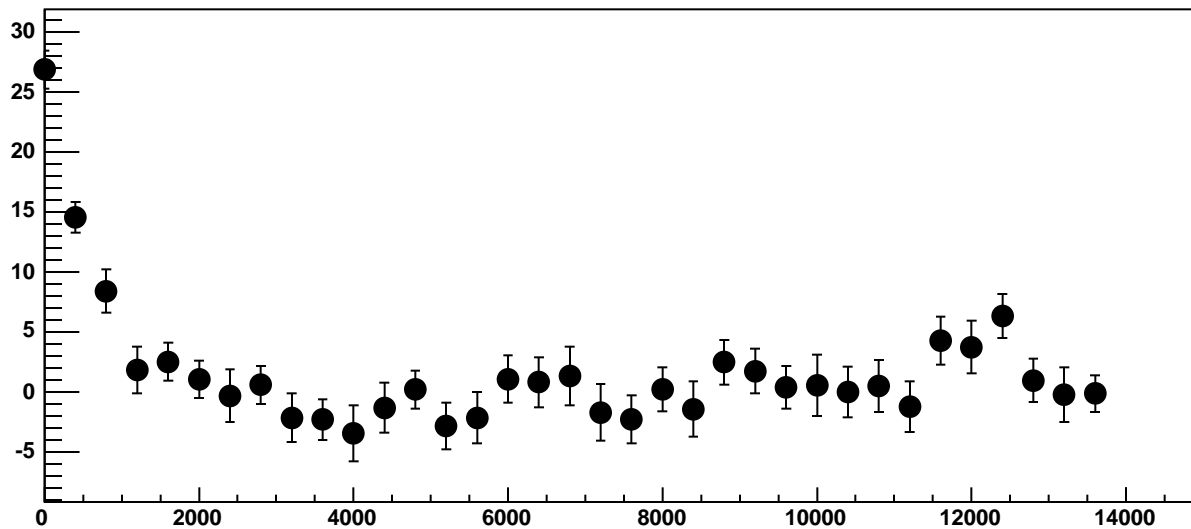


χ^2 / ndf 18.55 / 23
p0 193.4 ± 0.84
p1 0.0263 ± 0.0001323

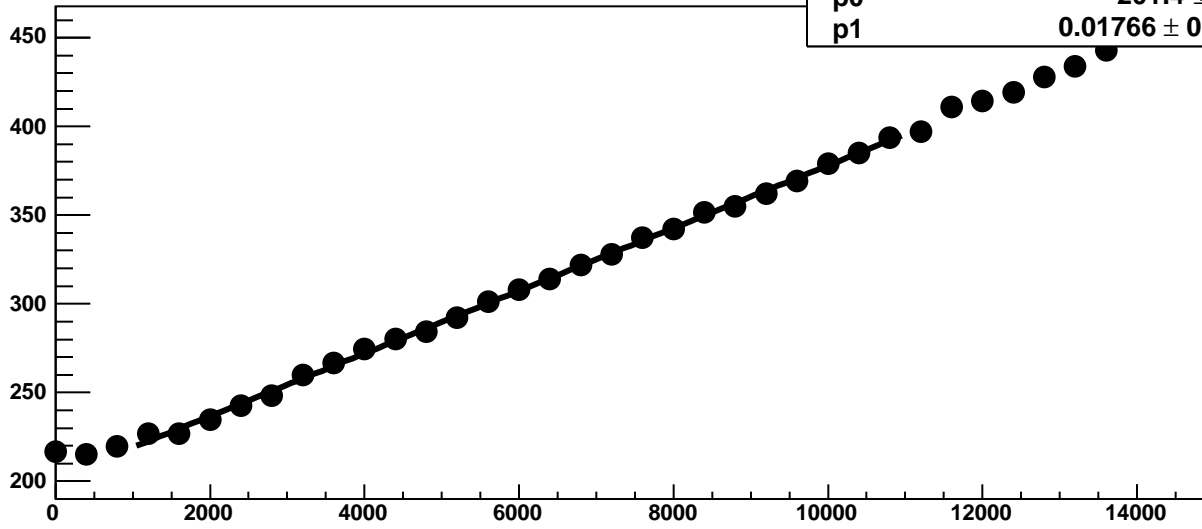
Chip 4, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

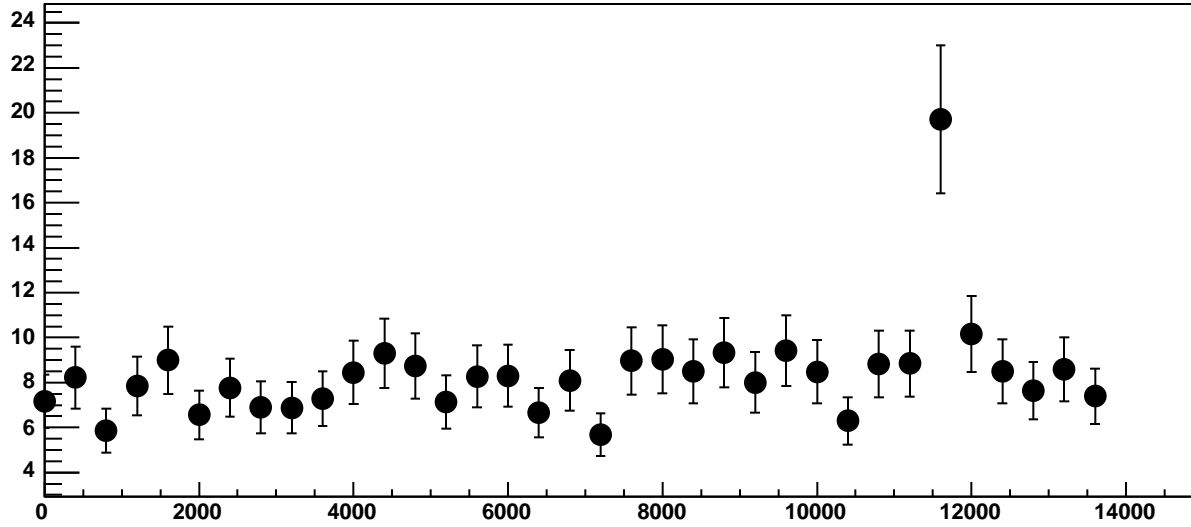


Chip 4, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

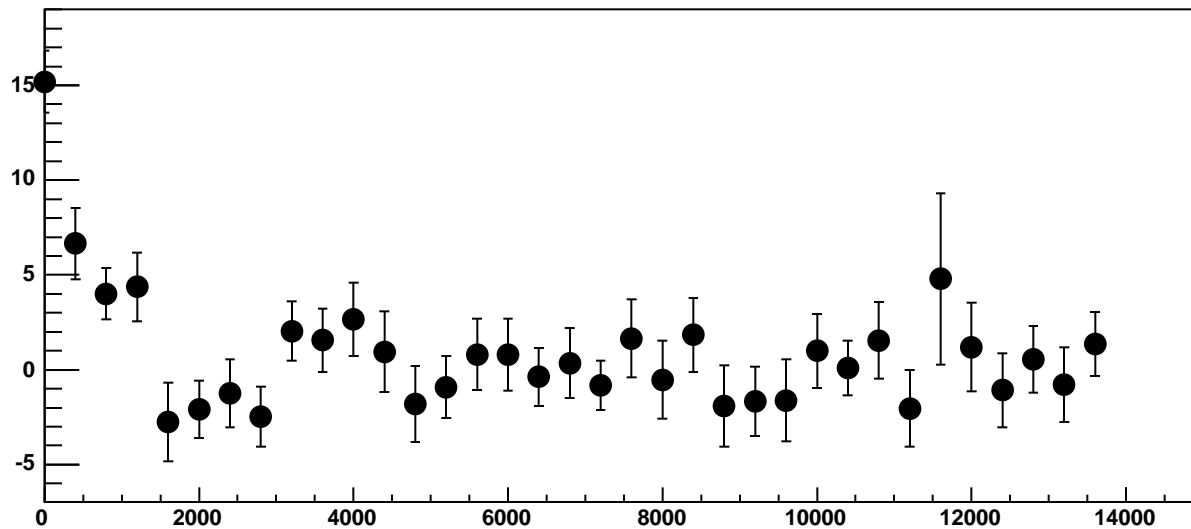


χ^2 / ndf 23.69 / 23
p0 201.4 ± 0.8105
p1 0.01766 ± 0.000124

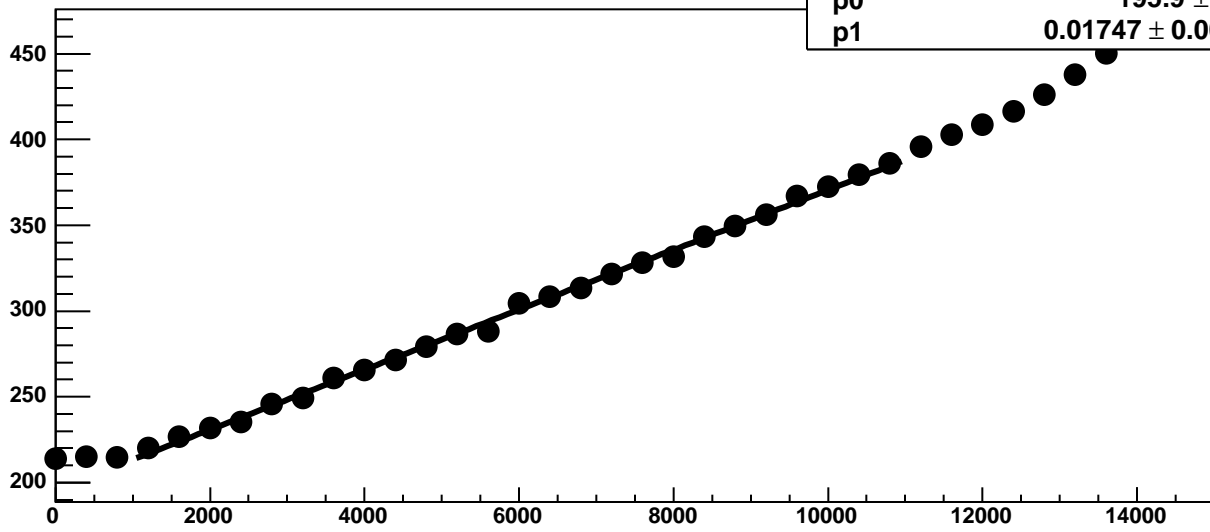
Chip 4, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

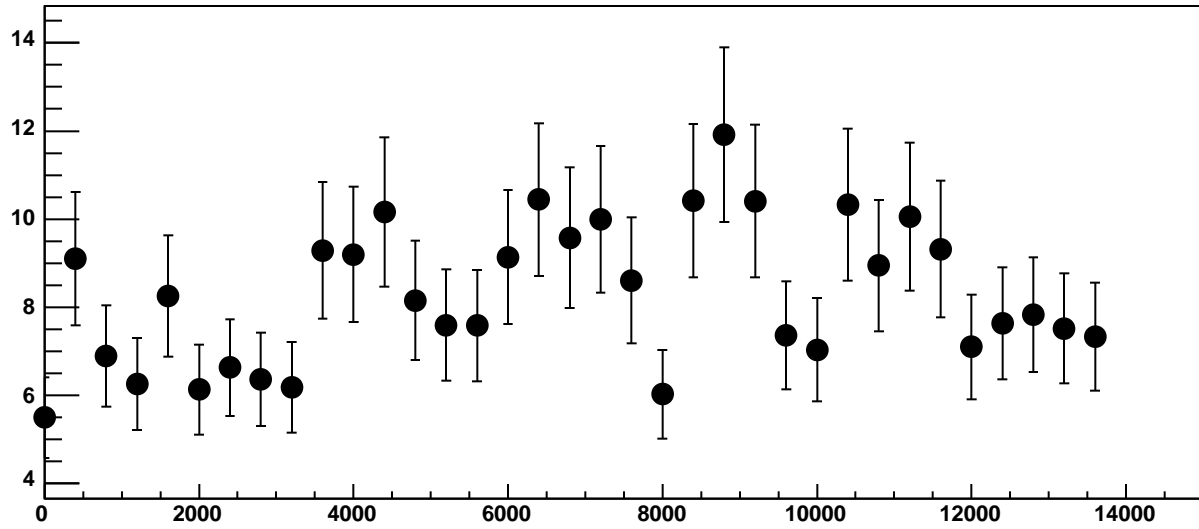


Chip 4, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

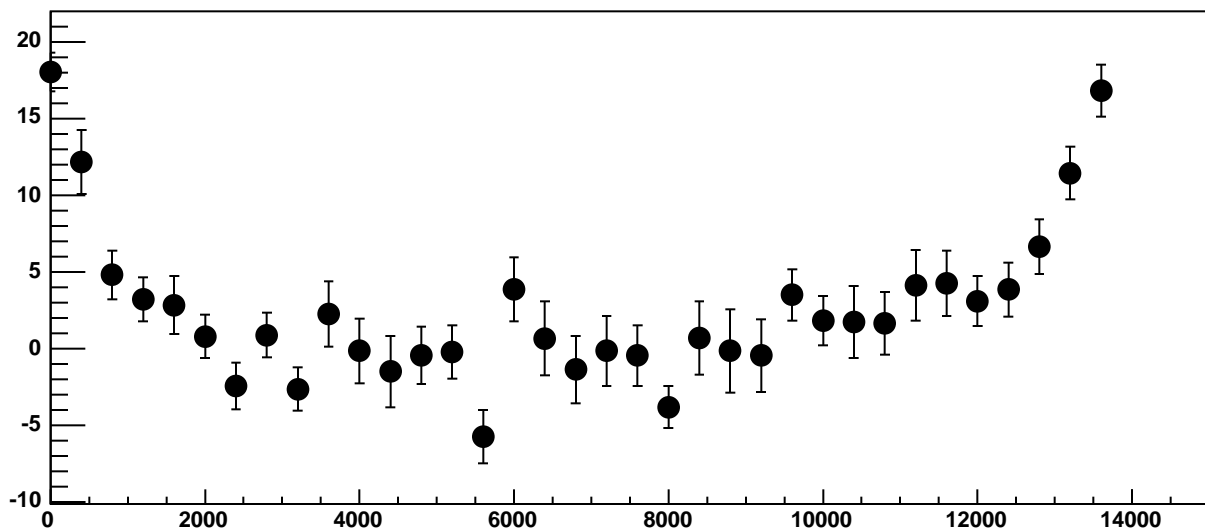


χ^2 / ndf 44.8 / 23
p0 195.9 ± 0.7619
p1 0.01747 ± 0.0001224

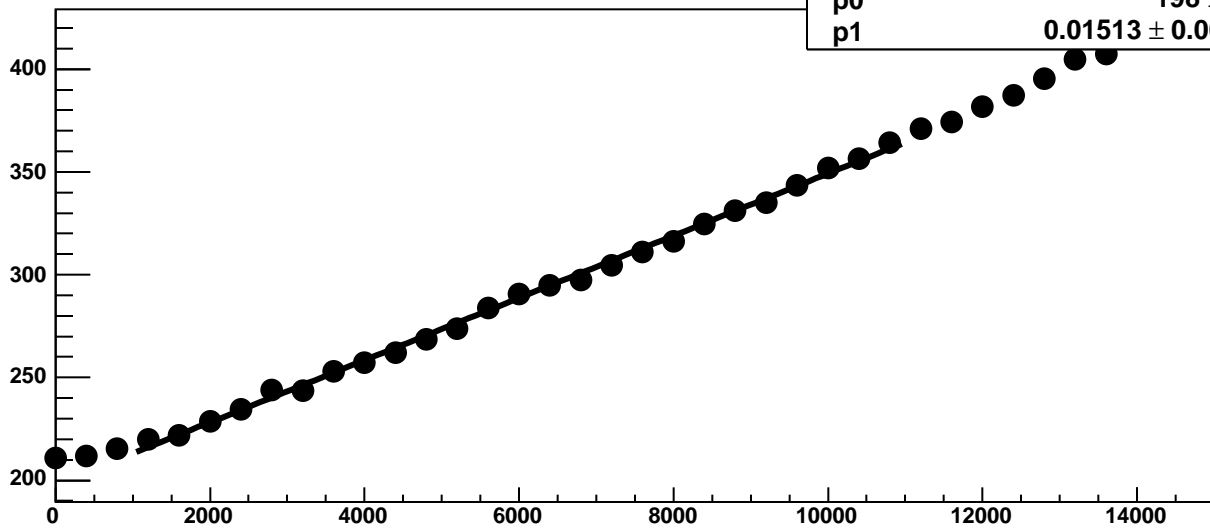
Chip 4, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

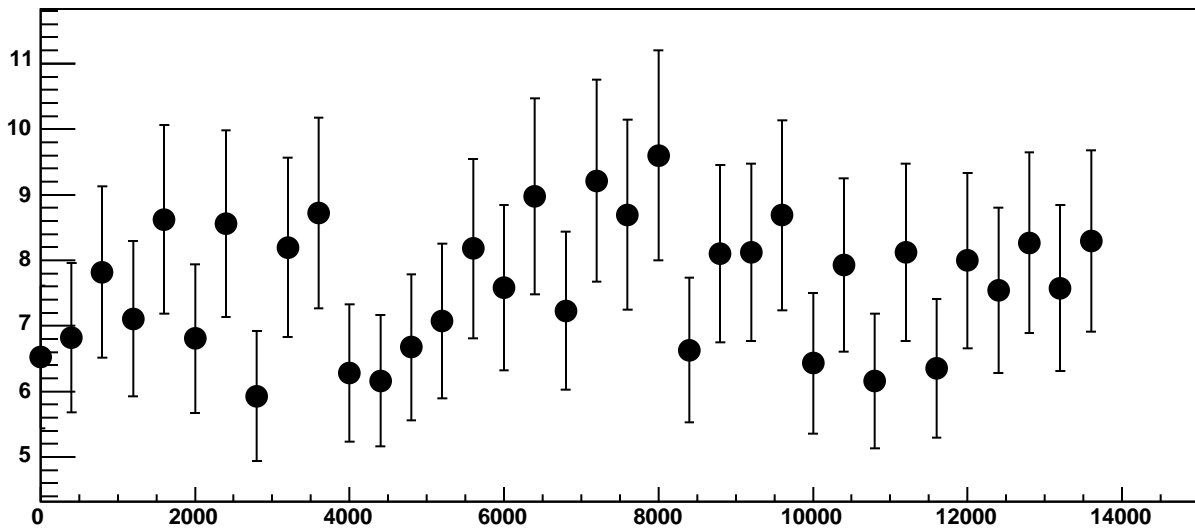


Chip 4, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

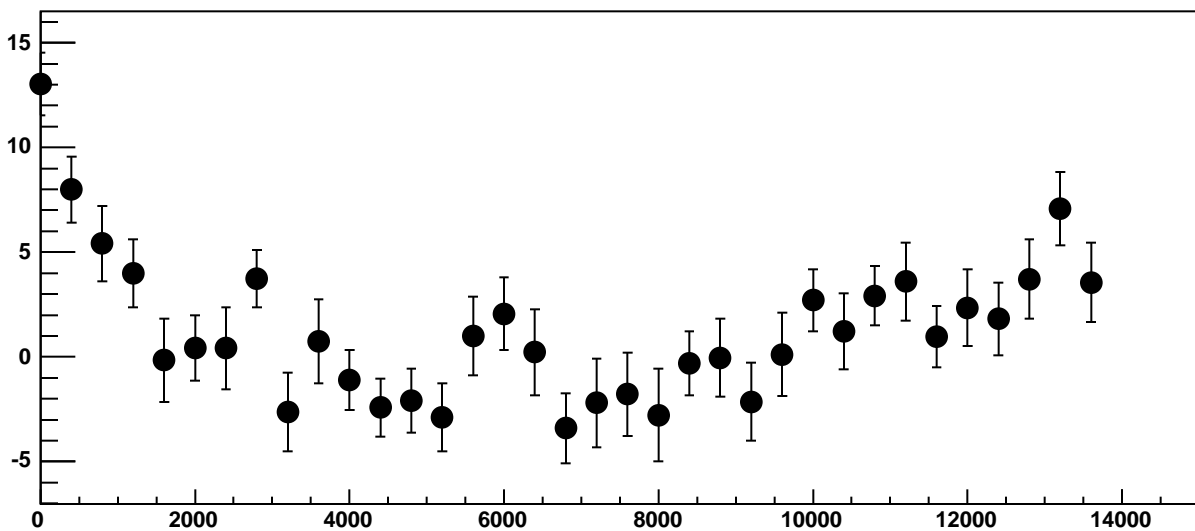


χ^2 / ndf 43.31 / 23
p0 198 ± 0.766
p1 0.01513 ± 0.0001158

Chip 4, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

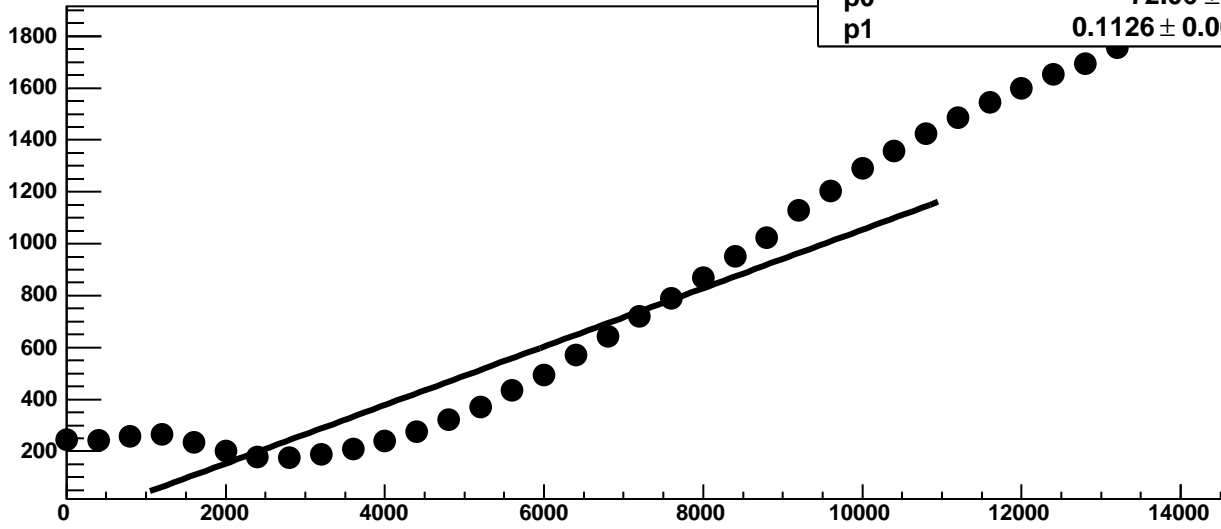


Chip 4, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

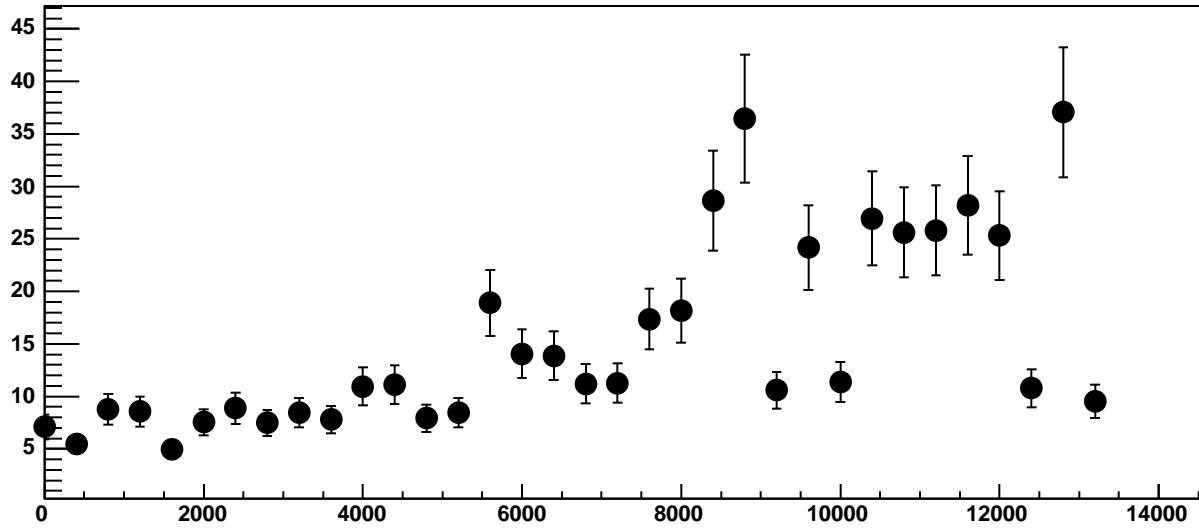


Chip 4, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

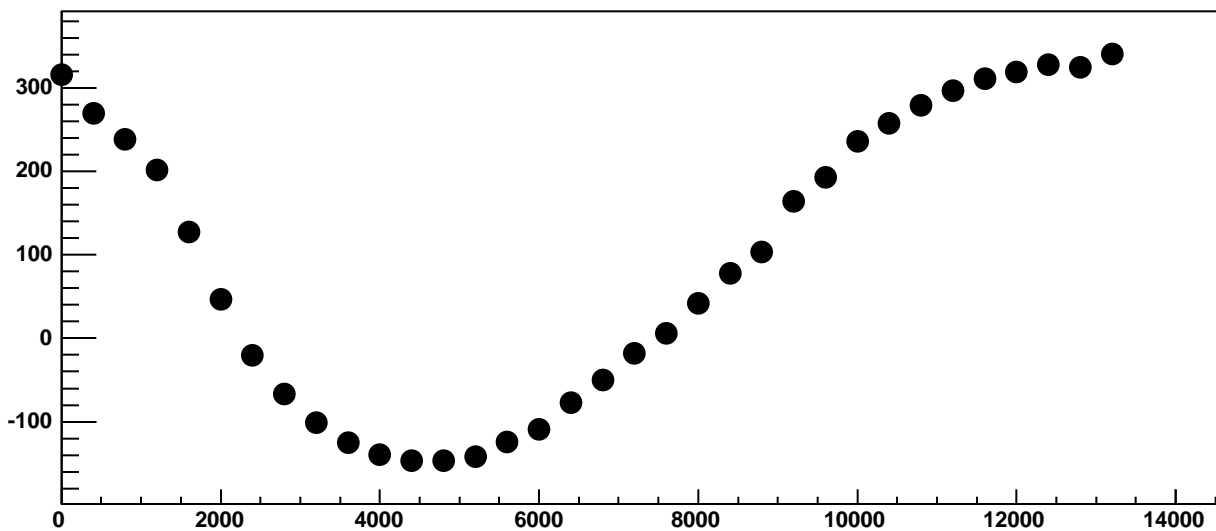
χ^2 / ndf 7.258e+04 / 23
p0 -72.06 ± 0.8982
p1 0.1126 ± 0.0001849



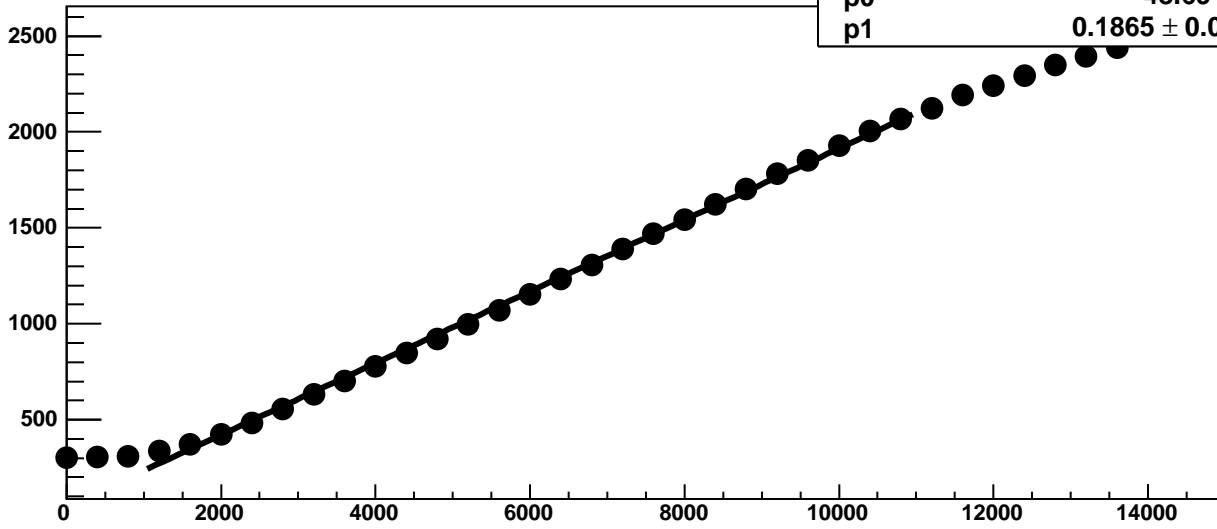
Chip 4, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

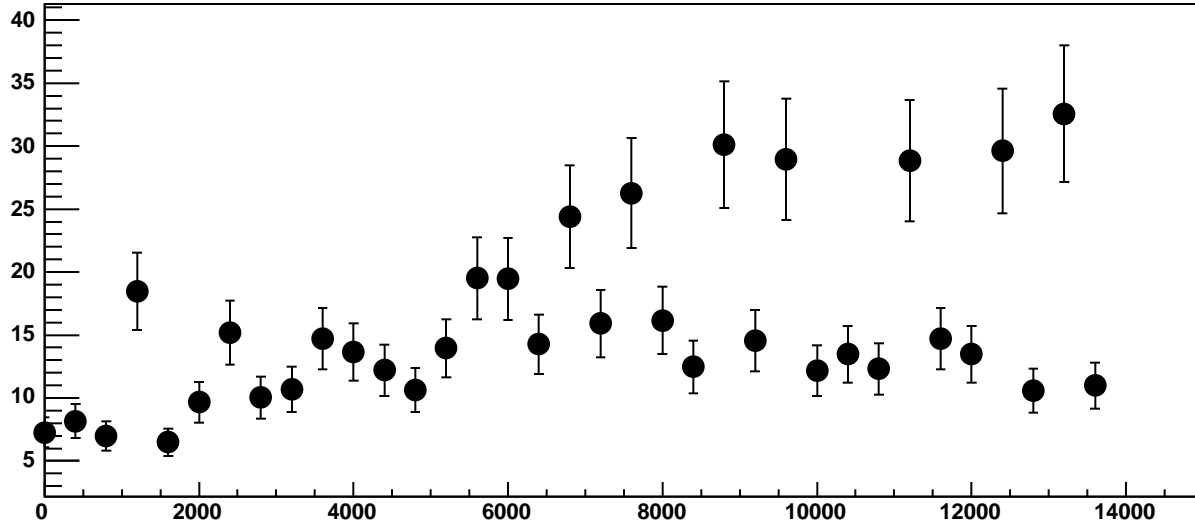


Chip 4, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

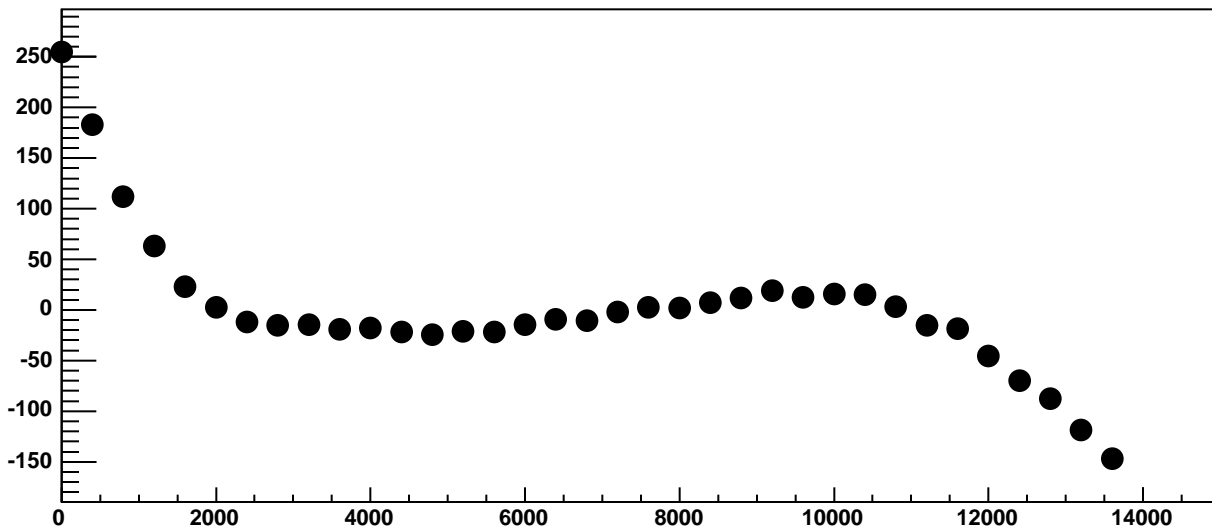


χ^2 / ndf 969.9 / 23
p0 48.69 ± 1.153
p1 0.1865 ± 0.0001961

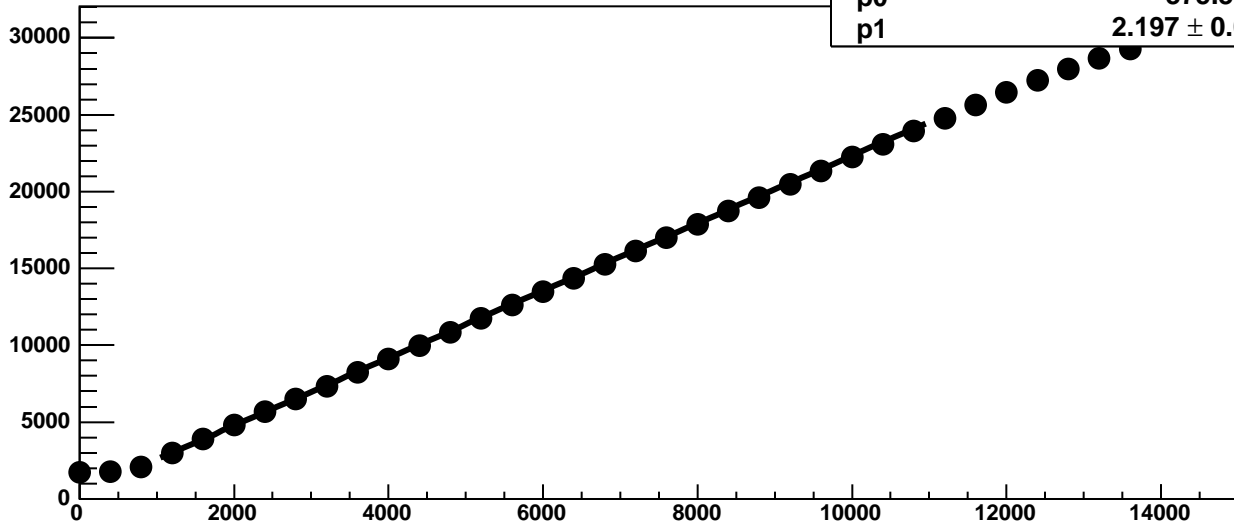
Chip 4, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



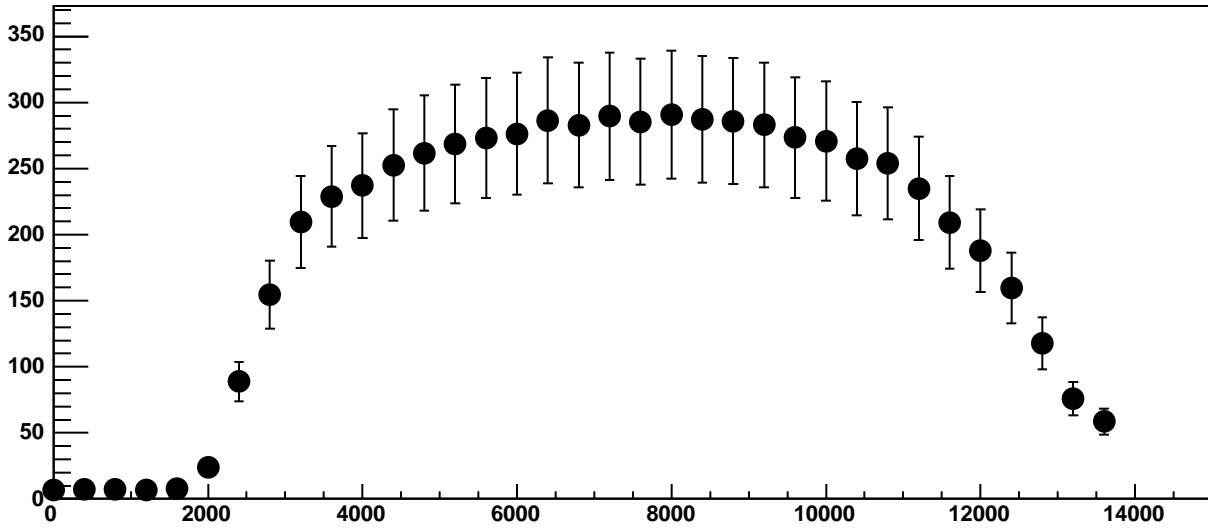
Chip 4, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



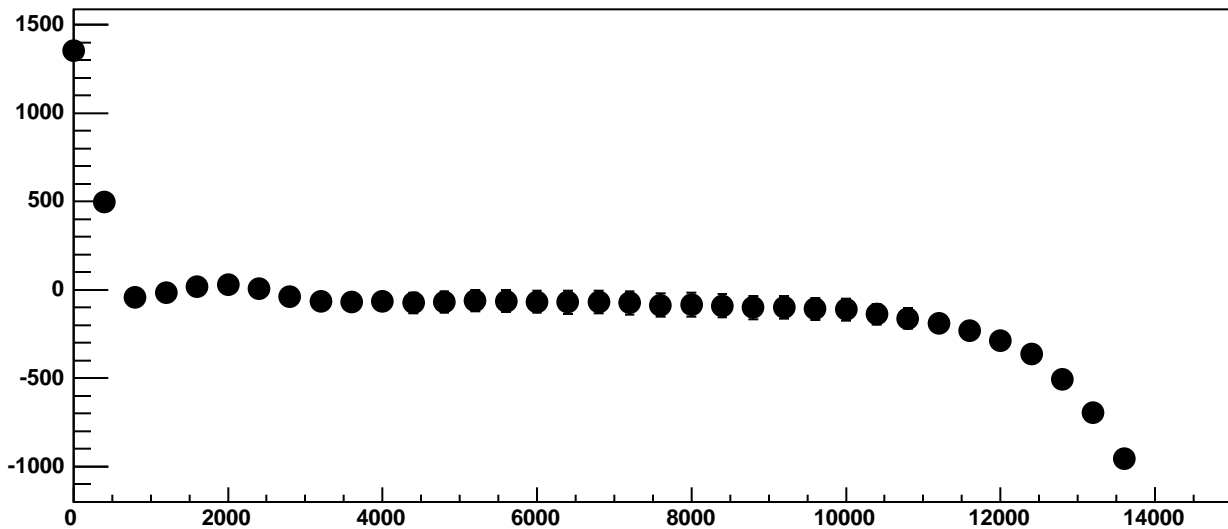
Chip 4, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC



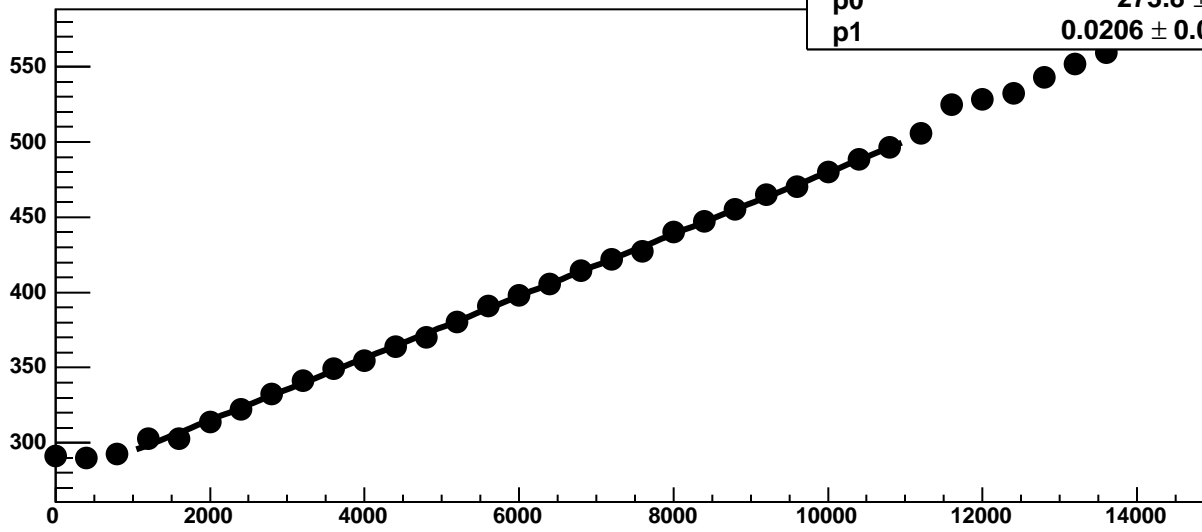
Chip 4, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



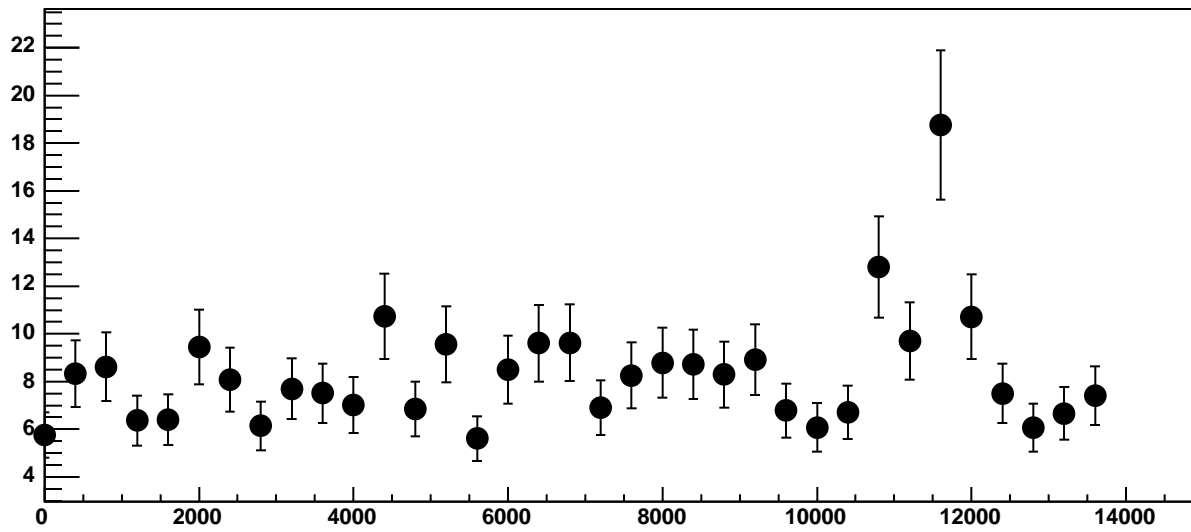
Chip 4, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC



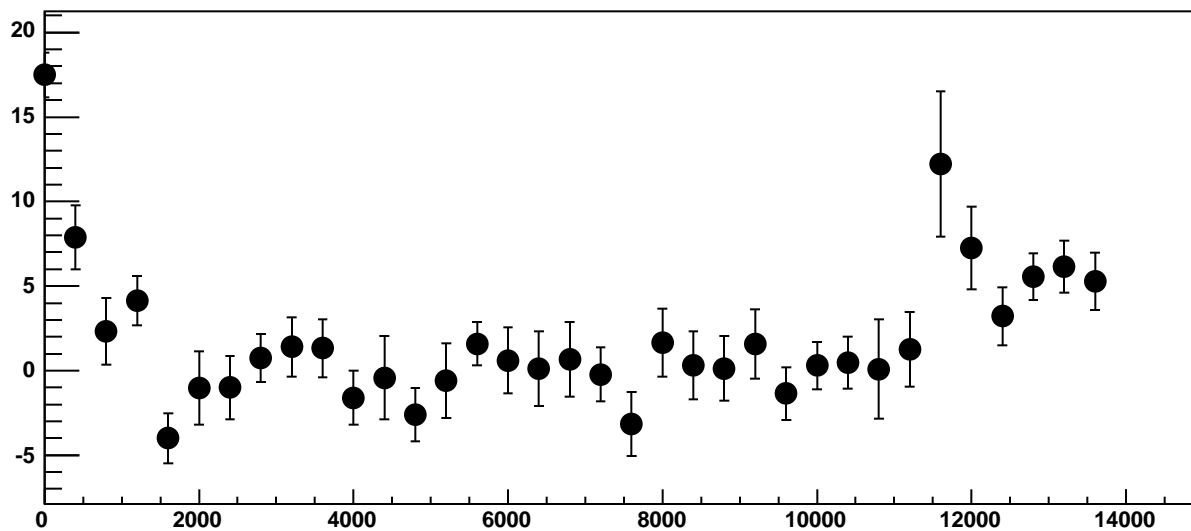
Chip 4, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



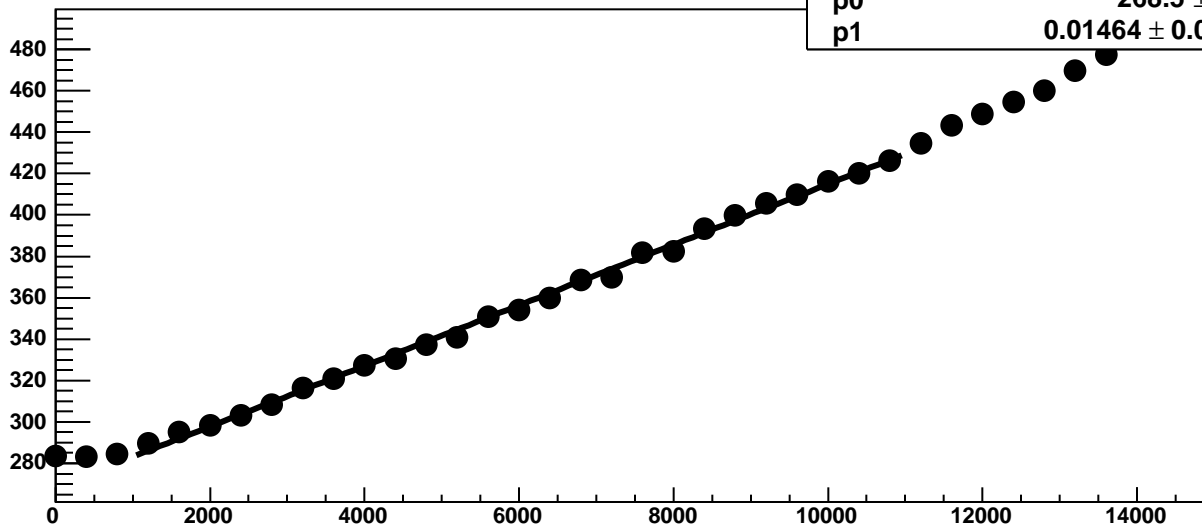
Chip 4, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

35.96 / 23

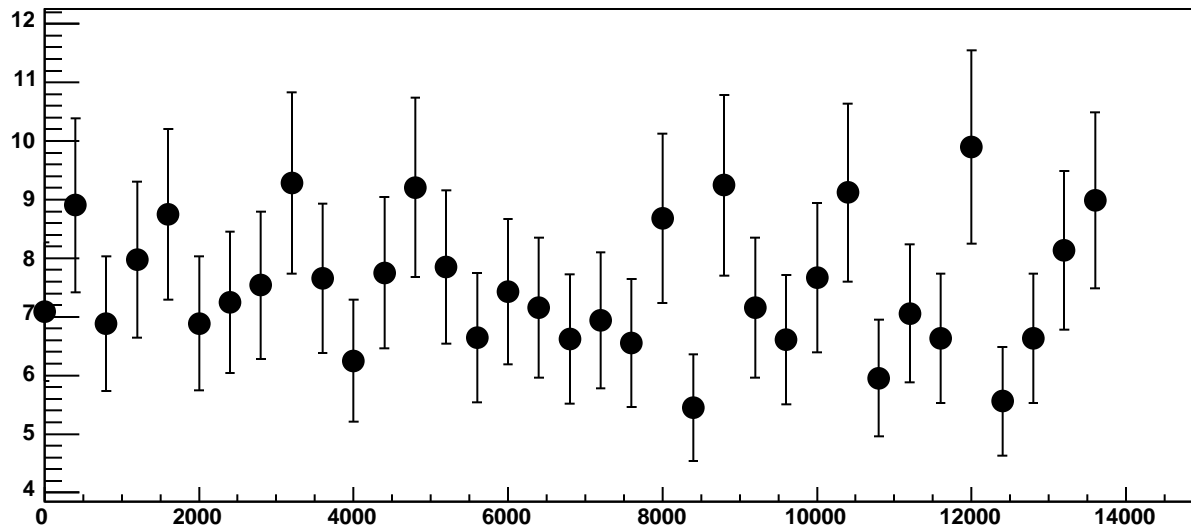
p0

268.5 ± 0.8022

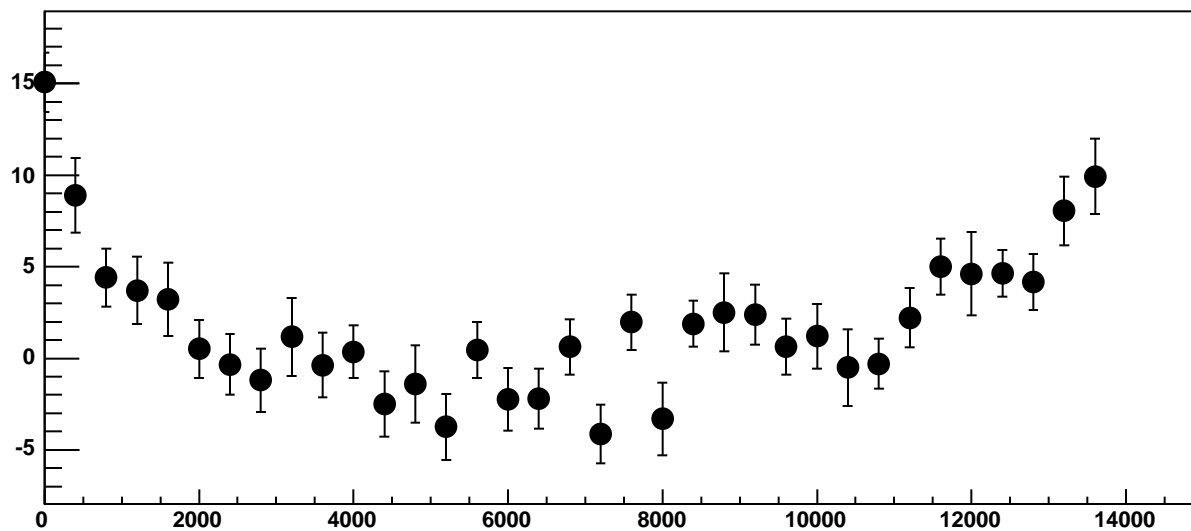
p1

0.01464 ± 0.0001174

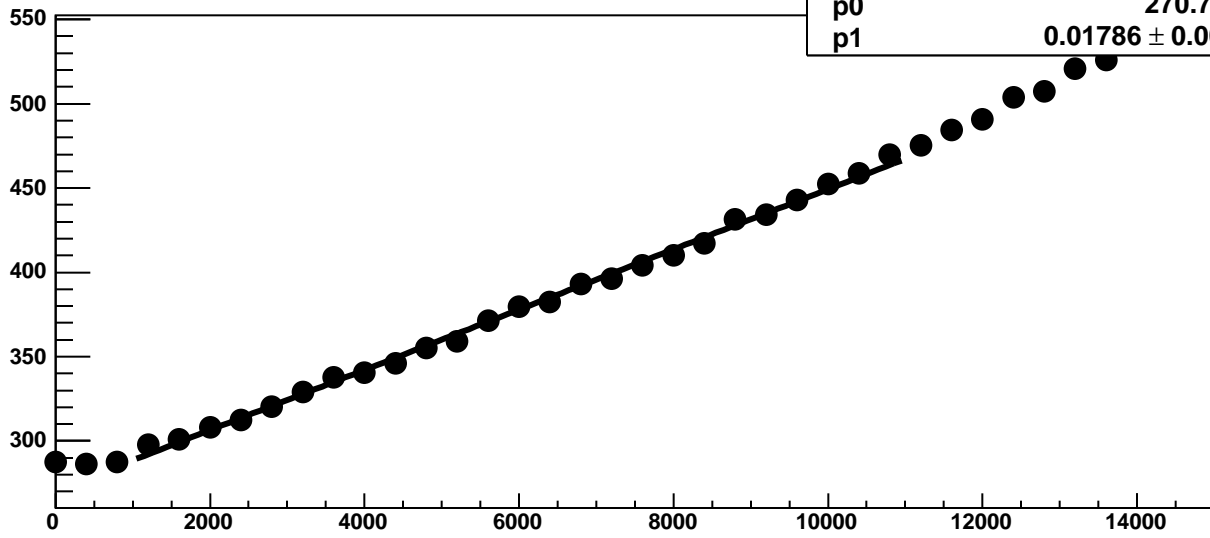
Chip 4, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

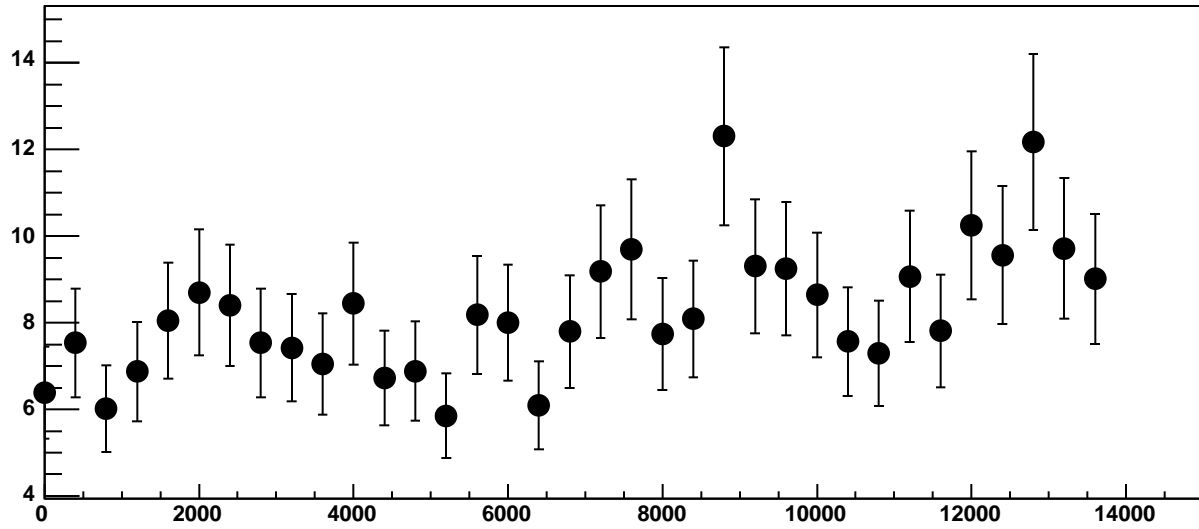


Chip 4, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

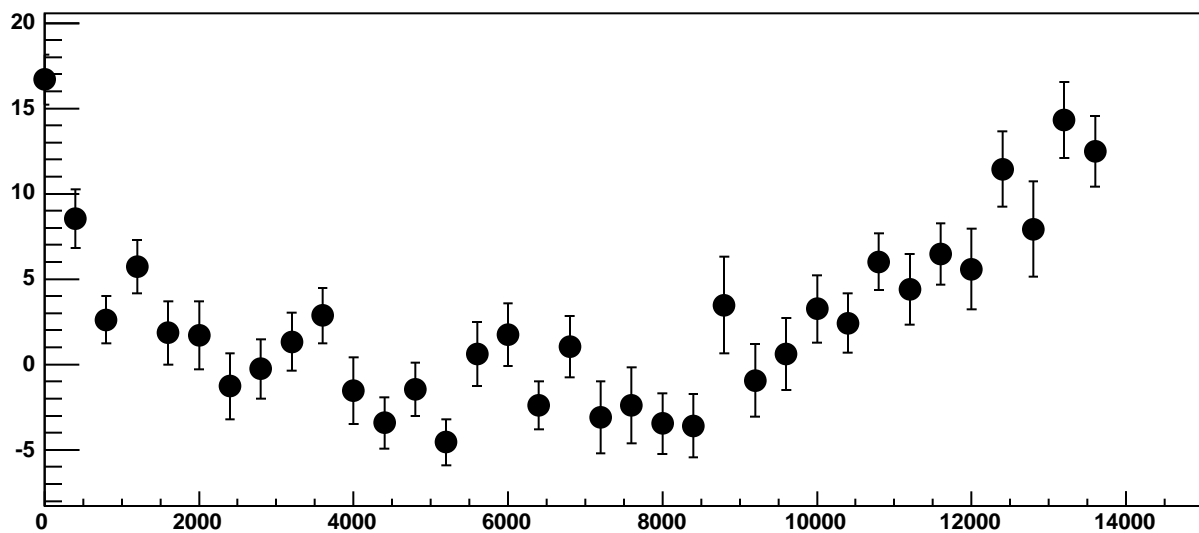


χ^2 / ndf 71.83 / 23
p0 270.7 ± 0.81
p1 0.01786 ± 0.0001266

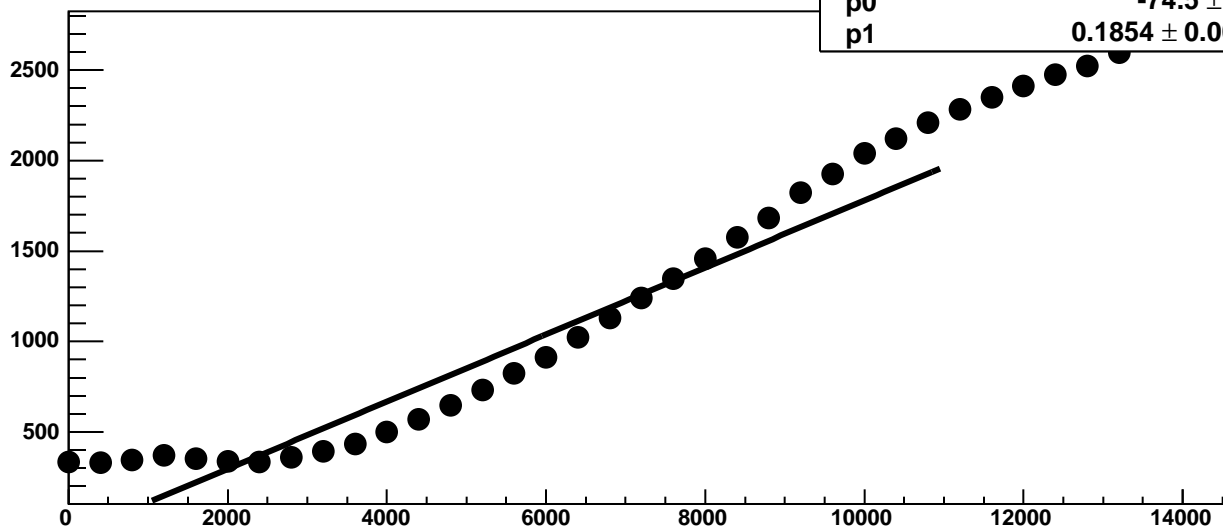
Chip 4, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



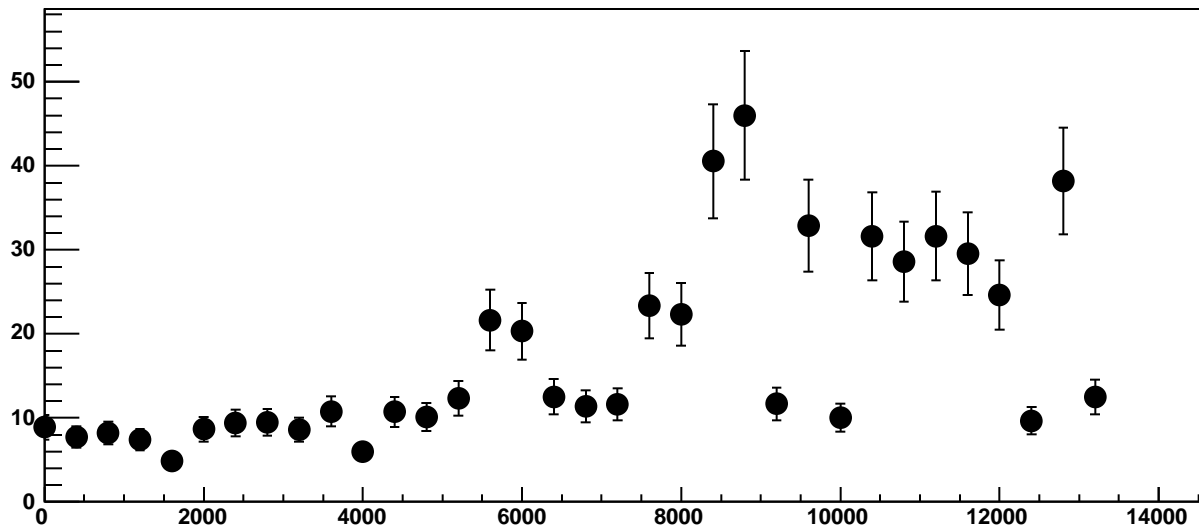
Chip 4, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC



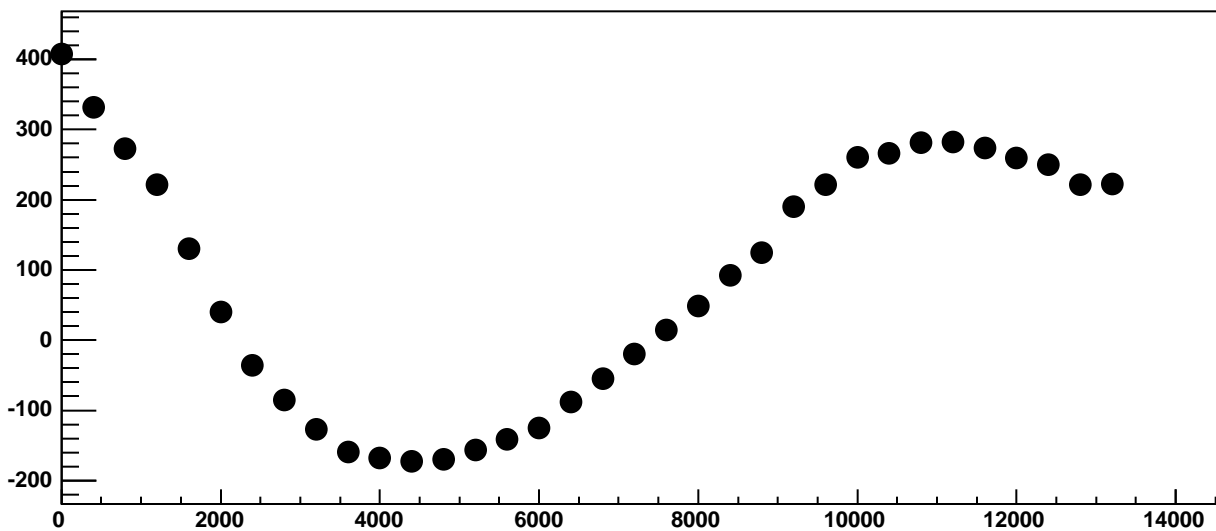
Chip 4, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



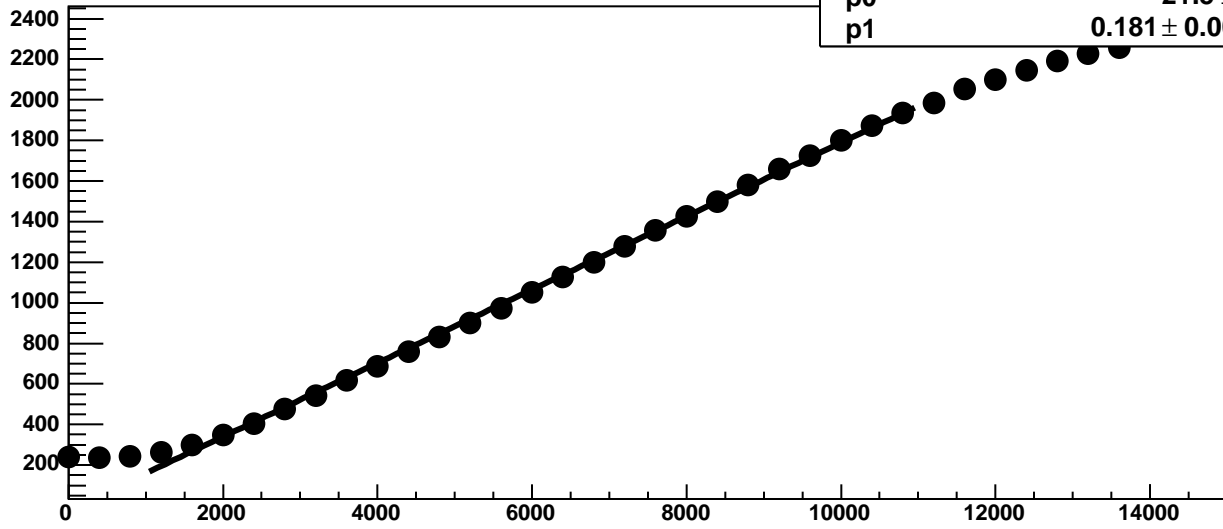
Chip 4, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



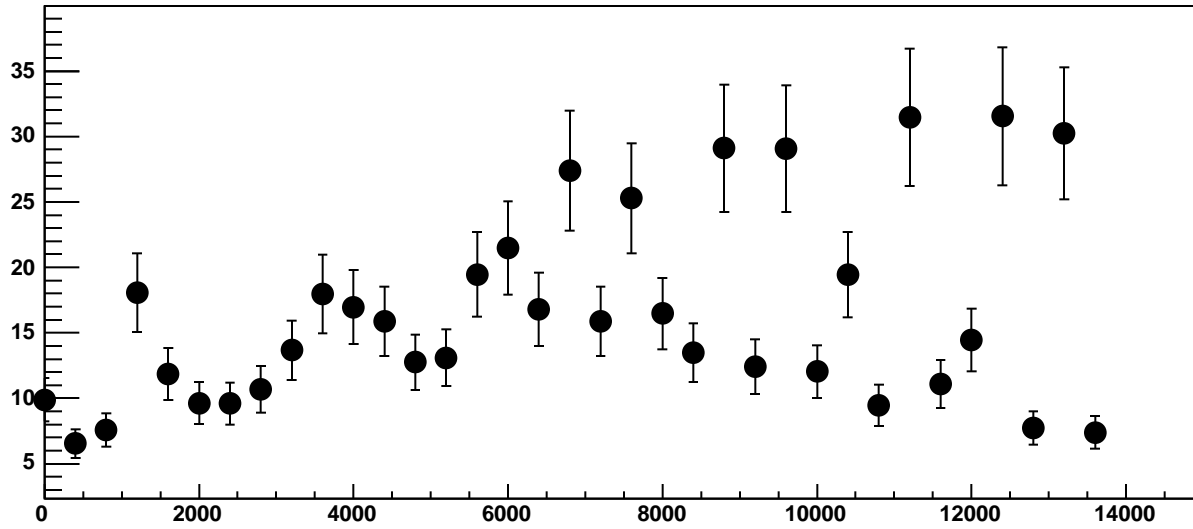
Chip 4, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



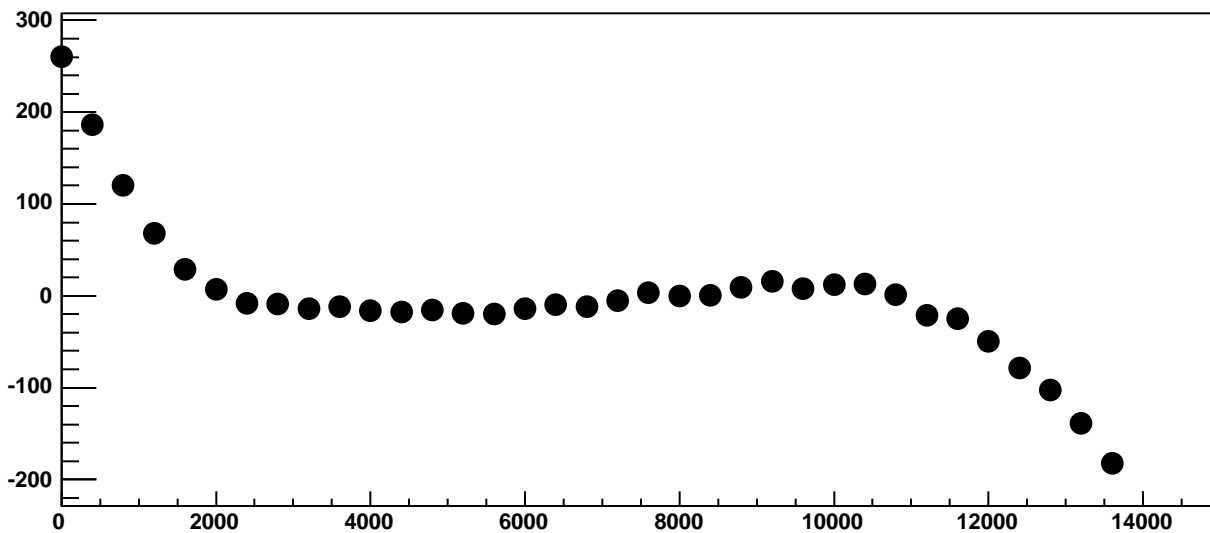
Chip 4, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



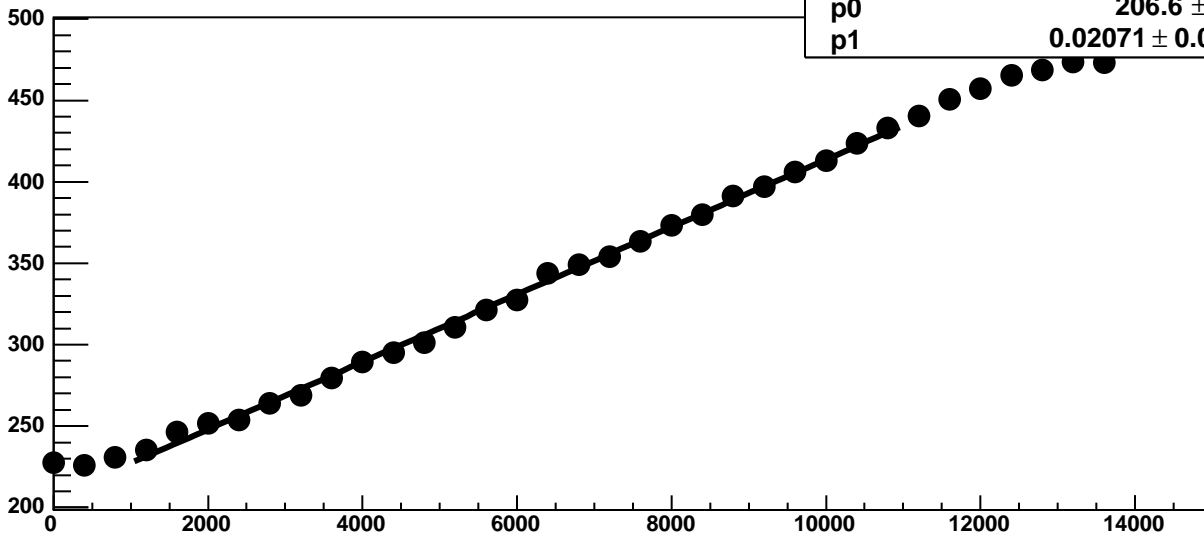
Chip 4, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



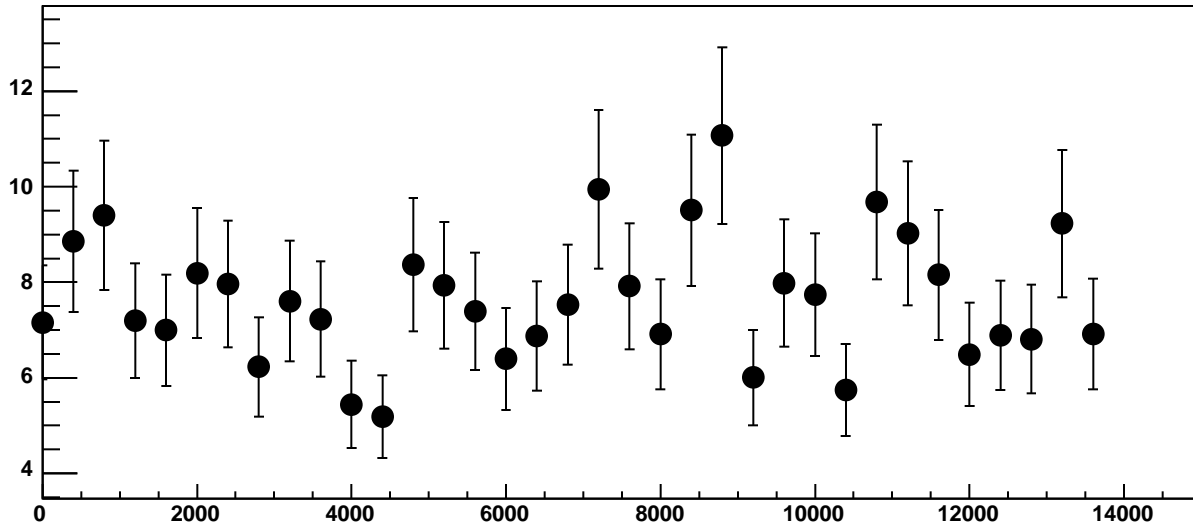
Chip 4, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



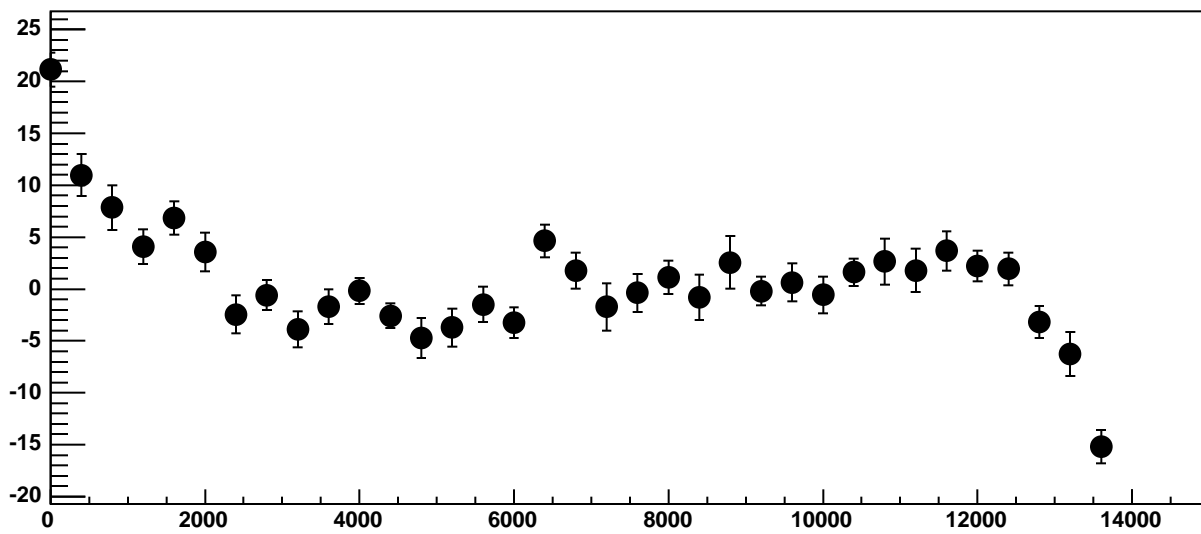
Chip 4, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC



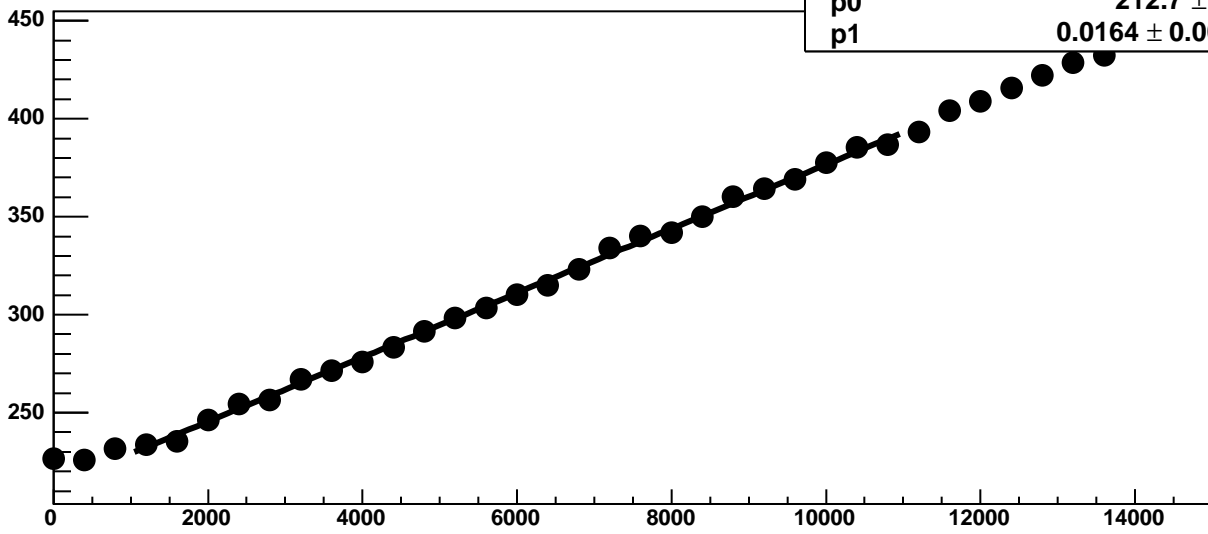
Chip 4, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC

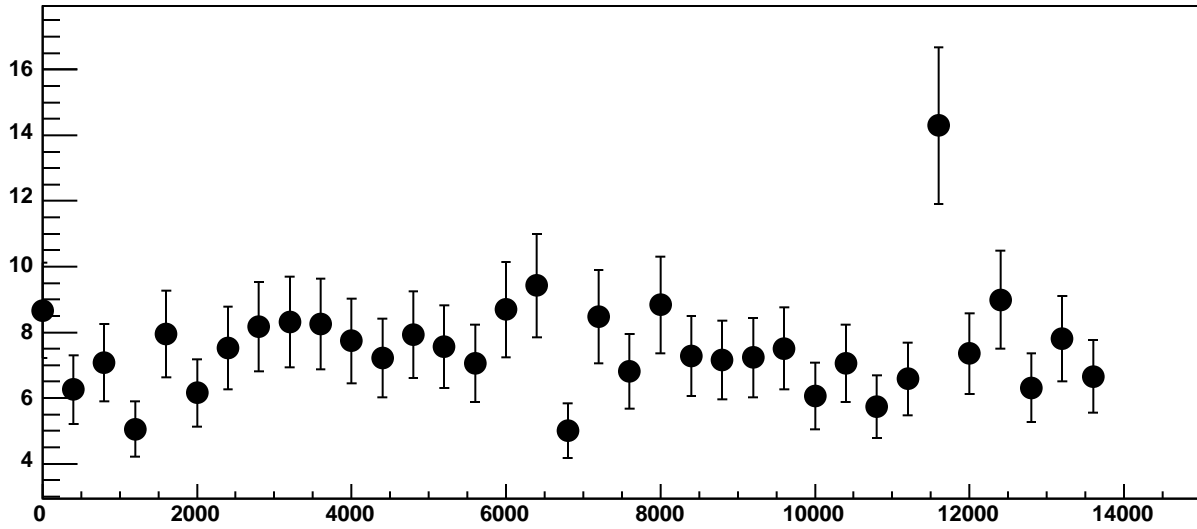


Chip 4, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

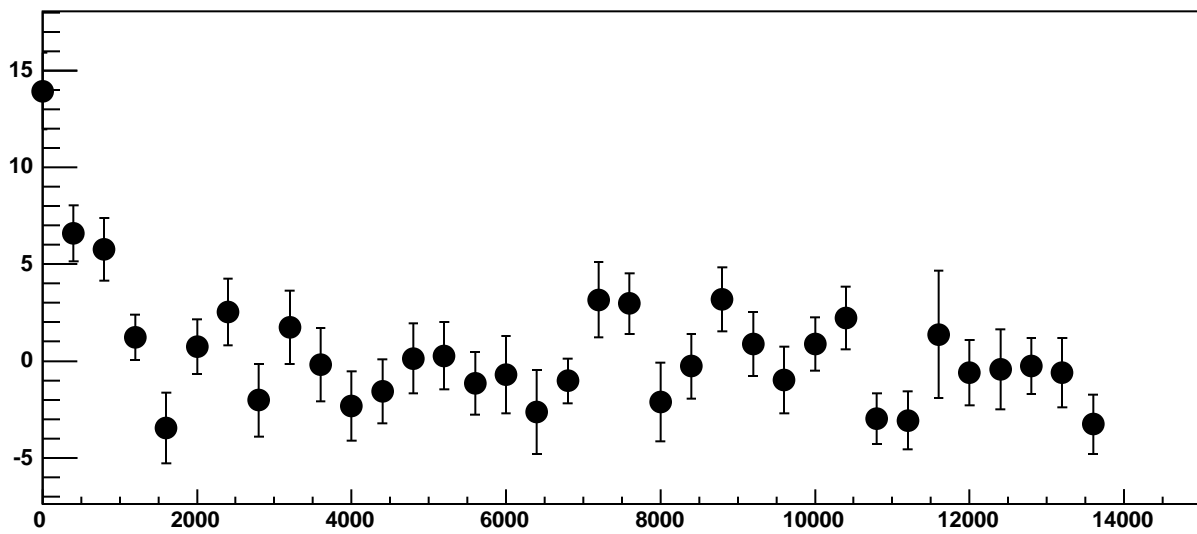


χ^2 / ndf 33.72 / 23
p0 212.7 ± 0.7168
p1 0.0164 ± 0.0001055

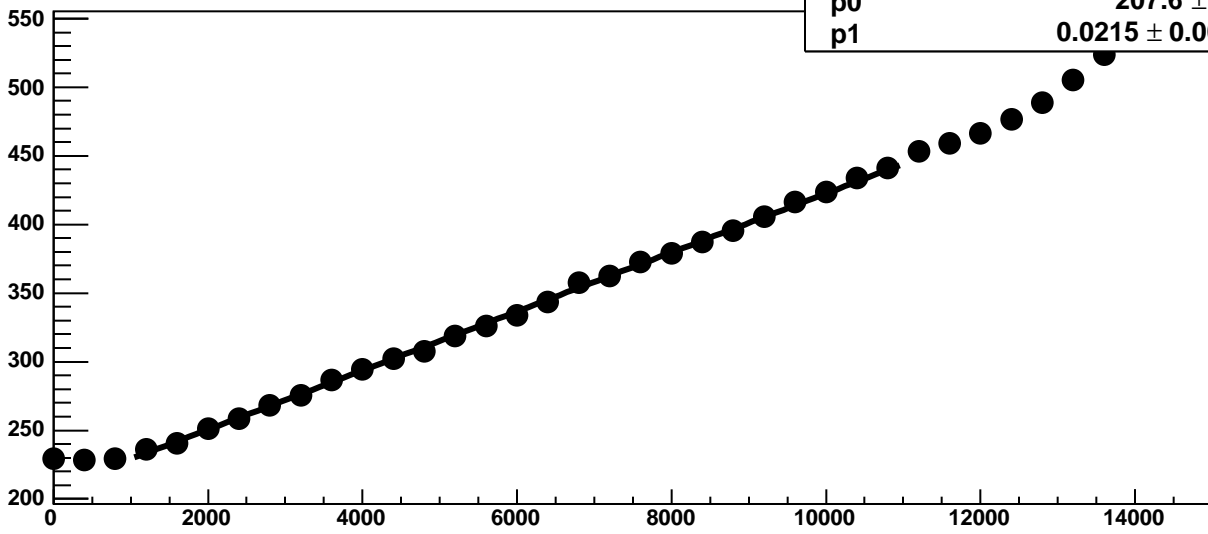
Chip 4, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

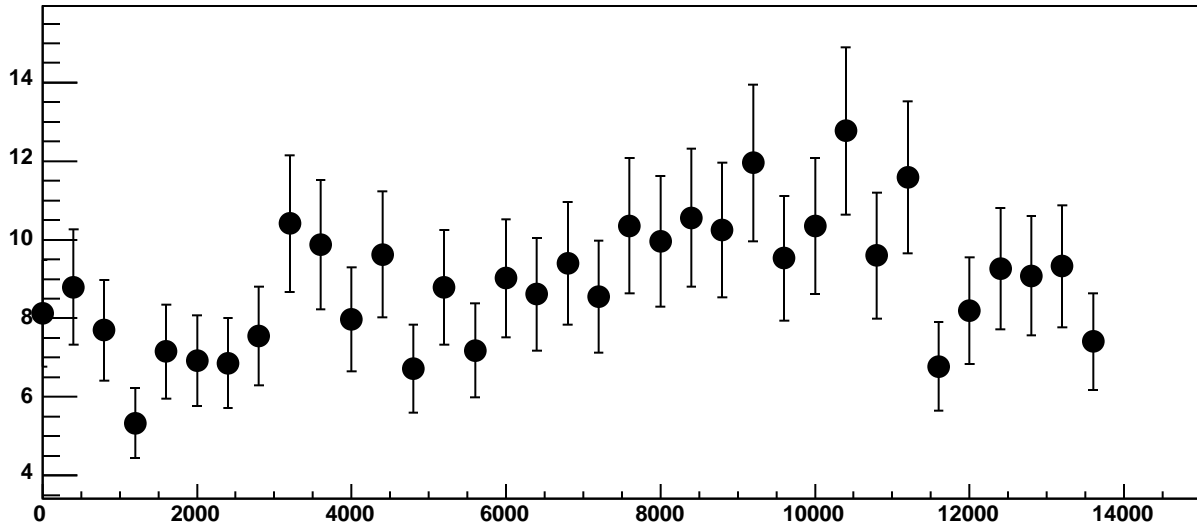


Chip 4, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

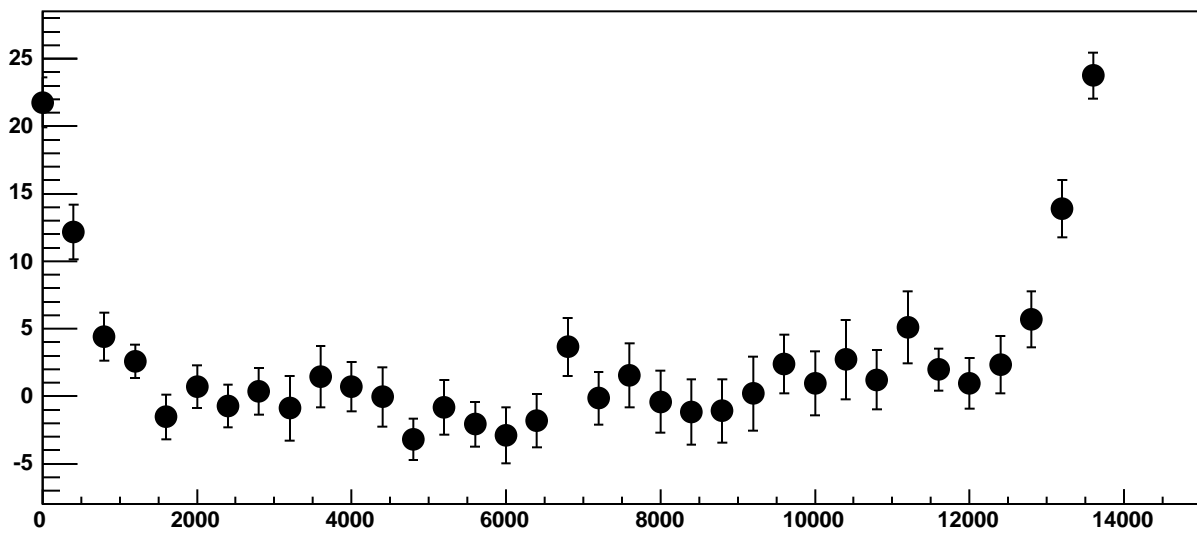


χ^2 / ndf 21.63 / 23
p0 207.6 ± 0.7869
p1 0.0215 ± 0.0001348

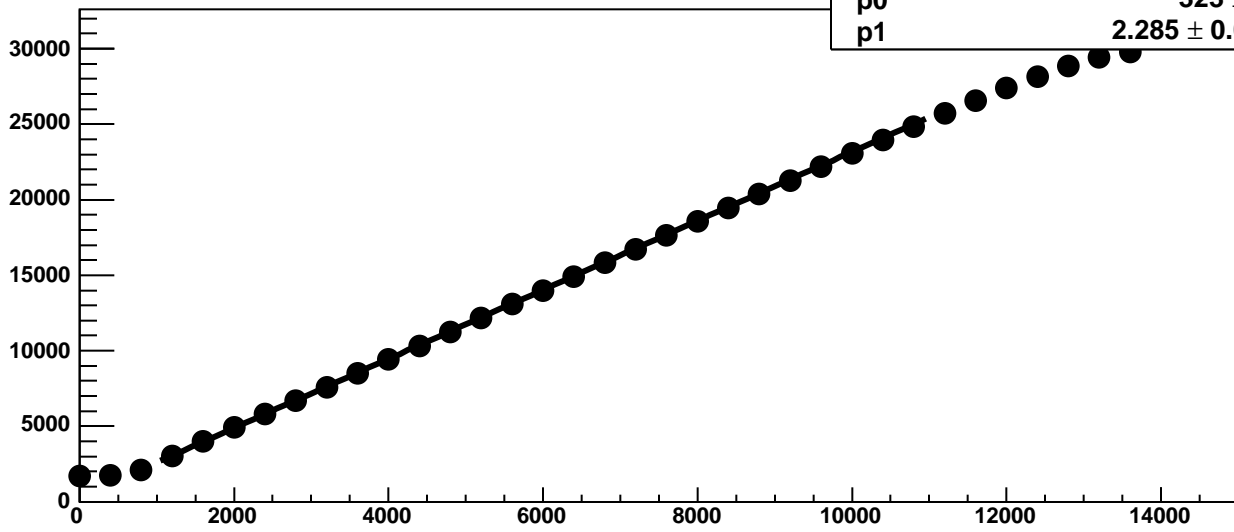
Chip 4, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



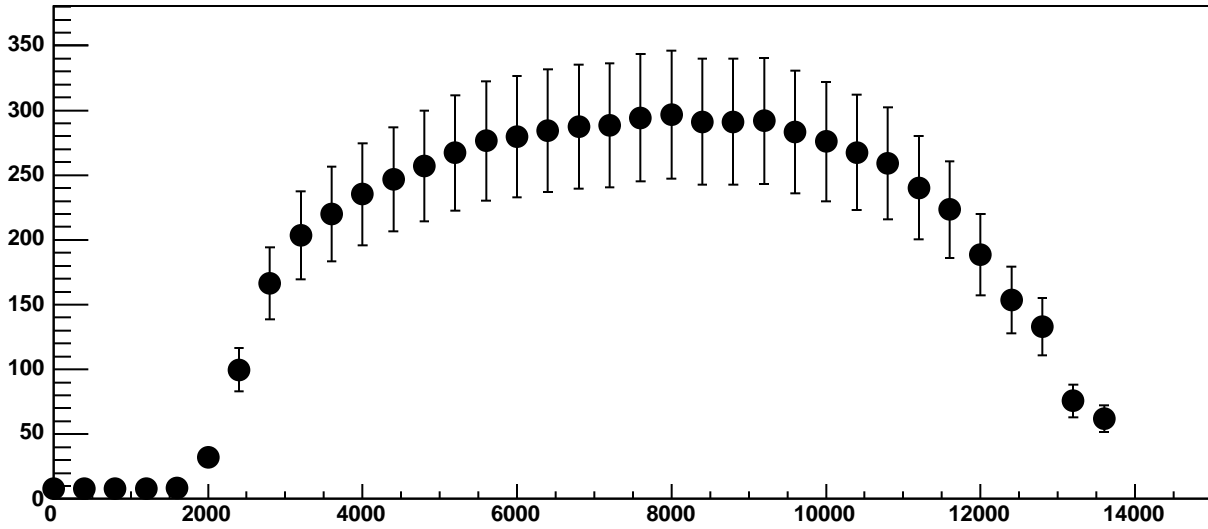
Chip 4, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



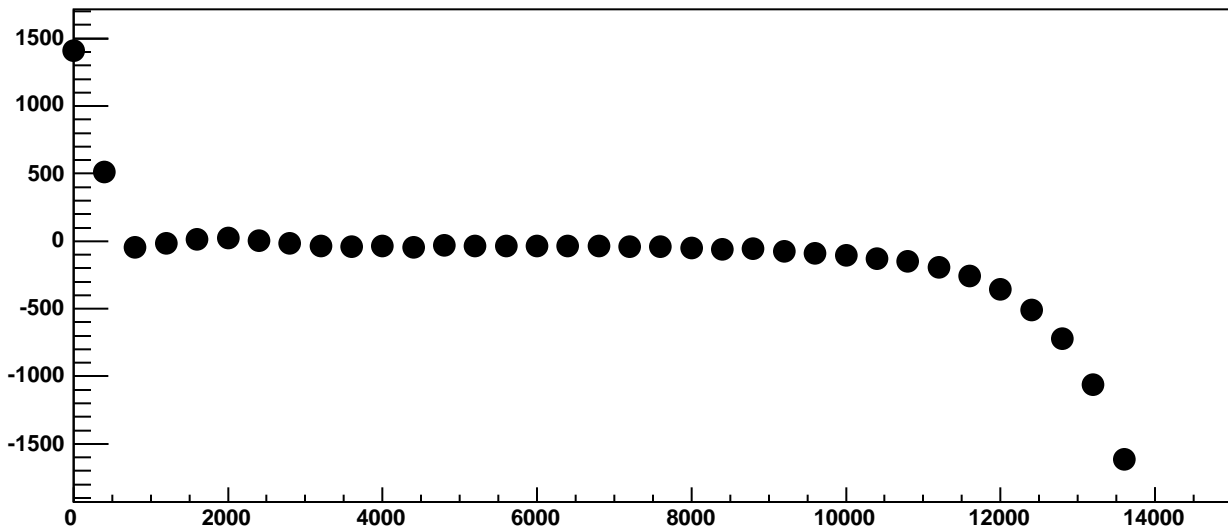
Chip 4, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC



Chip 4, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC

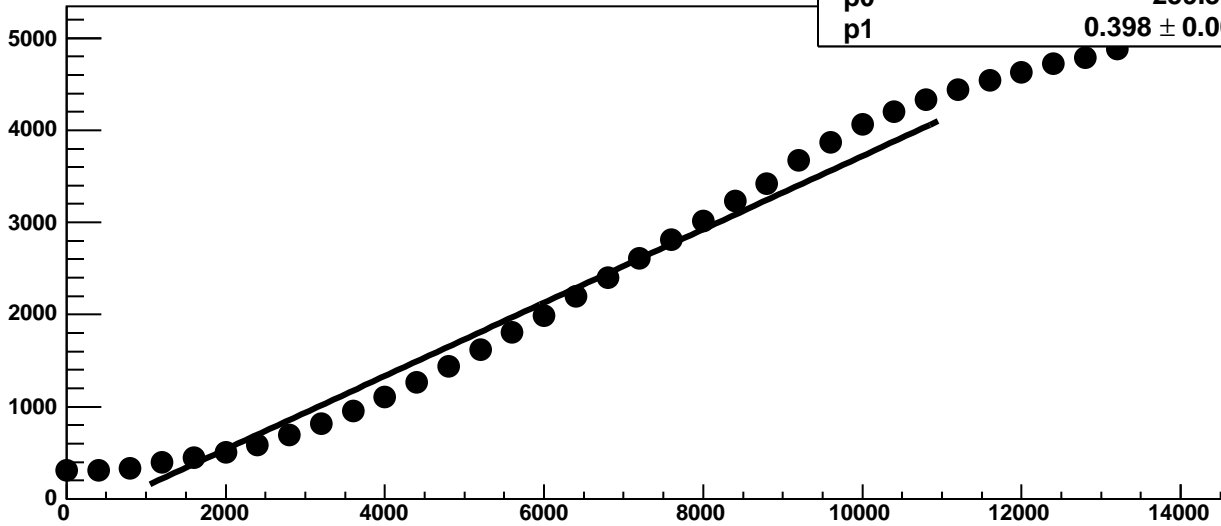


Chip 4, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC

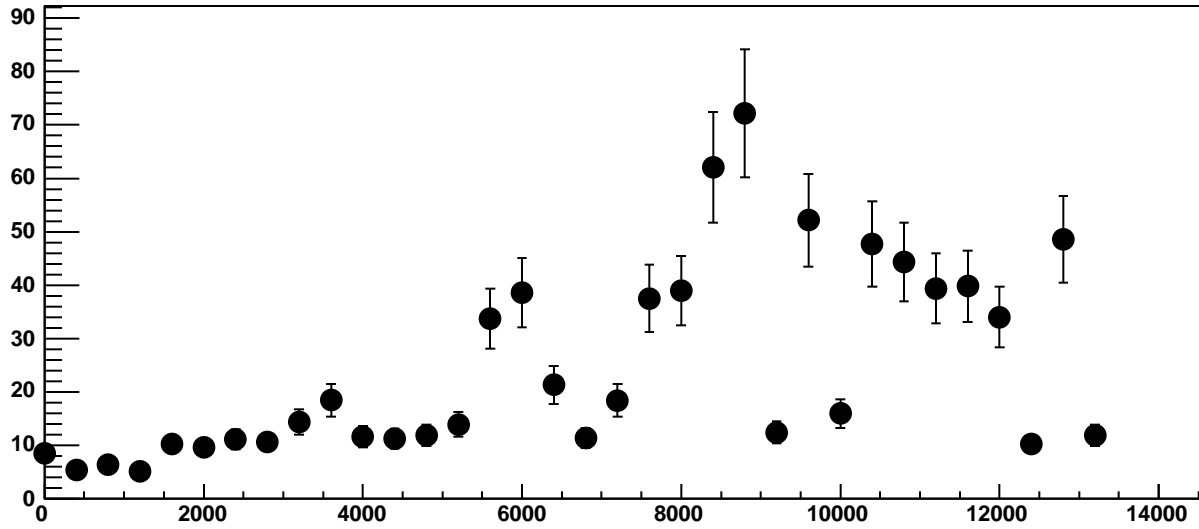


Chip 4, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

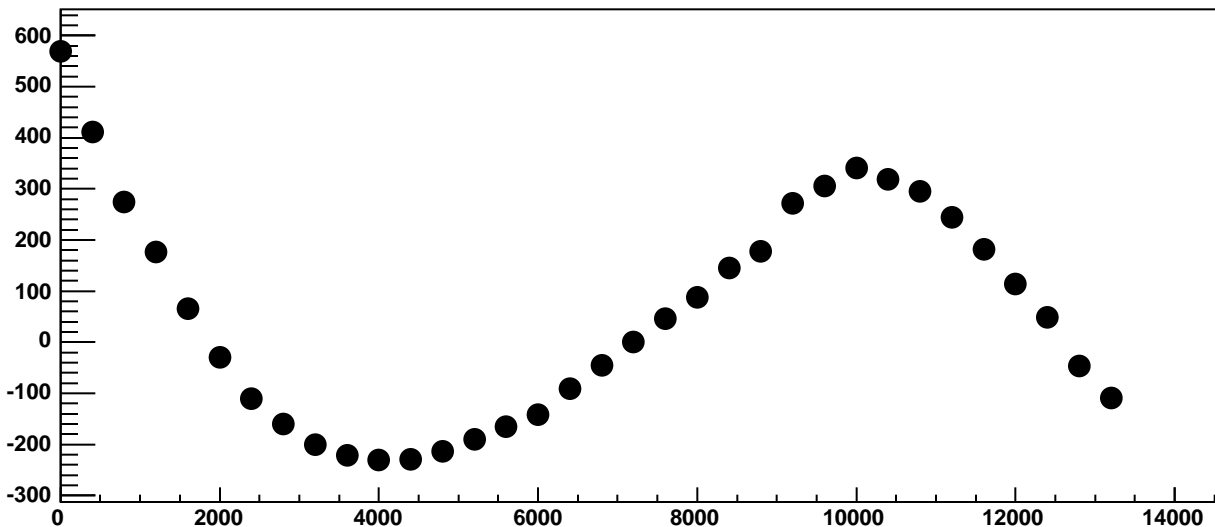
χ^2 / ndf 8.236e+04 / 23
p0 -259.3 ± 1.06
p1 0.398 ± 0.0002379



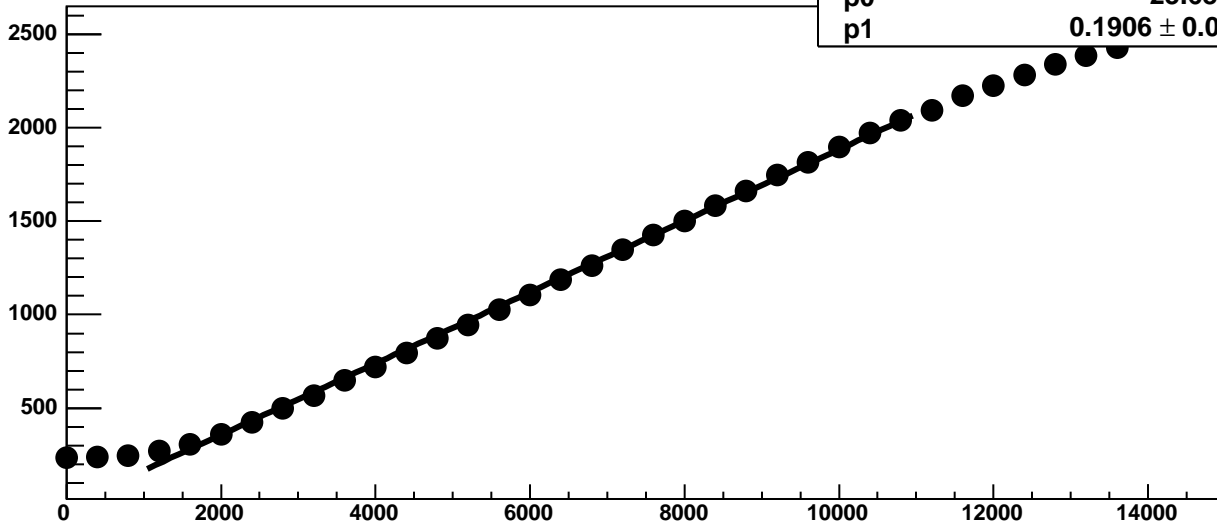
Chip 4, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



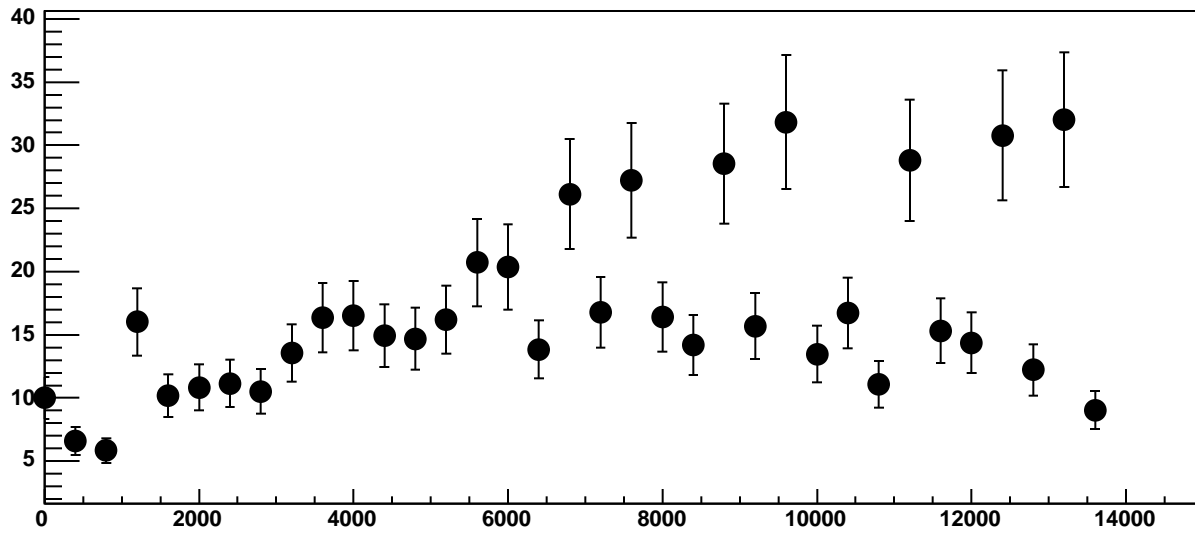
Chip 4, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



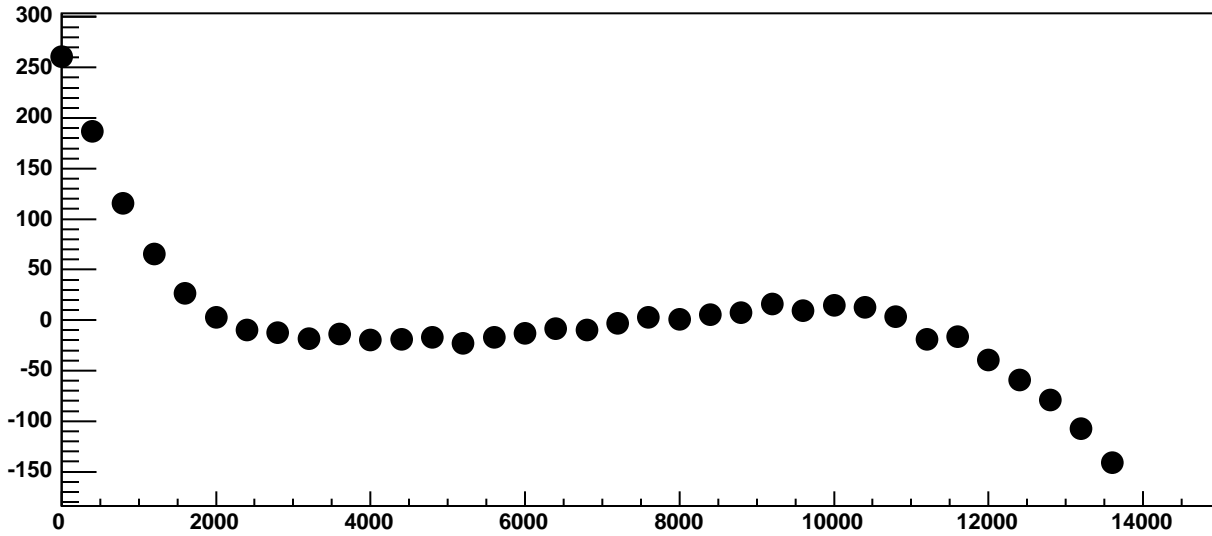
Chip 4, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



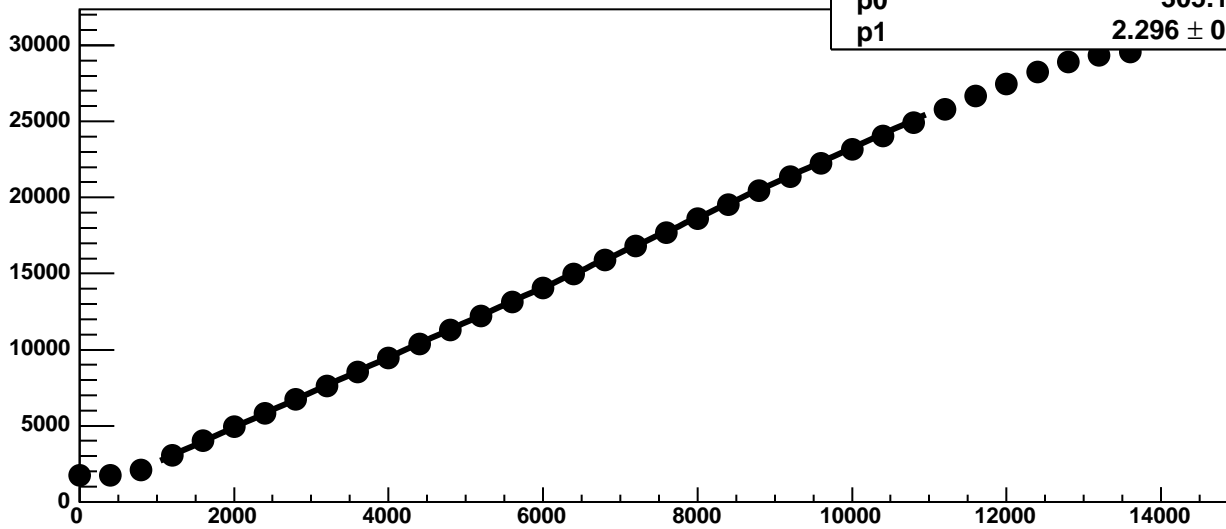
Chip 4, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



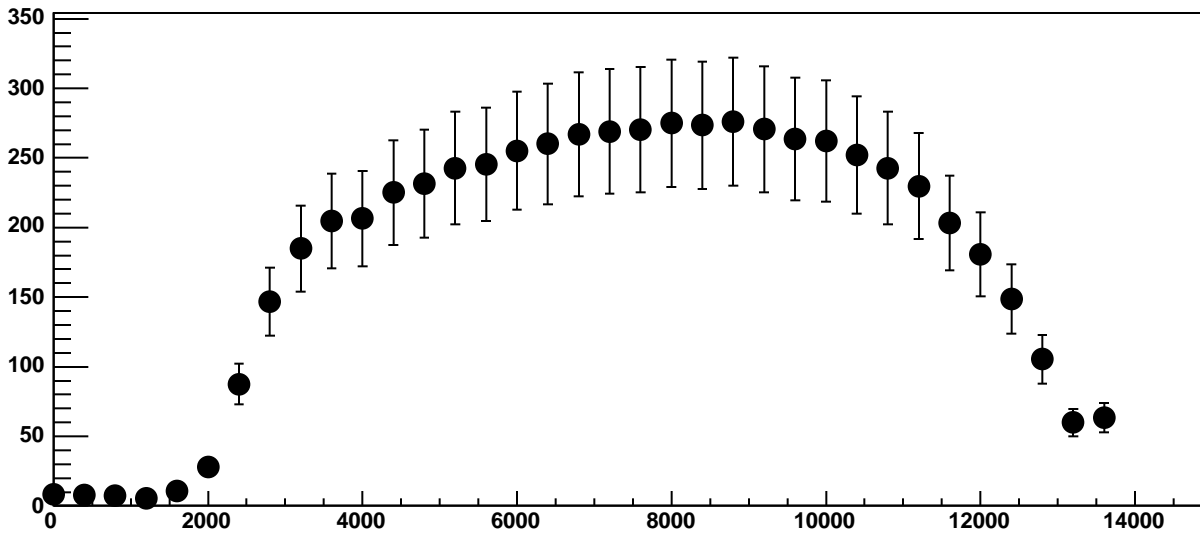
Chip 4, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



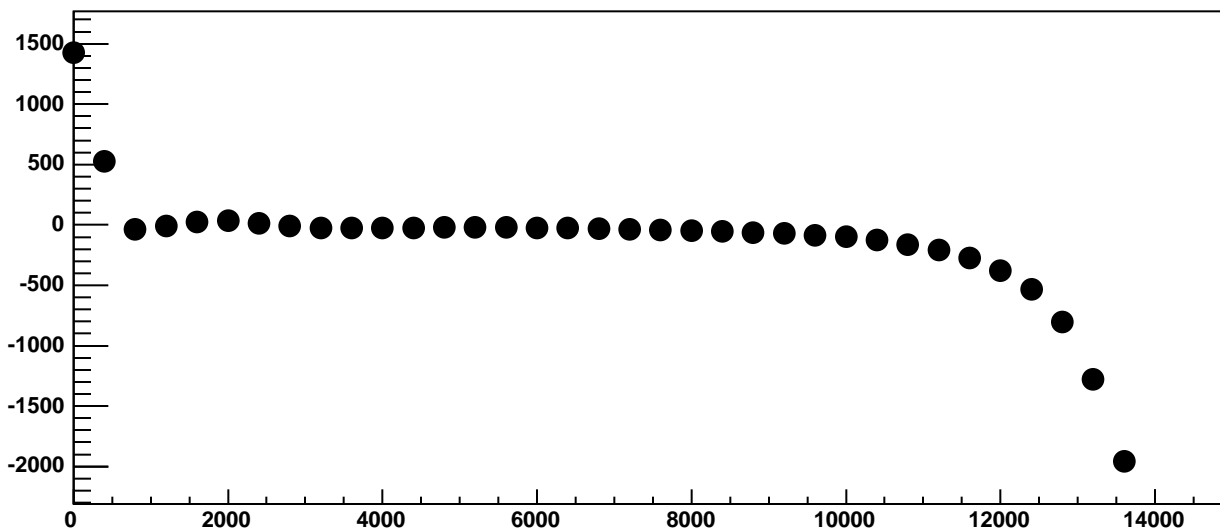
Chip 4, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC



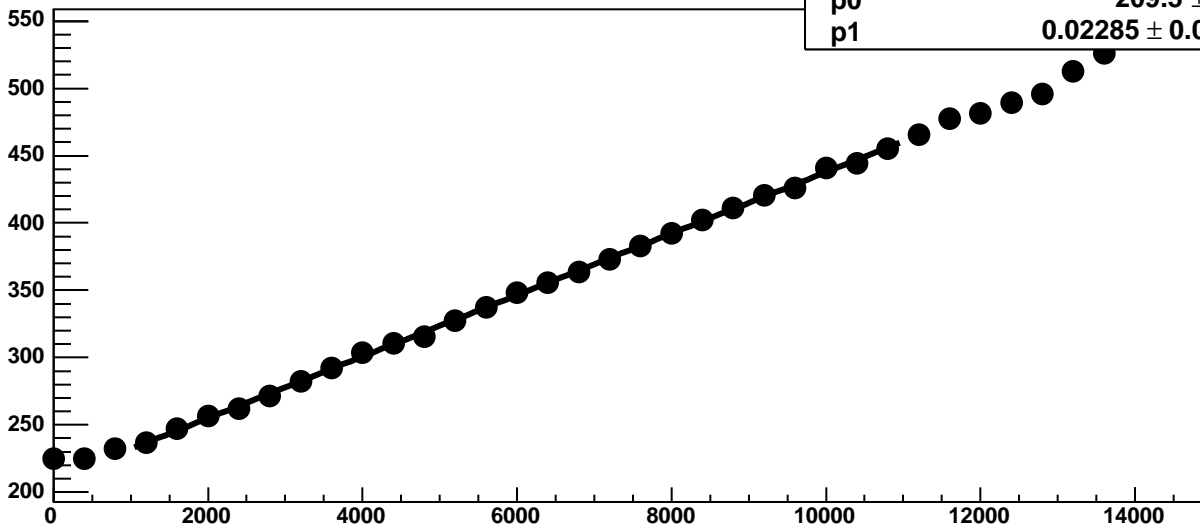
Chip 4, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC

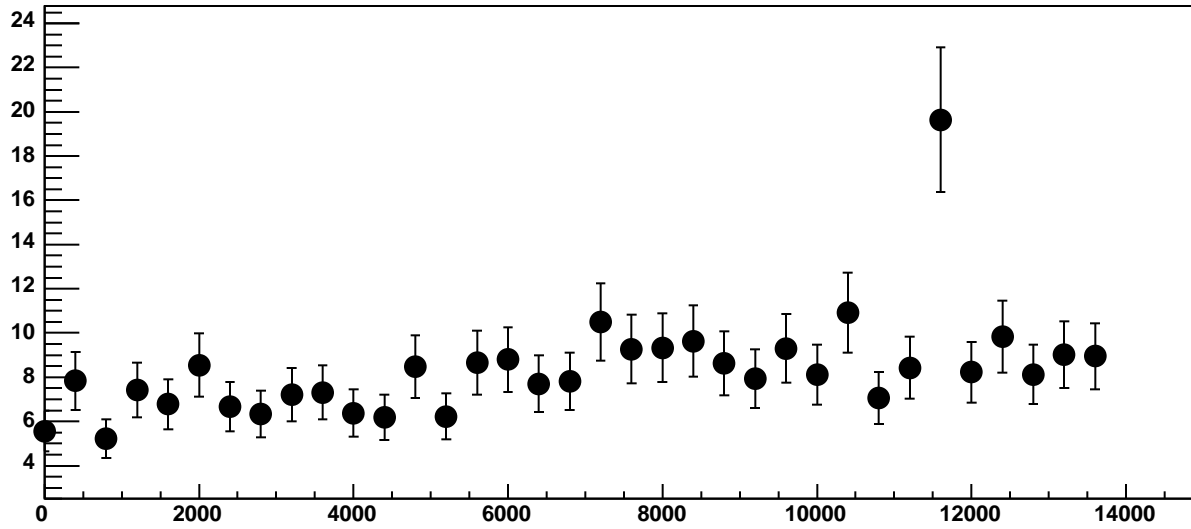


Chip 4, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

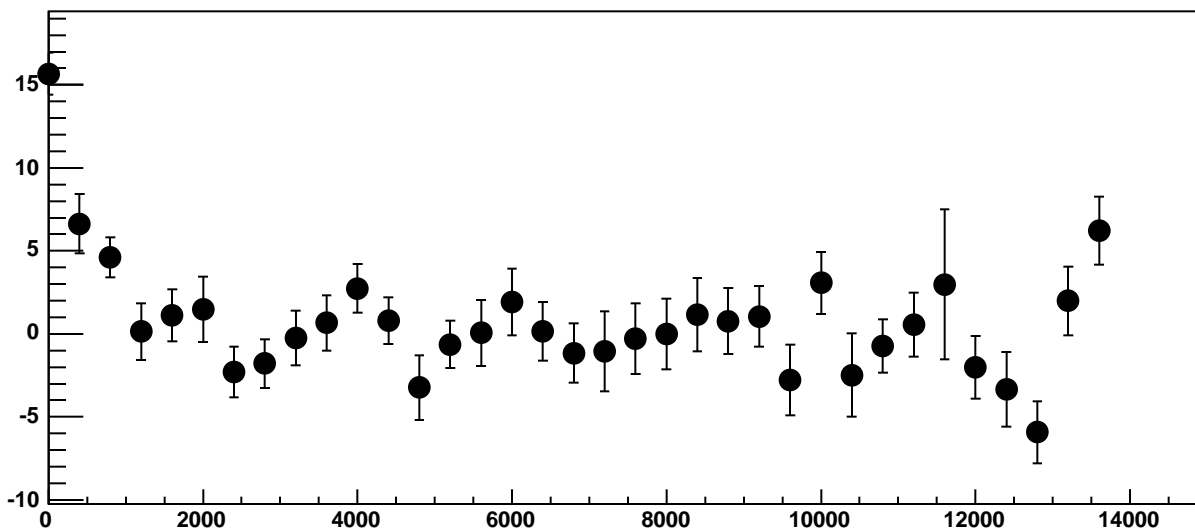


χ^2 / ndf 19.67 / 23
p0 209.3 ± 0.7702
p1 0.02285 ± 0.0001242

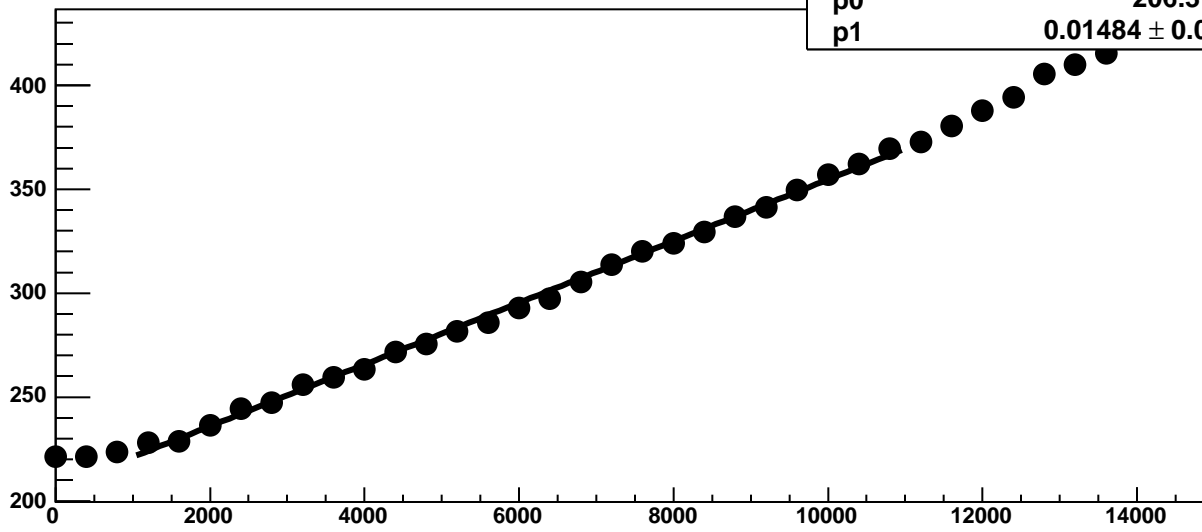
Chip 4, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

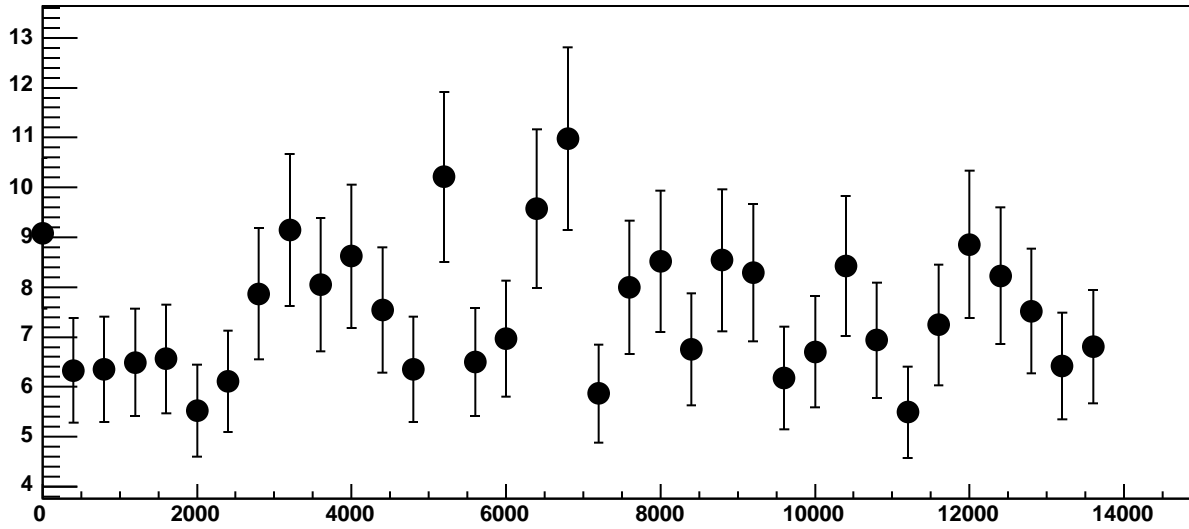


Chip 4, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

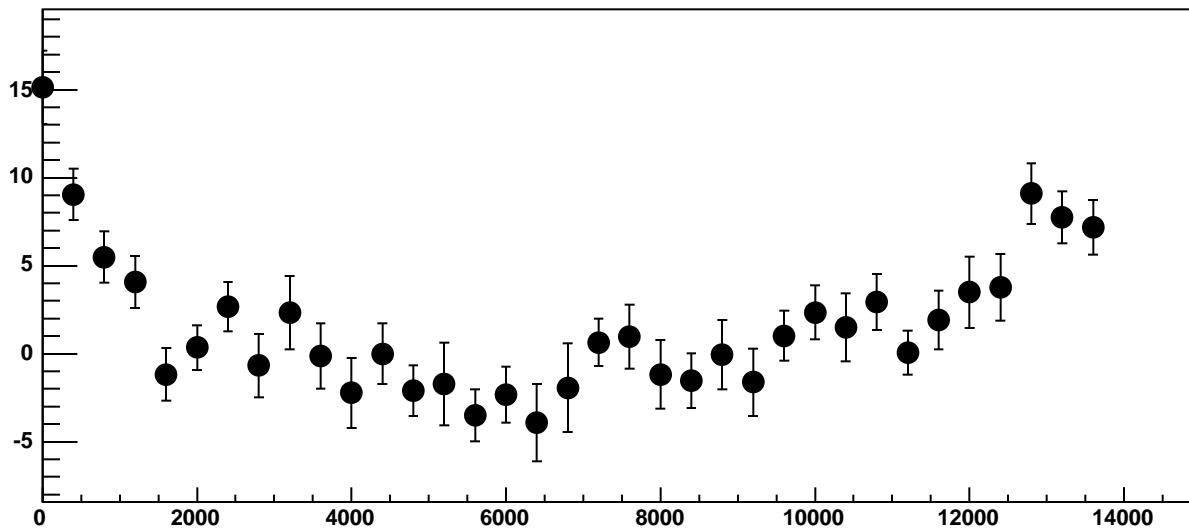


χ^2 / ndf 37.96 / 23
p0 206.3 ± 0.724
p1 0.01484 ± 0.0001104

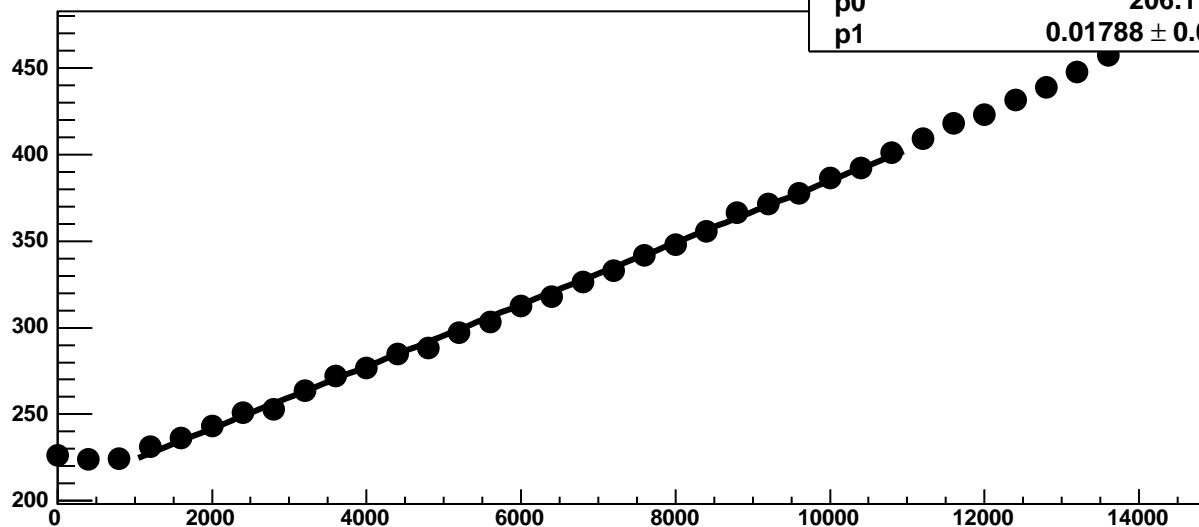
Chip 4, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

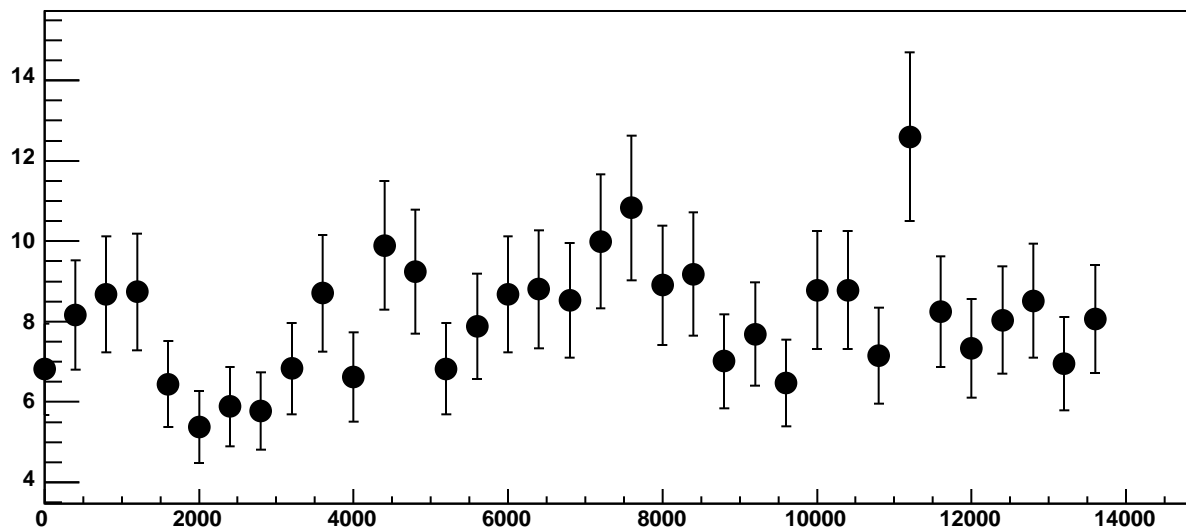


Chip 4, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

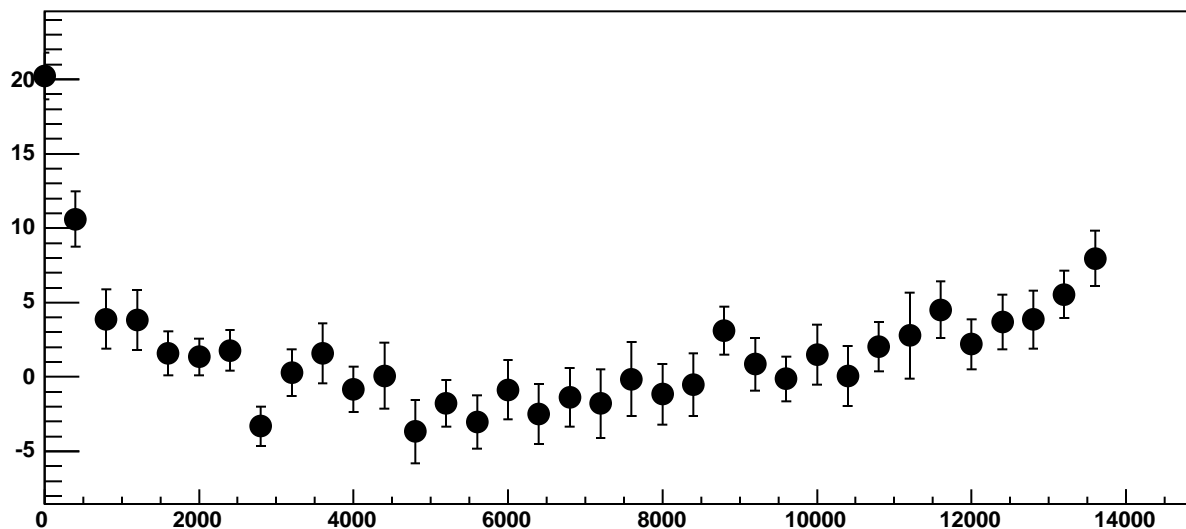


χ^2 / ndf 31.27 / 23
p0 206.1 ± 0.7221
p1 0.01788 ± 0.0001144

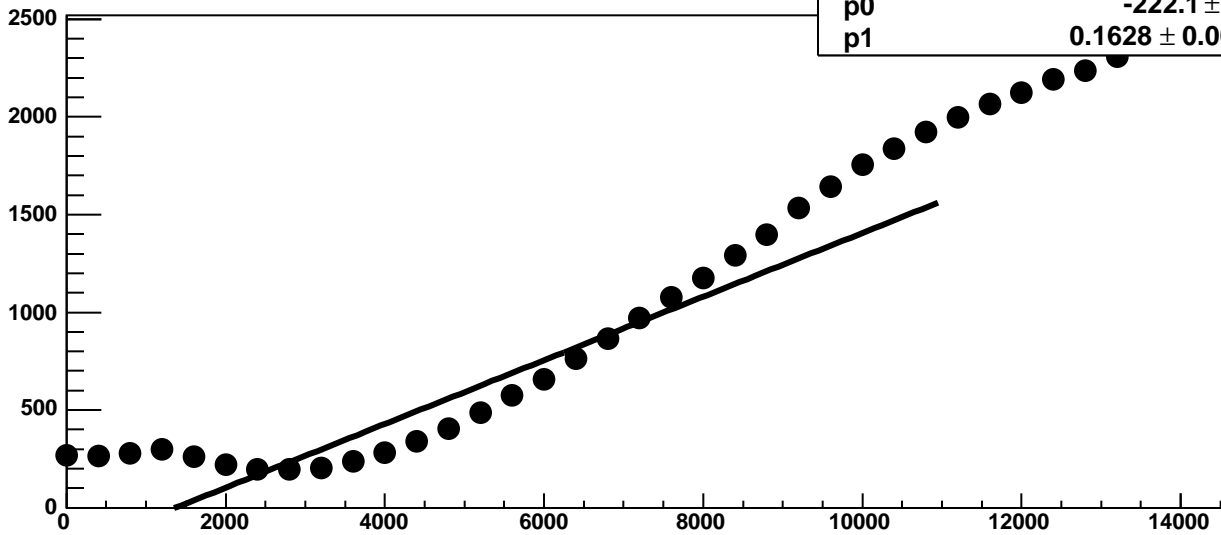
Chip 4, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



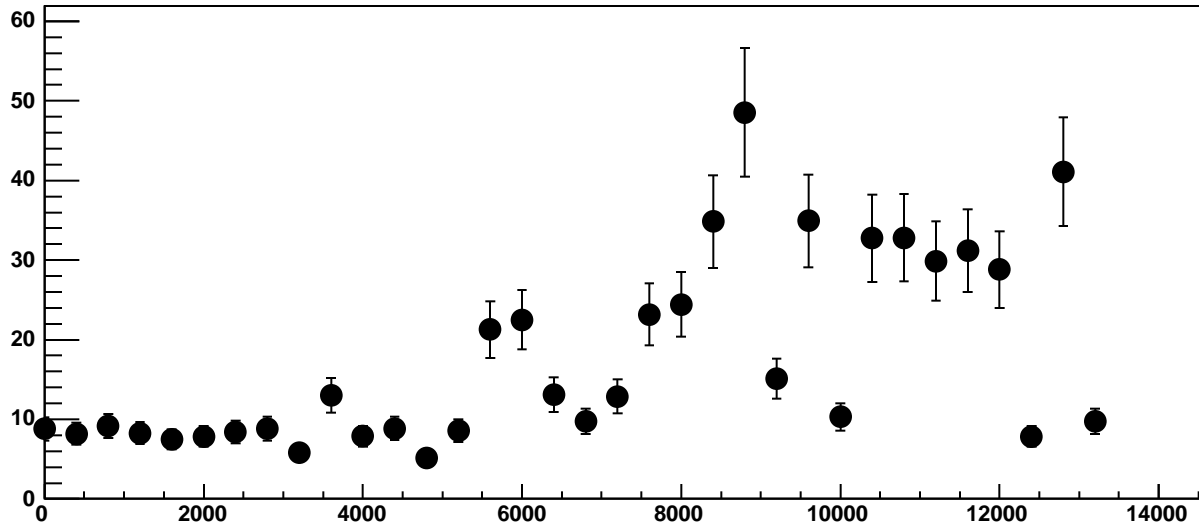
Chip 4, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



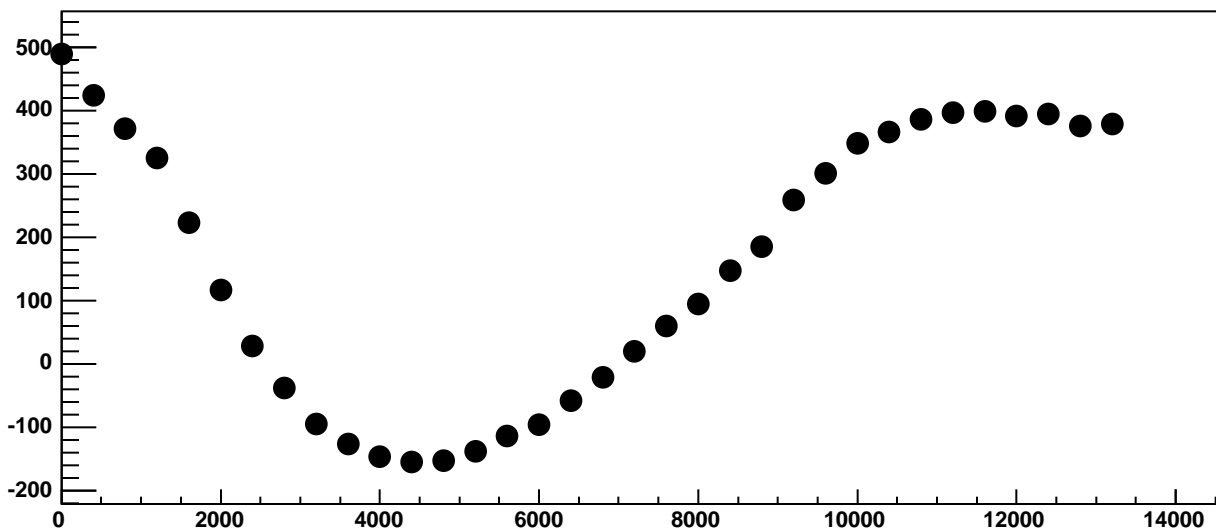
Chip 4, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



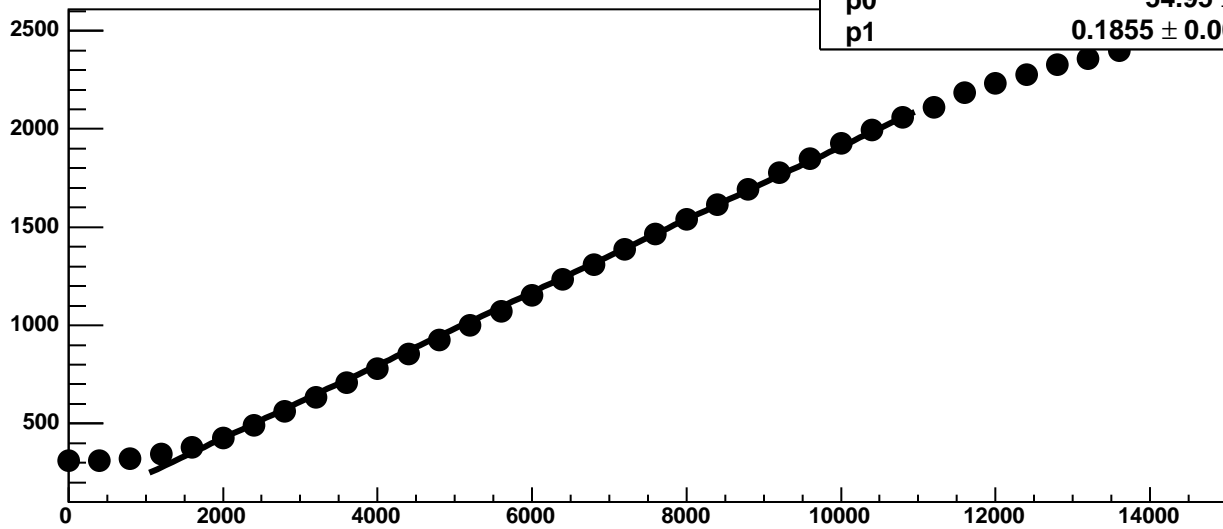
Chip 4, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

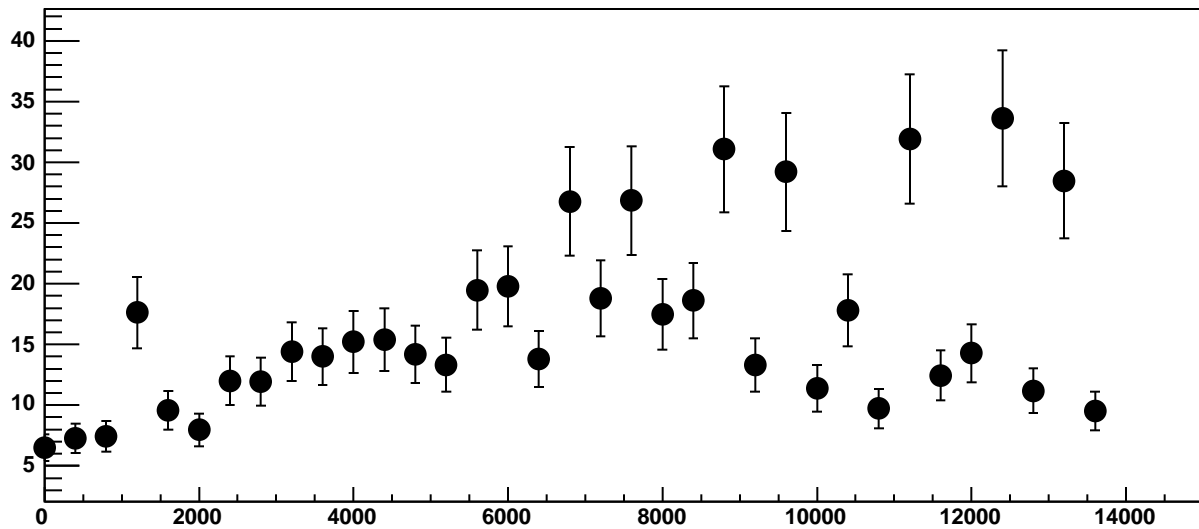


Chip 4, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

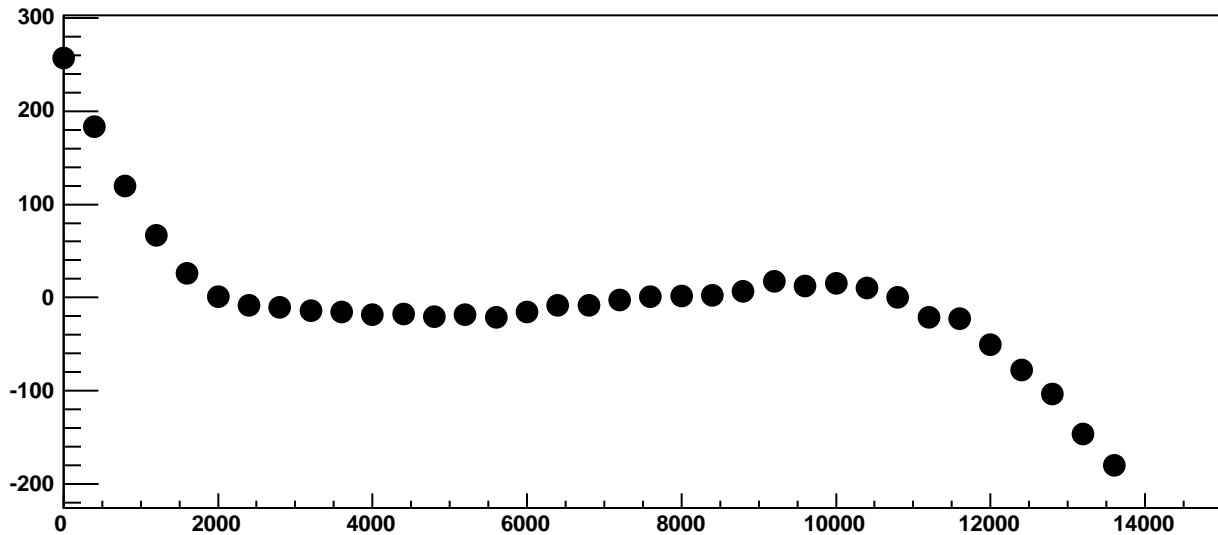


χ^2 / ndf 719.4 / 23
p0 54.95 ± 1.254
p1 0.1855 ± 0.0002013

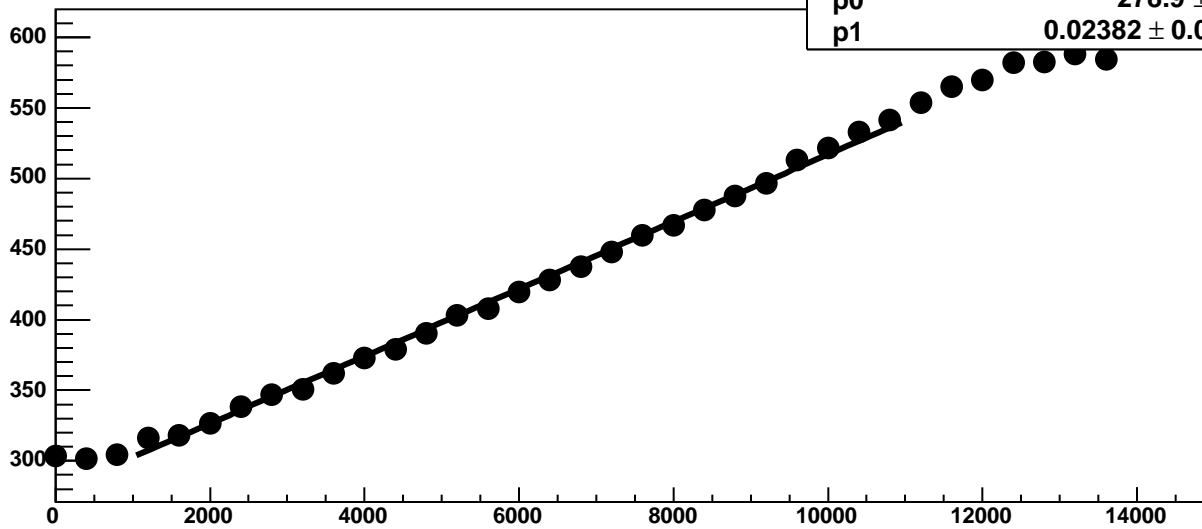
Chip 4, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

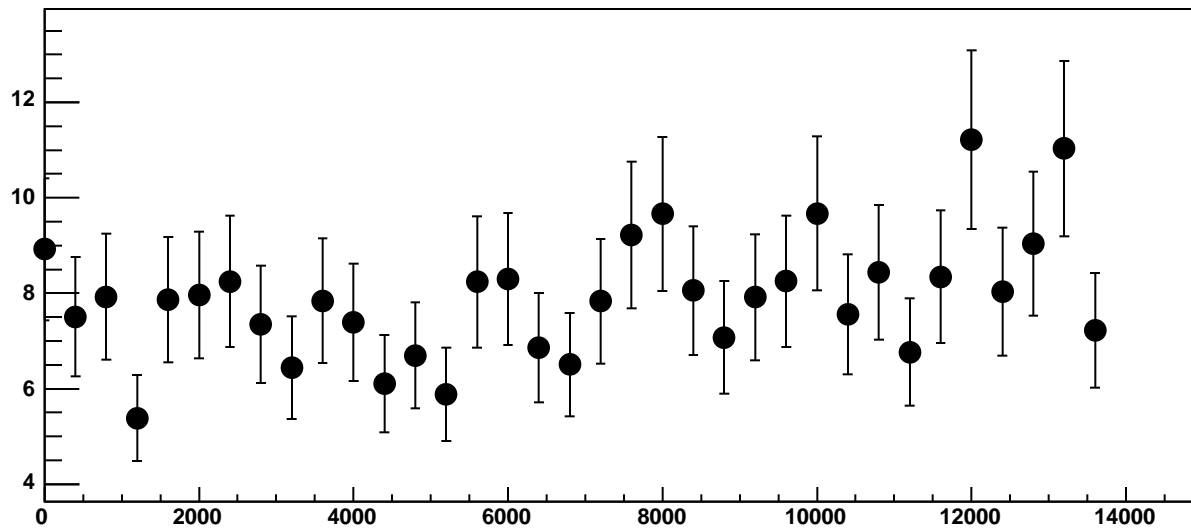


Chip 4, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

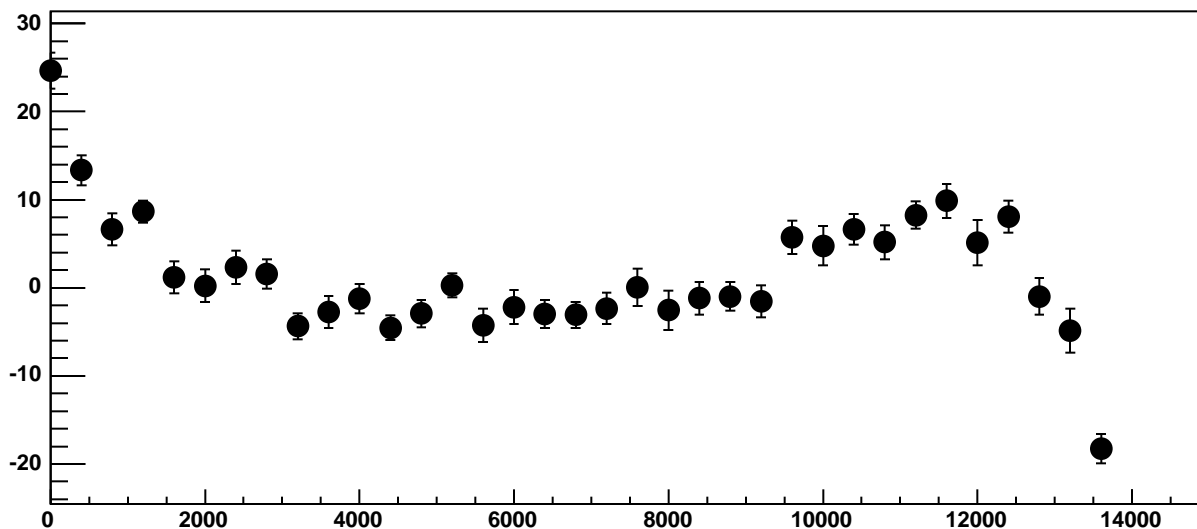


χ^2 / ndf 132.1 / 23
p0 278.9 ± 0.7498
p1 0.02382 ± 0.0001194

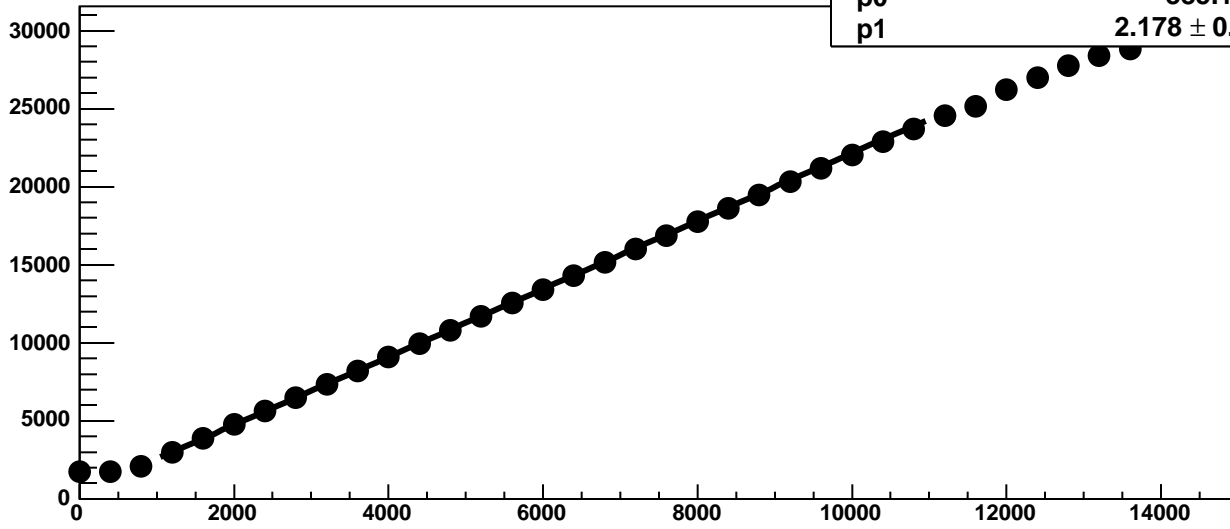
Chip 4, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

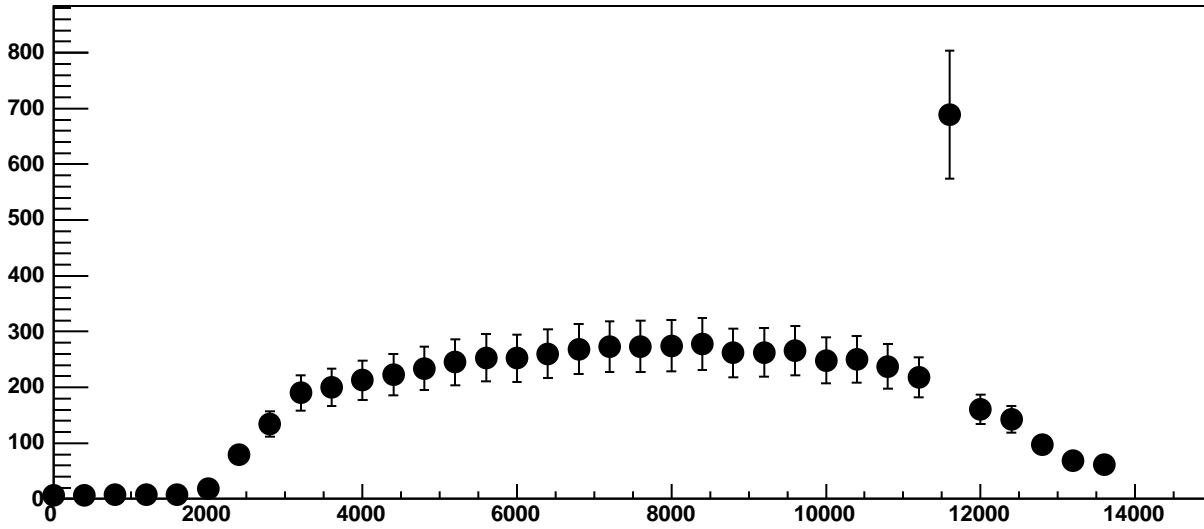


Chip 4, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC

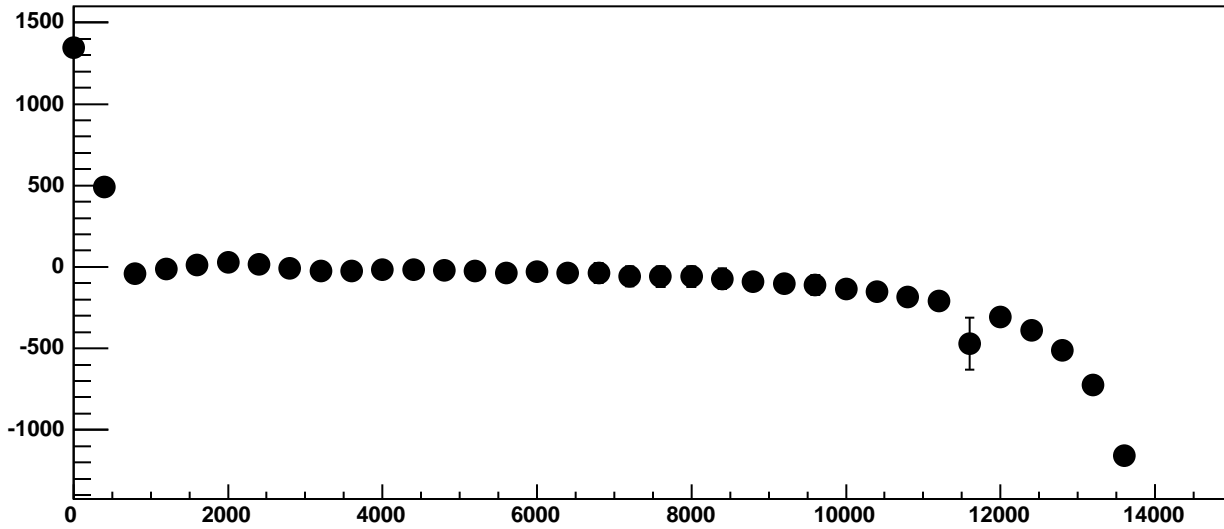


χ^2 / ndf 198.9 / 23
p0 385.1 ± 3.141
p1 2.178 ± 0.001961

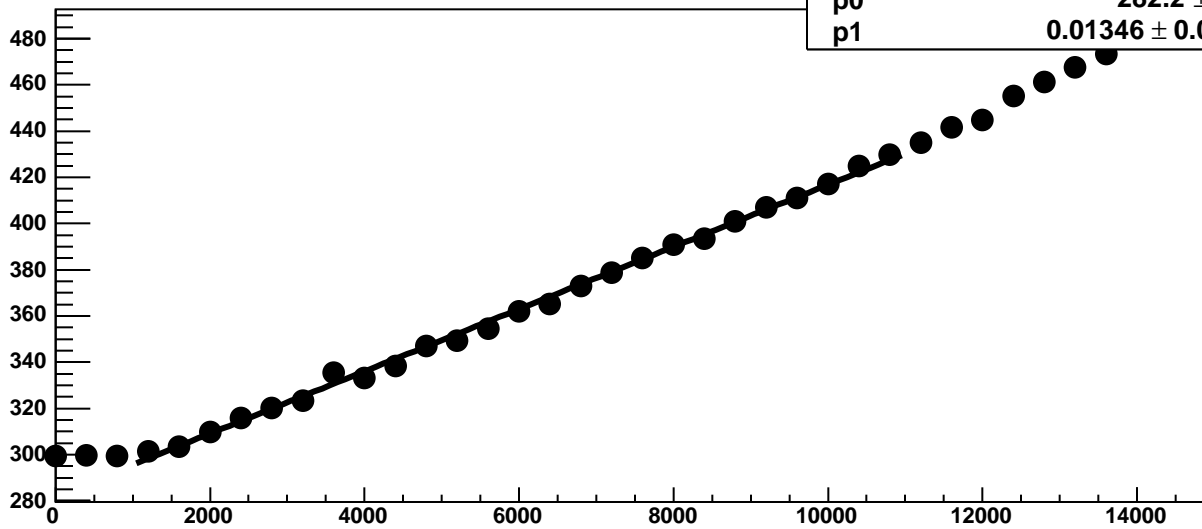
Chip 4, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

36.05 / 23

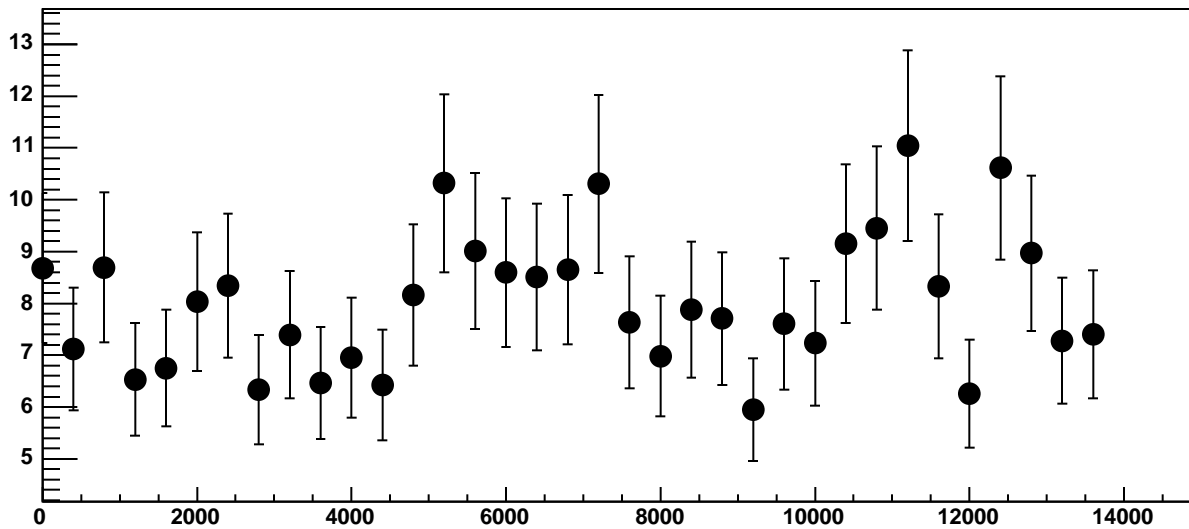
p0

282.2 ± 0.7648

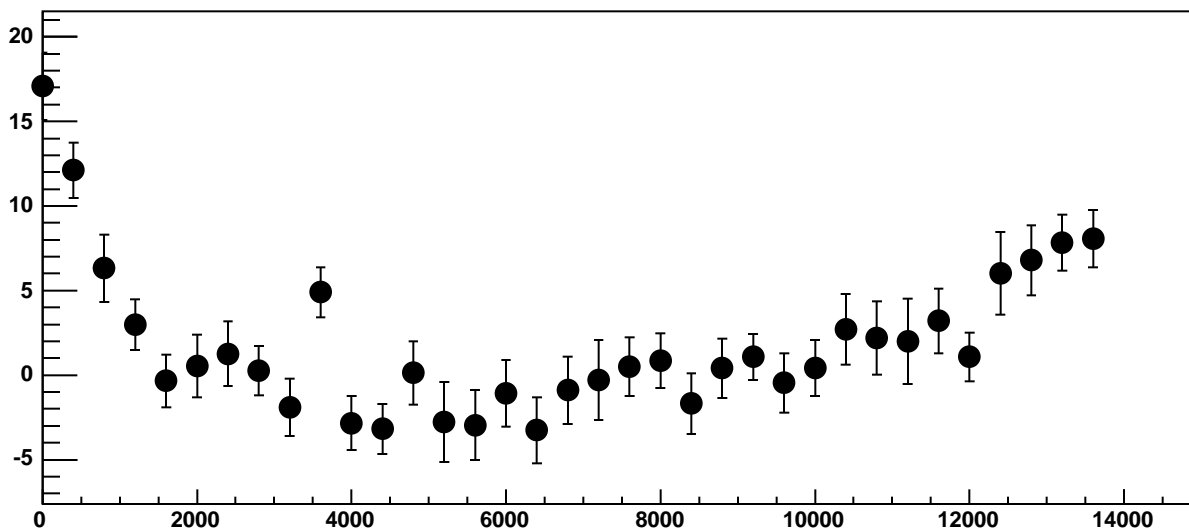
p1

0.01346 ± 0.0001182

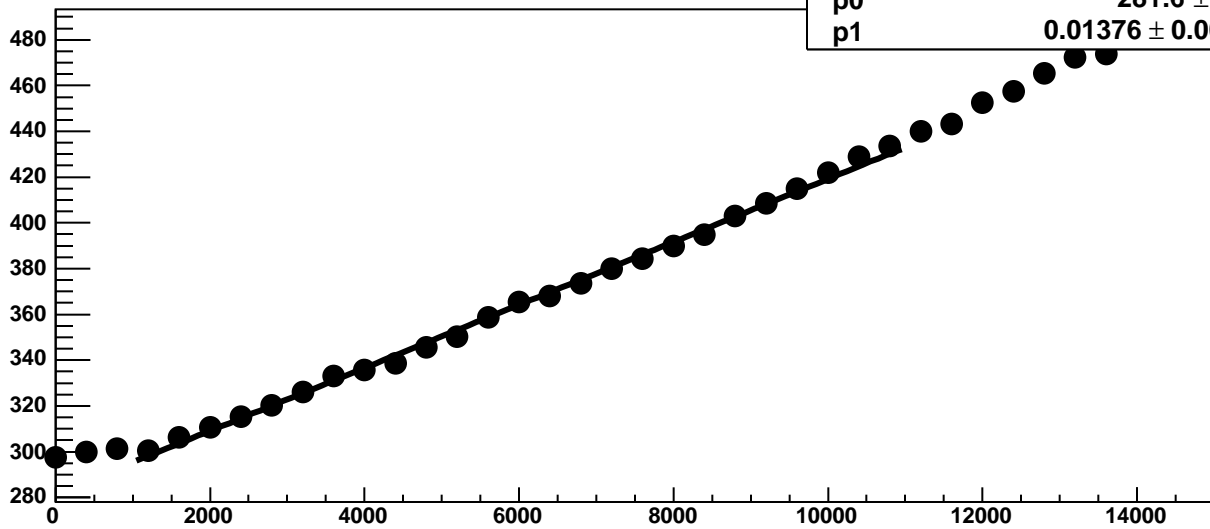
Chip 4, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

34.83 / 23

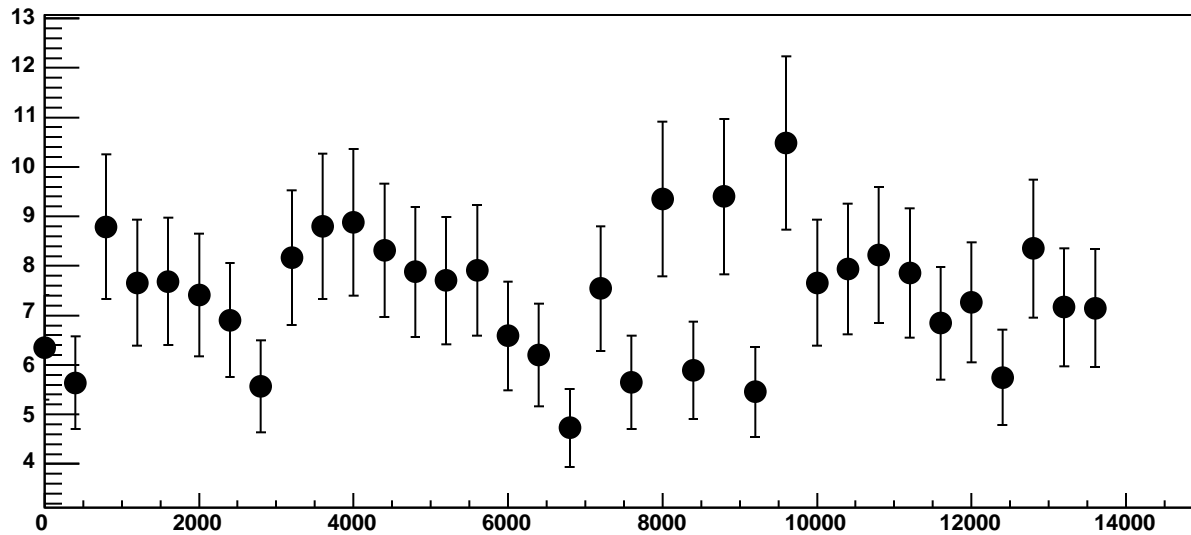
p0

281.6 ± 0.7864

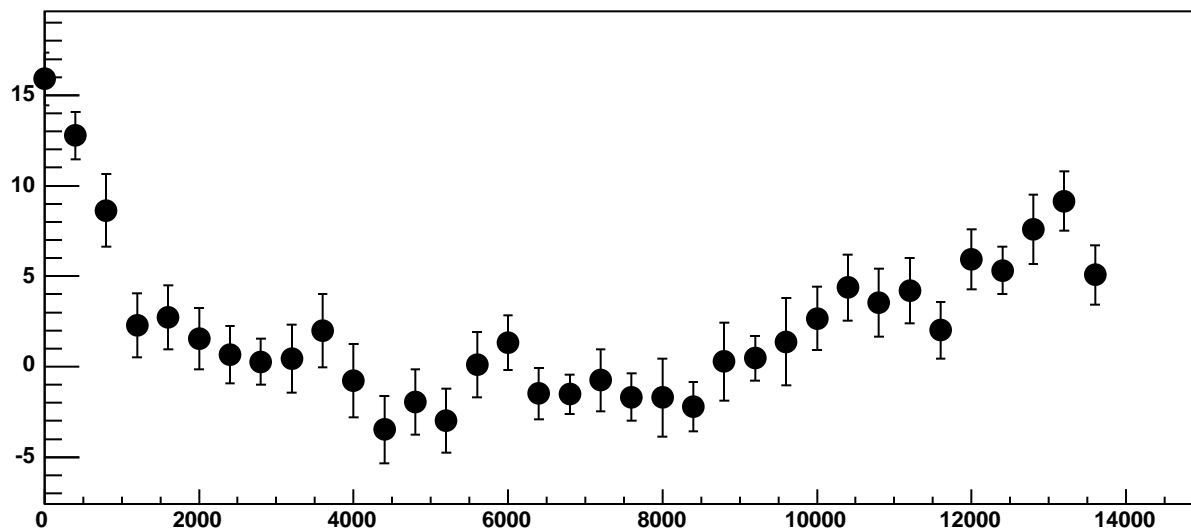
p1

0.01376 ± 0.0001184

Chip 4, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

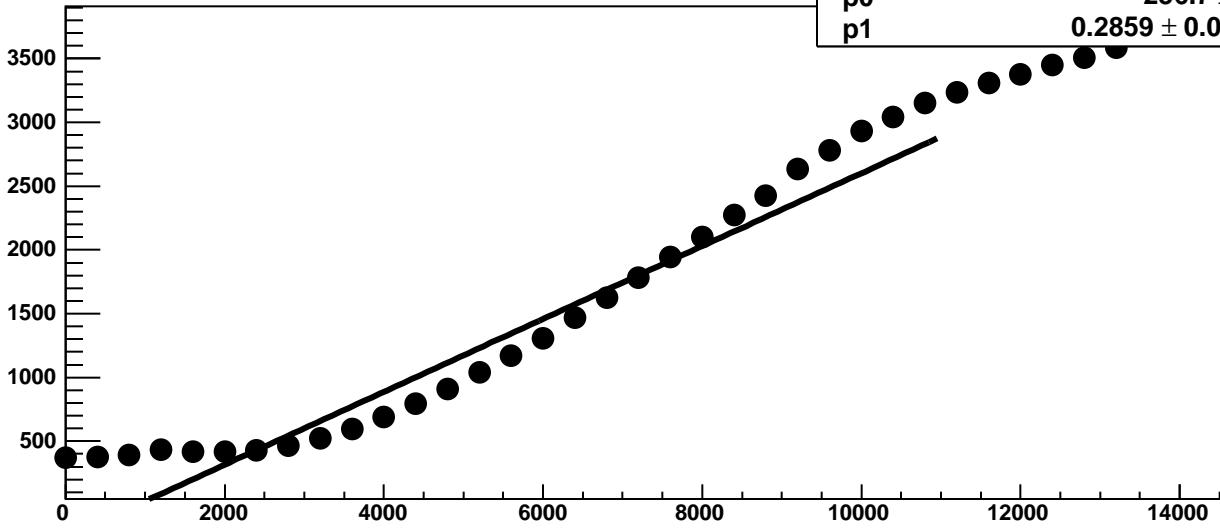


Chip 4, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

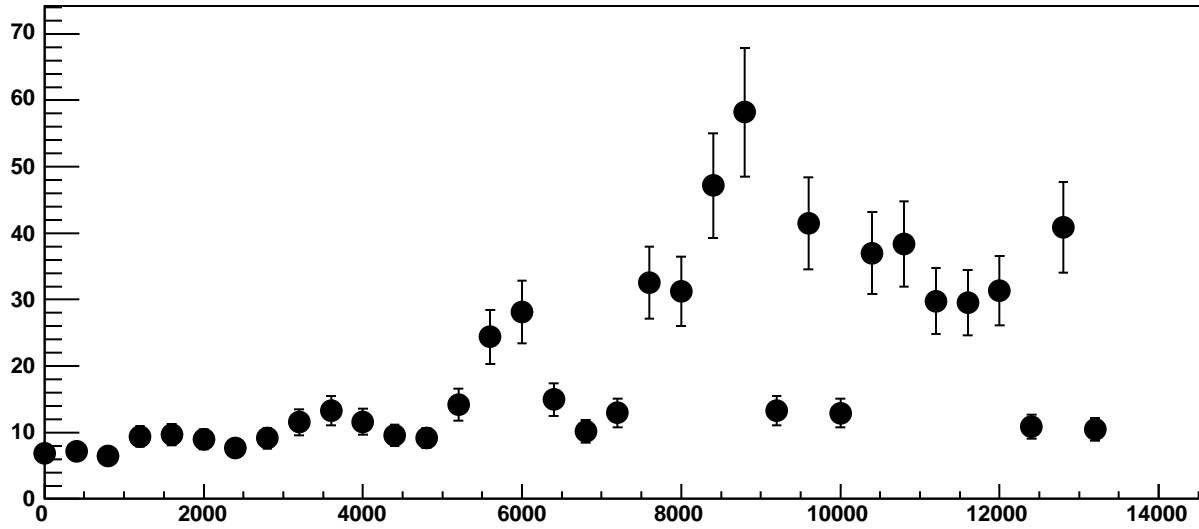


Chip 4, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC

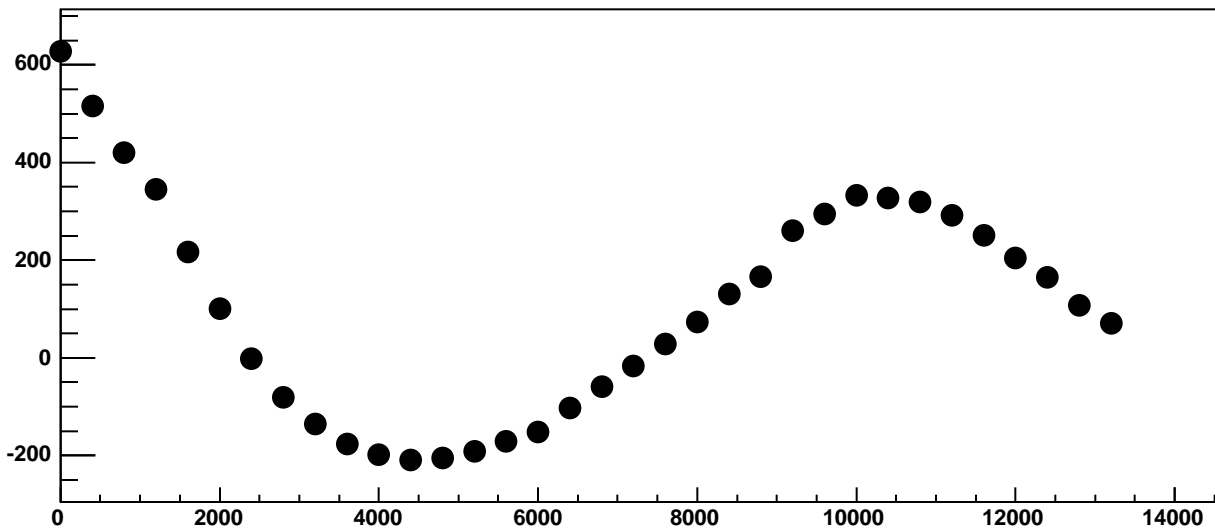
χ^2 / ndf 9.964e+04 / 23
p0 -256.7 ± 1.169
p1 0.2859 ± 0.0002371



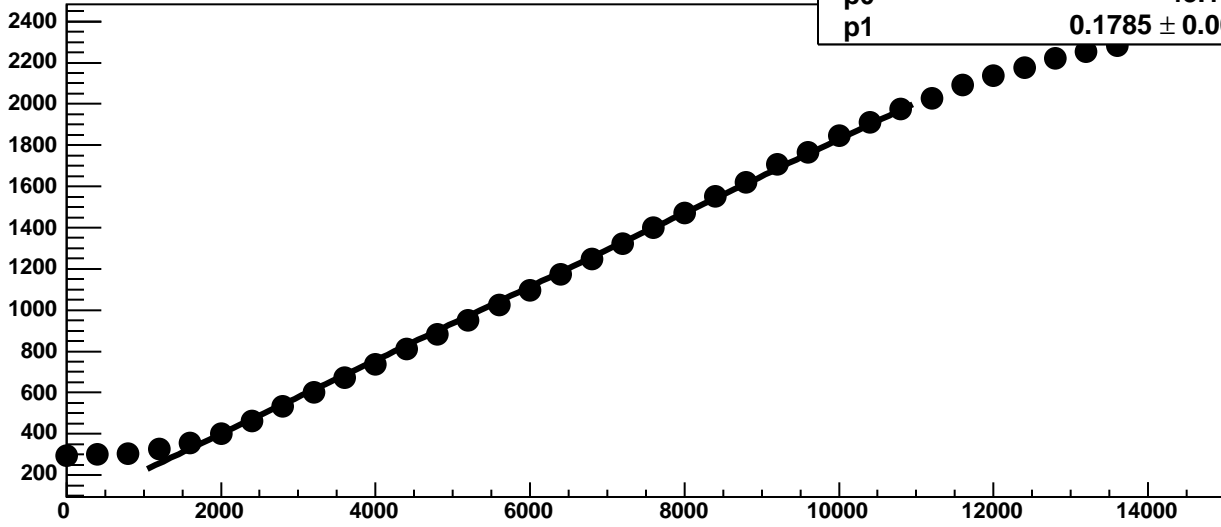
Chip 4, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC

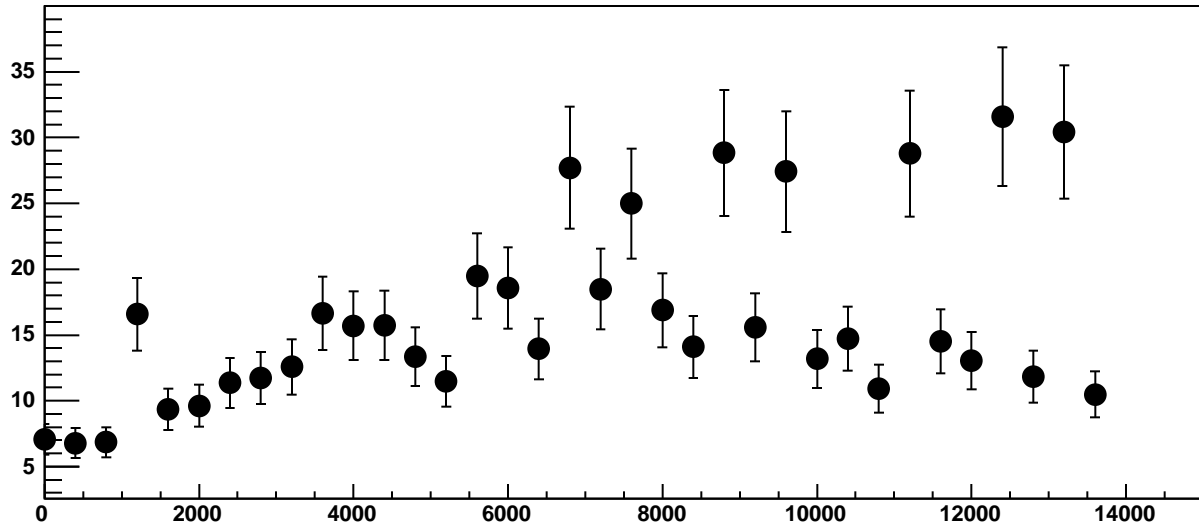


Chip 4, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

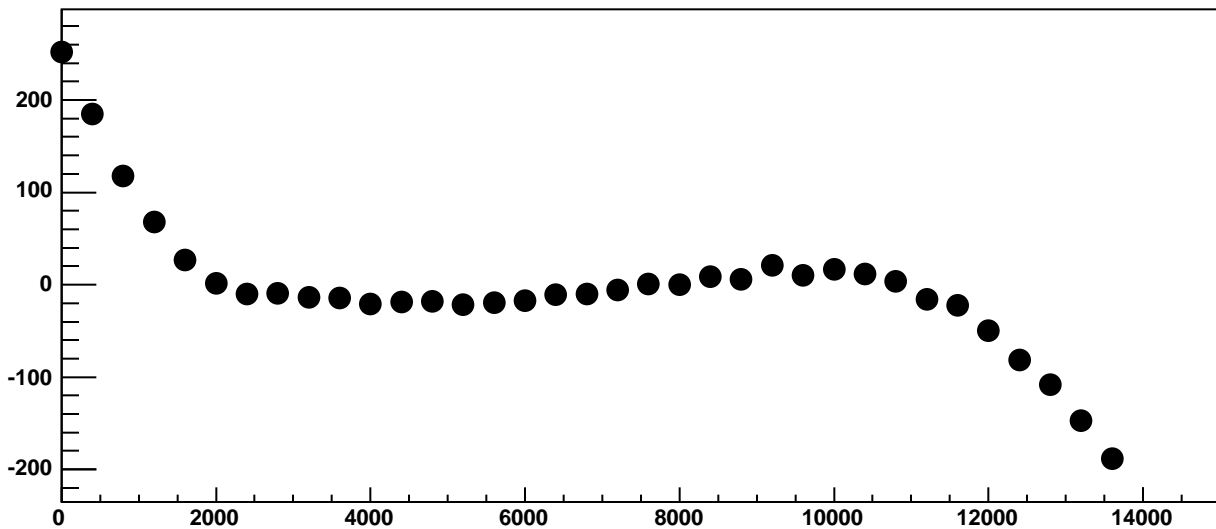


χ^2 / ndf 834.8 / 23
p0 43.1 ± 1.291
p1 0.1785 ± 0.0002093

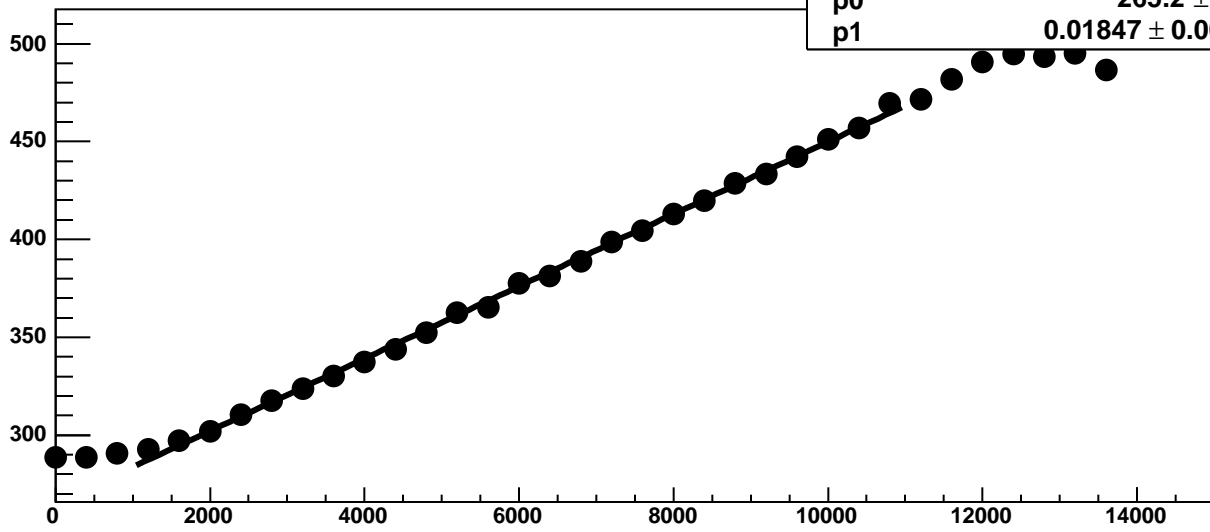
Chip 4, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



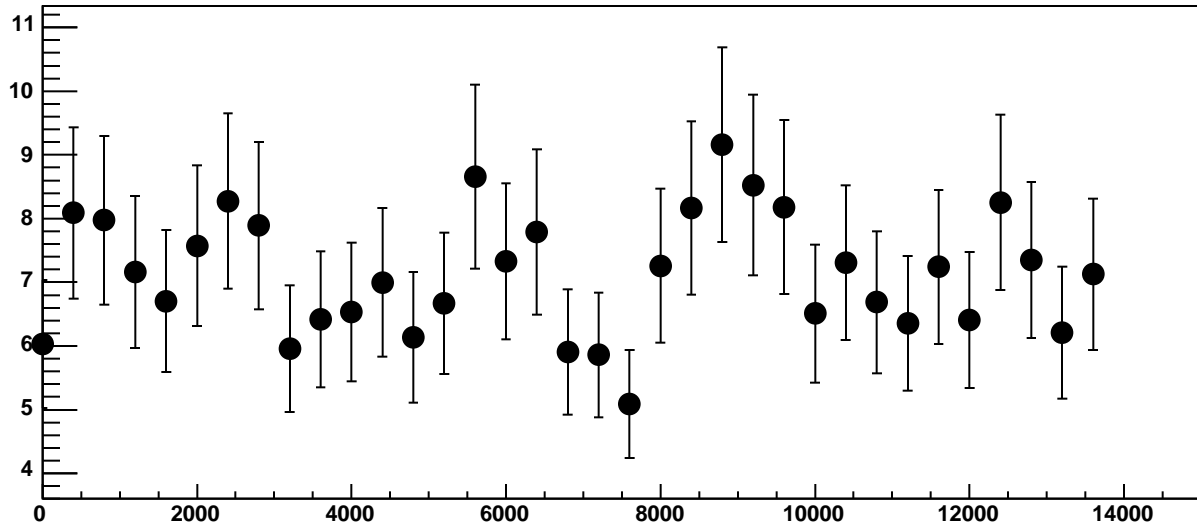
Chip 4, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



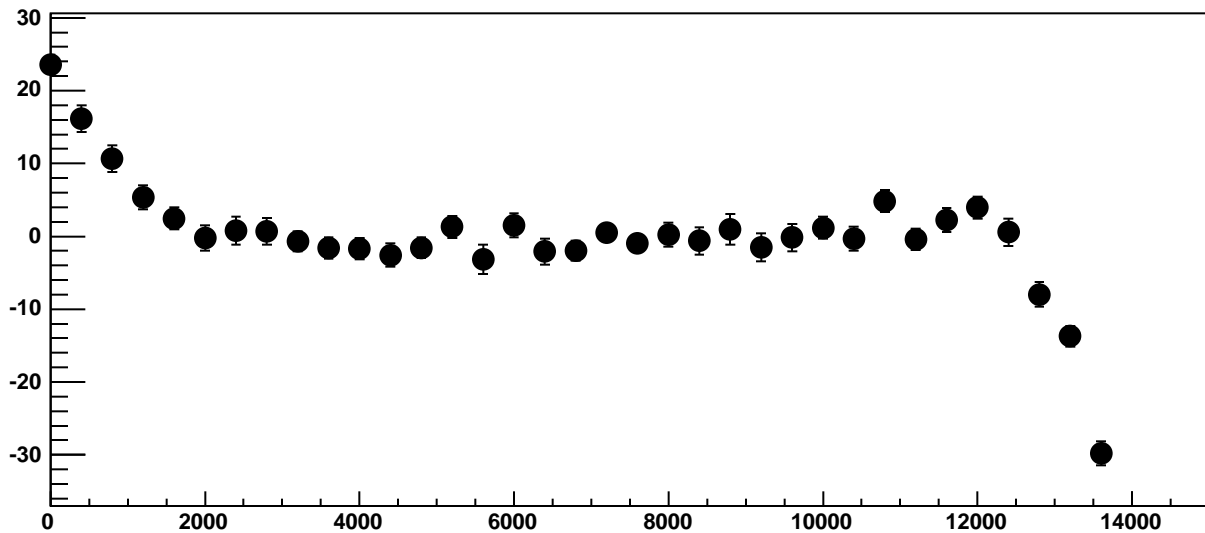
Chip 4, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



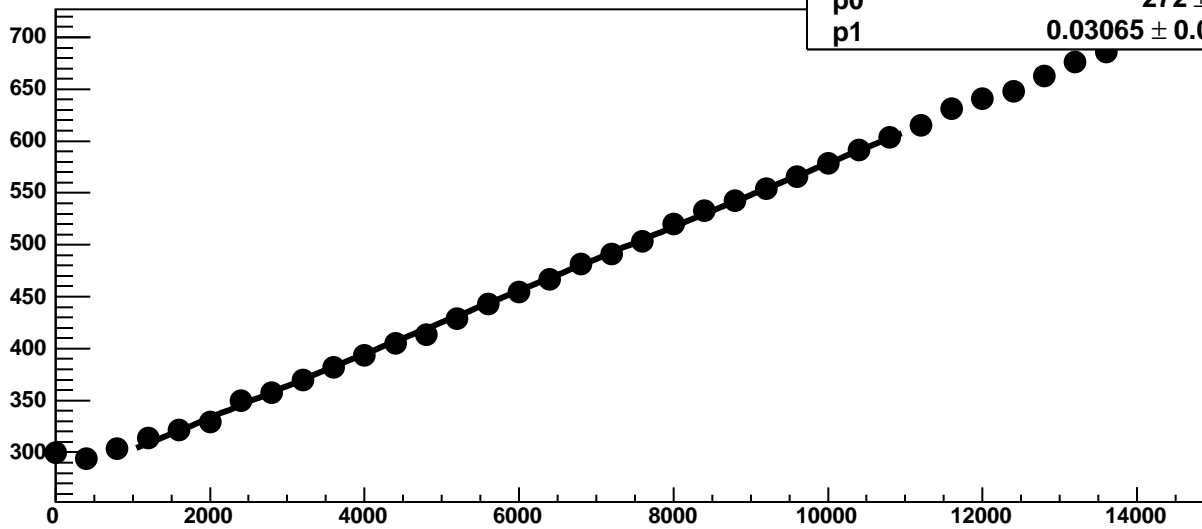
Chip 4, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

58.93 / 23

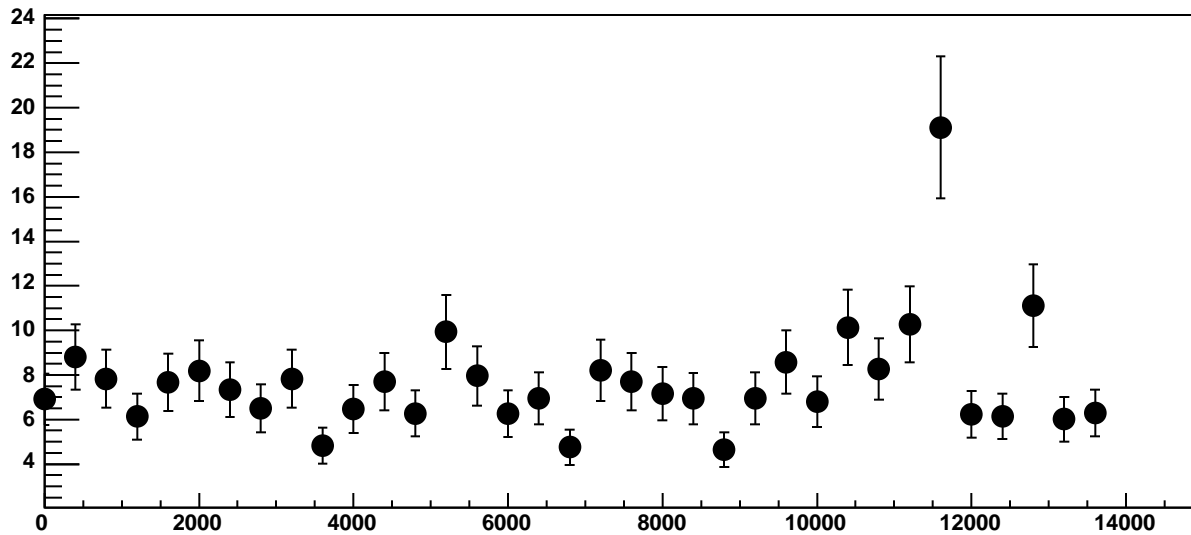
p0

272 ± 0.7356

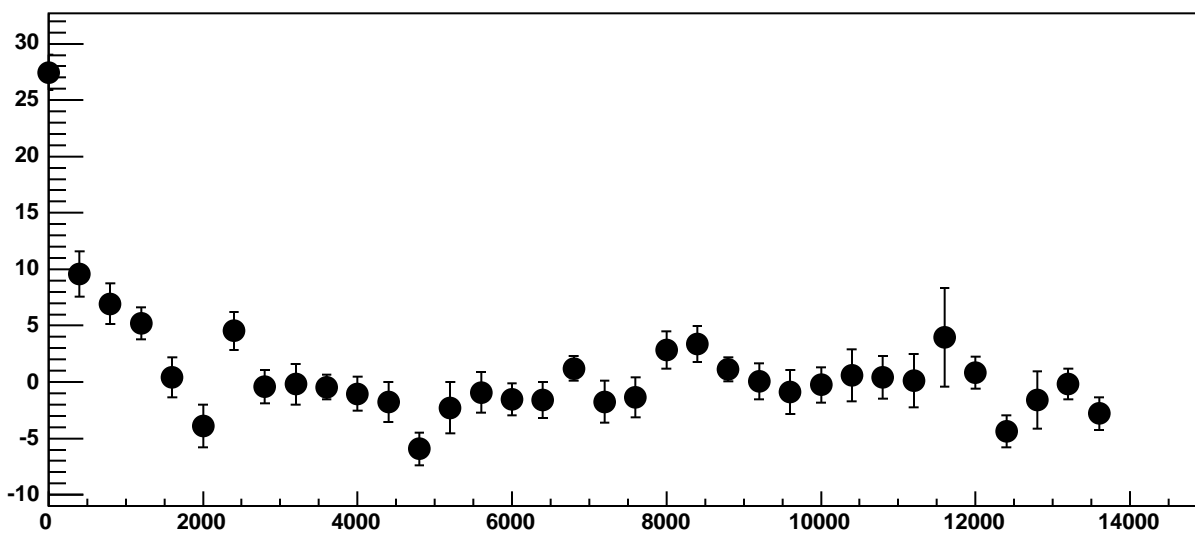
p1

0.03065 ± 0.0001129

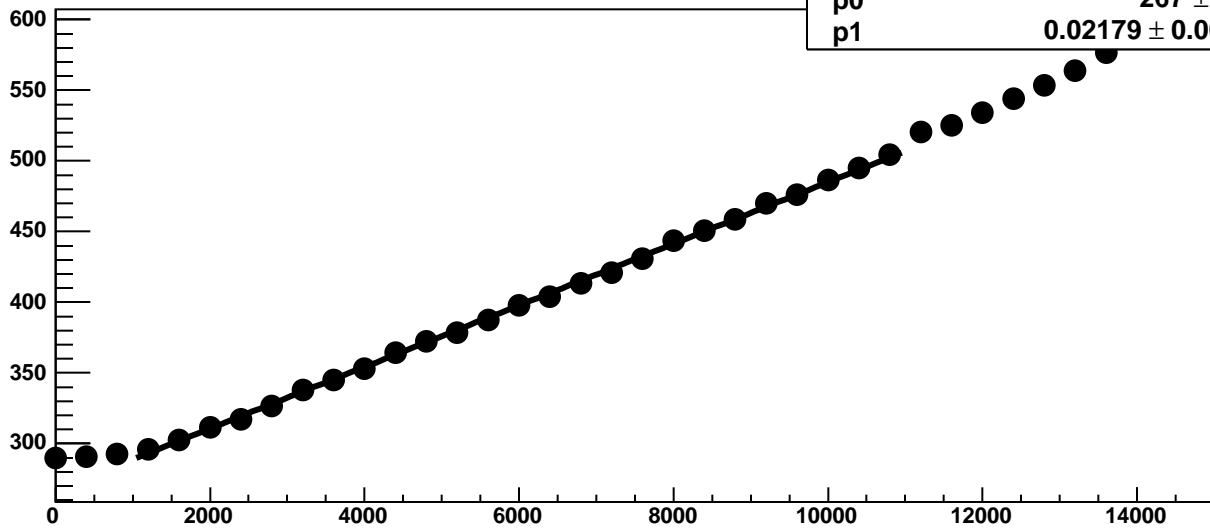
Chip 4, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

15.88 / 23

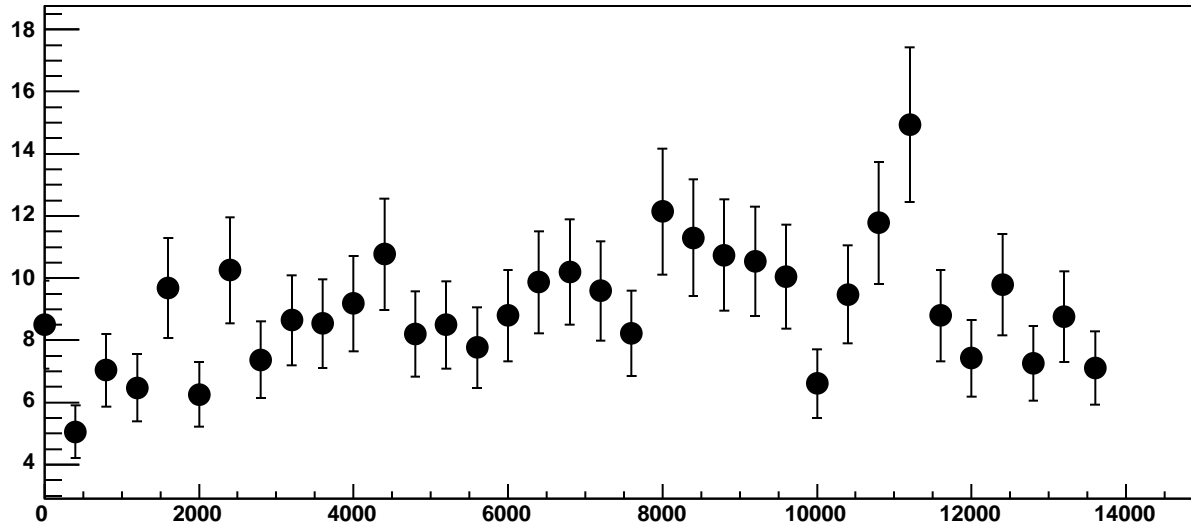
p0

267 ± 0.8507

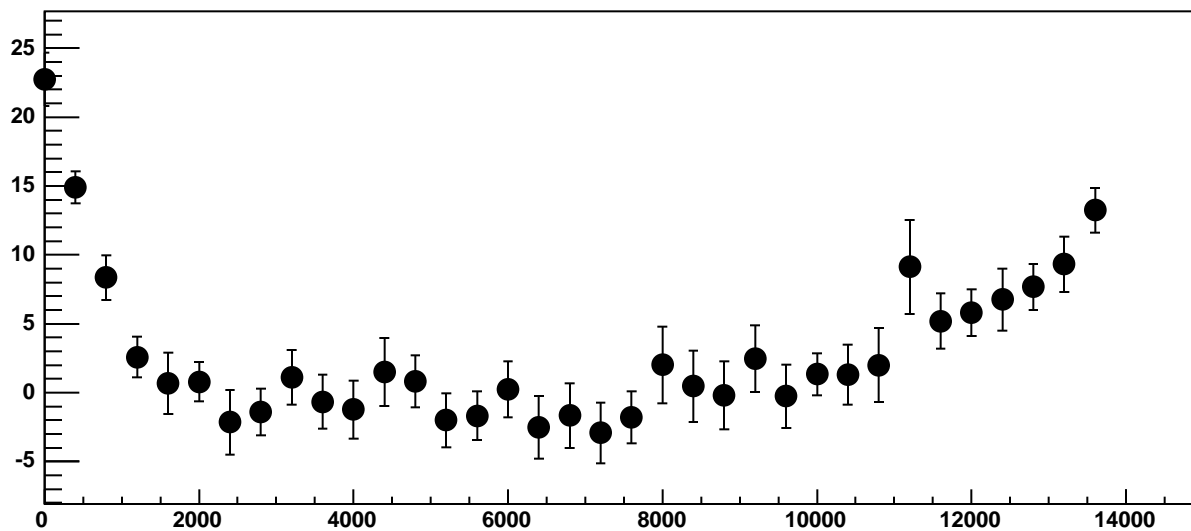
p1

0.02179 ± 0.0001357

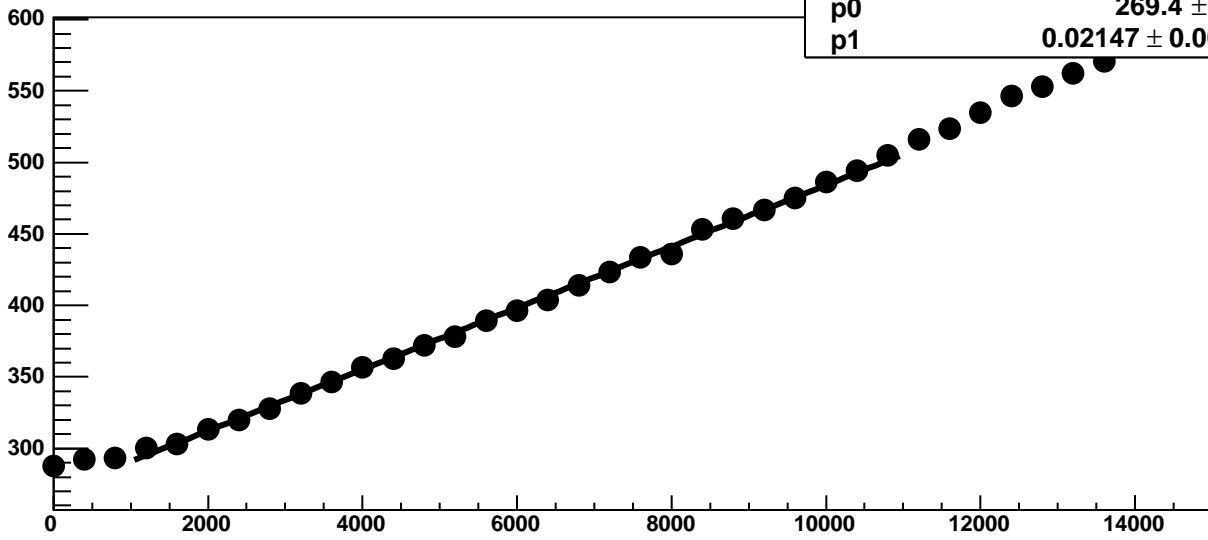
Chip 4, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

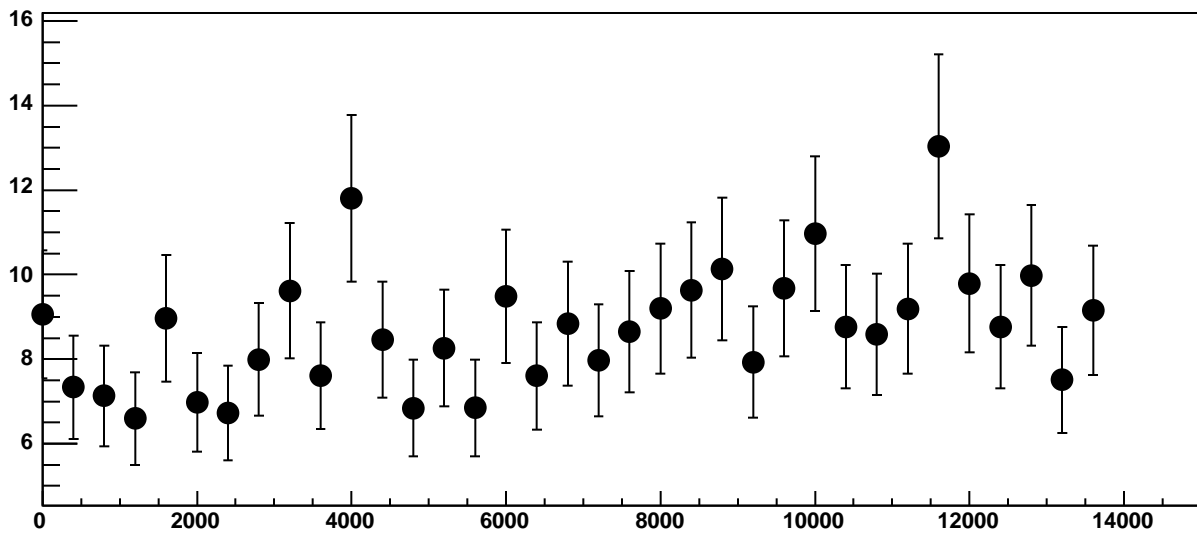


Chip 4, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

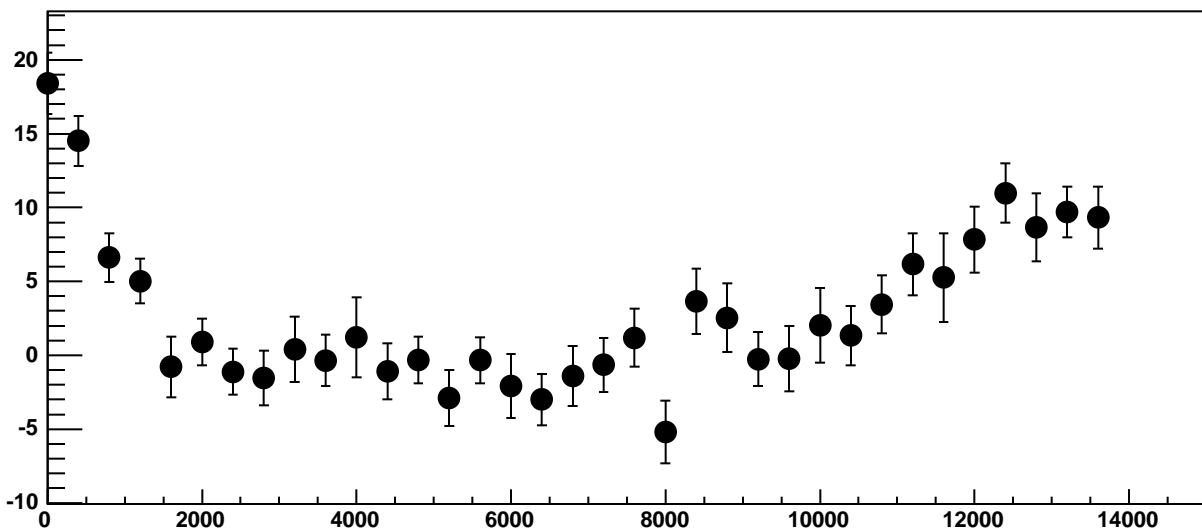


χ^2 / ndf 34.71 / 23
p0 269.4 ± 0.8279
p1 0.02147 ± 0.0001318

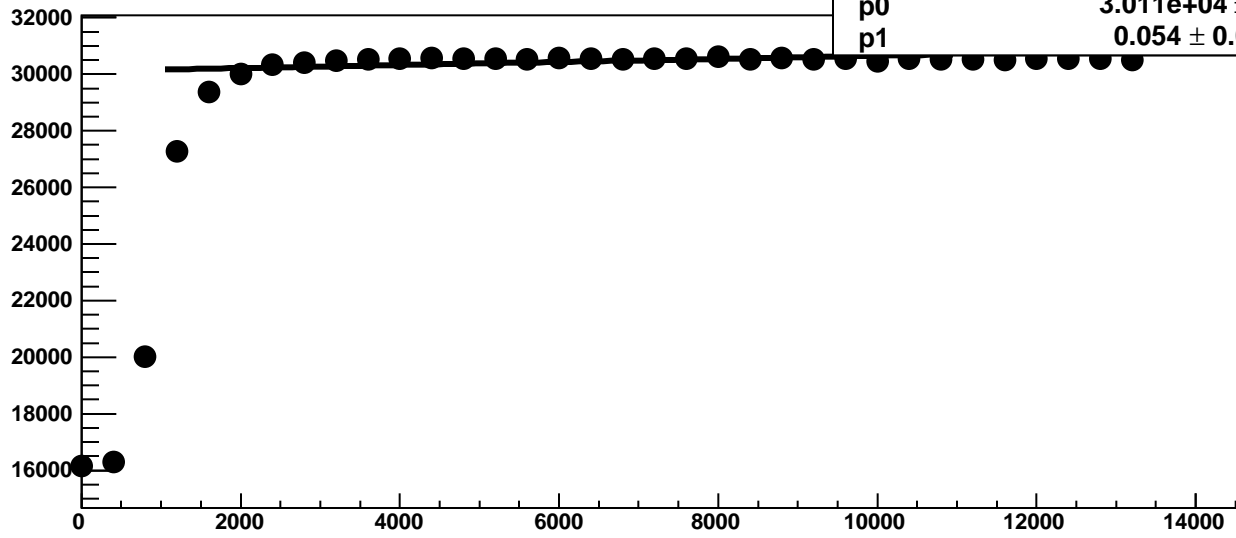
Chip 4, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



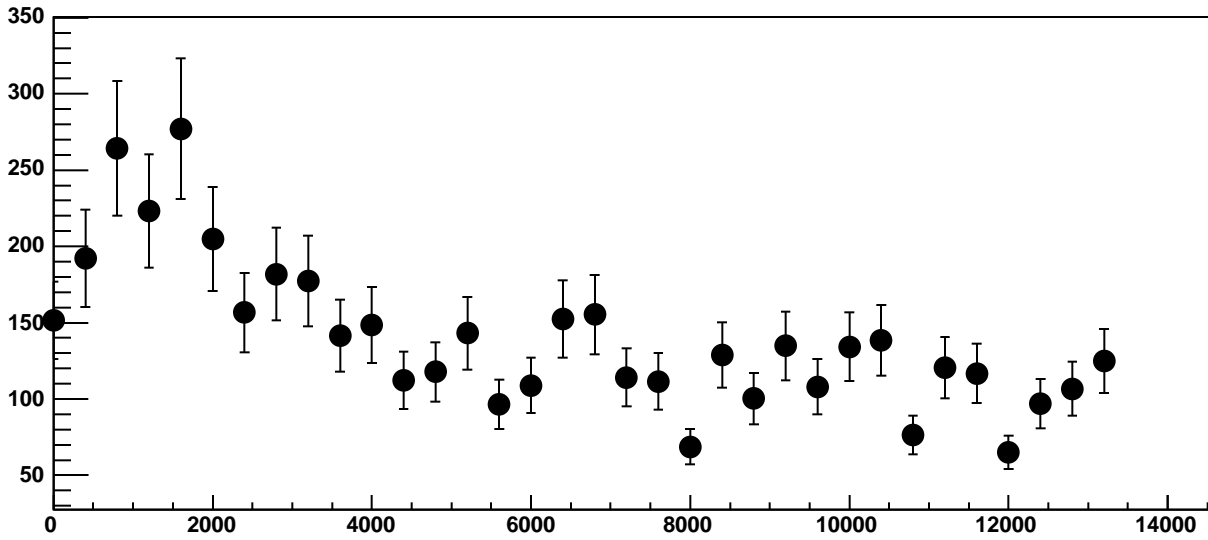
Chip 4, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



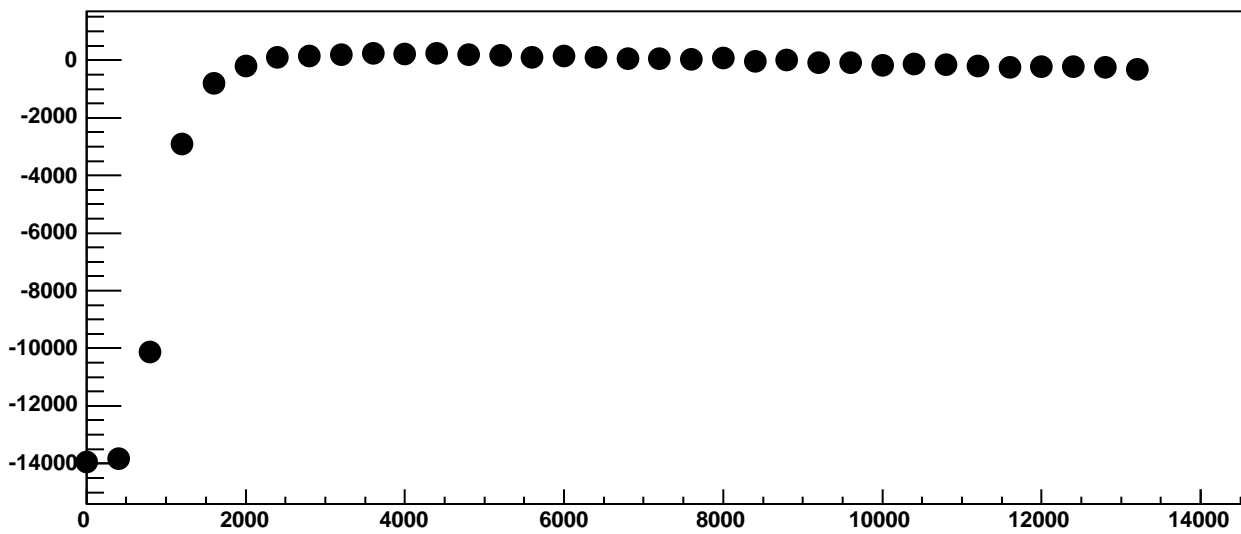
Chip 4, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC



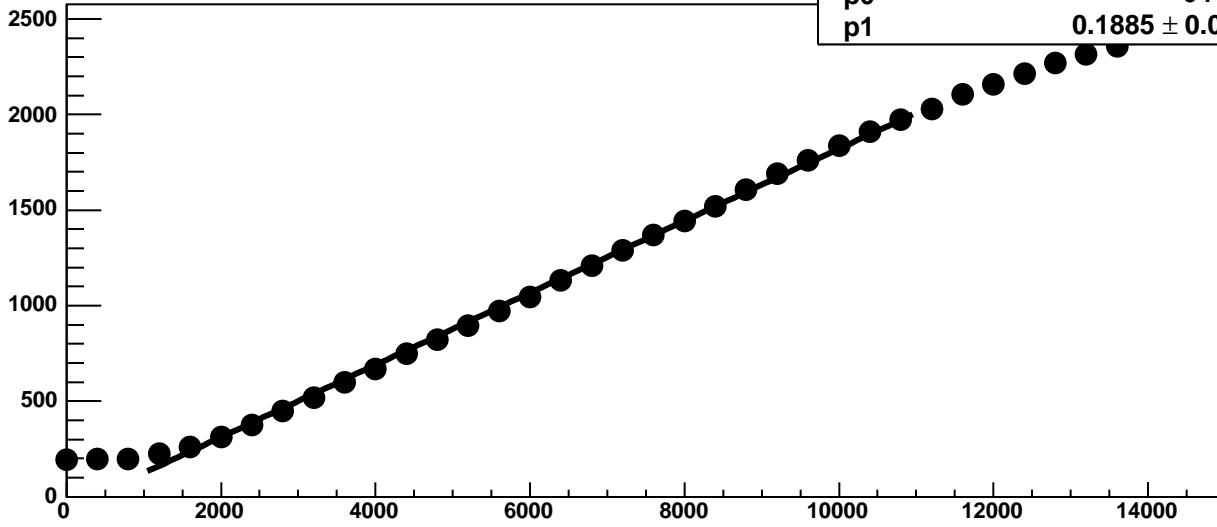
Chip 4, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

809.6 / 23

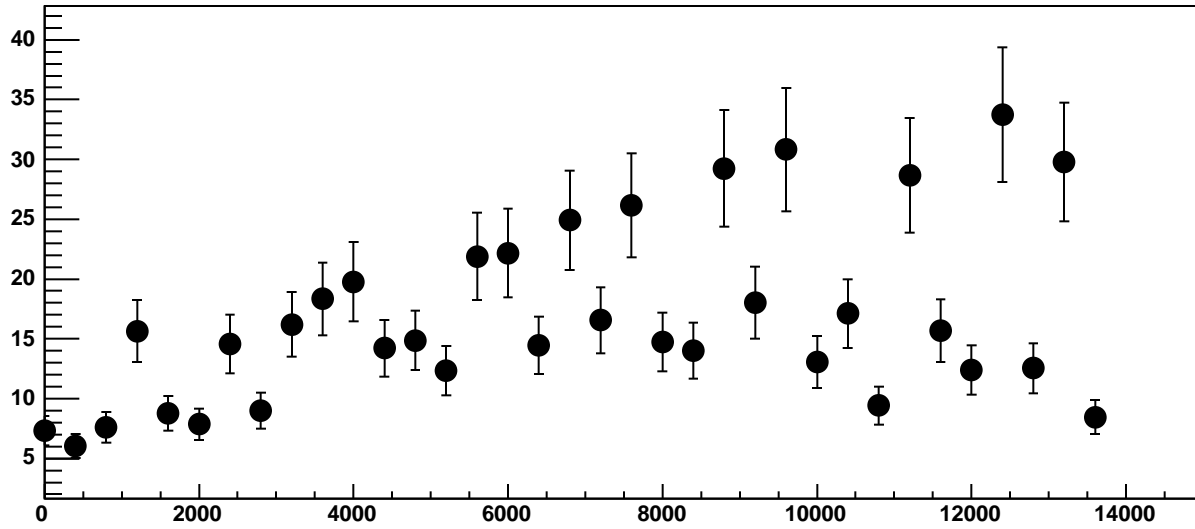
p0

-64 ± 1.222

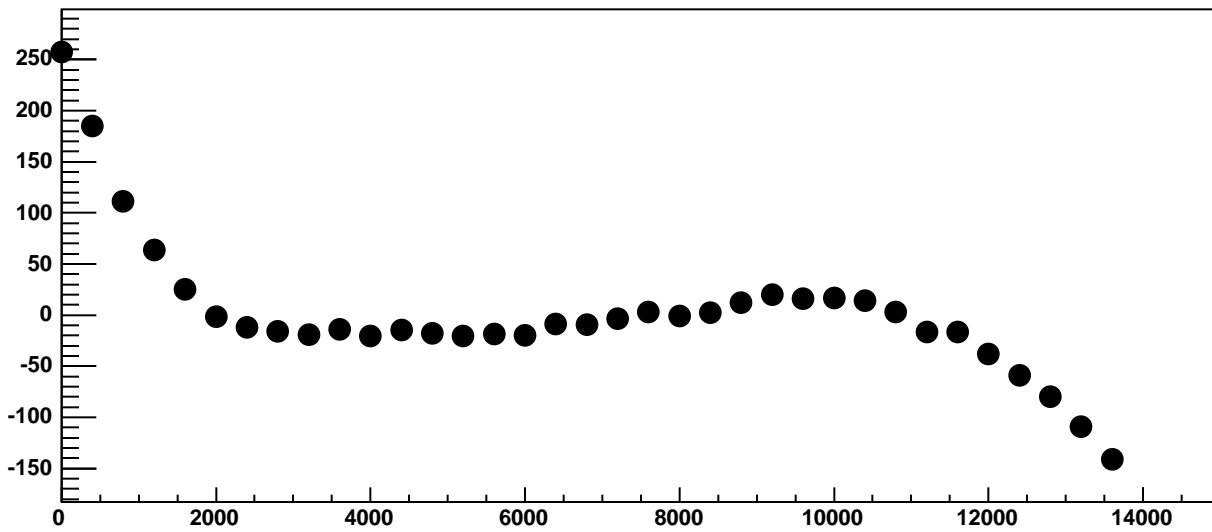
p1

0.1885 ± 0.0001991

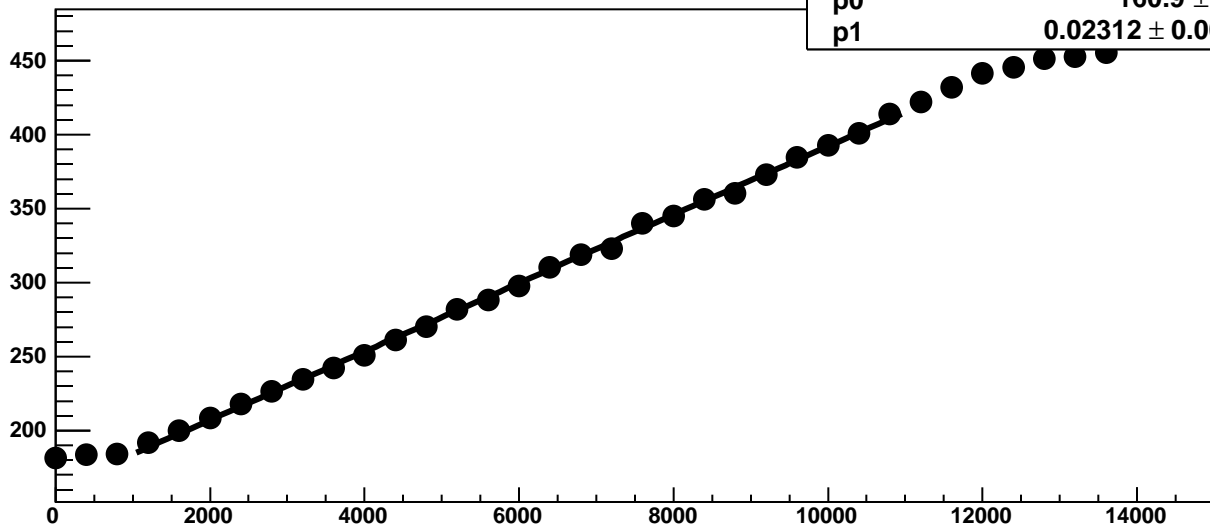
Chip 4, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

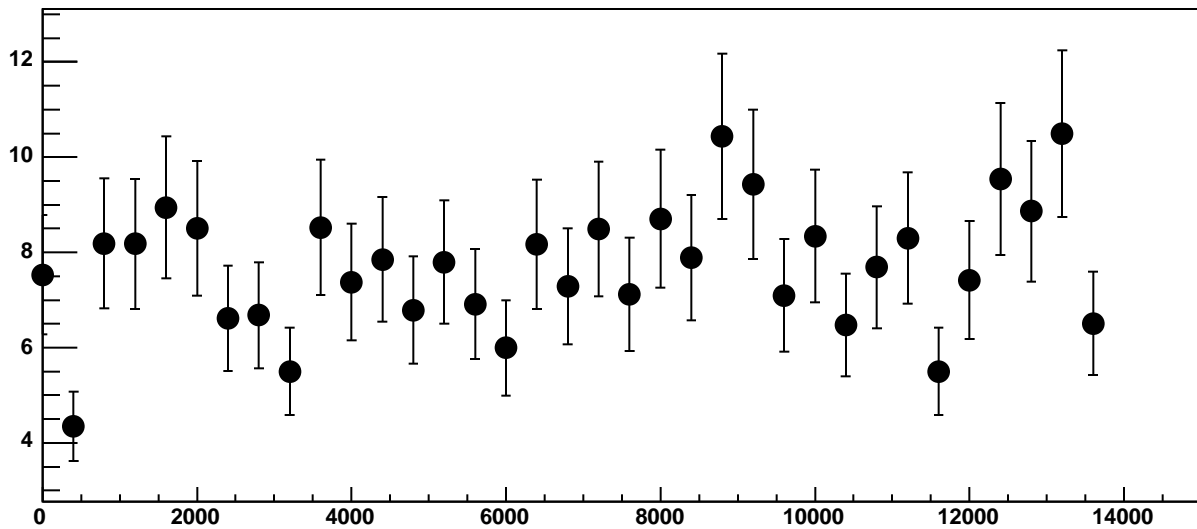


Chip 4, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

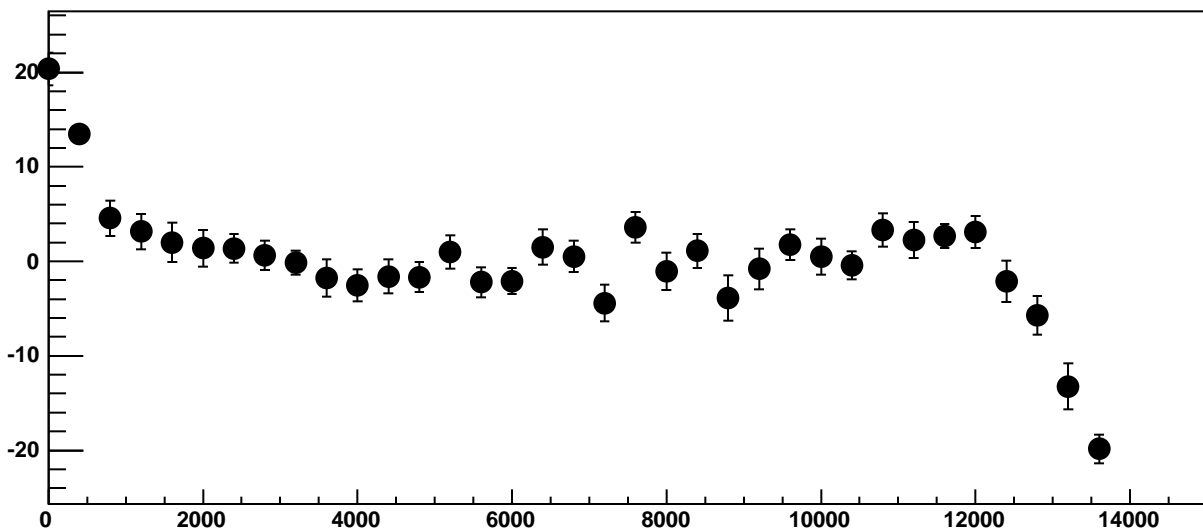


χ^2 / ndf 33.76 / 23
p0 160.9 ± 0.7896
p1 0.02312 ± 0.0001213

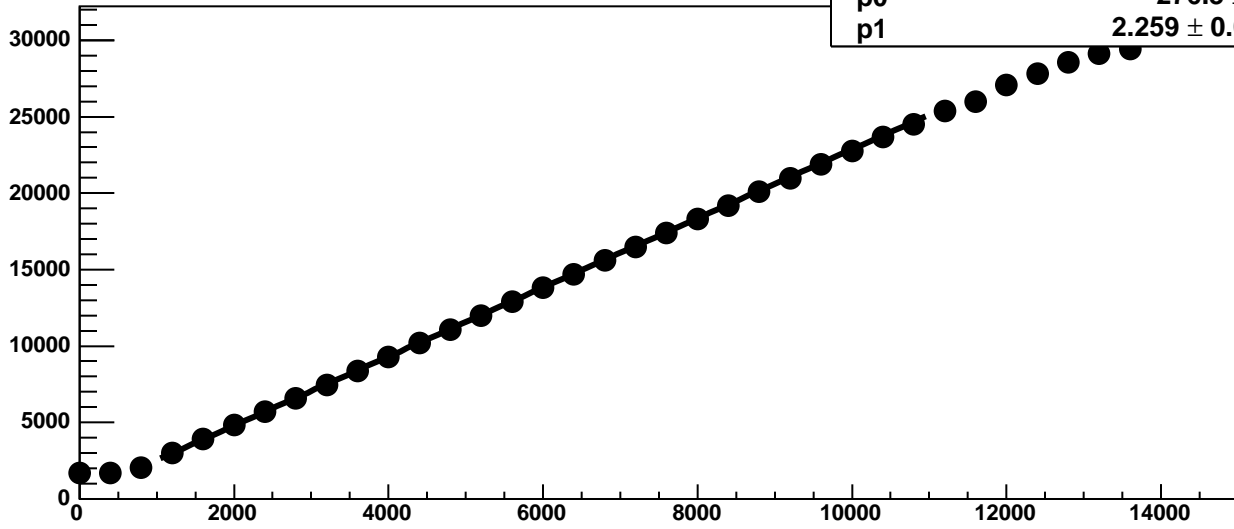
Chip 4, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

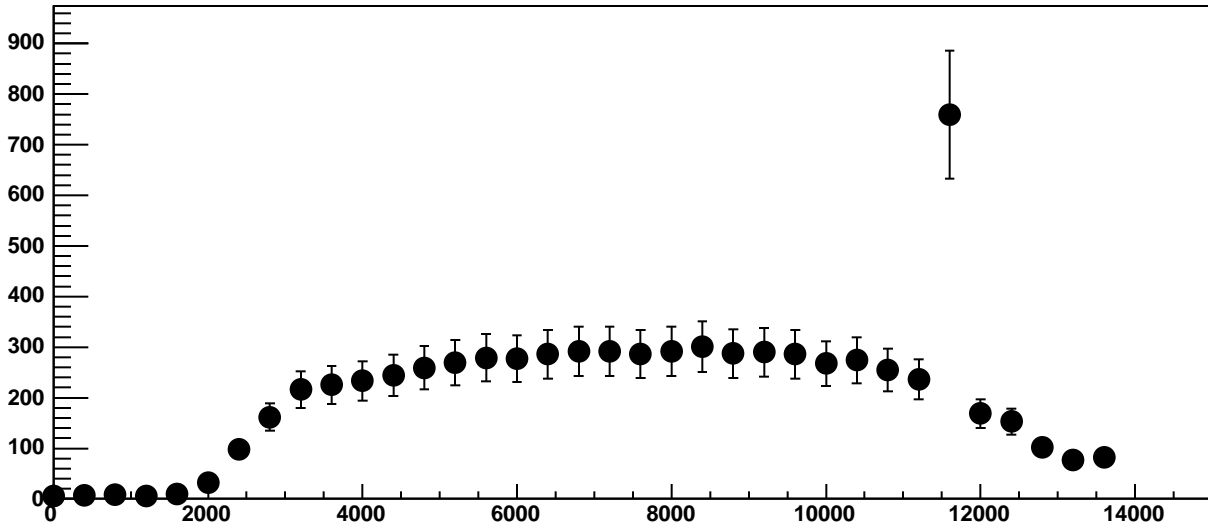


Chip 4, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC

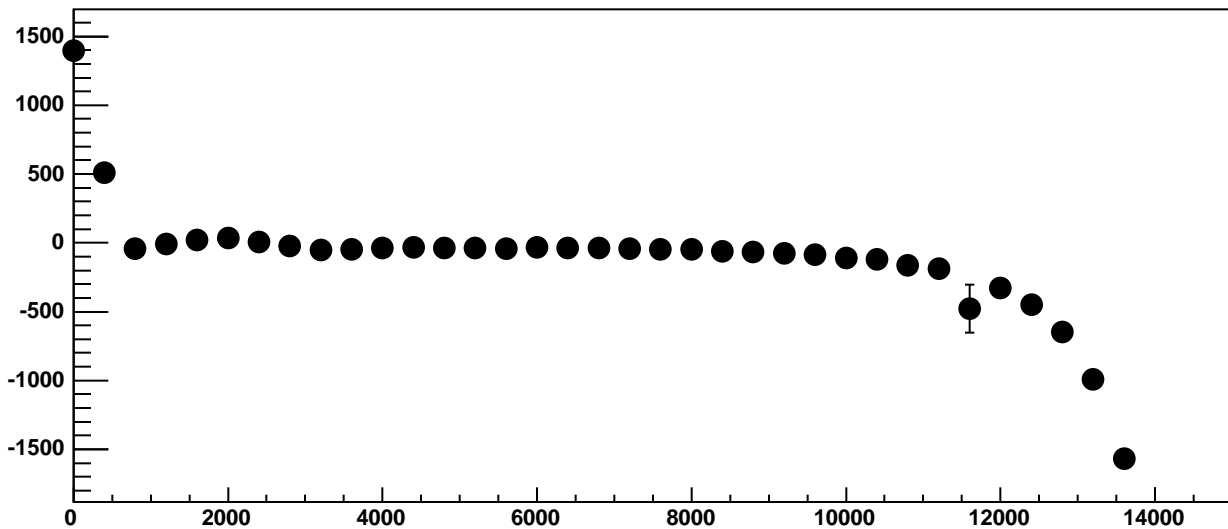


χ^2 / ndf 166.7 / 23
p0 276.3 ± 3.146
p1 2.259 ± 0.002135

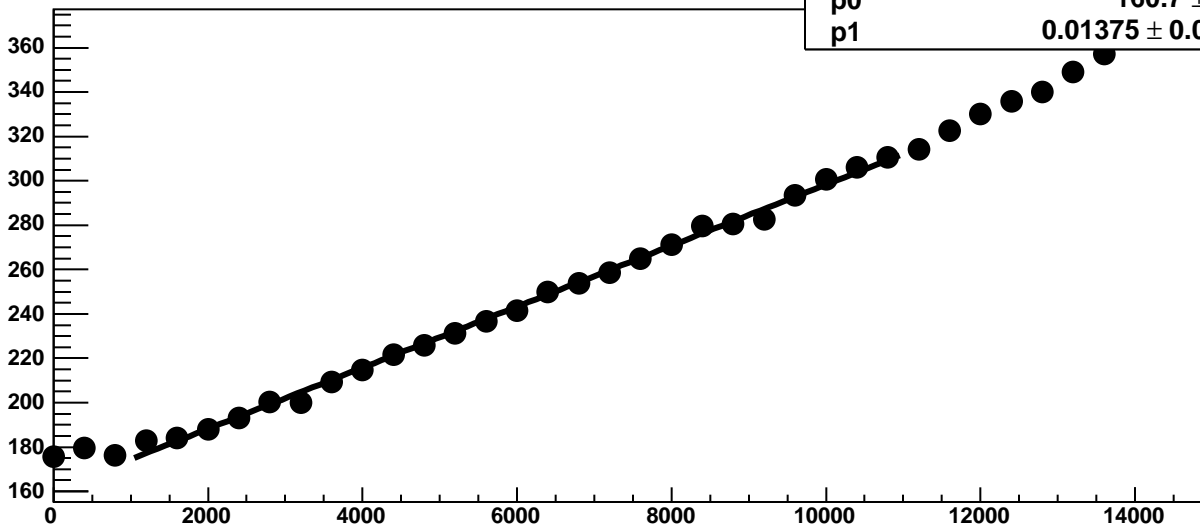
Chip 4, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC

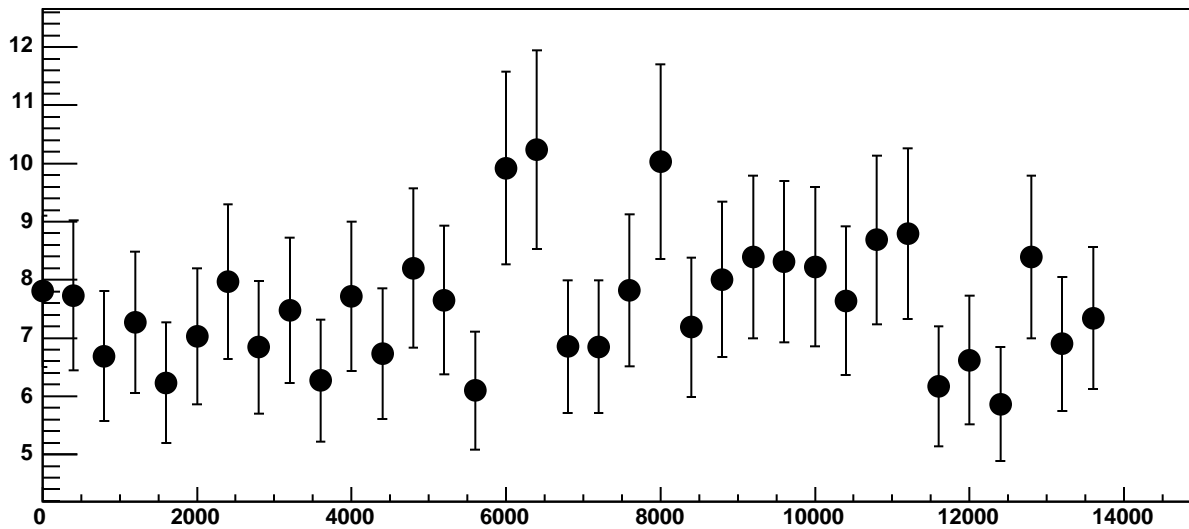


Chip 4, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

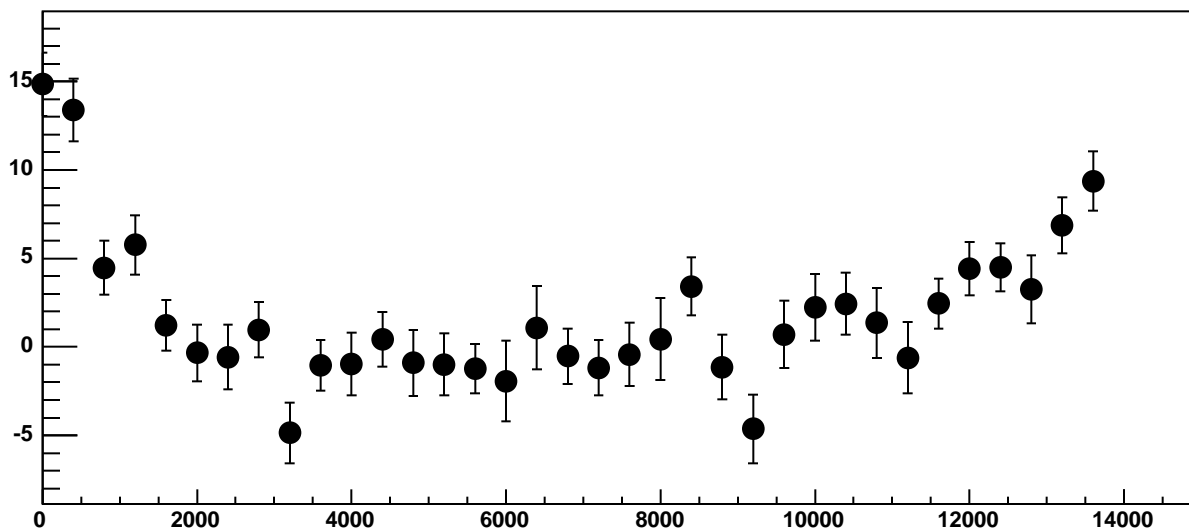


χ^2 / ndf 39.53 / 23
p0 160.7 ± 0.7622
p1 0.01375 ± 0.0001202

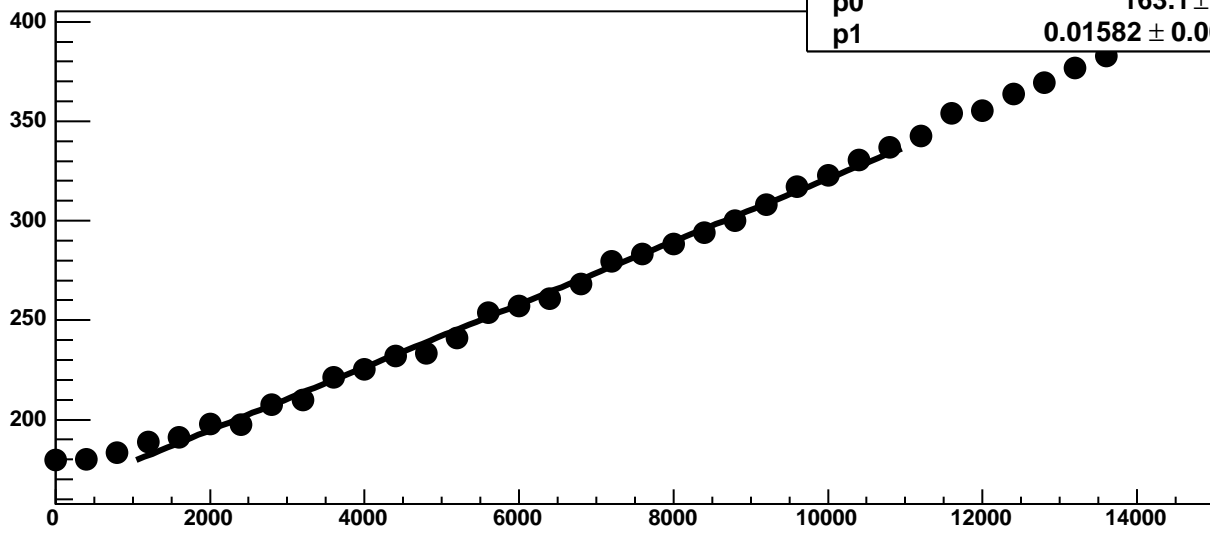
Chip 4, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

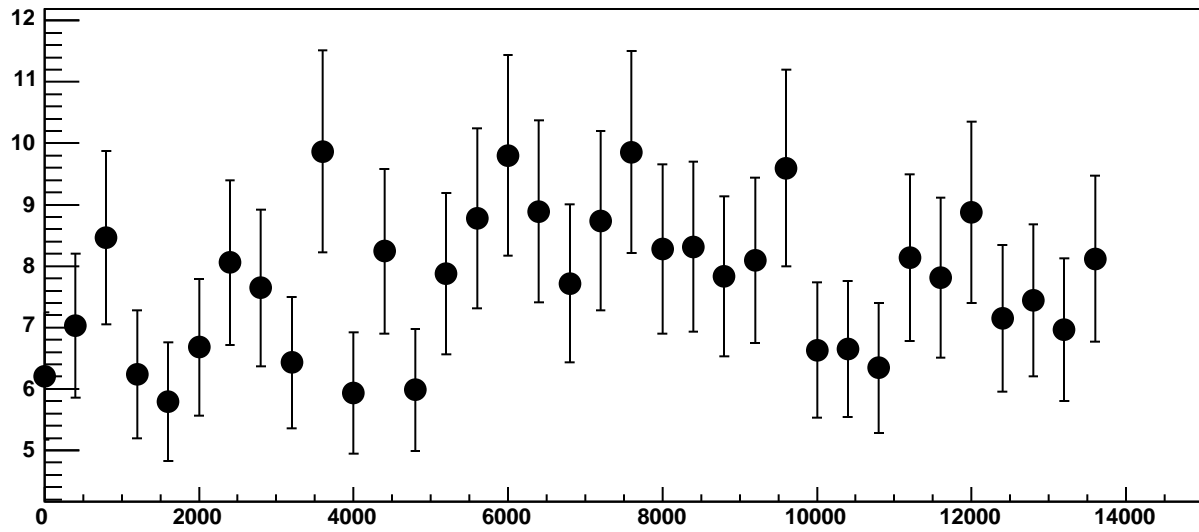


Chip 4, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

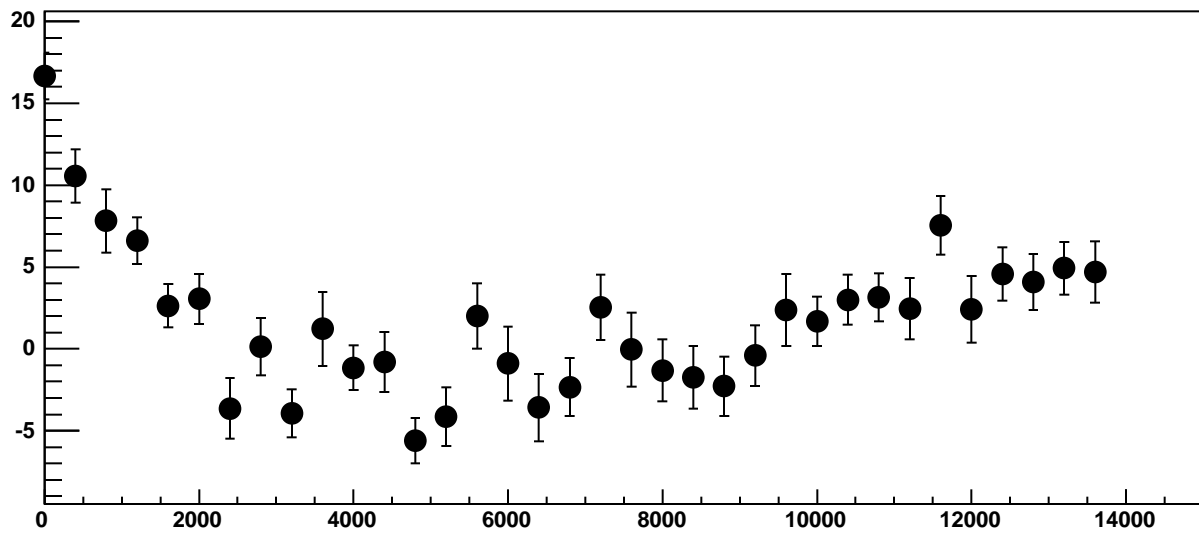


χ^2 / ndf 84.72 / 23
p0 163.1 ± 0.7187
p1 0.01582 ± 0.0001103

Chip 4, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC

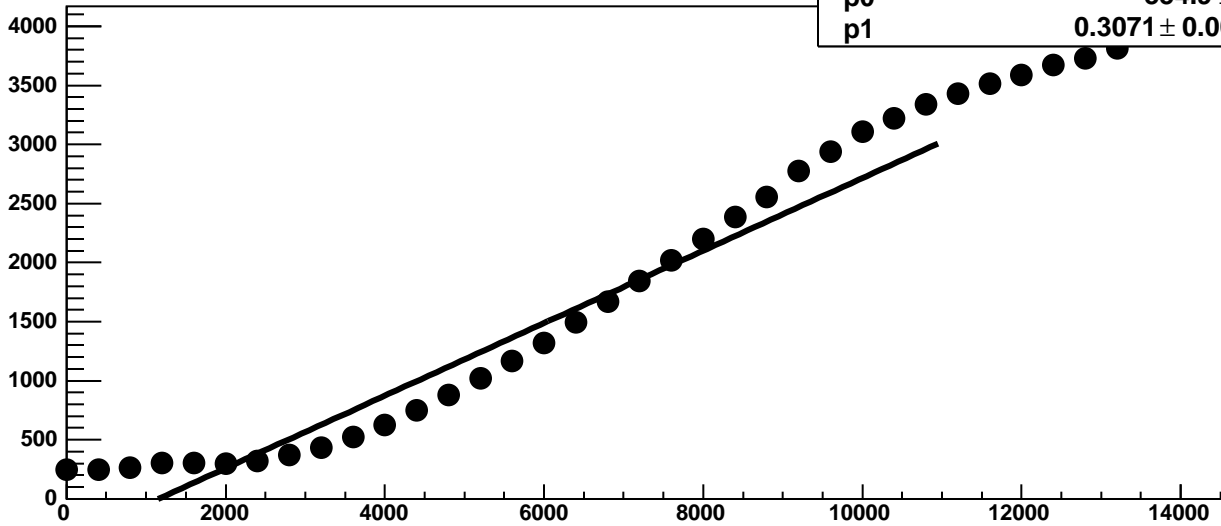


Chip 4, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

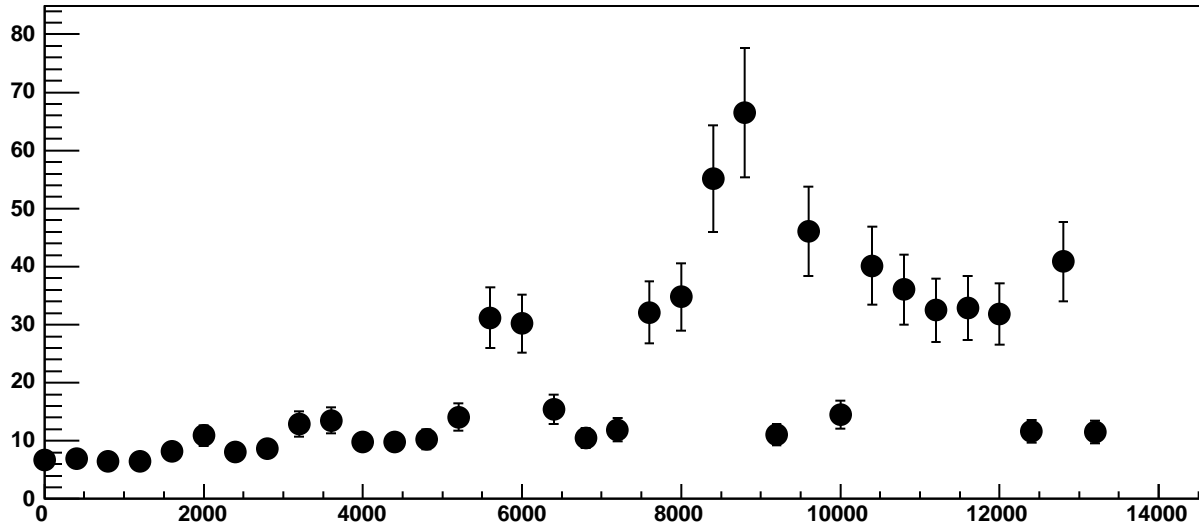


Chip 4, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC

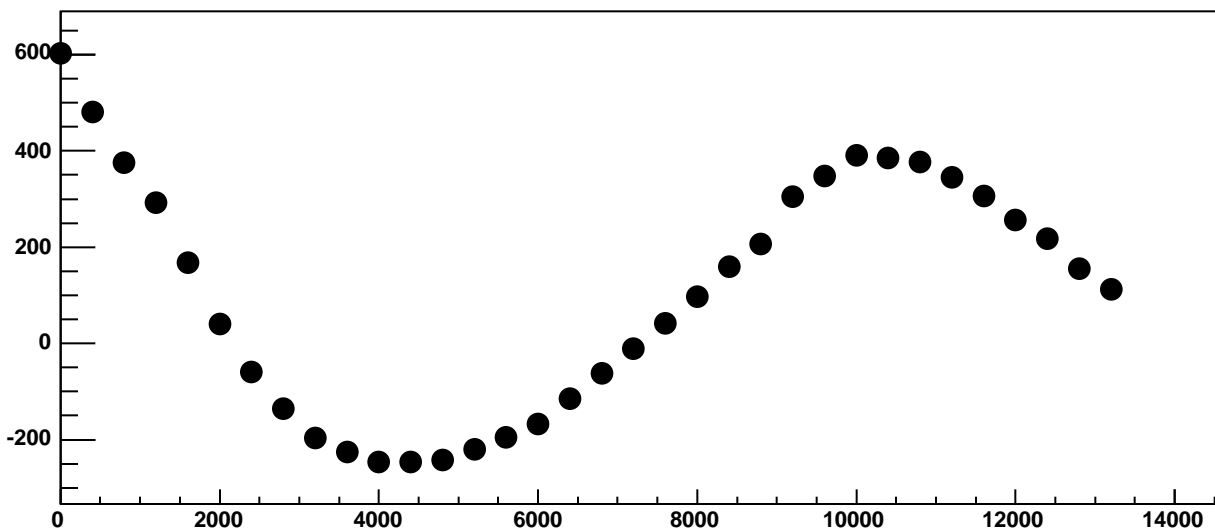
χ^2 / ndf 1.386e+05 / 23
p0 -354.9 ± 1.059
p1 0.3071 ± 0.0002223



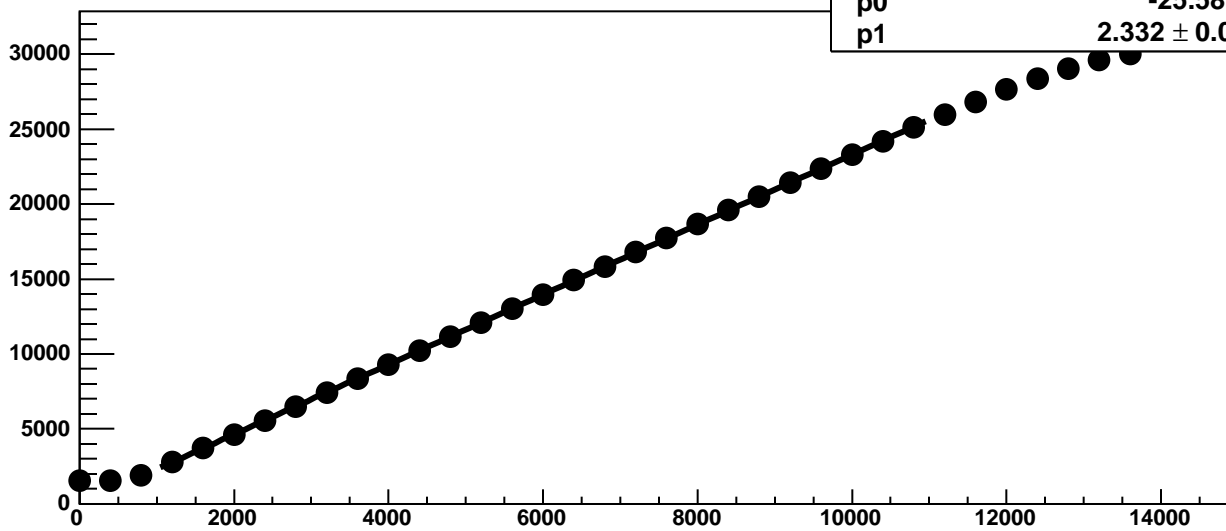
Chip 4, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



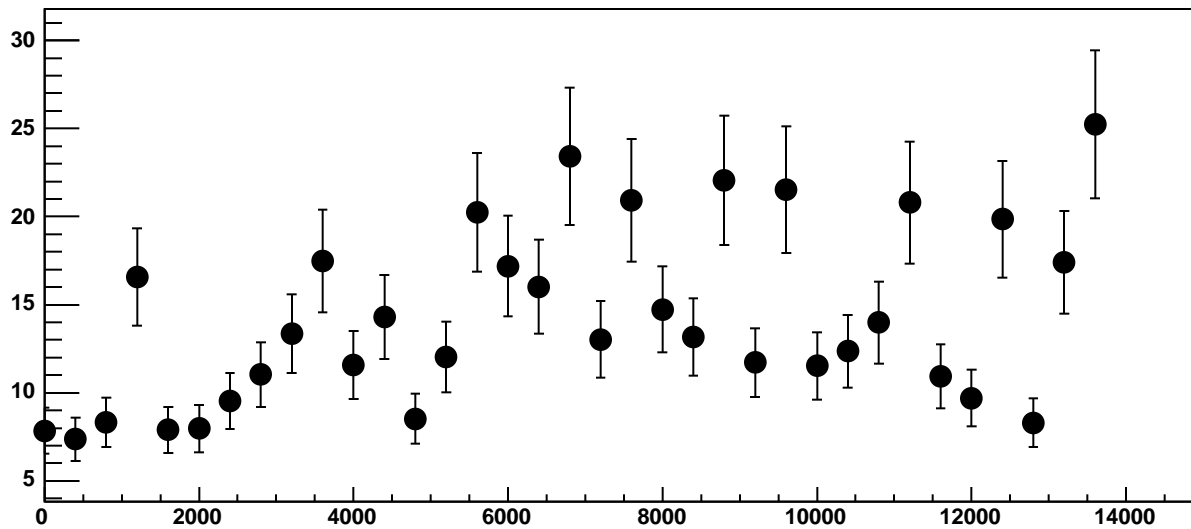
Chip 4, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



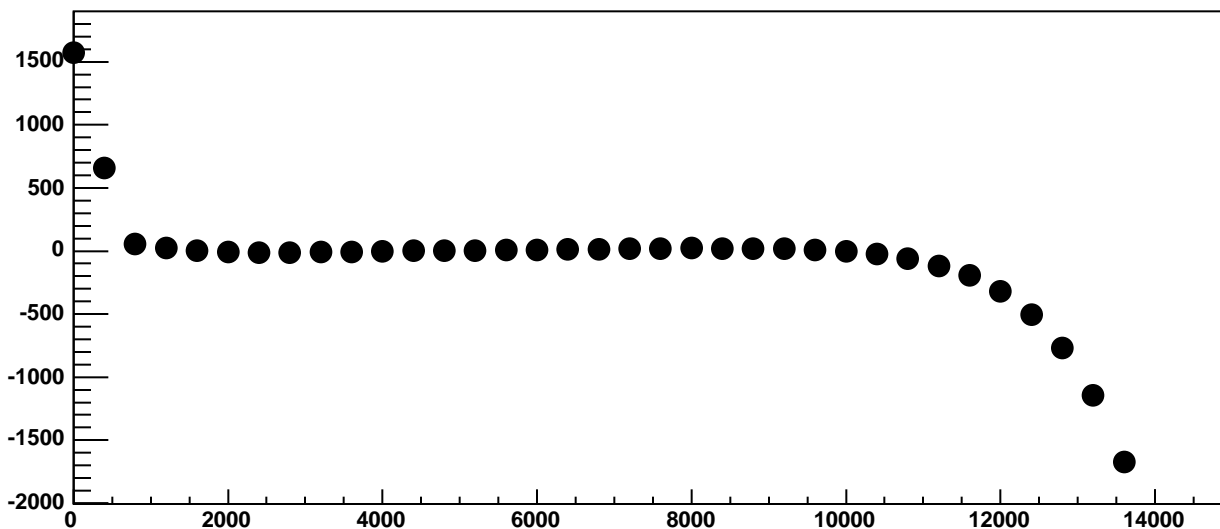
Chip 4, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



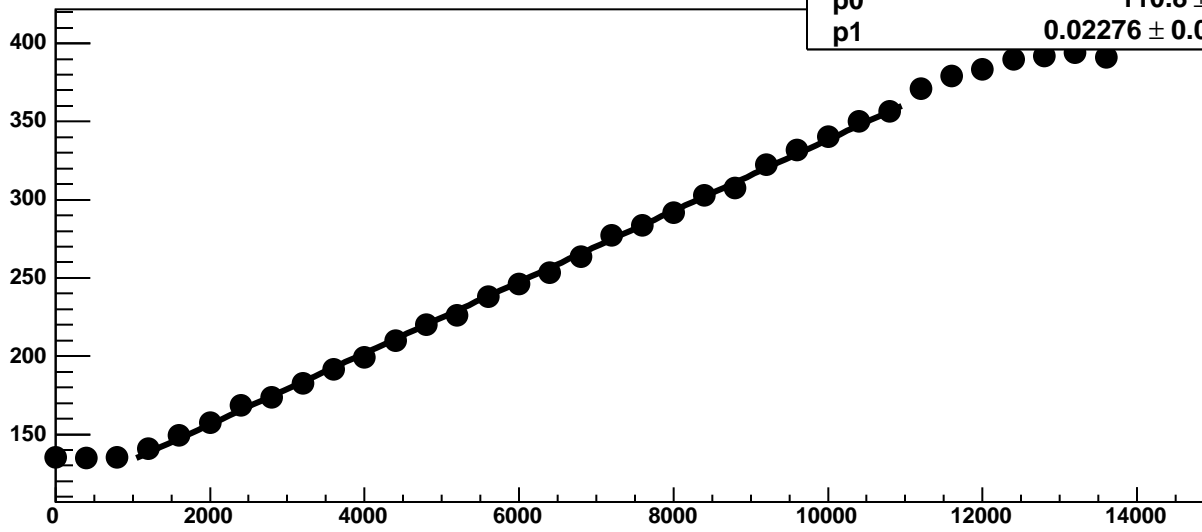
Chip 4, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



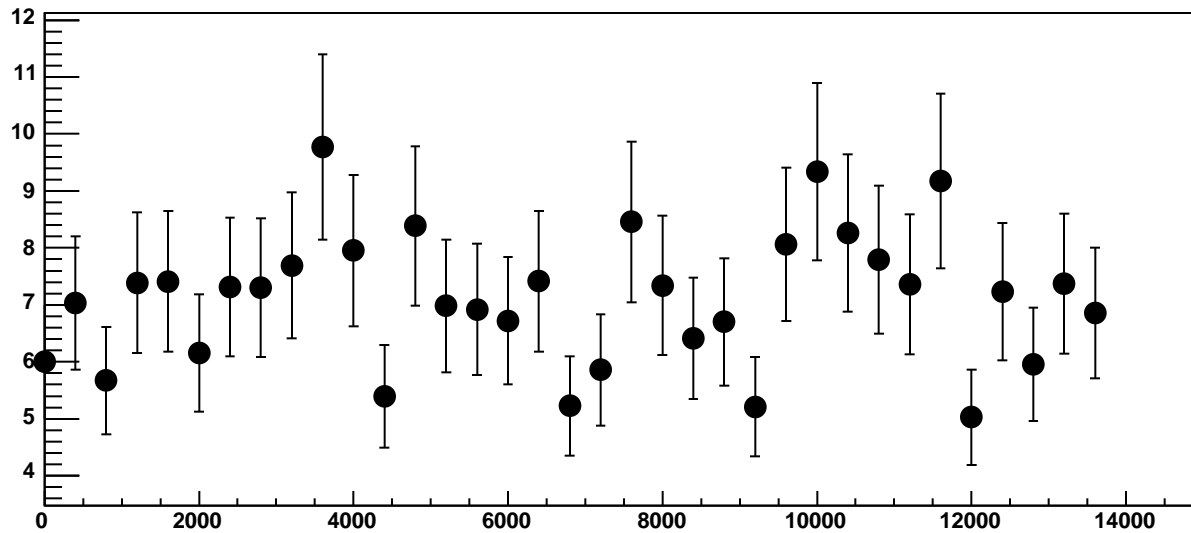
Chip 4, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC



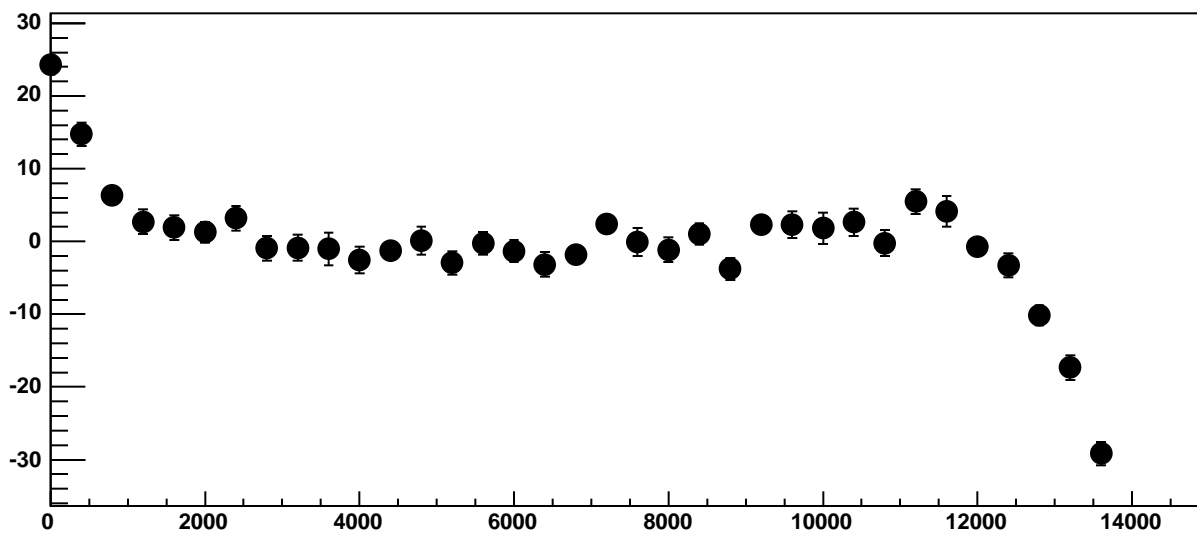
Chip 4, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



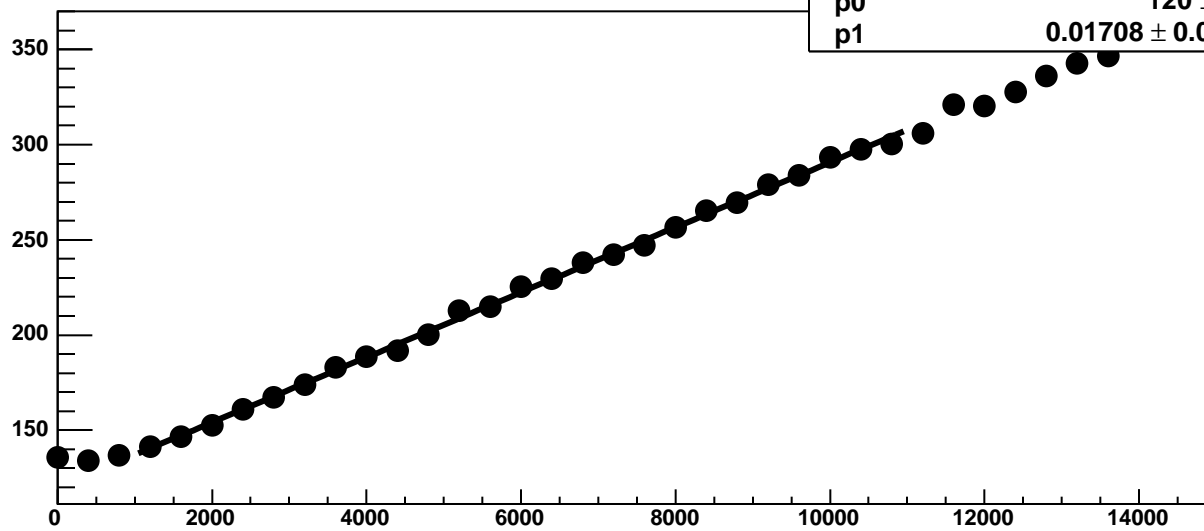
Chip 4, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

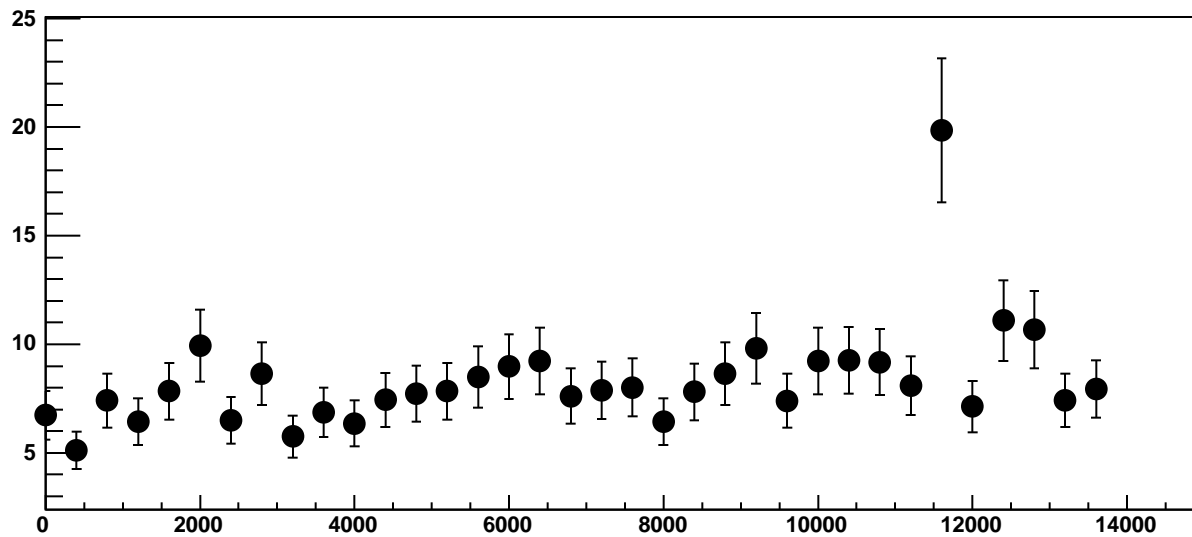


Chip 4, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

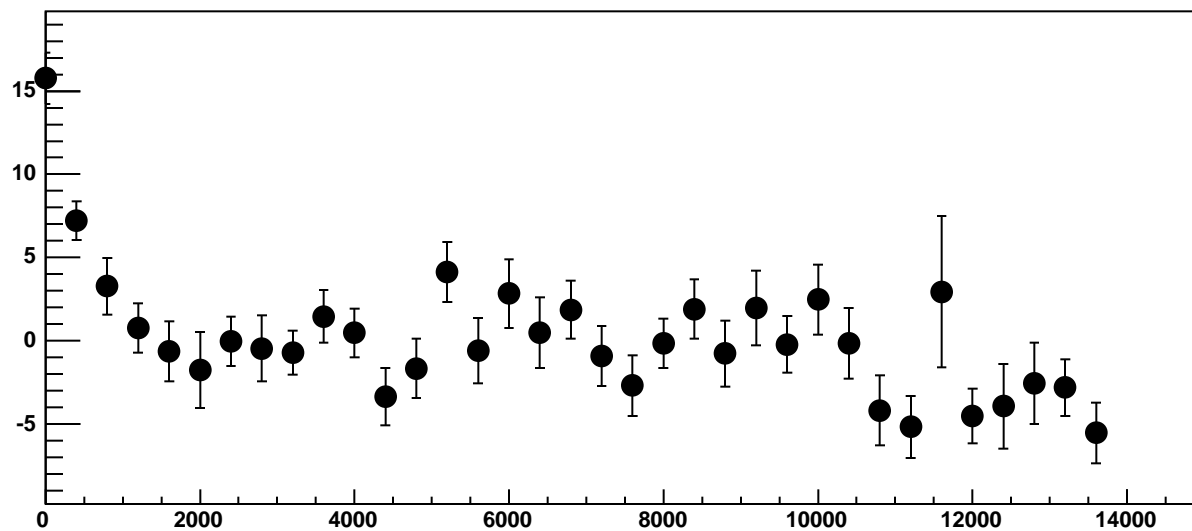


χ^2 / ndf 25.33 / 23
p0 120 ± 0.7861
p1 0.01708 ± 0.0001253

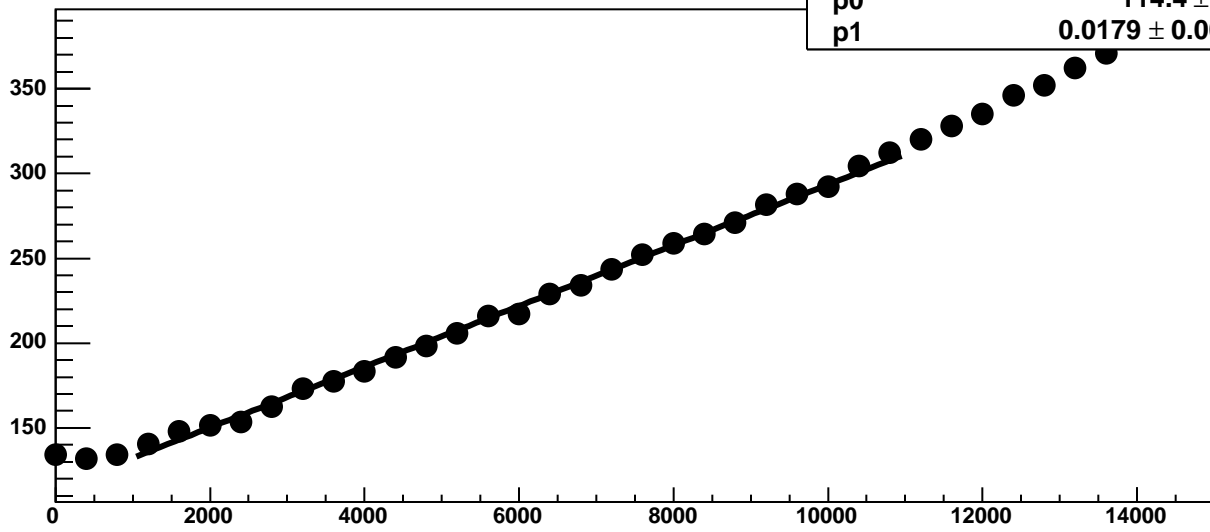
Chip 4, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

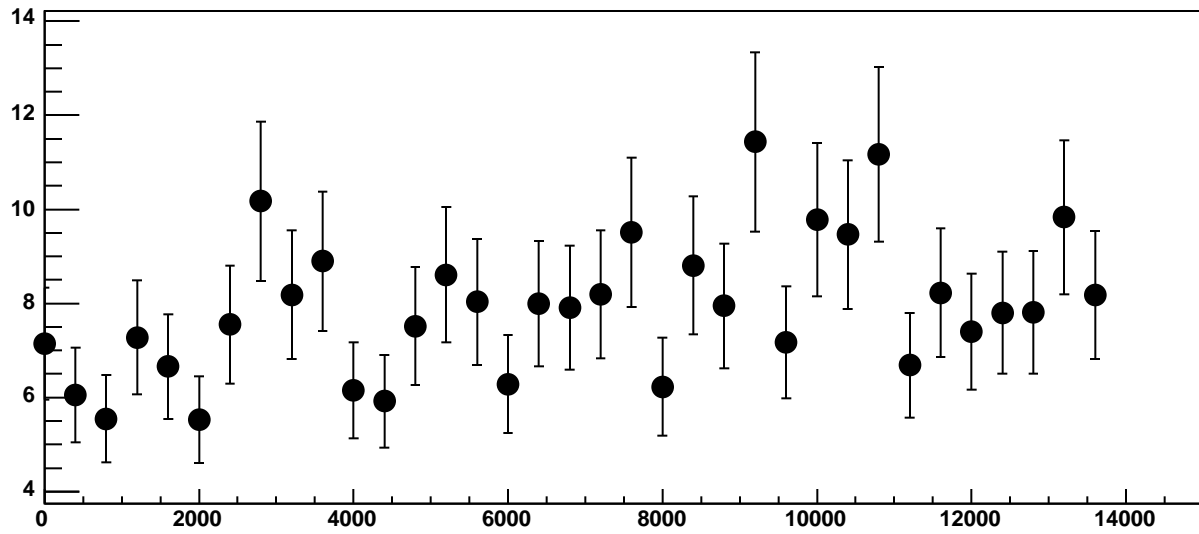


Chip 4, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

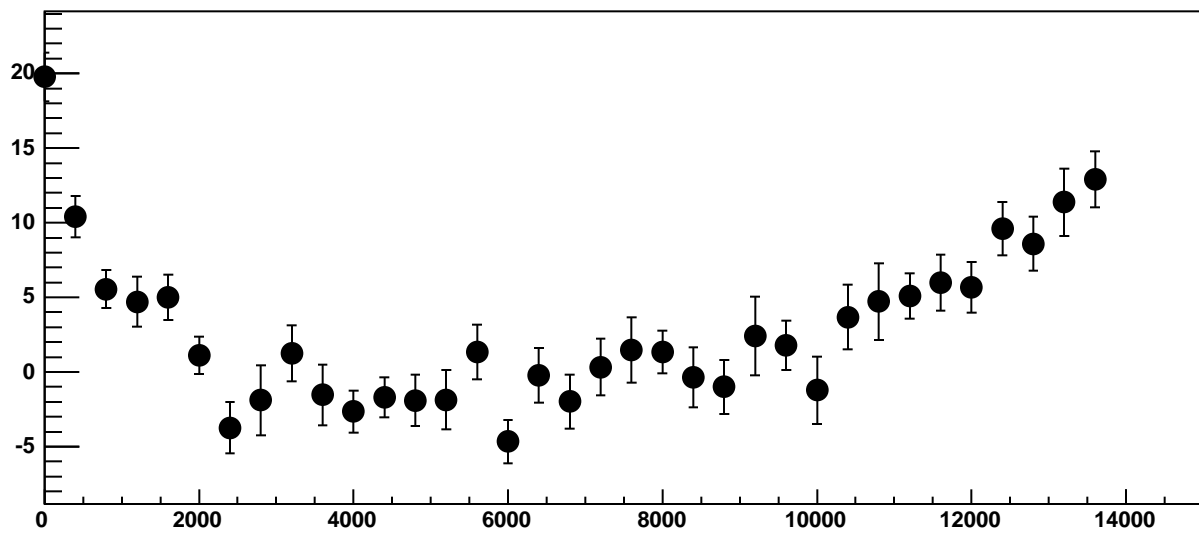


χ^2 / ndf 55.4 / 23
p0 114.4 ± 0.7792
p1 0.0179 ± 0.0001267

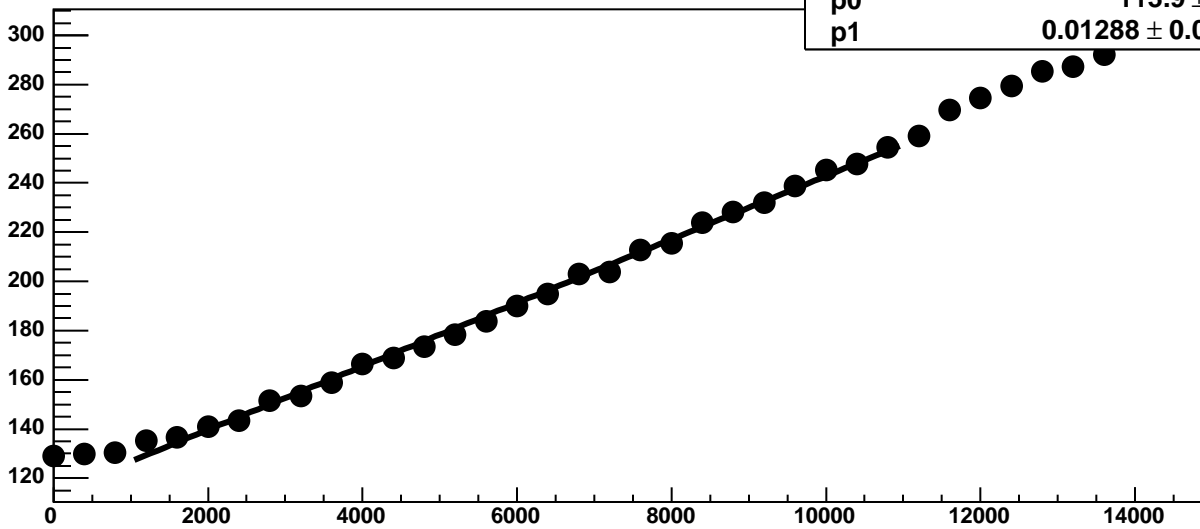
Chip 4, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

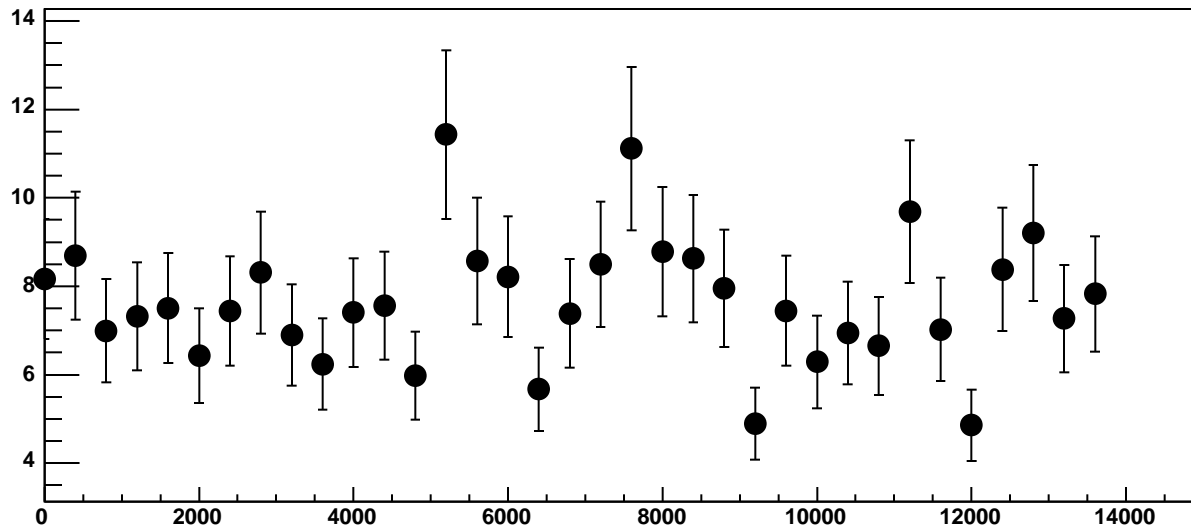


Chip 4, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

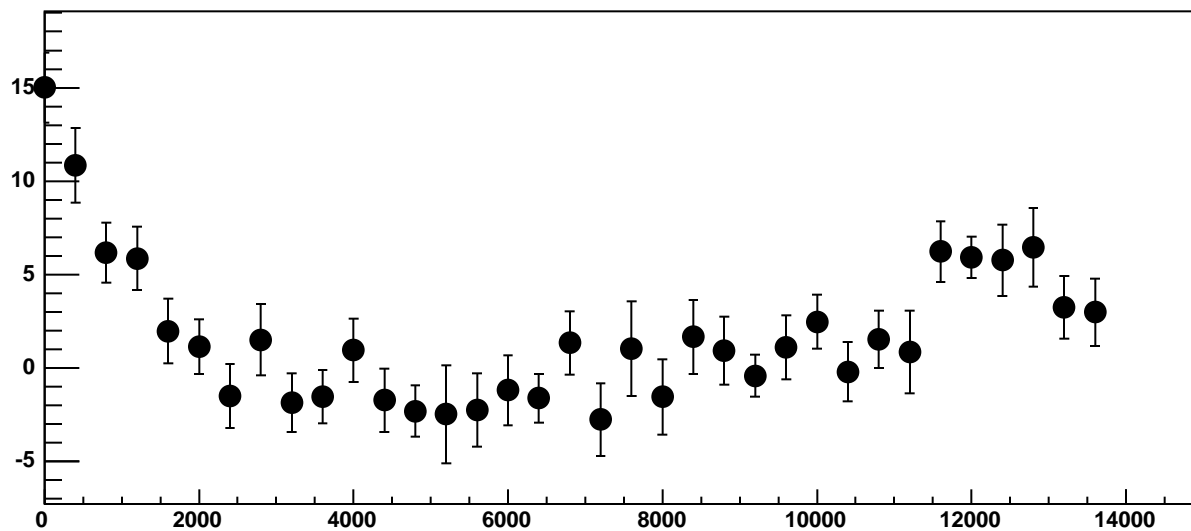


χ^2 / ndf 35.22 / 23
p0 113.9 ± 0.7509
p1 0.01288 ± 0.0001104

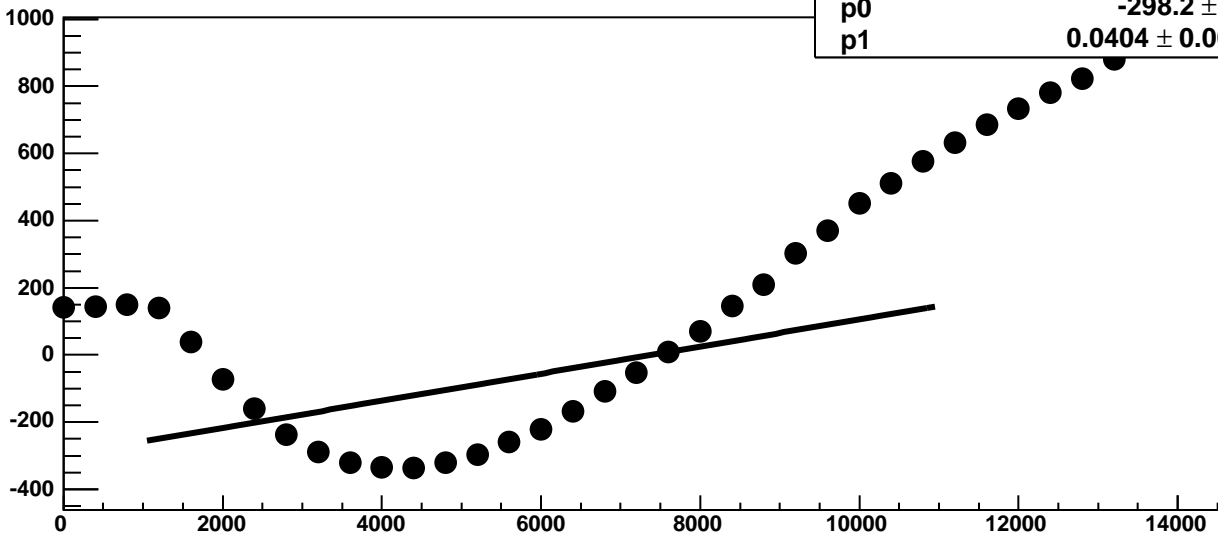
Chip 4, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

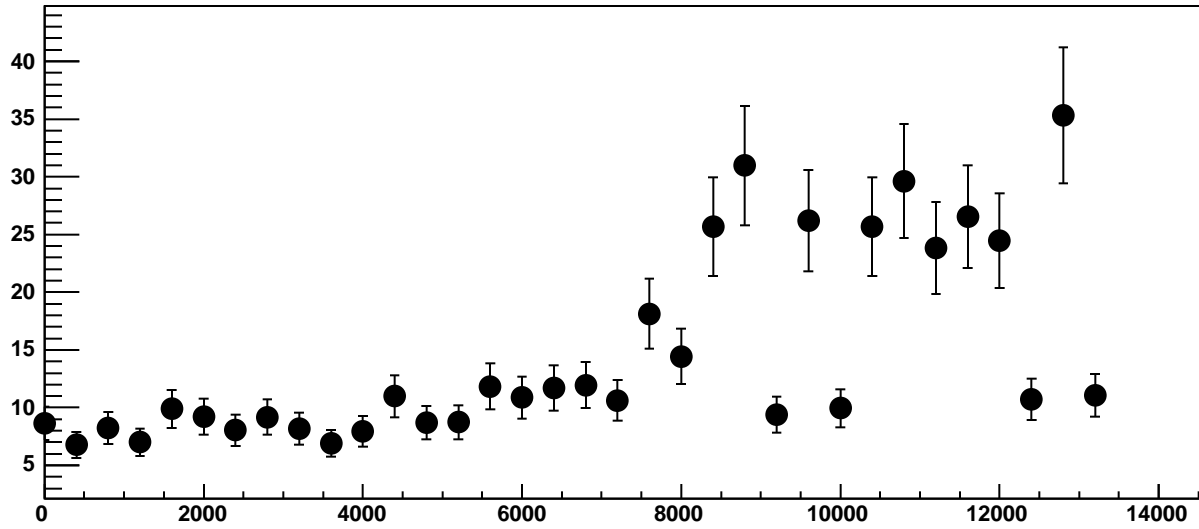


Chip 4, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

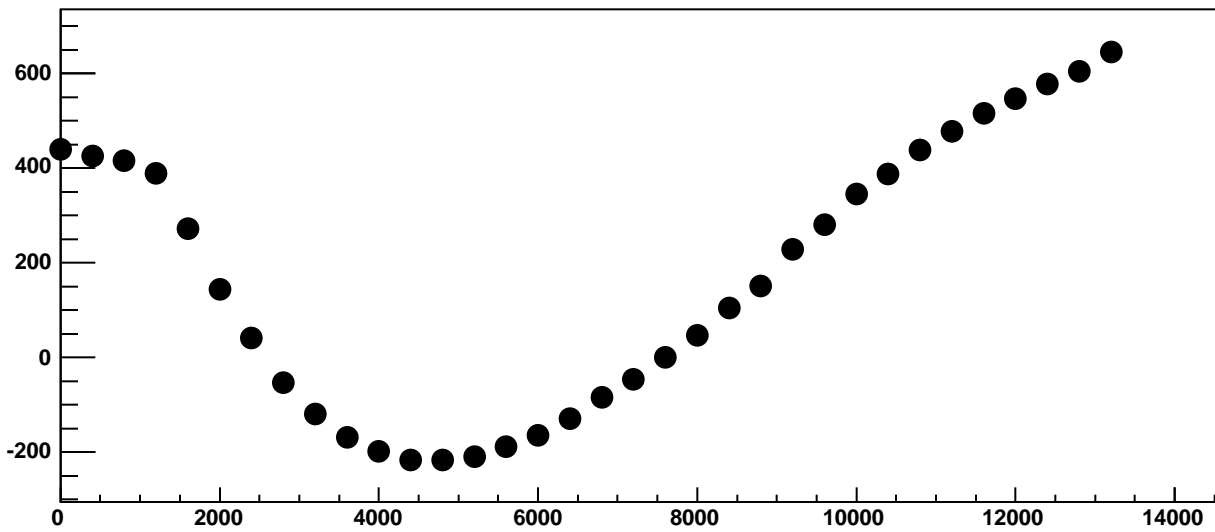


χ^2 / ndf 1.944e+05 / 23
p0 -298.2 ± 0.9874
p1 0.0404 ± 0.0001866

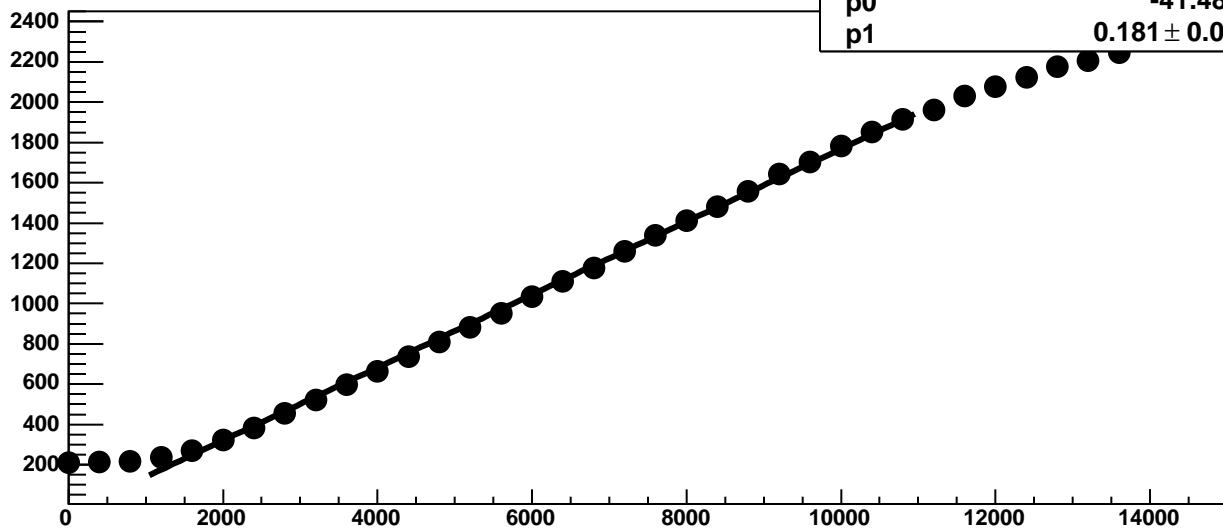
Chip 4, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



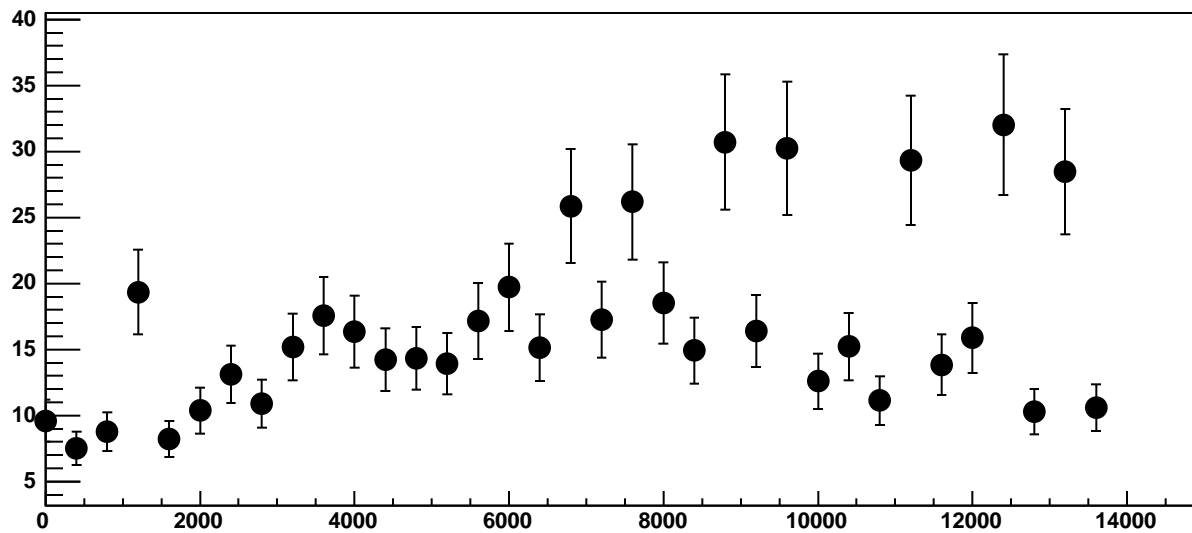
Chip 4, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



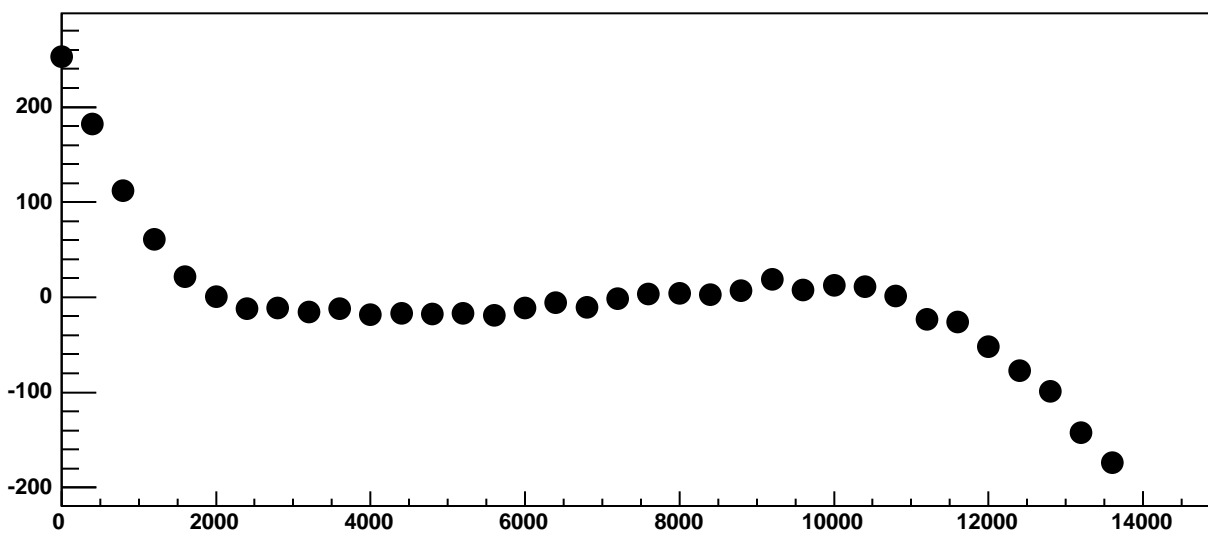
Chip 4, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC



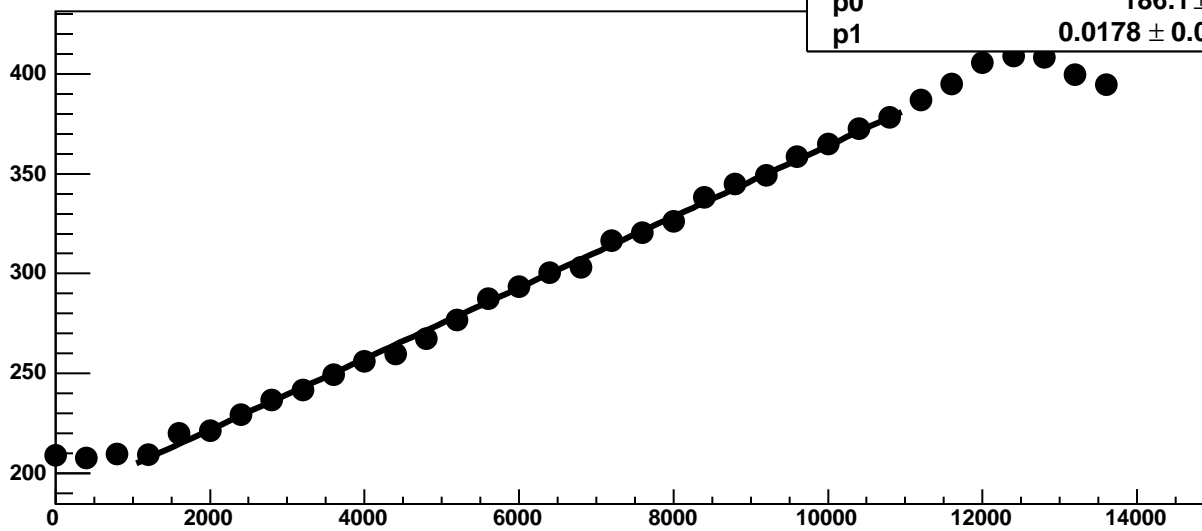
Chip 4, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



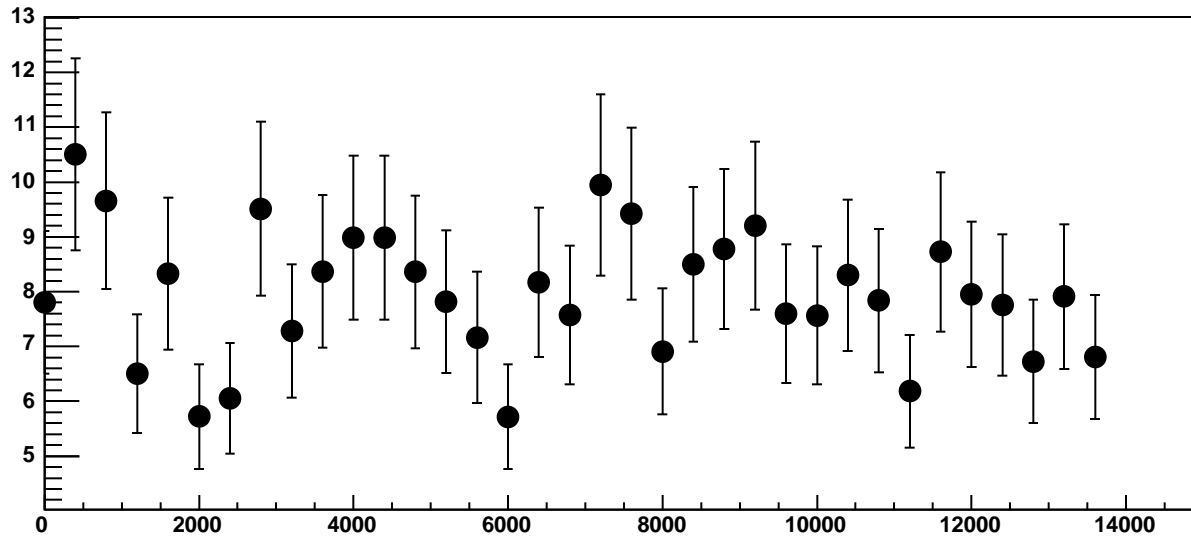
Chip 4, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC



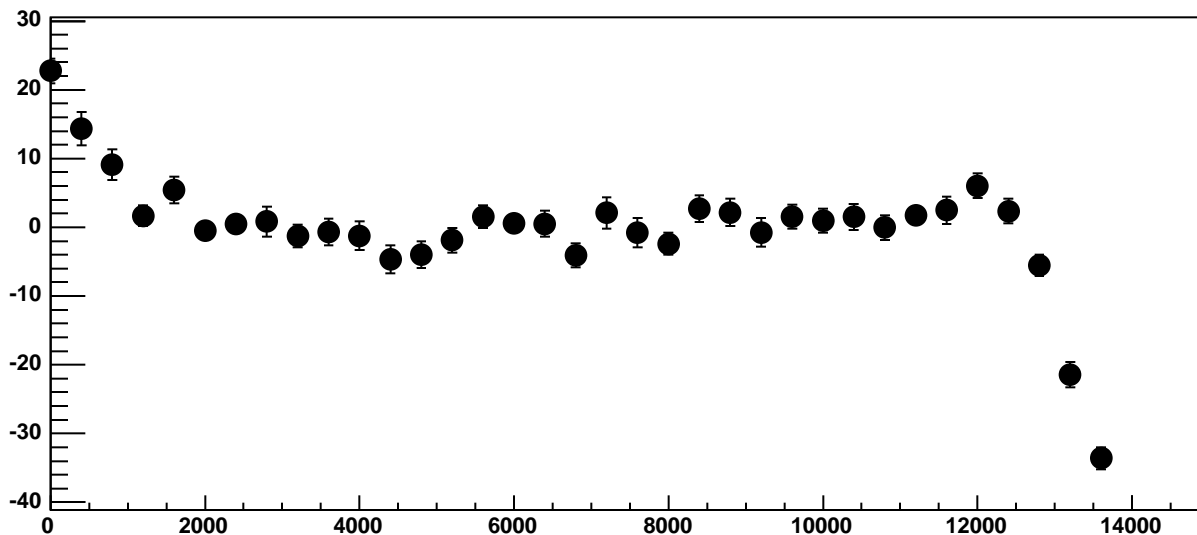
Chip 4, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



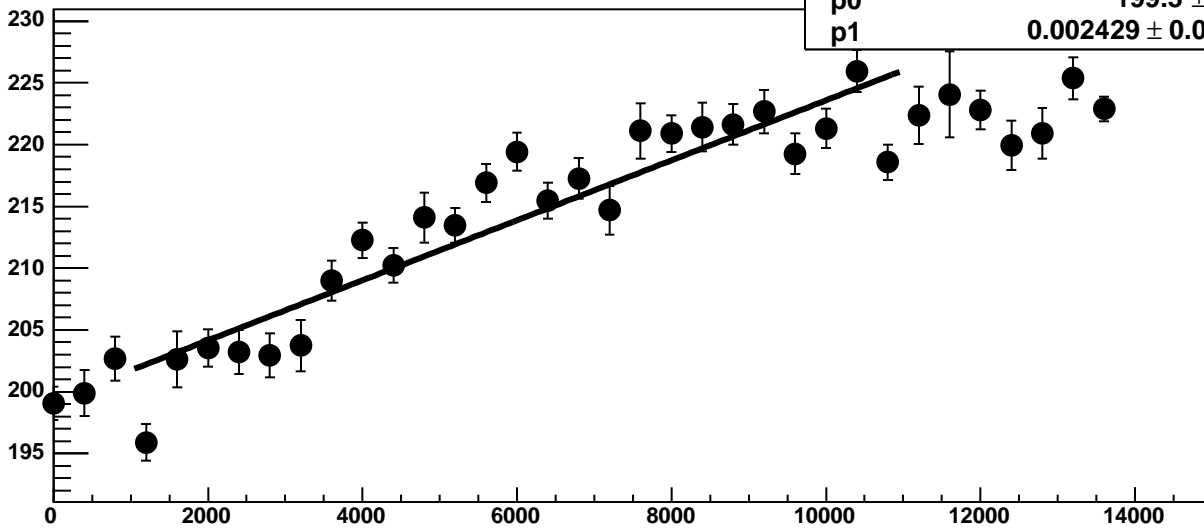
Chip 4, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

92.71 / 23

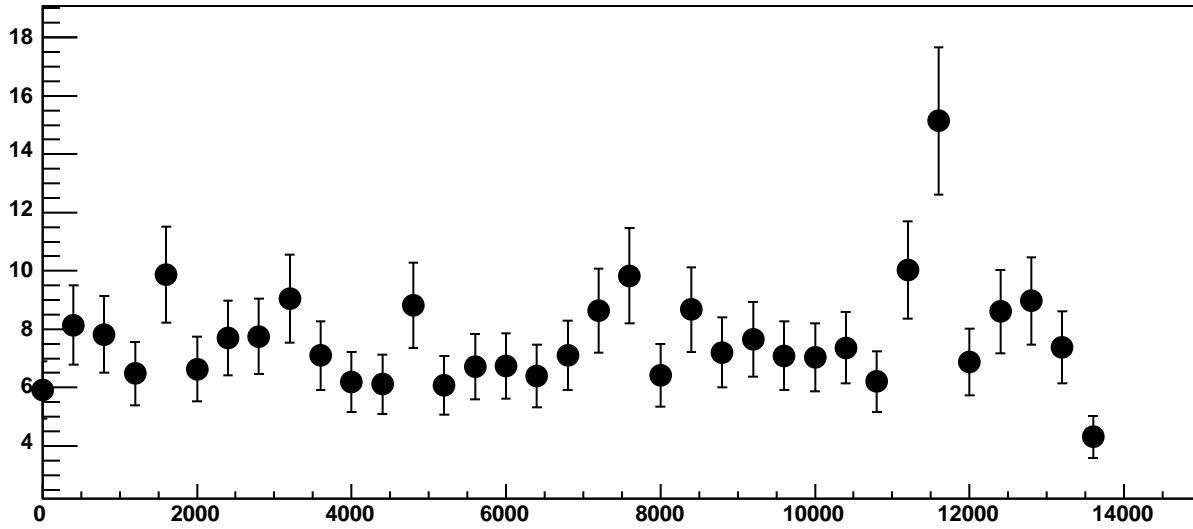
p0

199.3 ± 0.7652

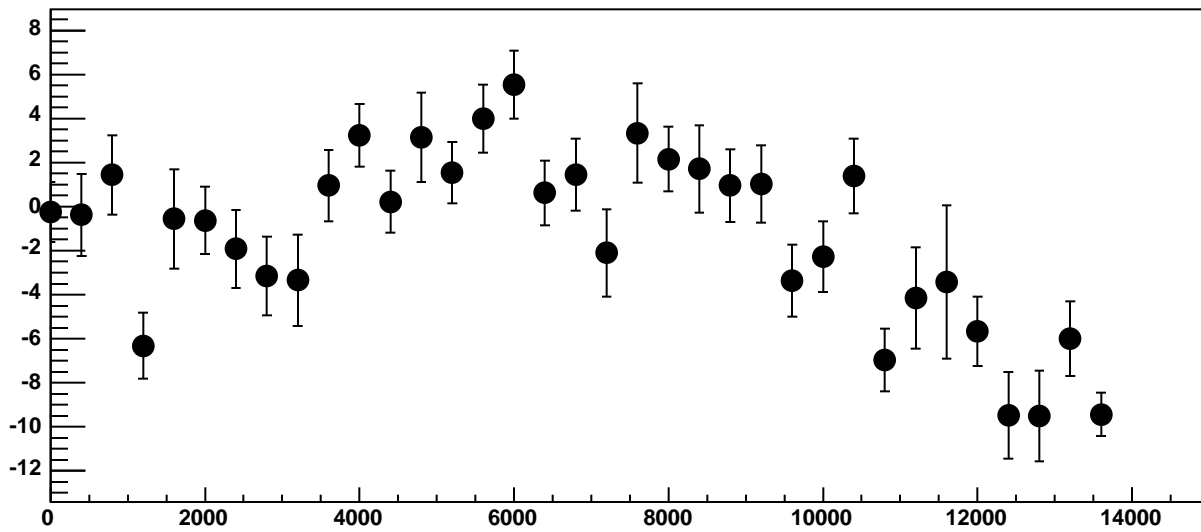
p1

0.002429 ± 0.0001144

Chip 4, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

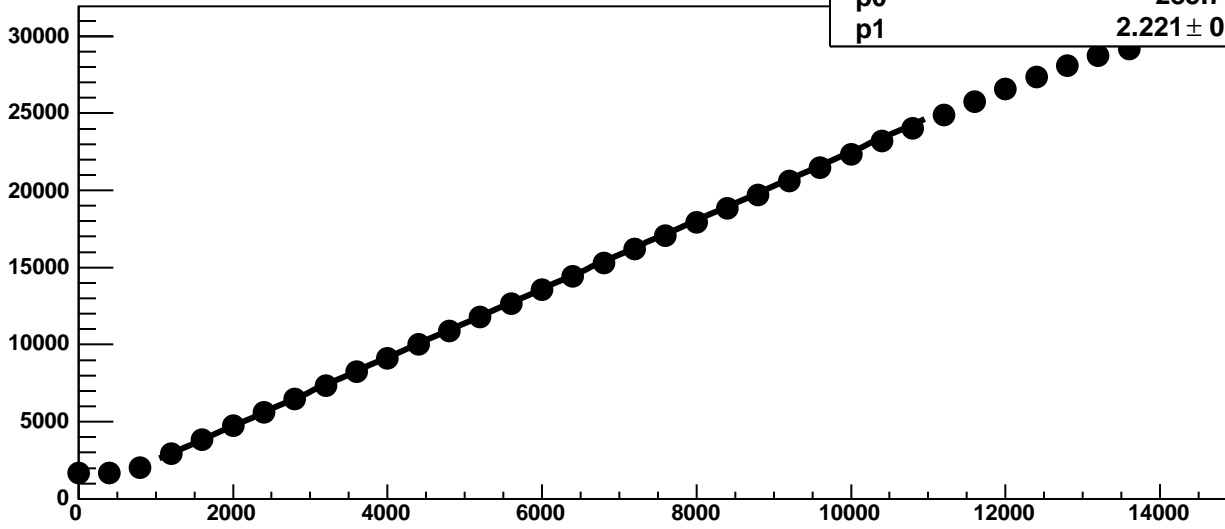


Chip 4, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

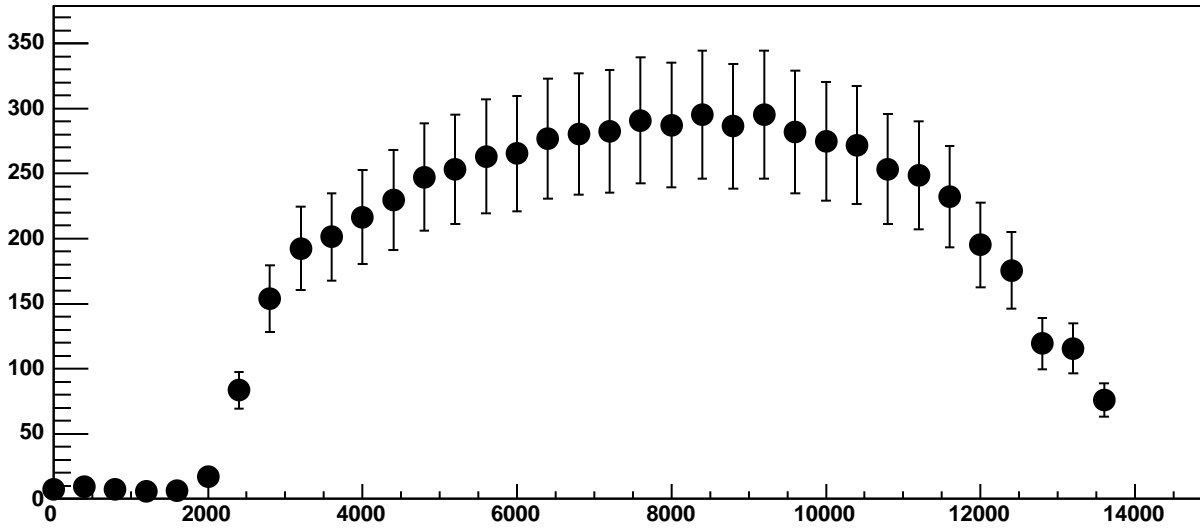


Chip 4, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC

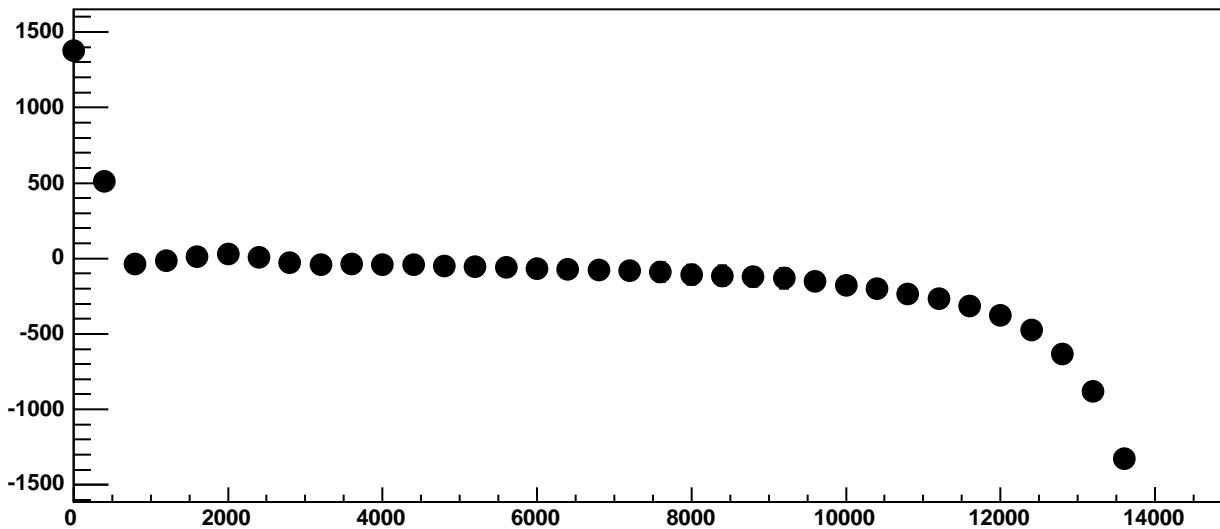
χ^2 / ndf 262.5 / 23
p0 285.7 ± 3.087
p1 2.221 ± 0.002007



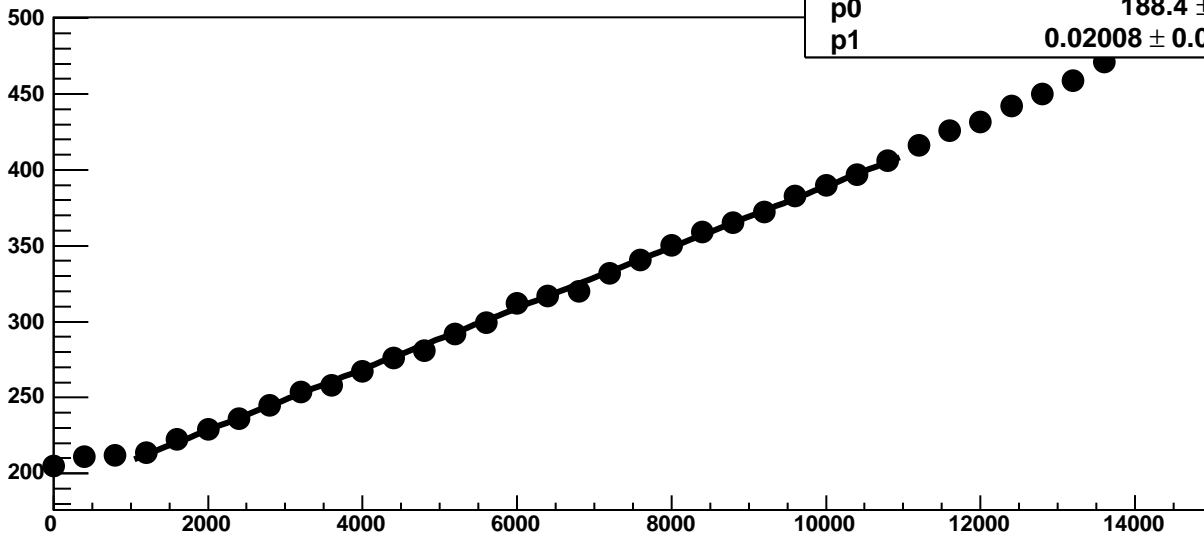
Chip 4, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

24.39 / 23

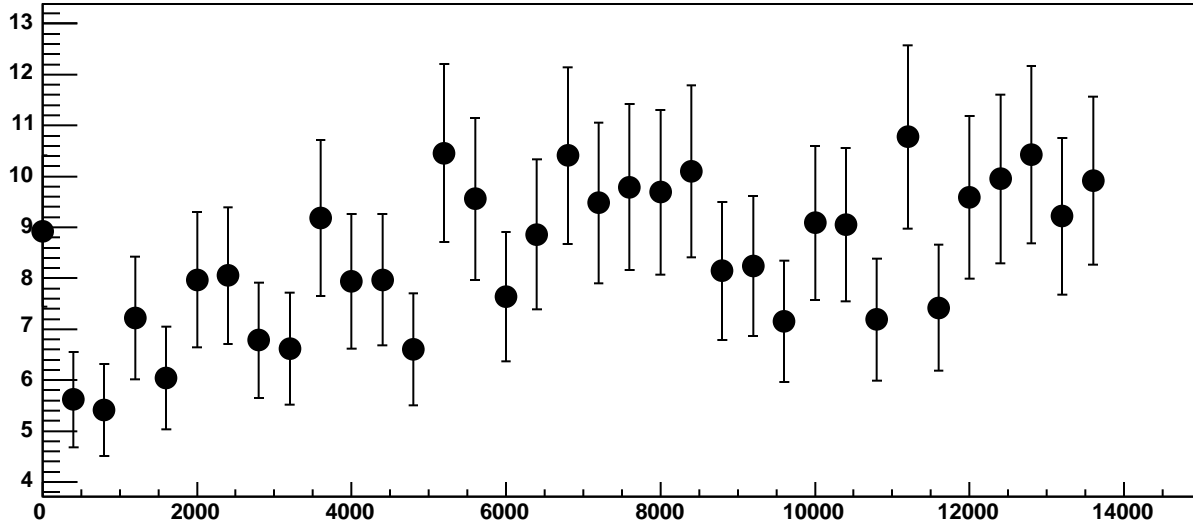
p0

188.4 ± 0.7791

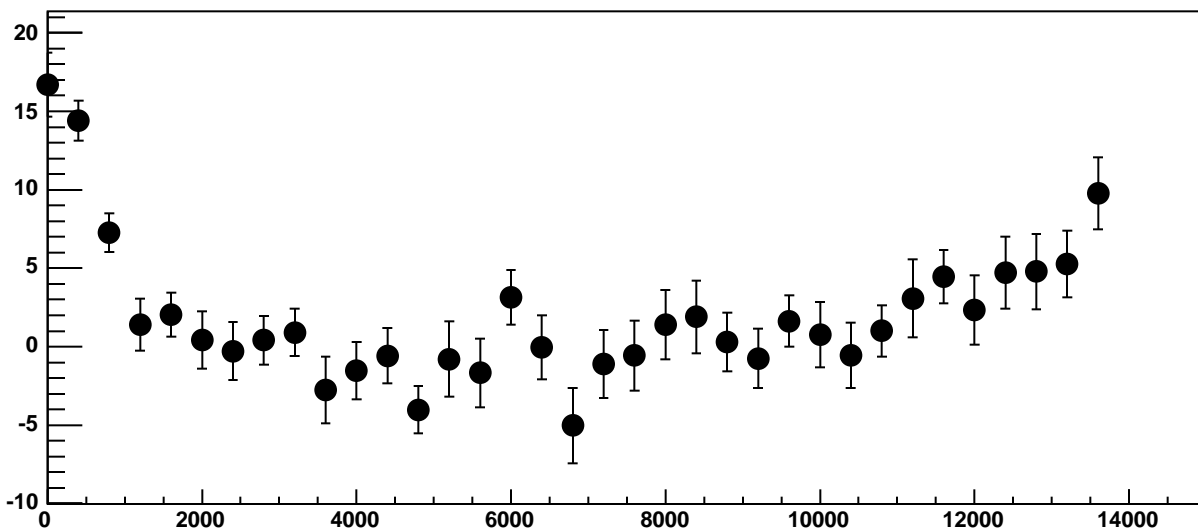
p1

0.02008 ± 0.0001221

Chip 4, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

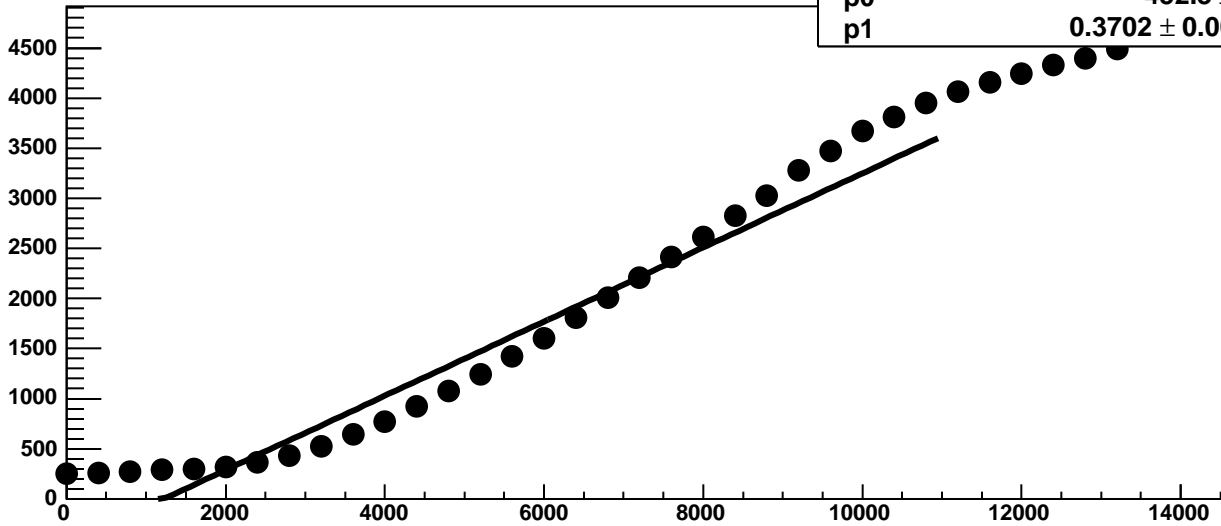


Chip 4, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

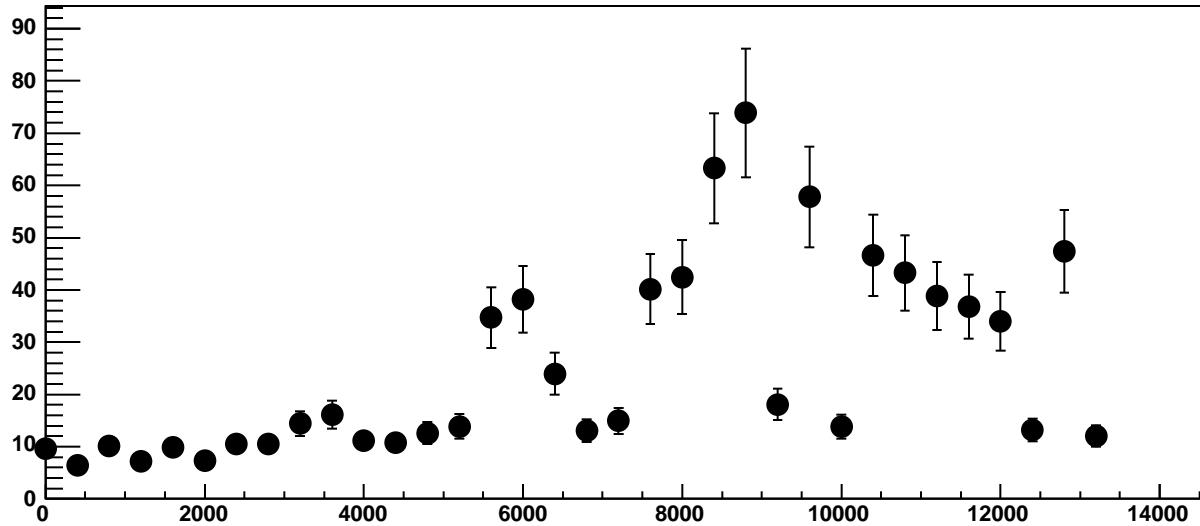


Chip 4, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

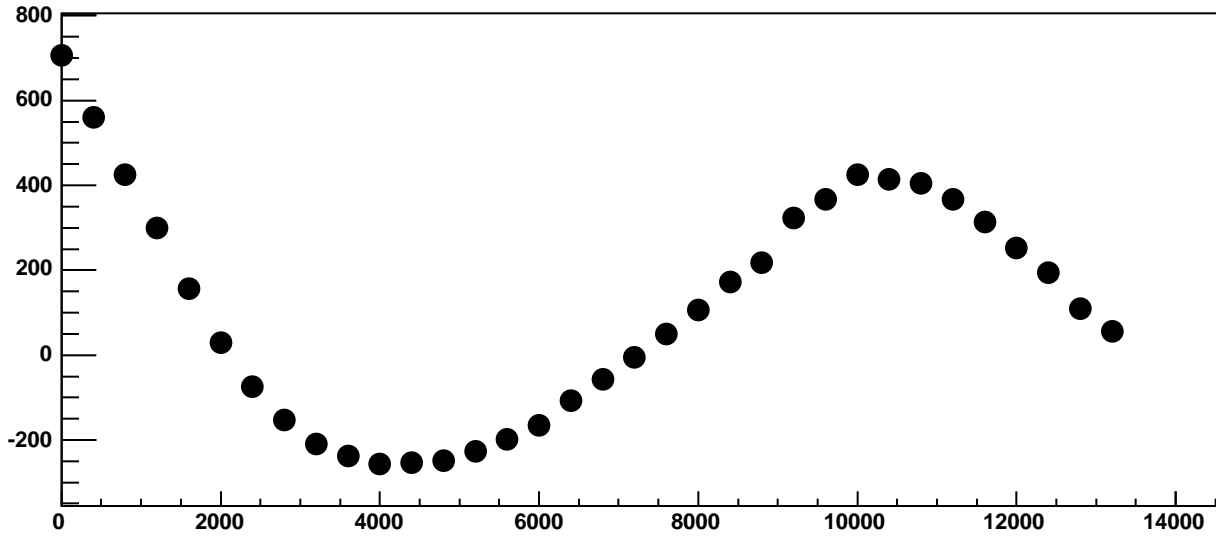
χ^2 / ndf 1.138e+05 / 23
p0 -452.3 ± 1.163
p1 0.3702 ± 0.0002588



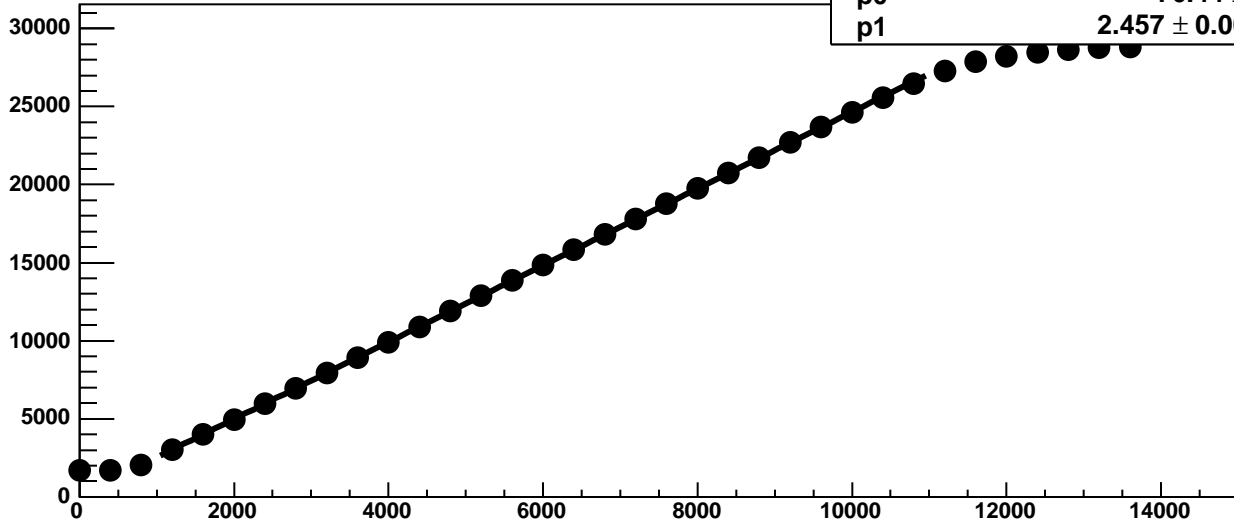
Chip 4, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



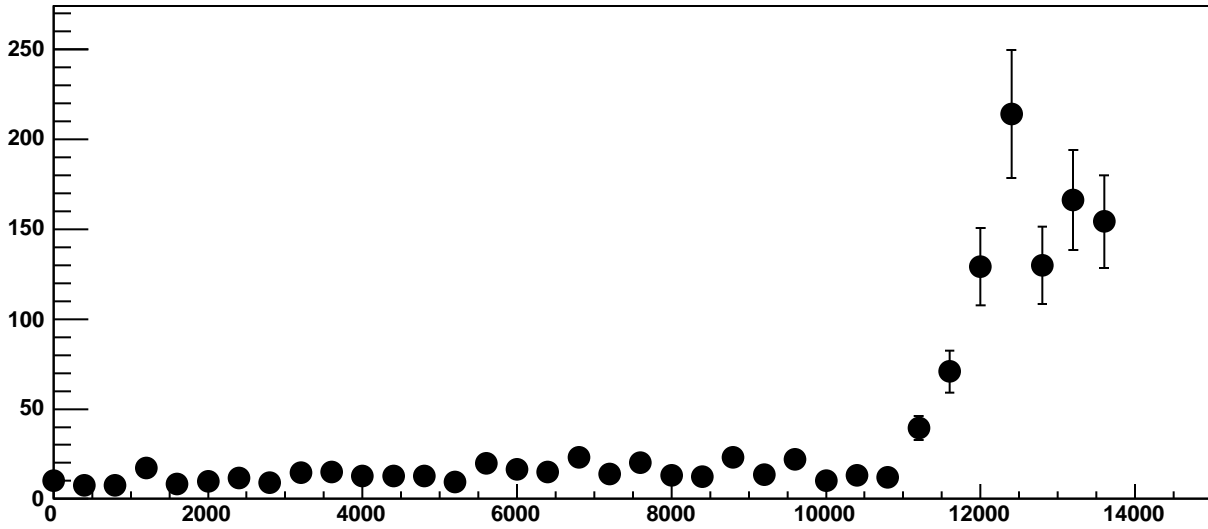
Chip 4, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



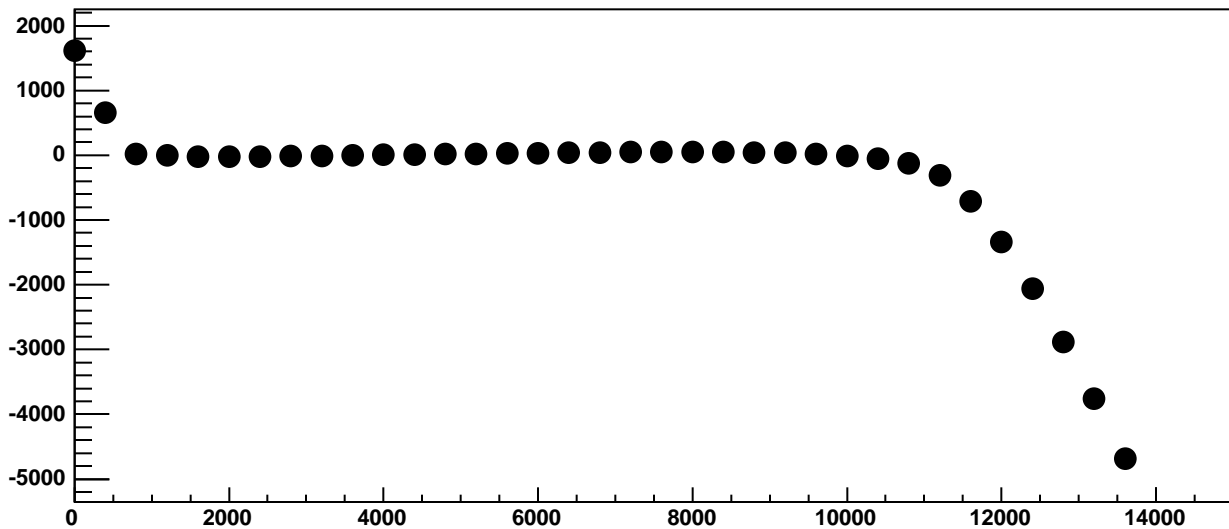
Chip 4, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC



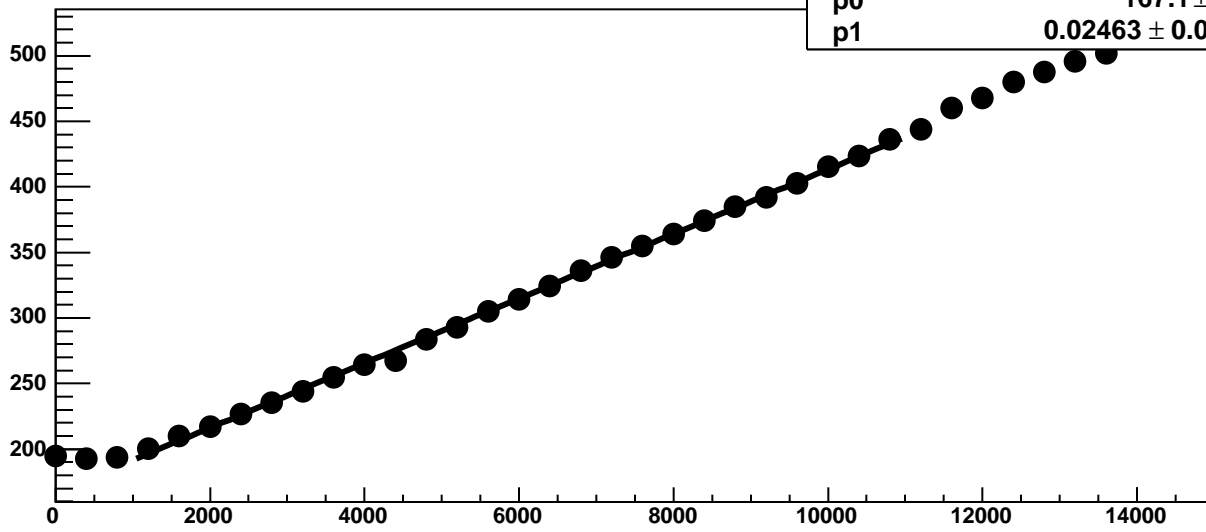
Chip 4, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC

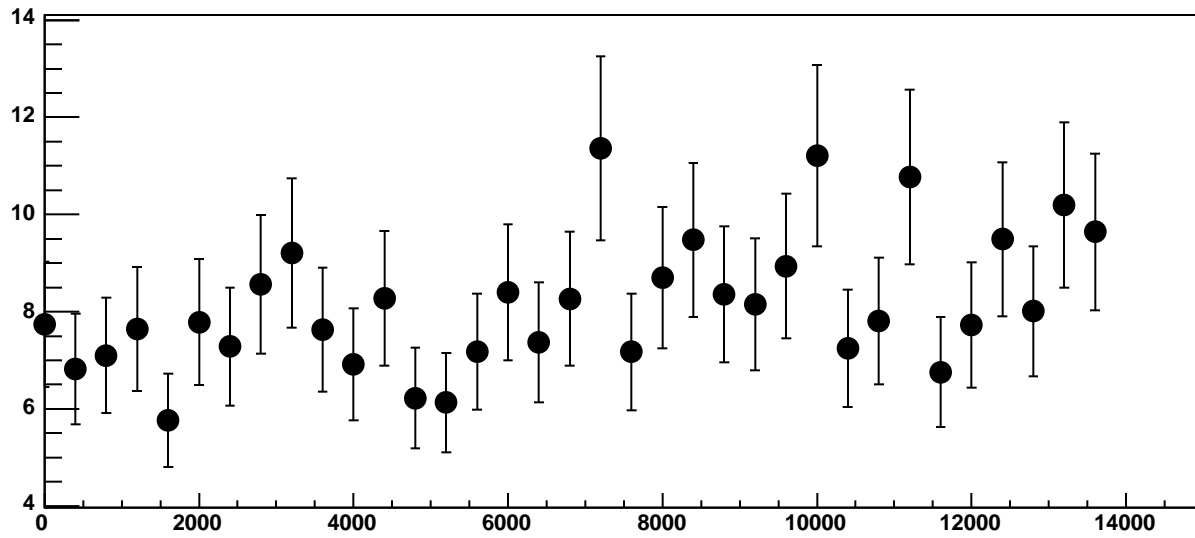


Chip 4, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

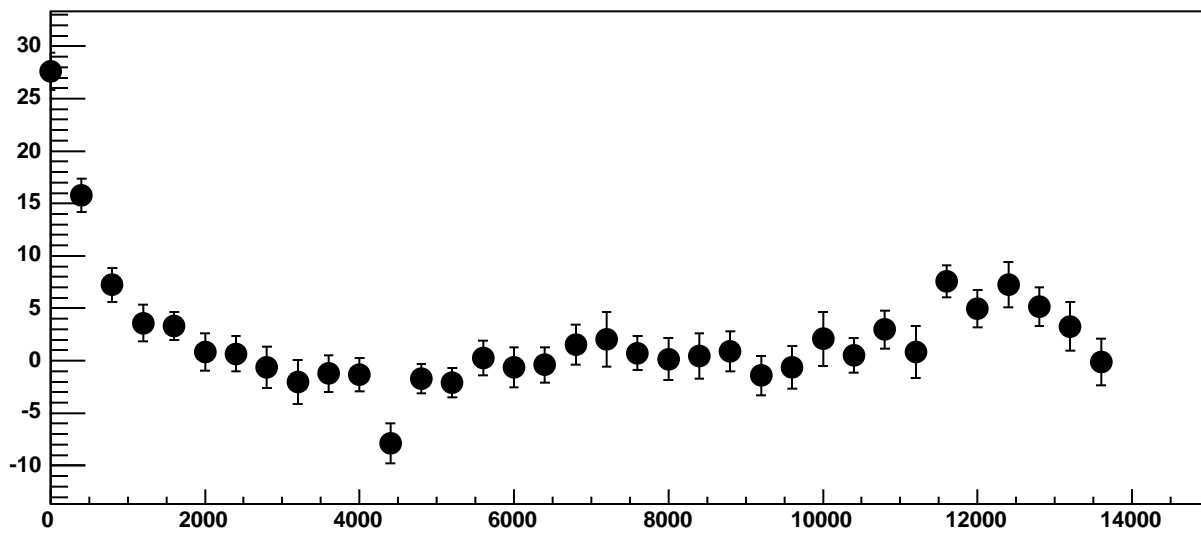


χ^2 / ndf 39.98 / 23
p0 167.1 ± 0.7835
p1 0.02463 ± 0.0001239

Chip 4, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

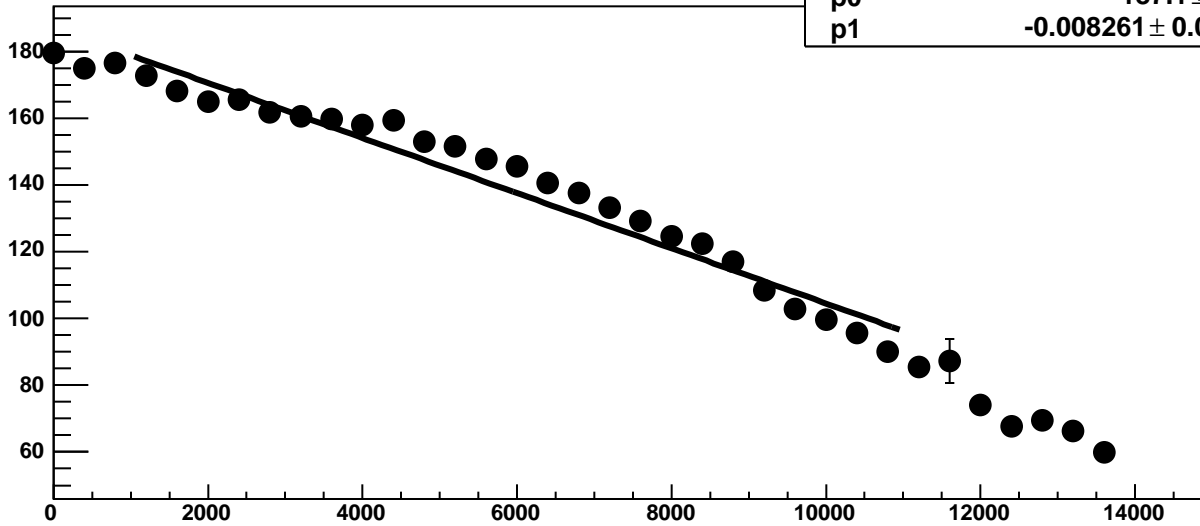


Chip 4, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

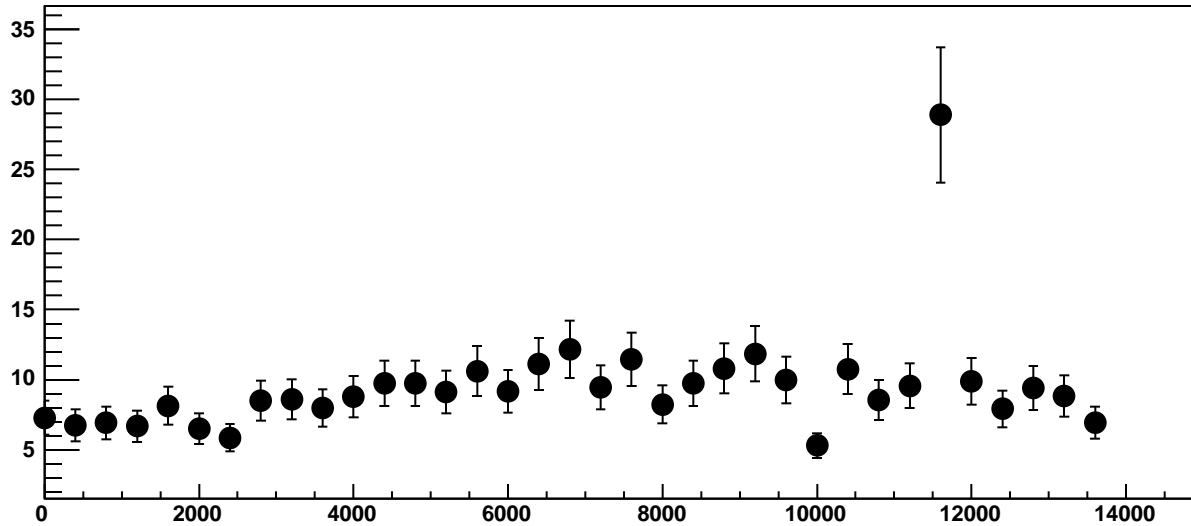


Chip 4, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

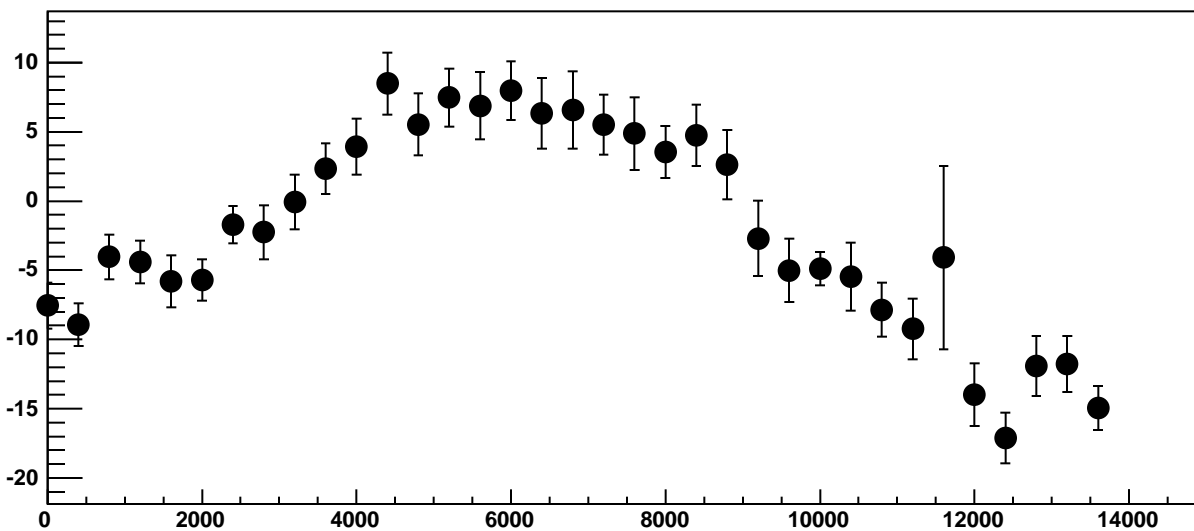
χ^2 / ndf 169.9 / 23
p0 187.1 ± 0.7957
p1 -0.008261 ± 0.0001241



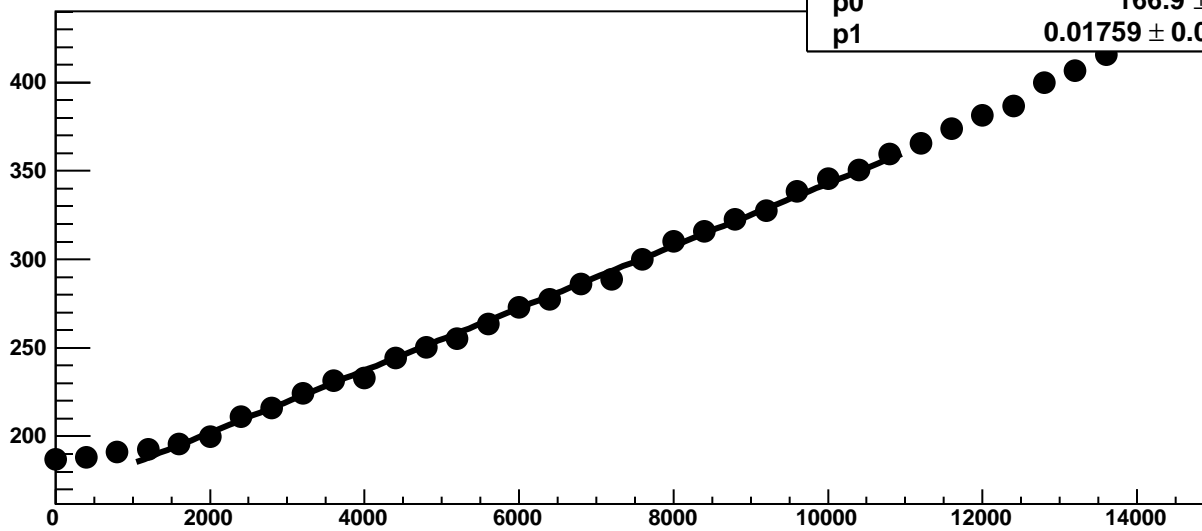
Chip 4, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

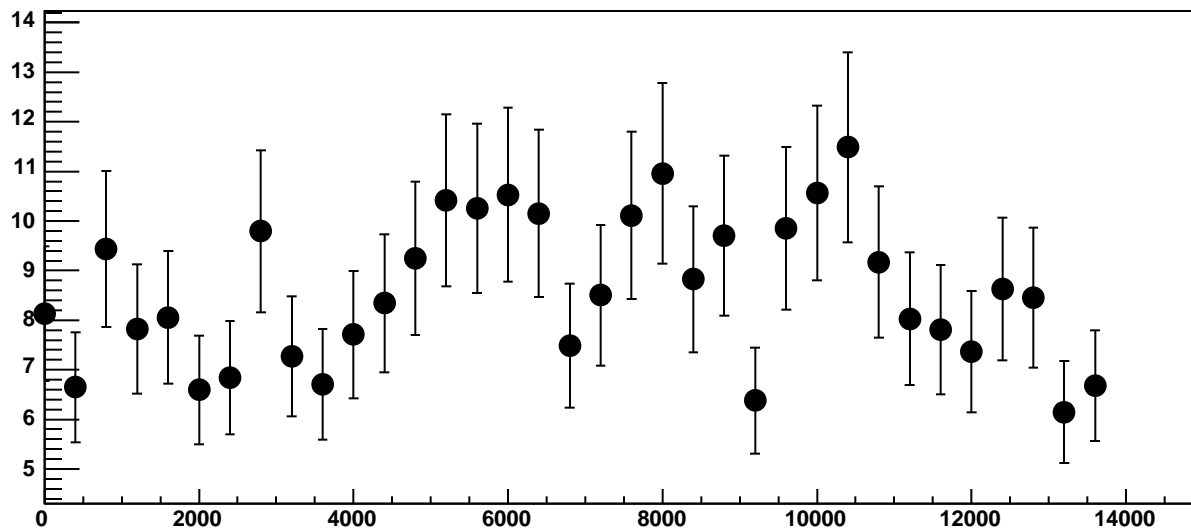


Chip 4, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

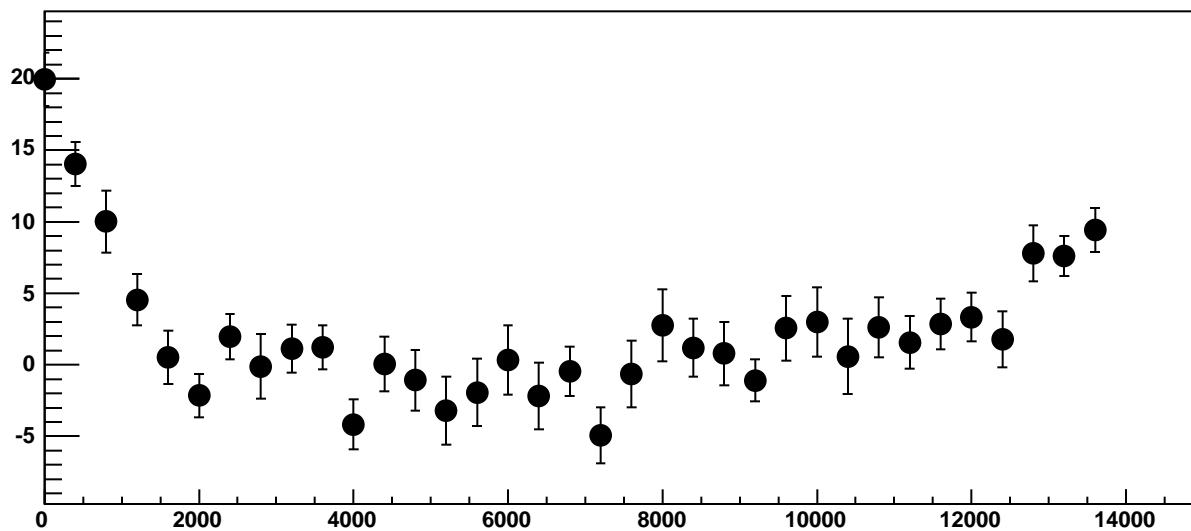


χ^2 / ndf 33.5 / 23
p0 166.9 ± 0.8343
p1 0.01759 ± 0.0001336

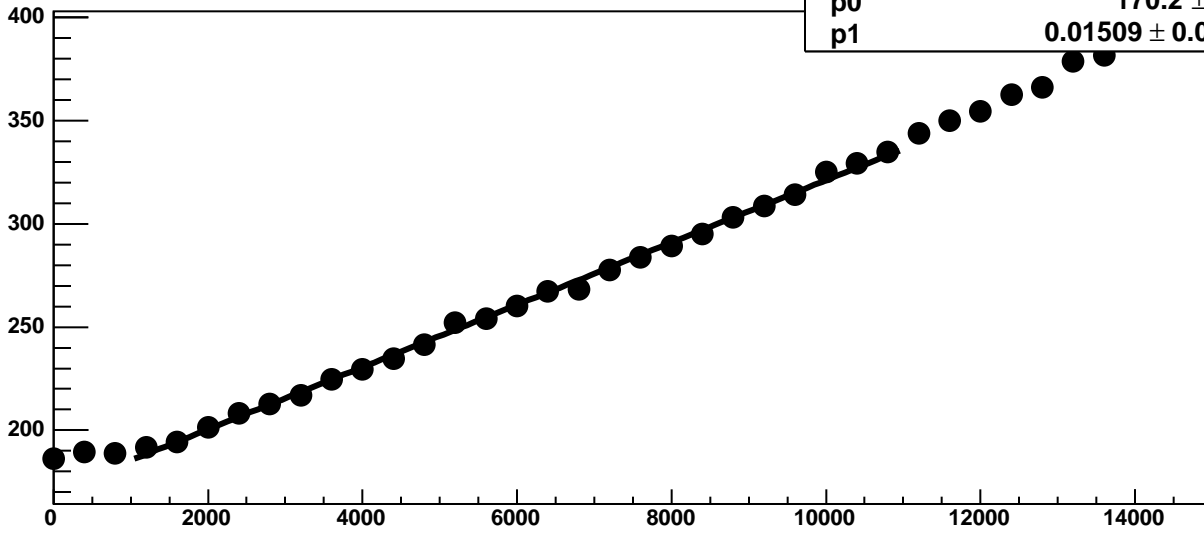
Chip 4, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

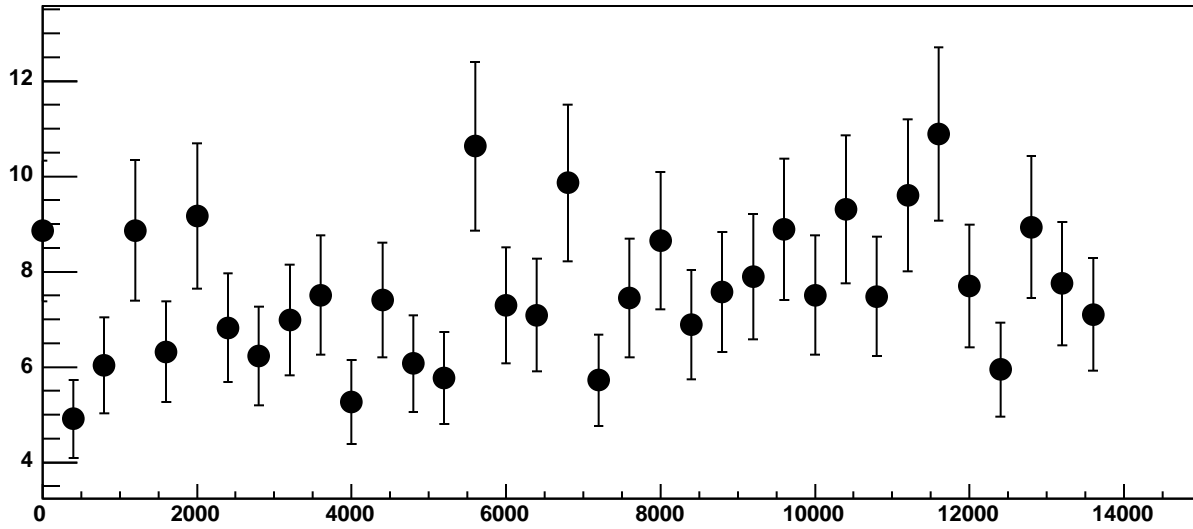


Chip 4, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

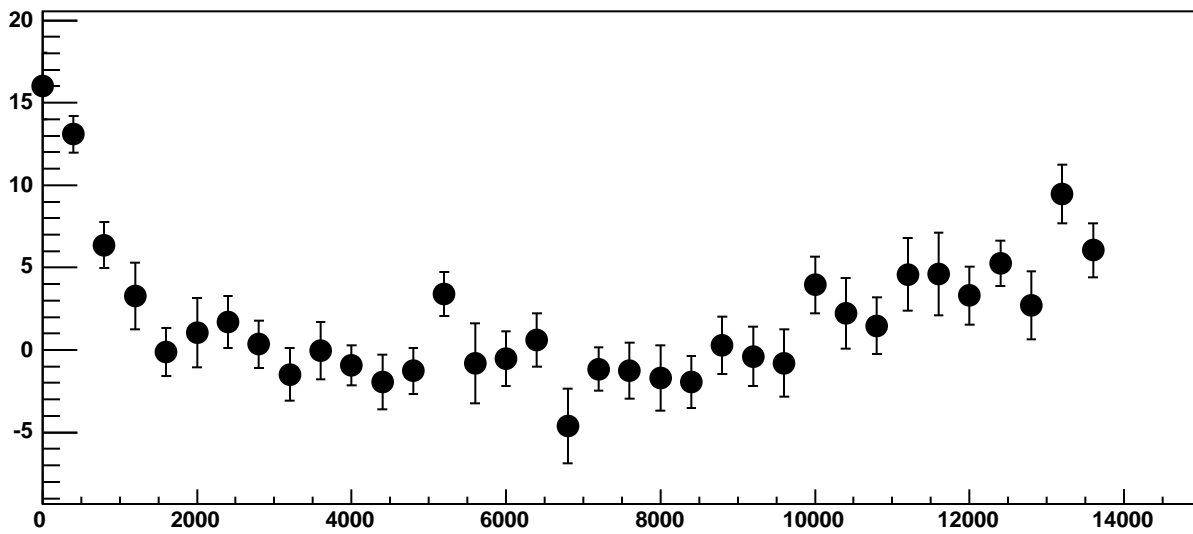


χ^2 / ndf 29.62 / 23
p0 170.2 ± 0.7637
p1 0.01509 ± 0.0001201

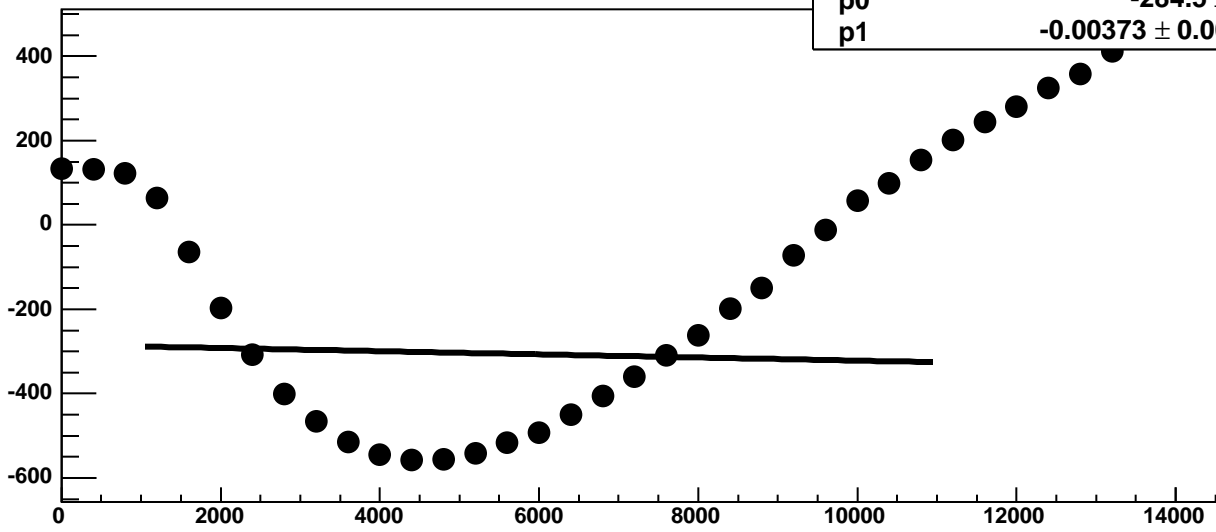
Chip 4, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

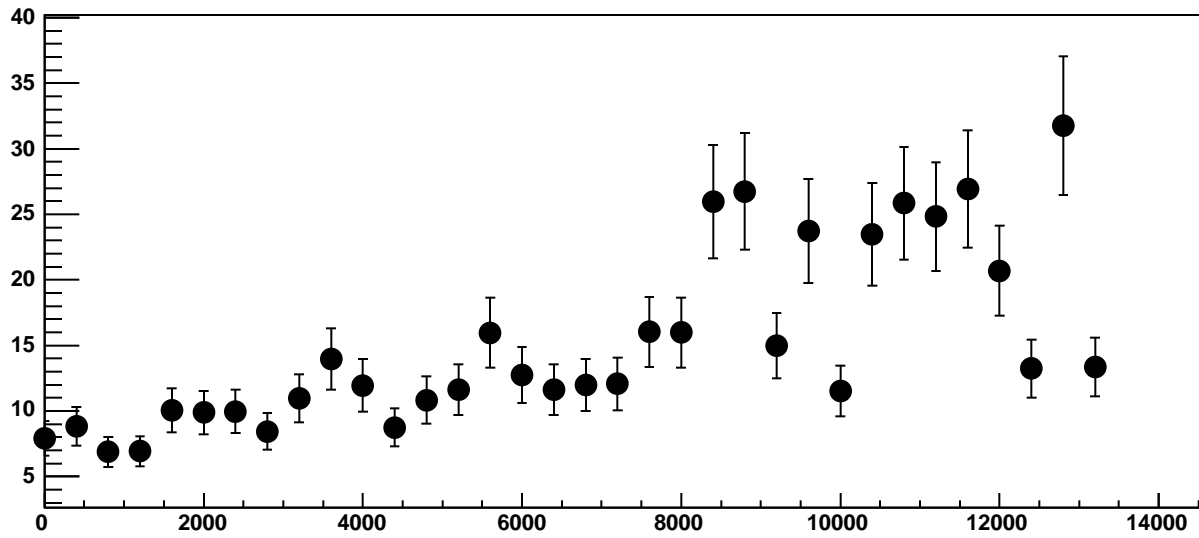


Chip 4, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

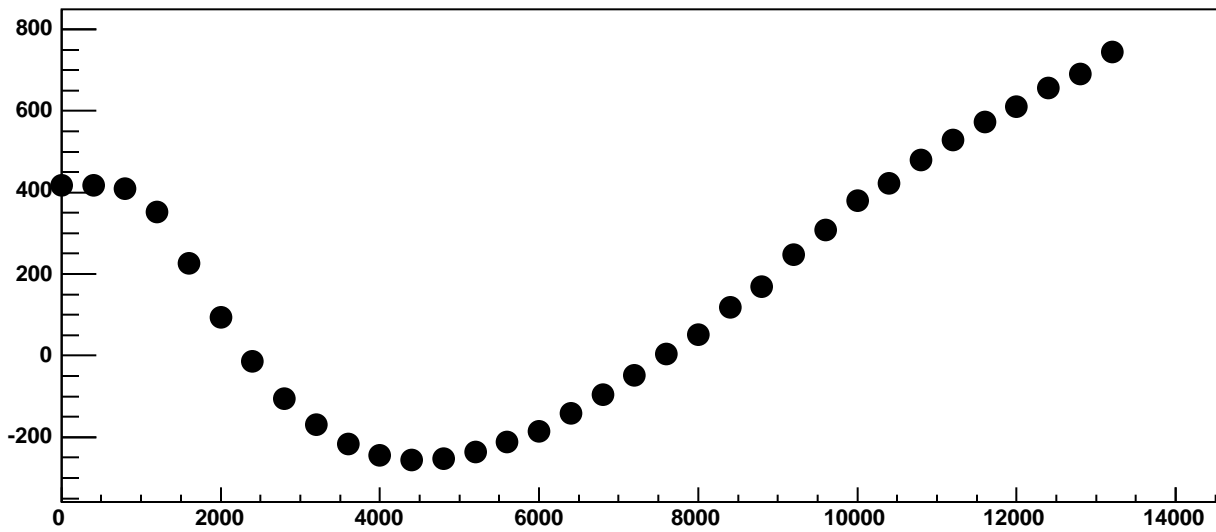


χ^2 / ndf 1.698e+05 / 23
p0 -284.5 ± 1.088
p1 -0.00373 ± 0.0002058

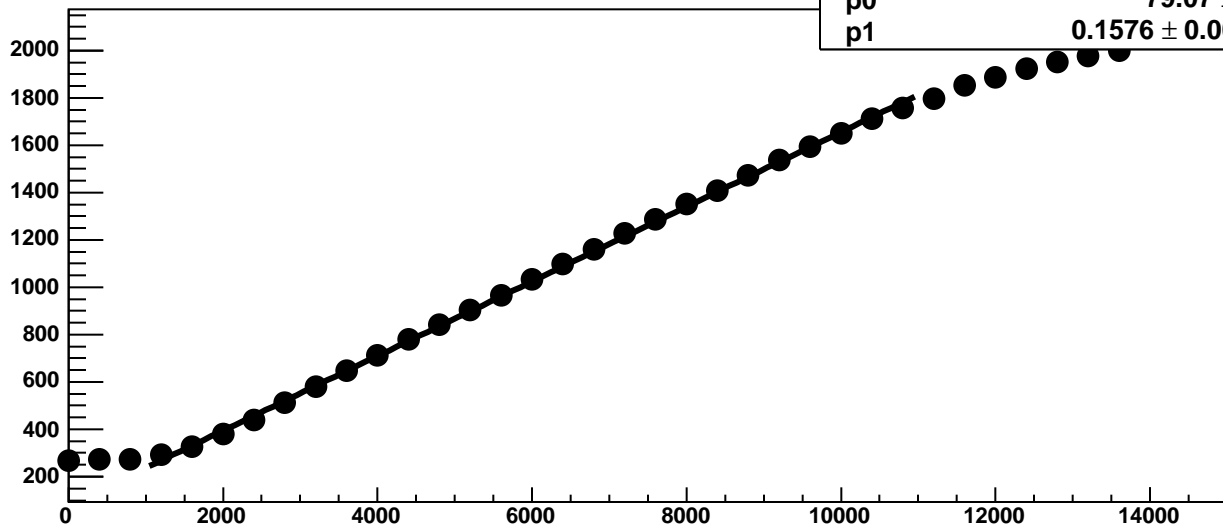
Chip 4, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

332.1 / 23

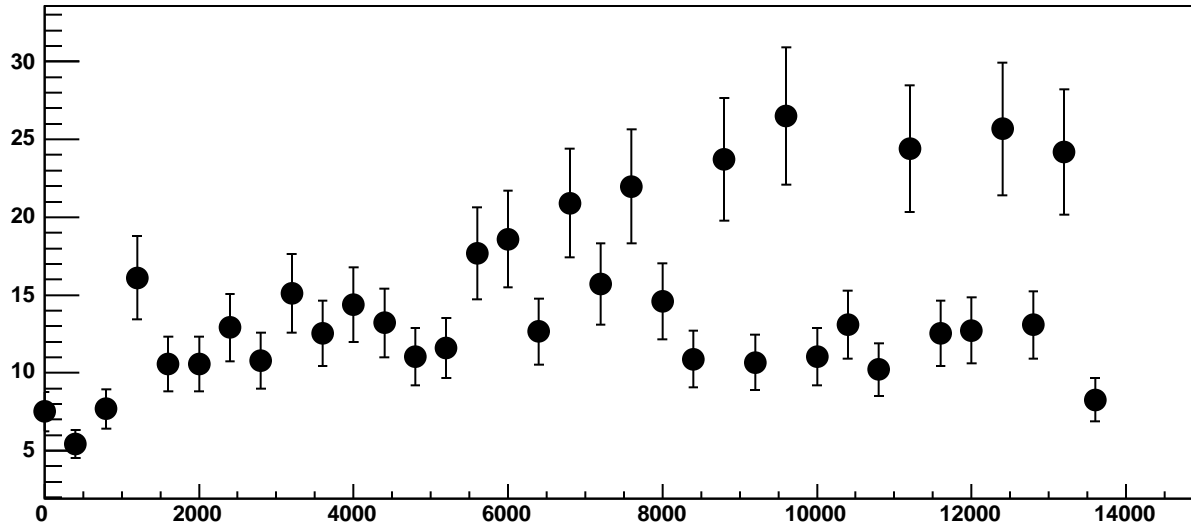
p0

79.07 ± 1.302

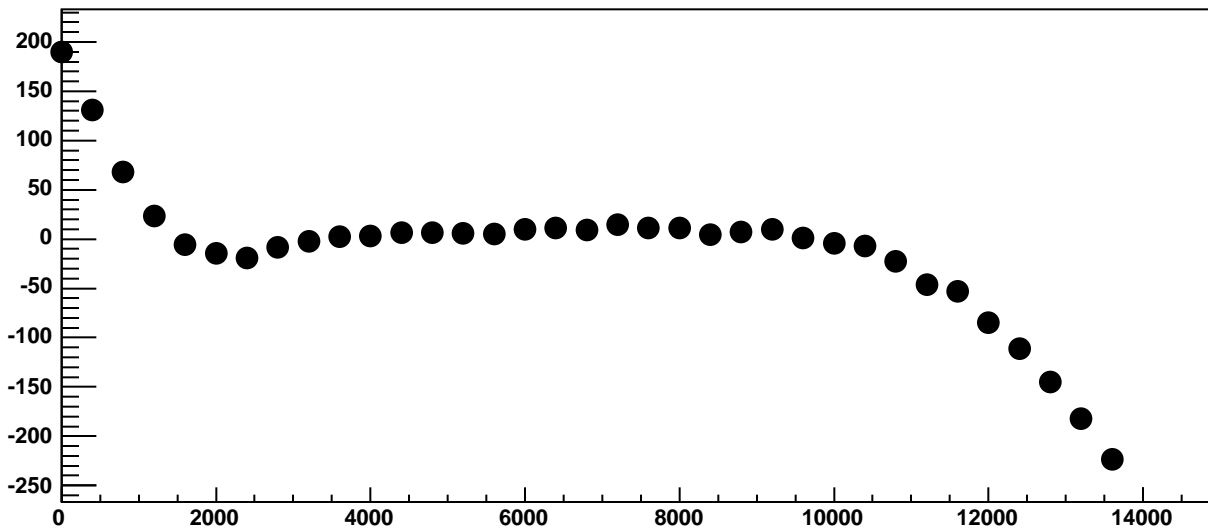
p1

0.1576 ± 0.0001968

Chip 4, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

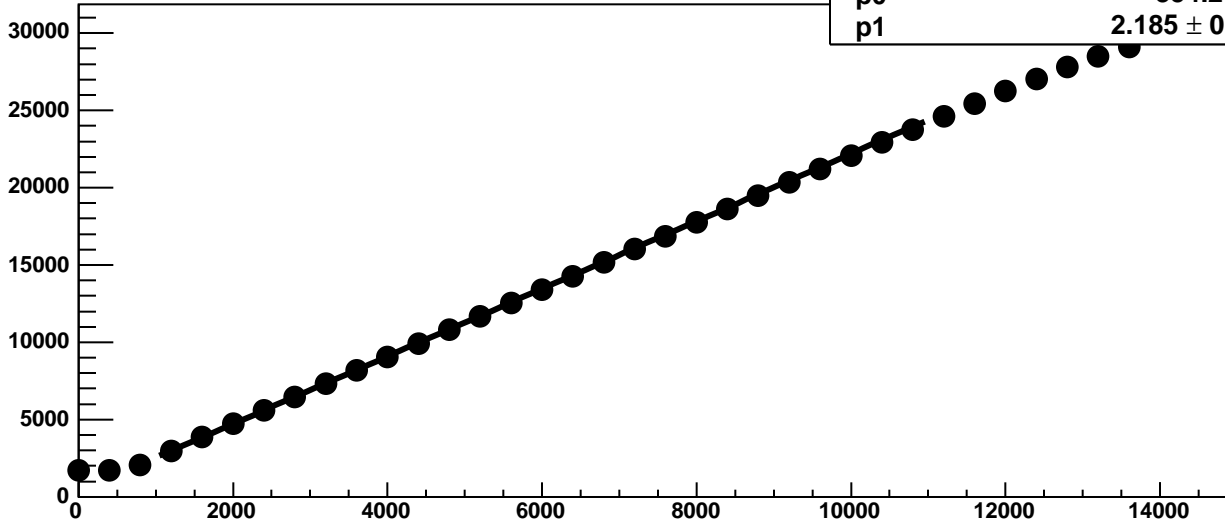


Chip 4, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

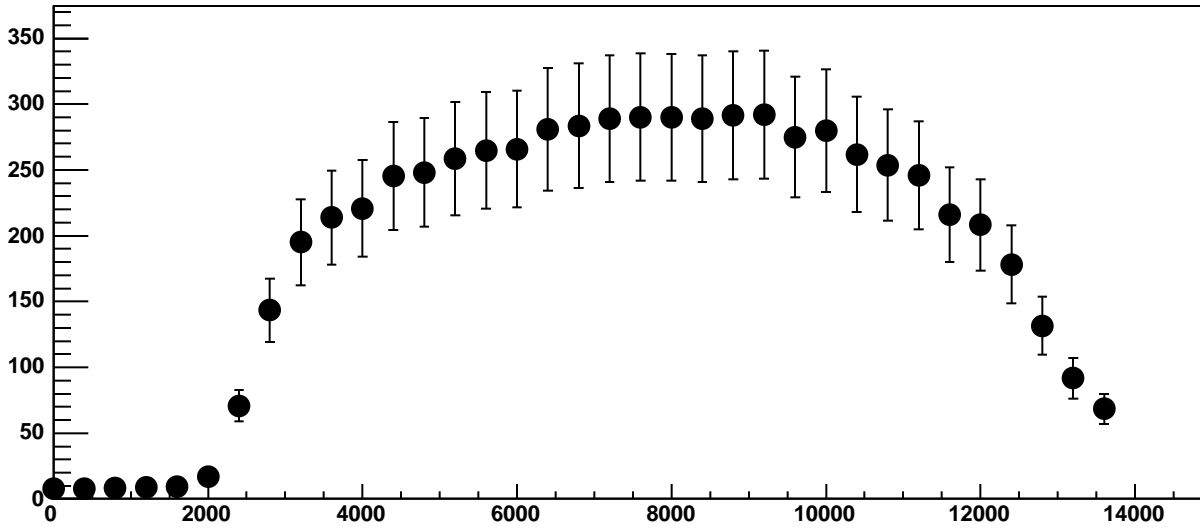


Chip 4, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC

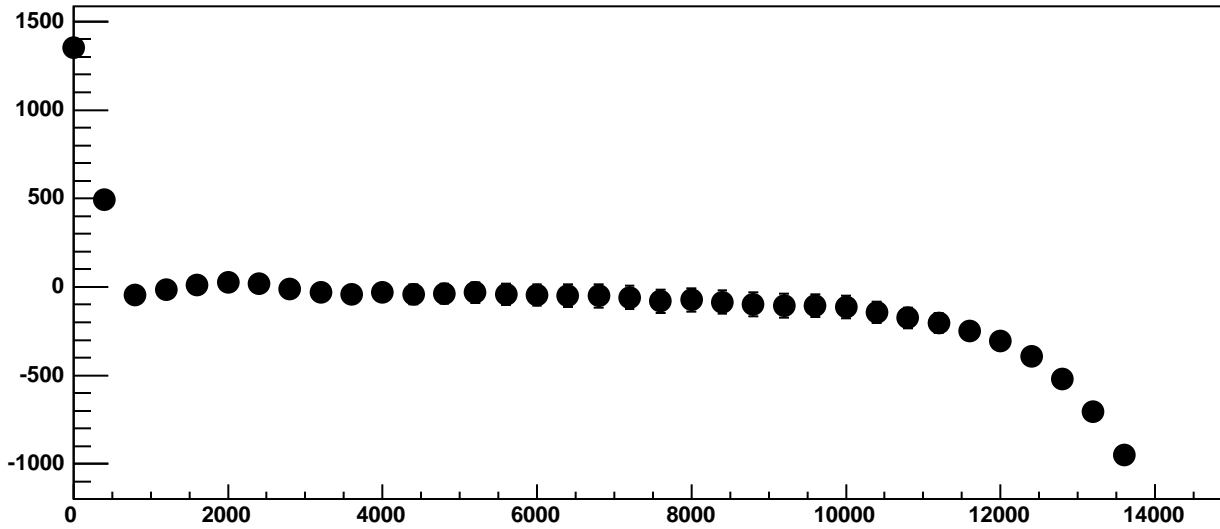
χ^2 / ndf 192.4 / 23
p0 354.2 ± 3.476
p1 2.185 ± 0.002102



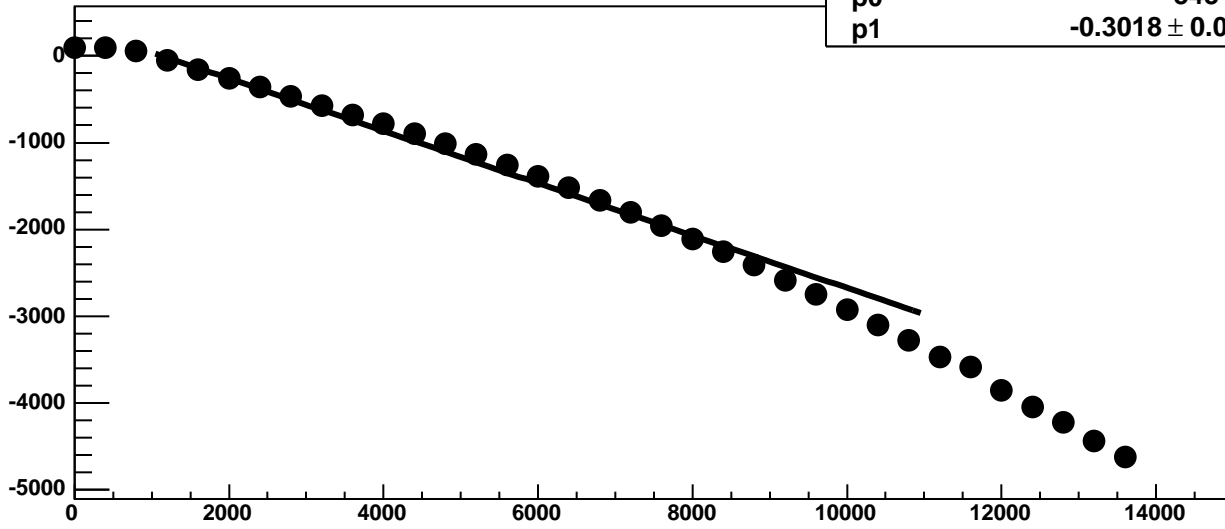
Chip 4, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

4120 / 23

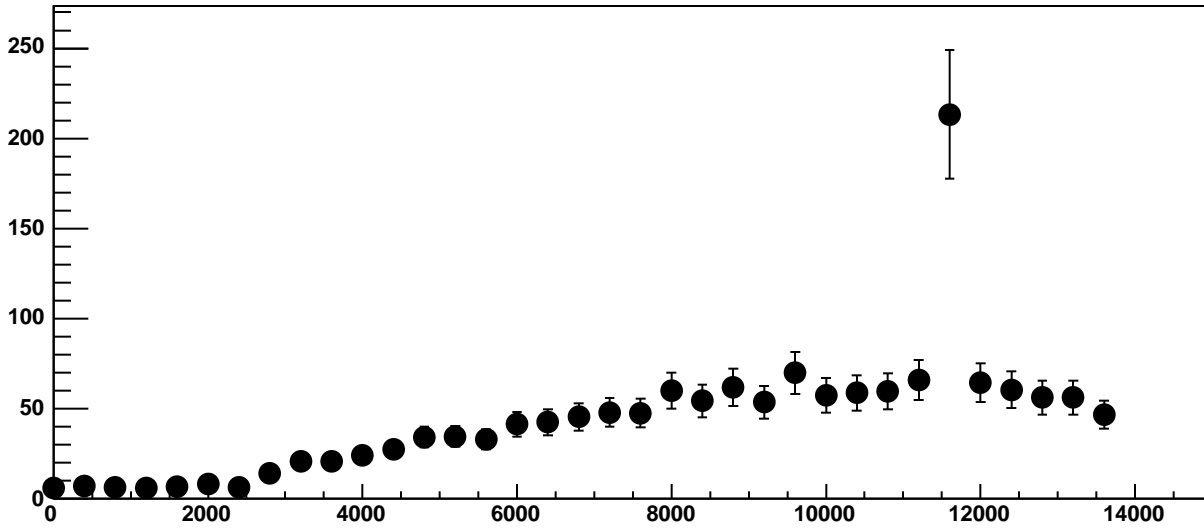
p0

343 ± 1.272

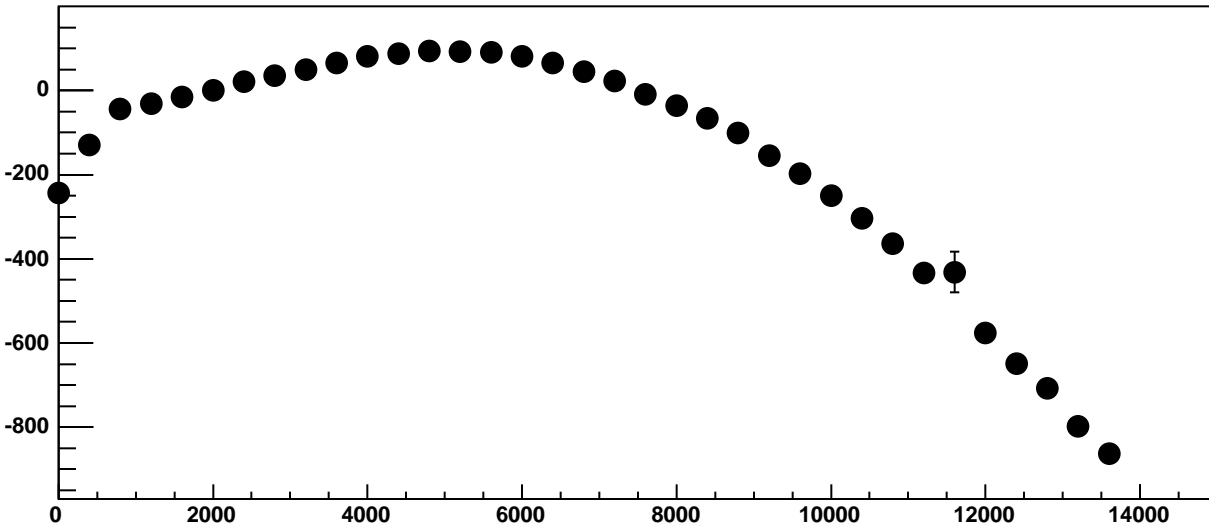
p1

-0.3018 ± 0.0004578

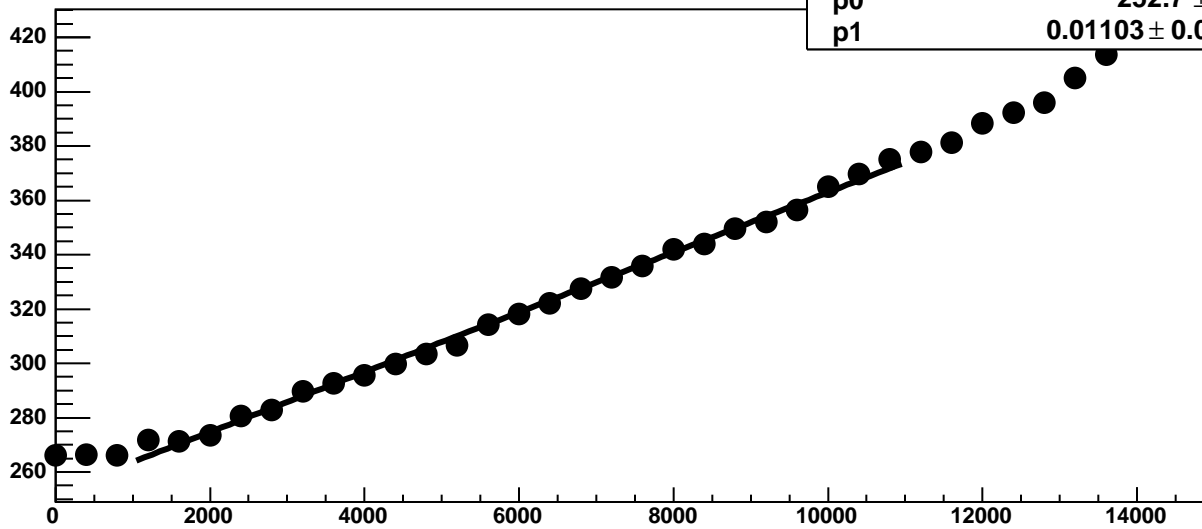
Chip 4, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

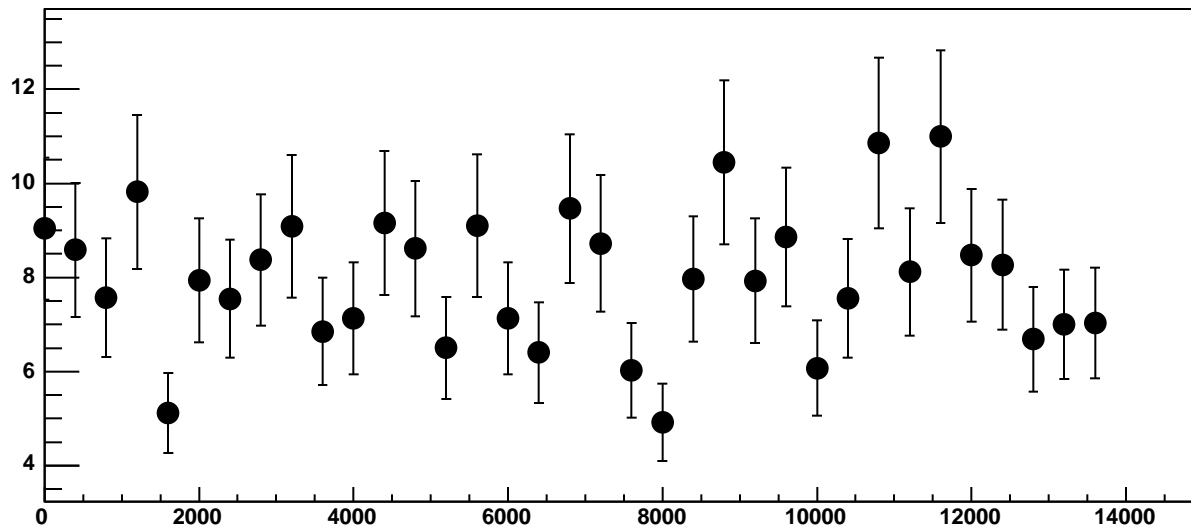


Chip 4, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

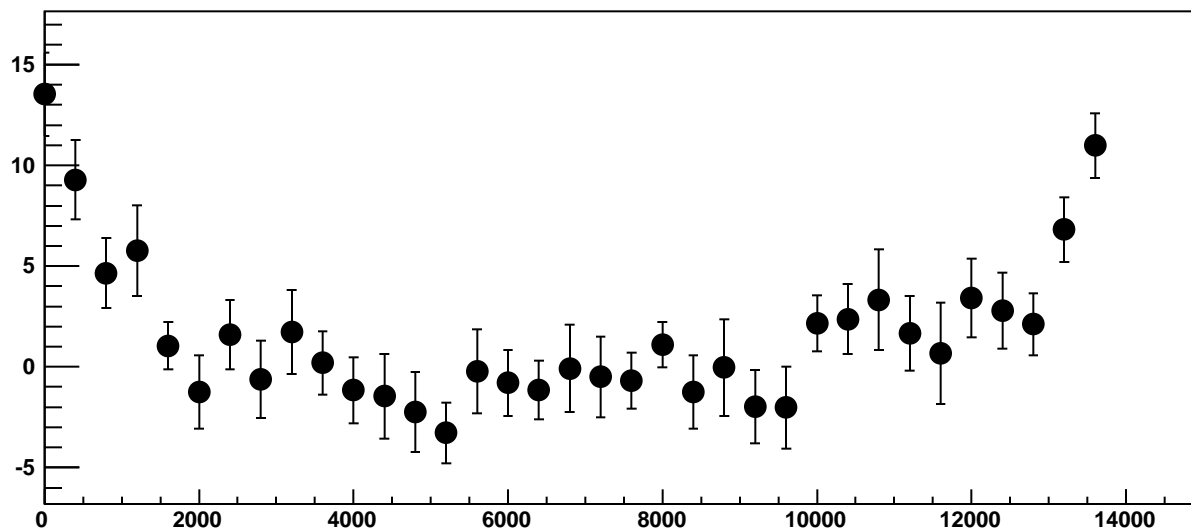


χ^2 / ndf 27.39 / 23
p0 252.7 ± 0.7873
p1 0.01103 ± 0.0001194

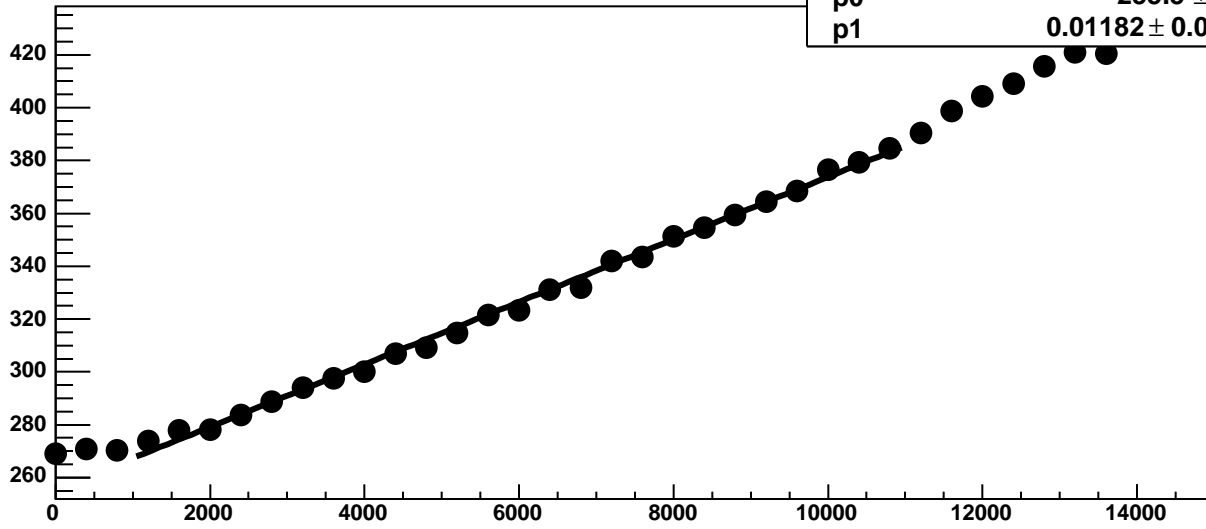
Chip 4, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

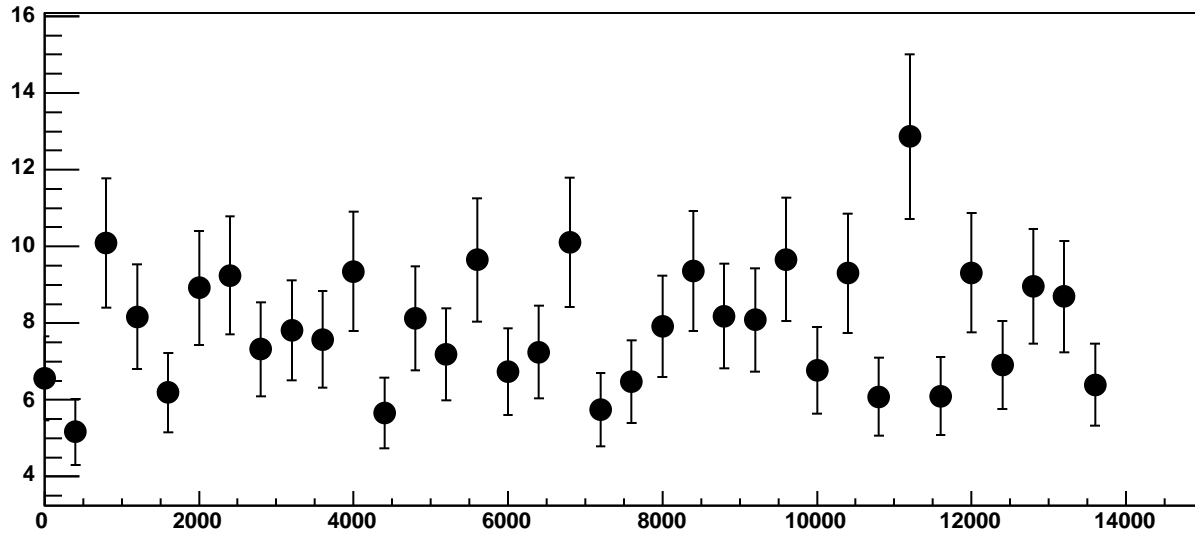


Chip 4, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC

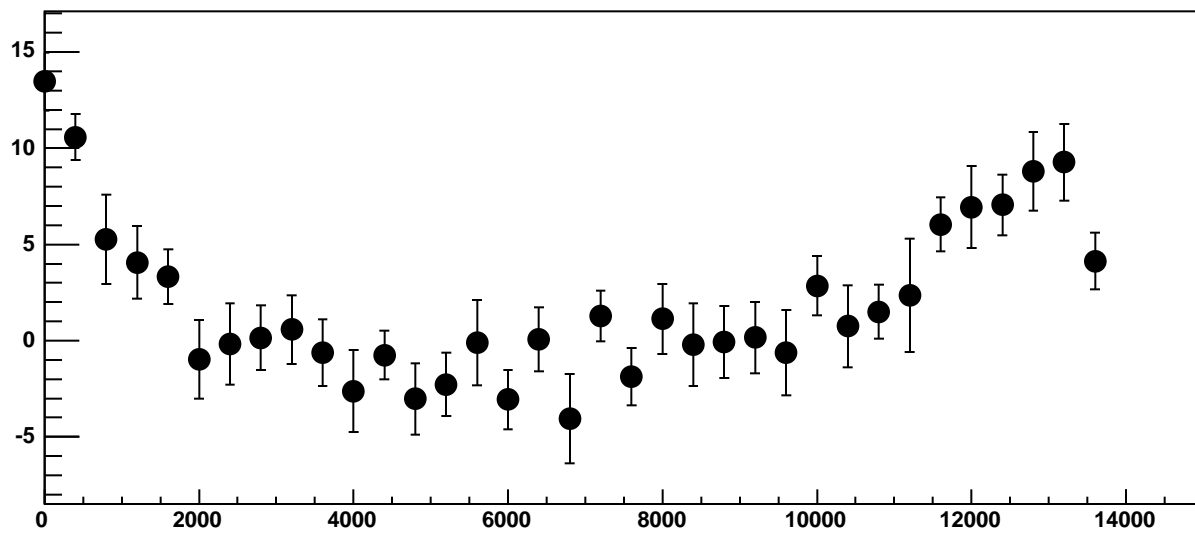


χ^2 / ndf 31.75 / 23
p0 255.5 ± 0.8016
p1 0.01182 ± 0.0001202

Chip 4, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC

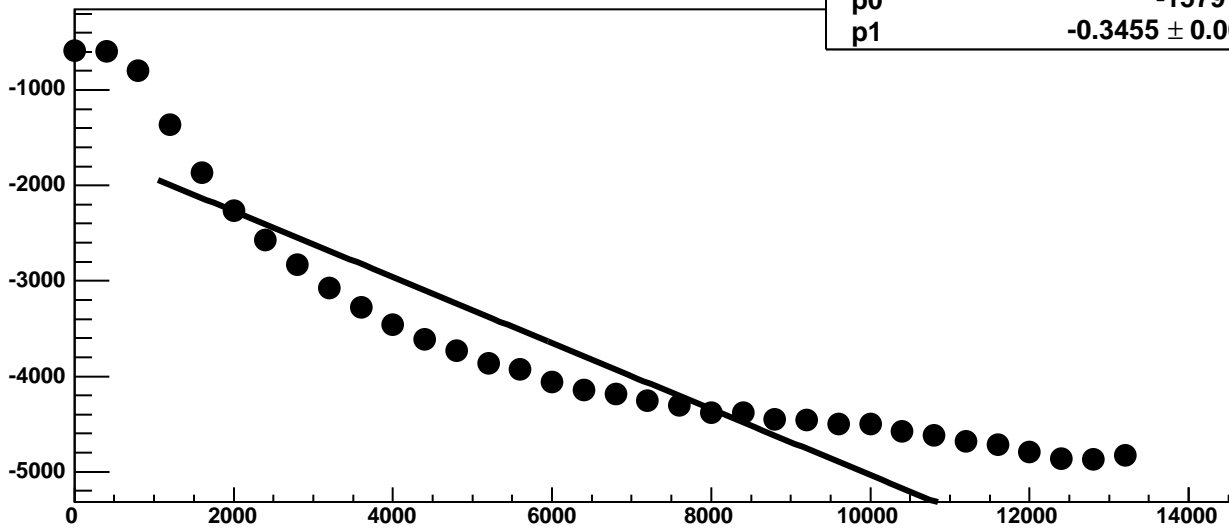


Chip 4, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC

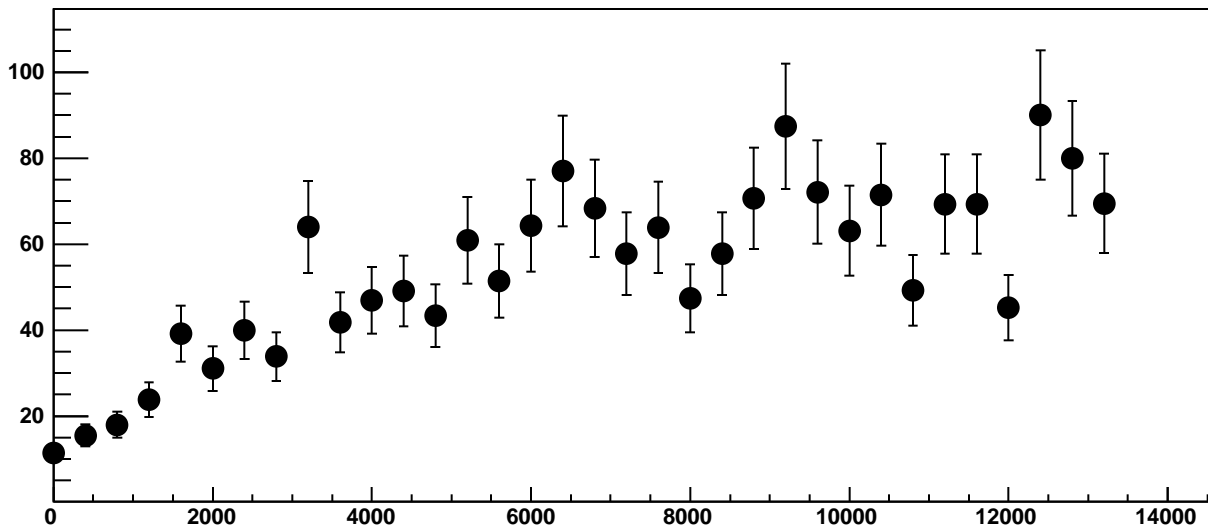


Chip 4, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

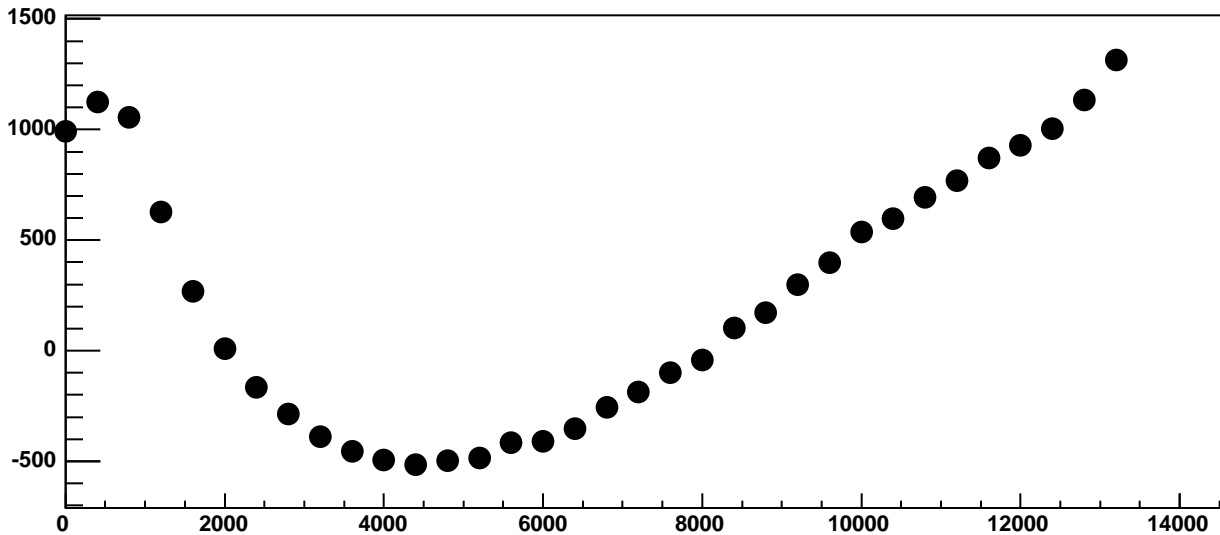
χ^2 / ndf 3.701e+04 / 23
p0 -1579 ± 3.941
p1 -0.3455 ± 0.0007356



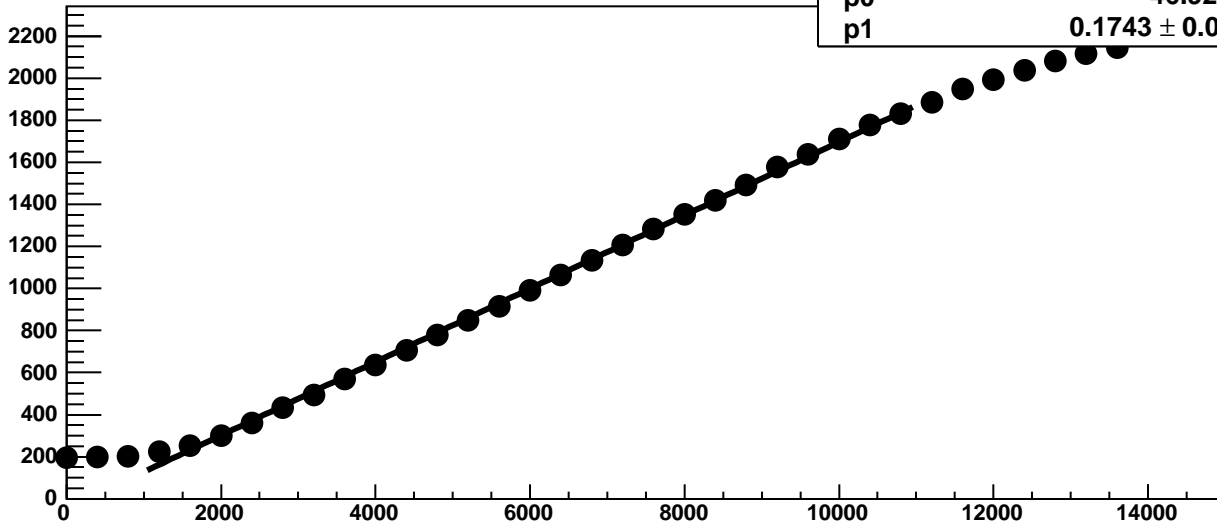
Chip 4, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

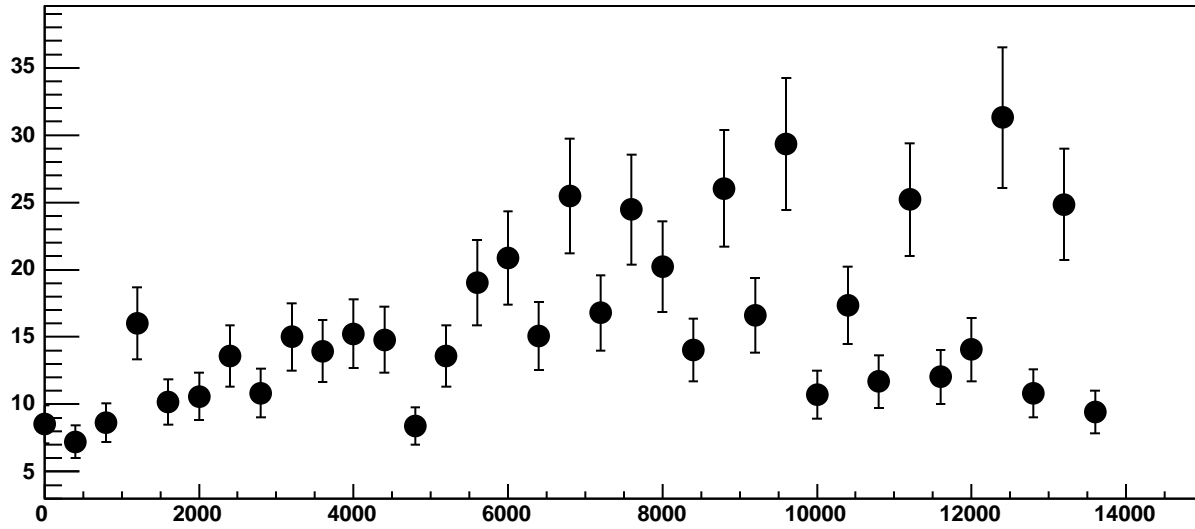


Chip 4, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

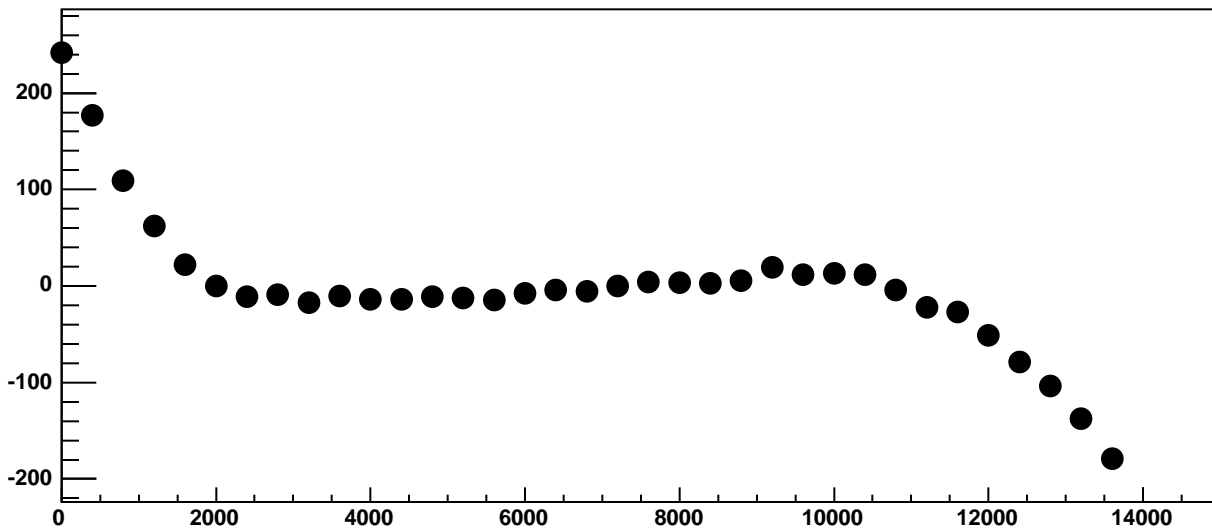


χ^2 / ndf 604.1 / 23
p0 -46.92 ± 1.331
p1 0.1743 ± 0.0002153

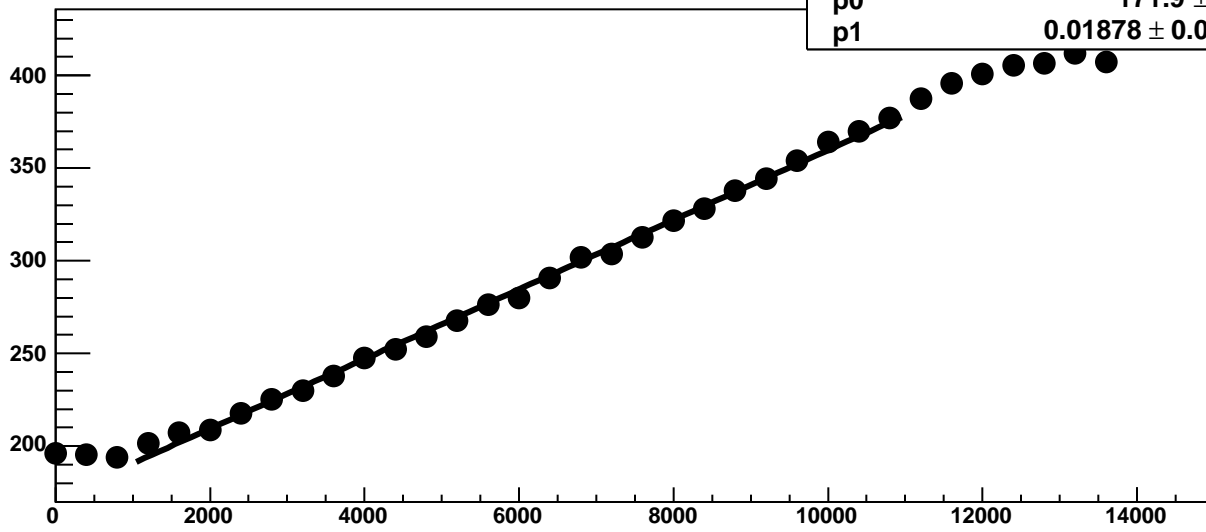
Chip 4, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



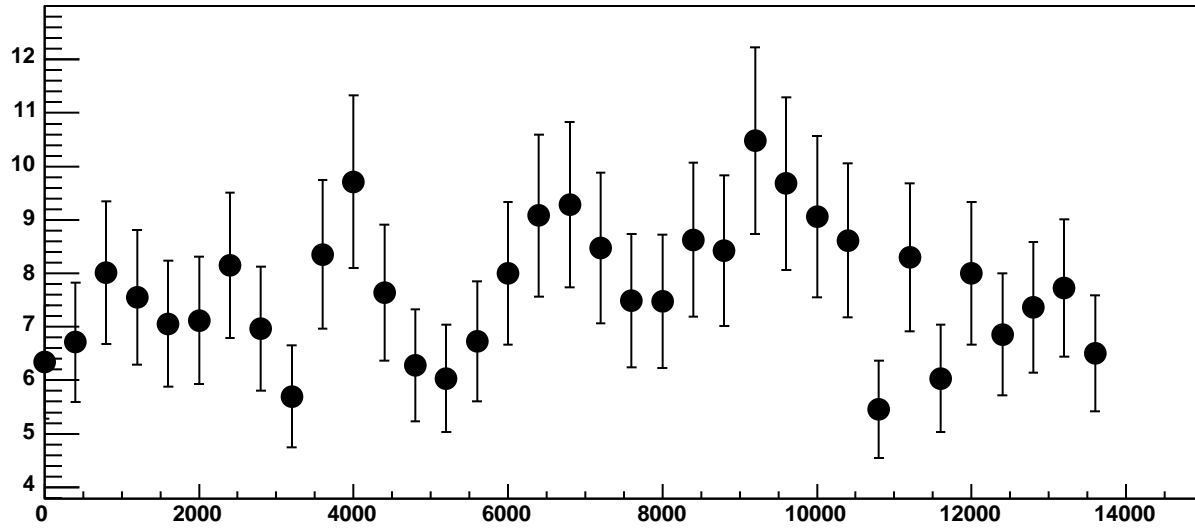
Chip 4, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



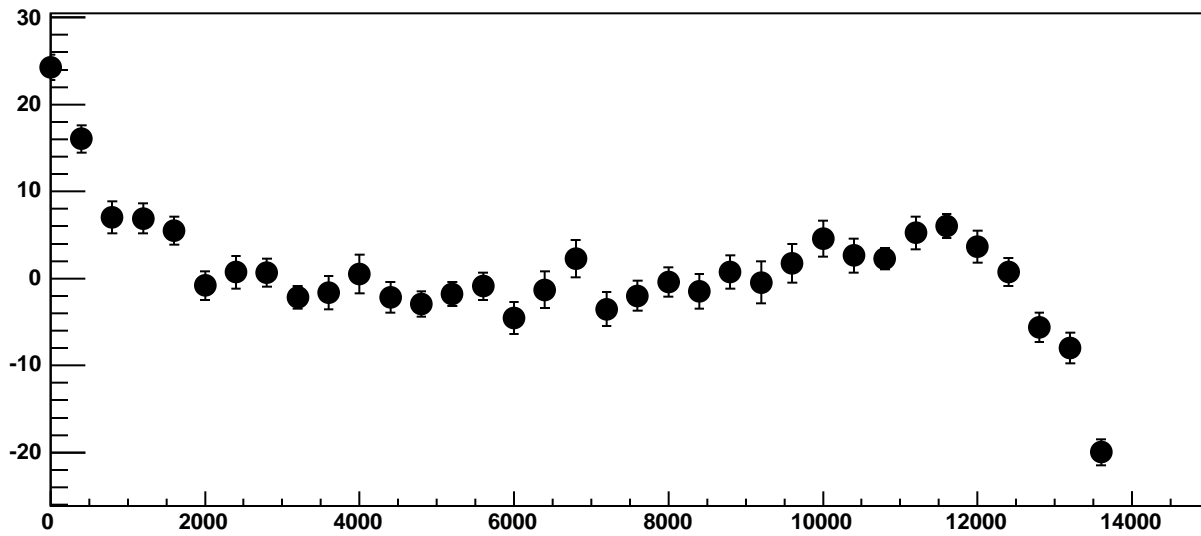
Chip 4, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC



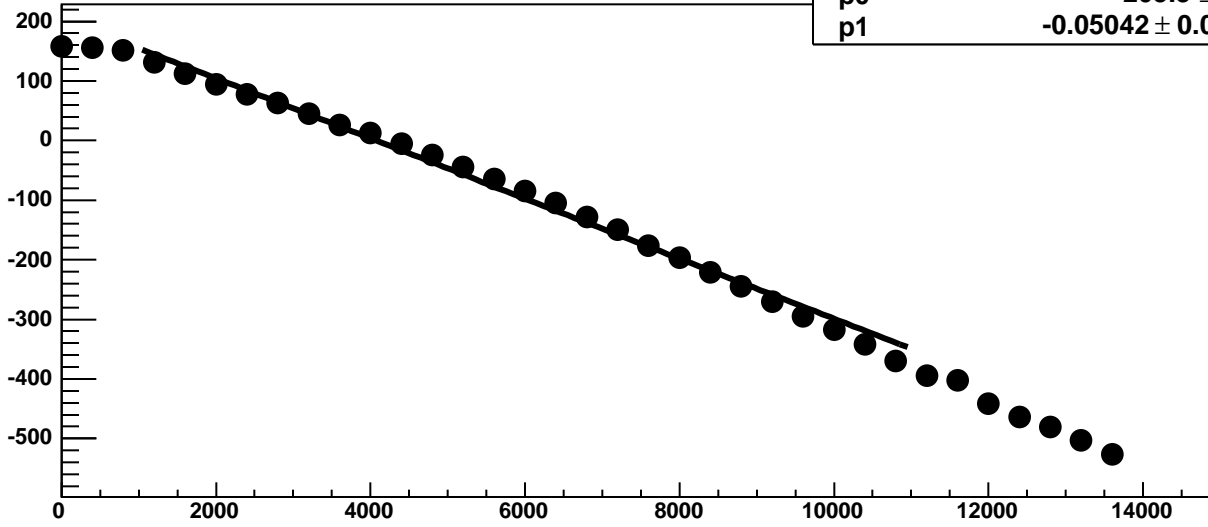
Chip 4, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

775.8 / 23

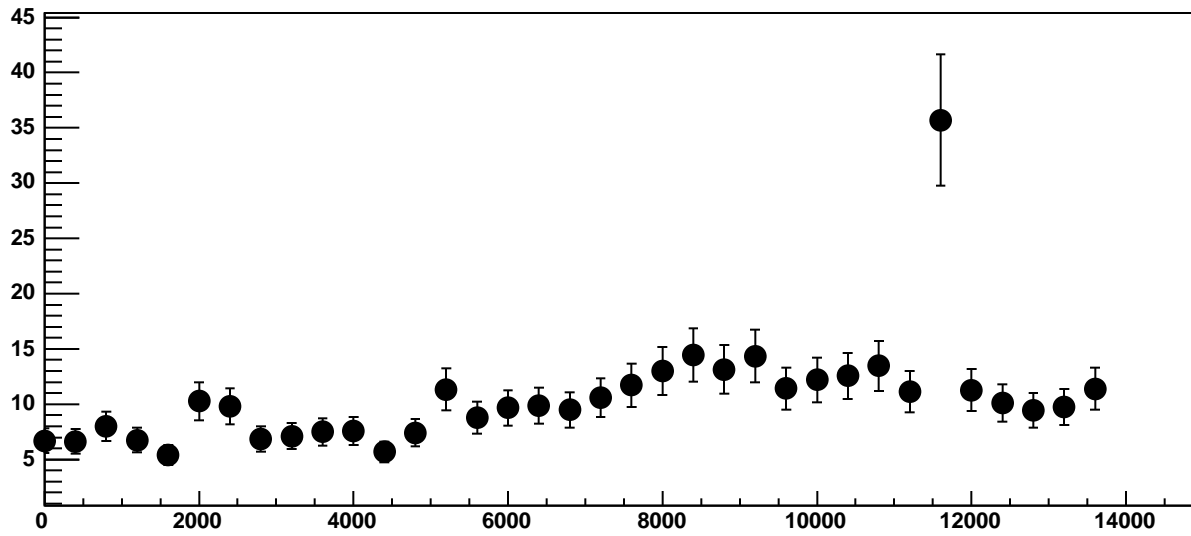
p0

205.3 ± 0.8303

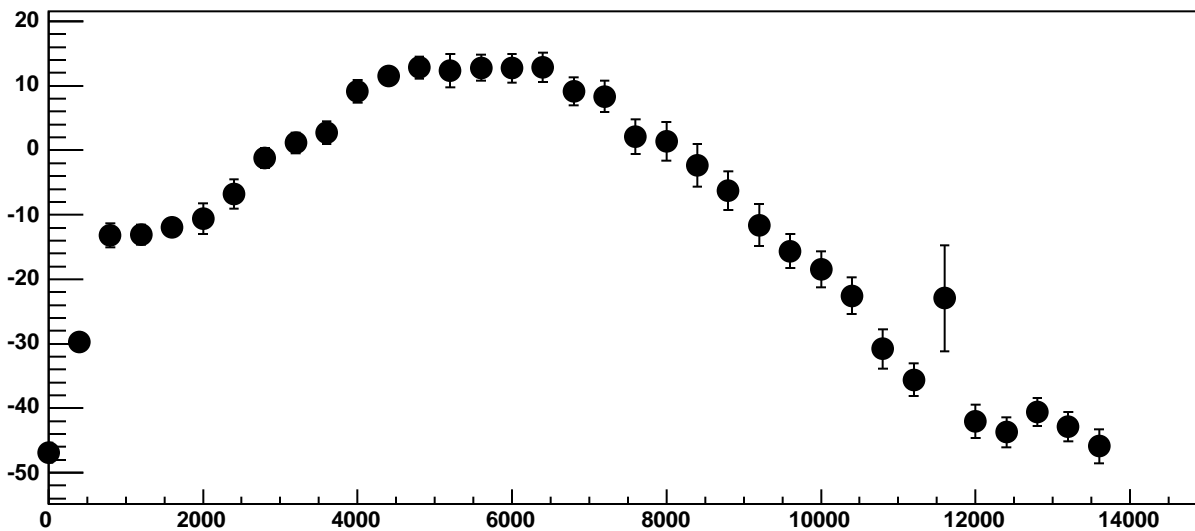
p1

-0.05042 ± 0.0001542

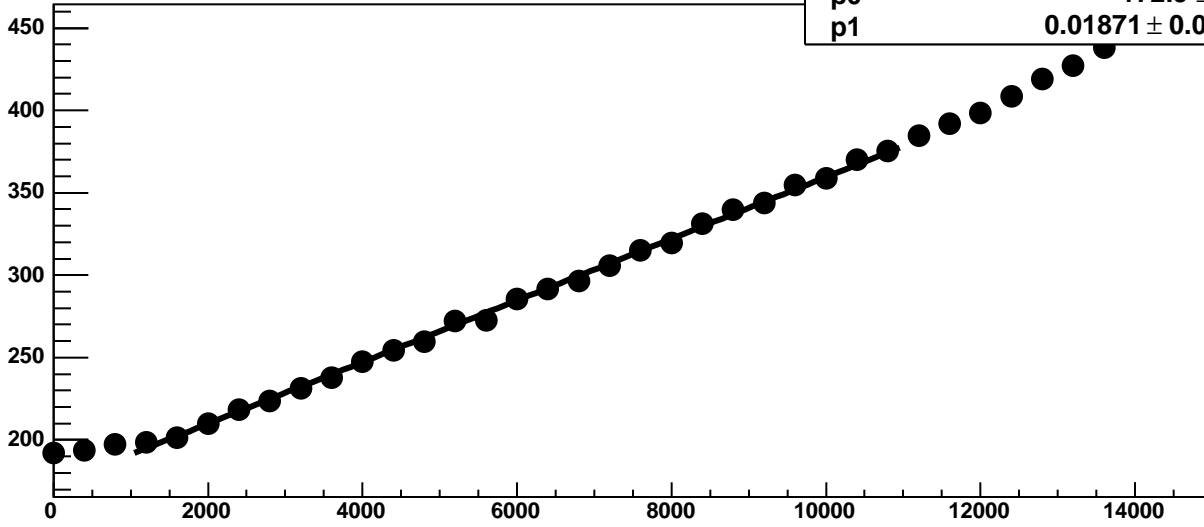
Chip 4, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

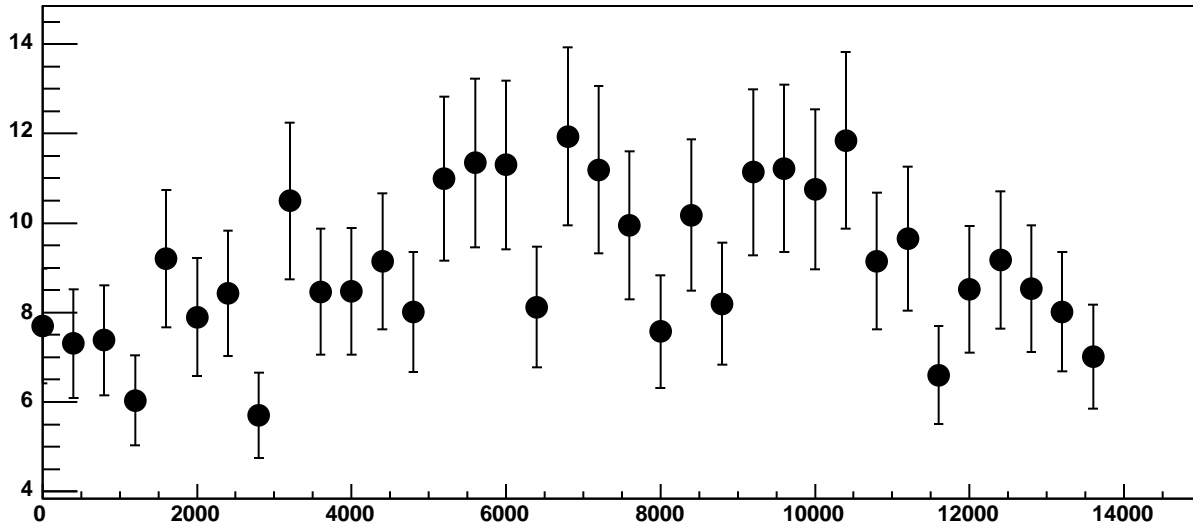


Chip 4, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

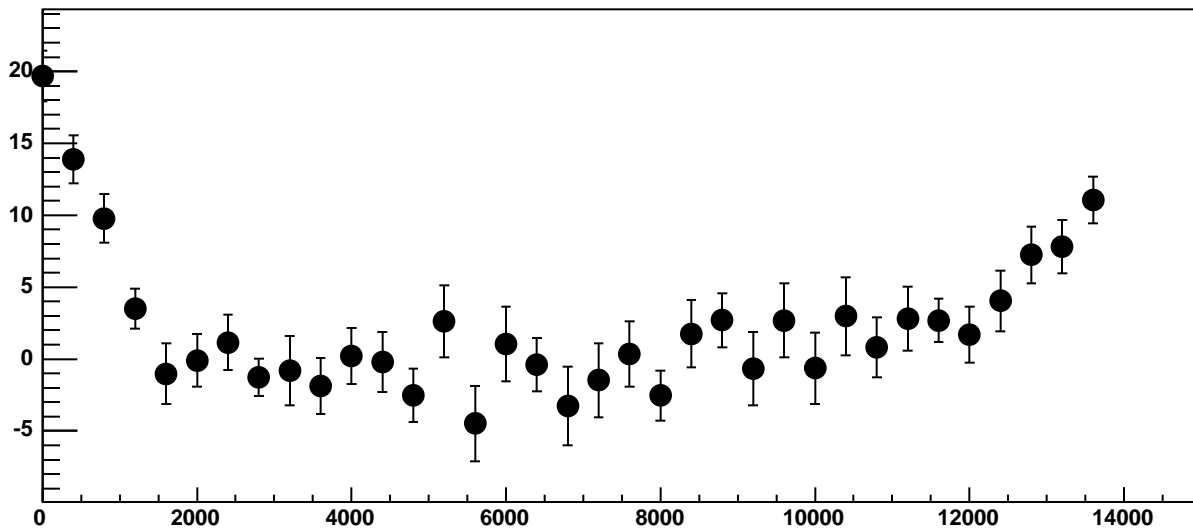


χ^2 / ndf 24.29 / 23
p0 172.5 ± 0.8401
p1 0.01871 ± 0.0001382

Chip 4, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

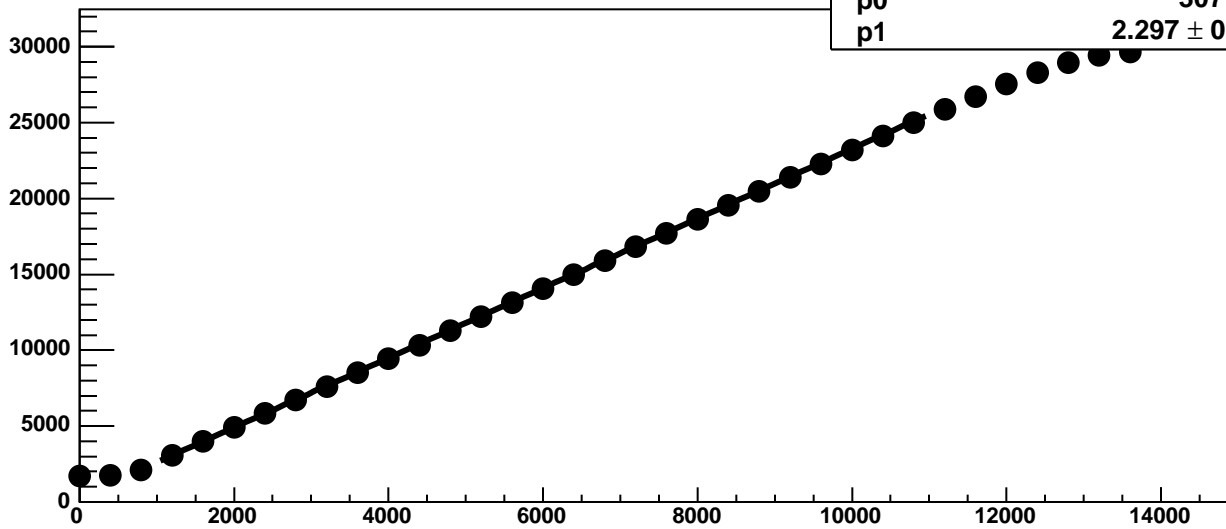


Chip 4, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

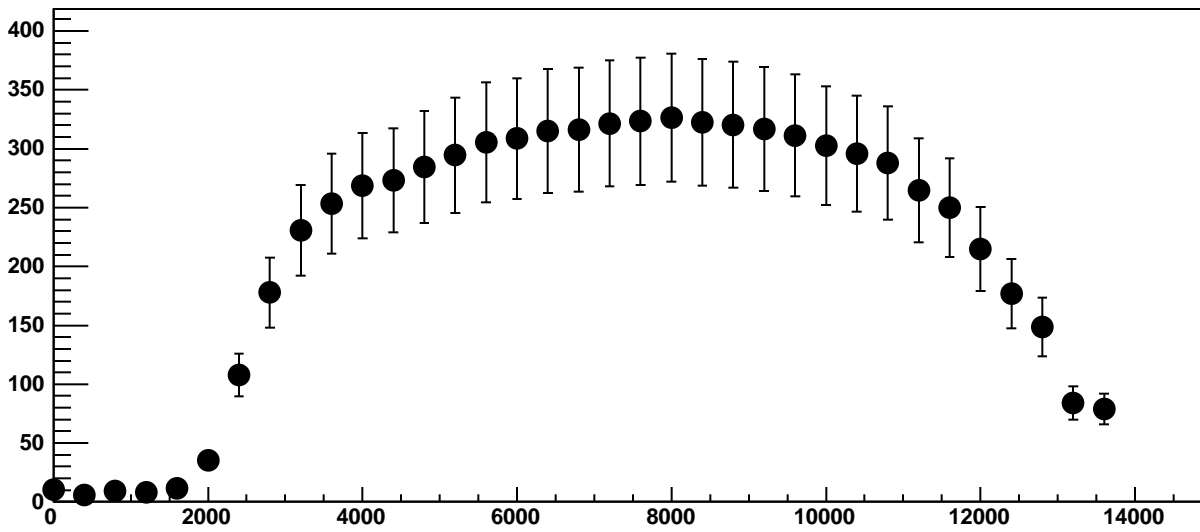


Chip 4, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

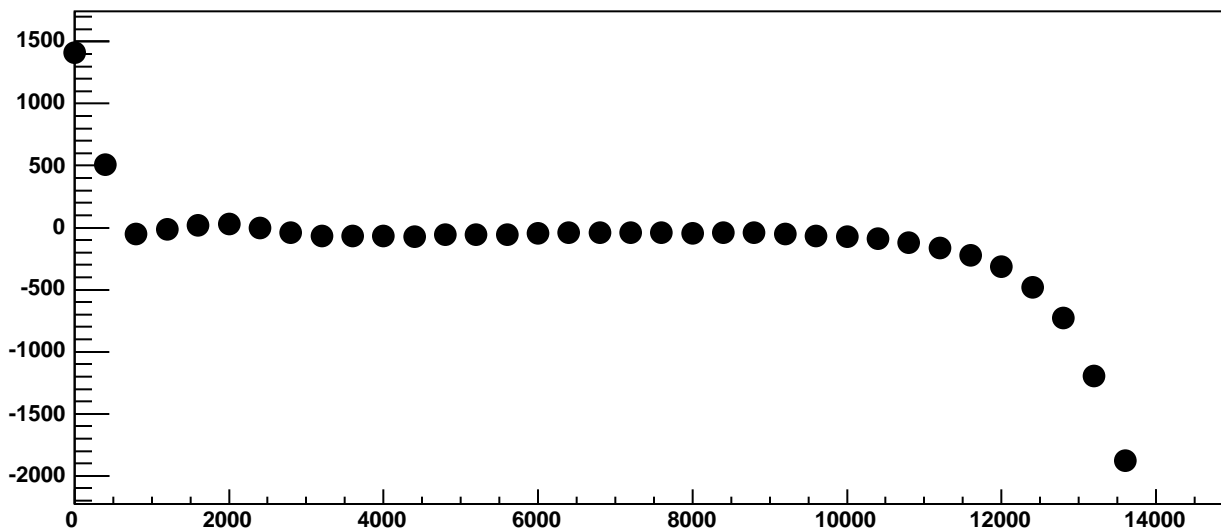
χ^2 / ndf 126.7 / 23
p0 307 ± 3.682
p1 2.297 ± 0.002393



Chip 4, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC

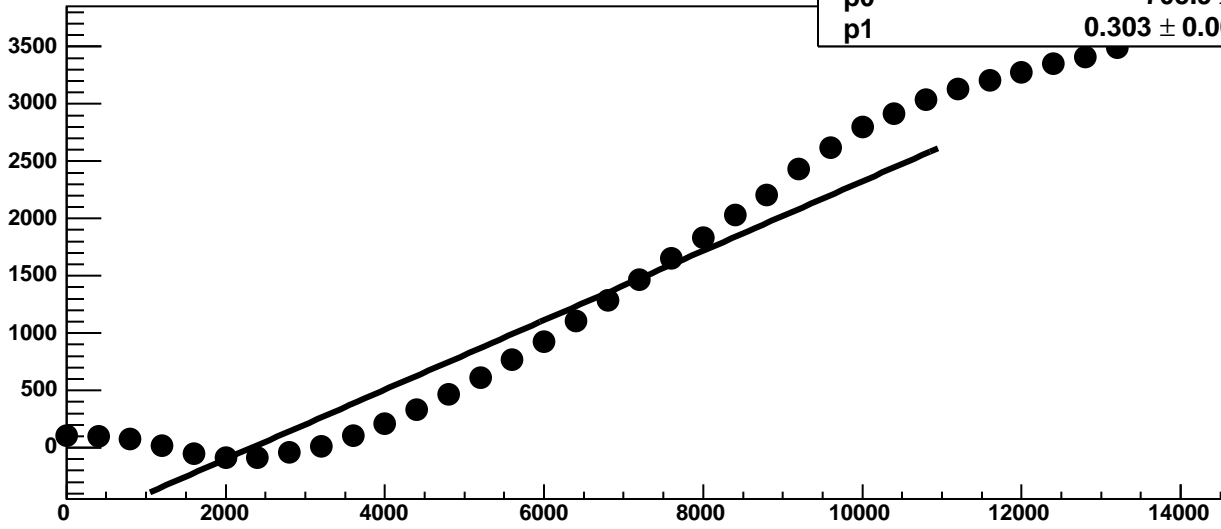


Chip 4, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC

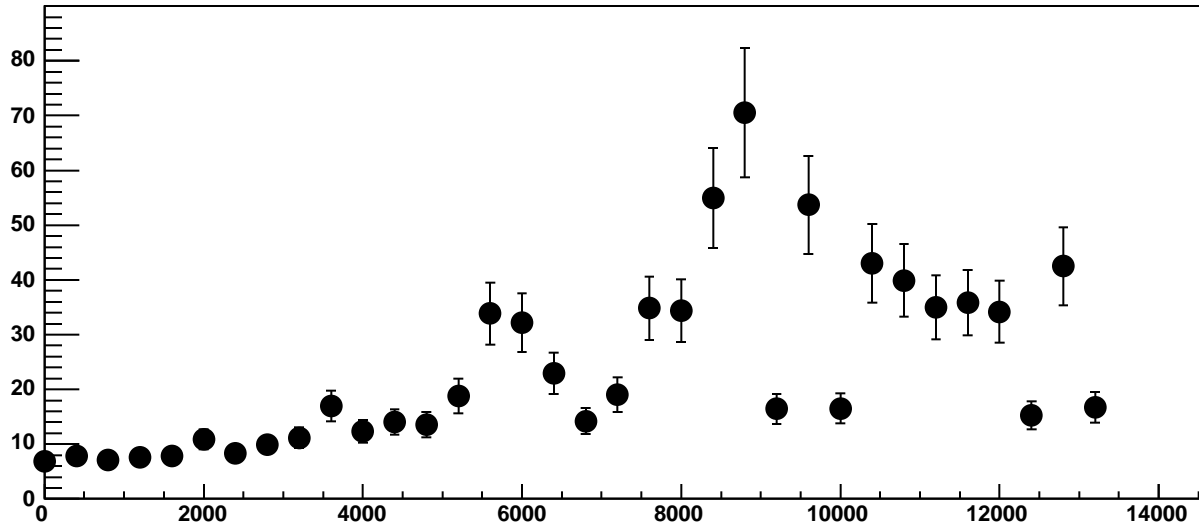


Chip 4, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

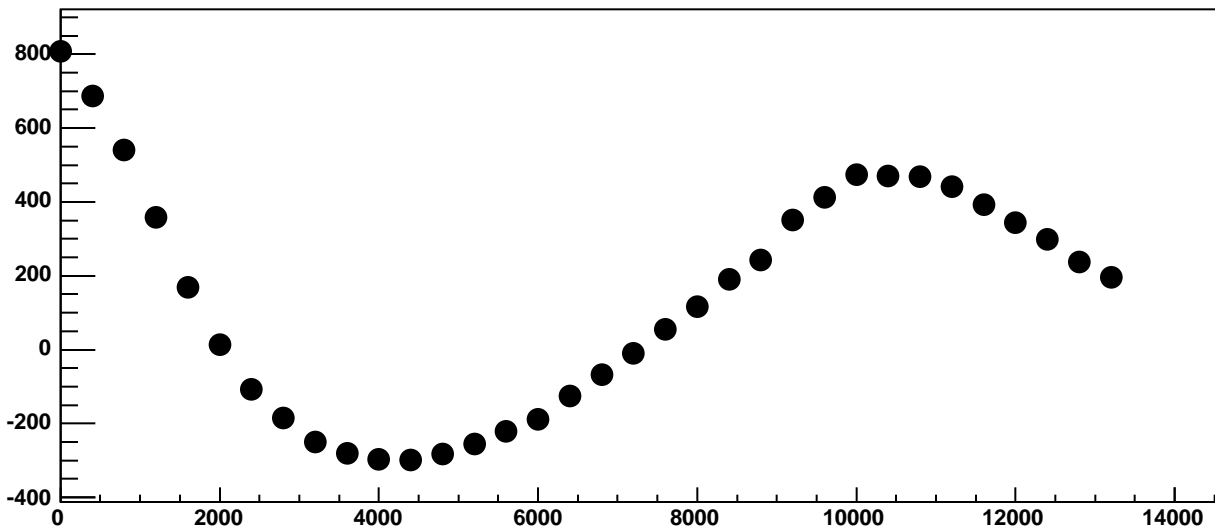
χ^2 / ndf 1.409e+05 / 23
p0 -705.9 ± 1.172
p1 0.303 ± 0.0002694



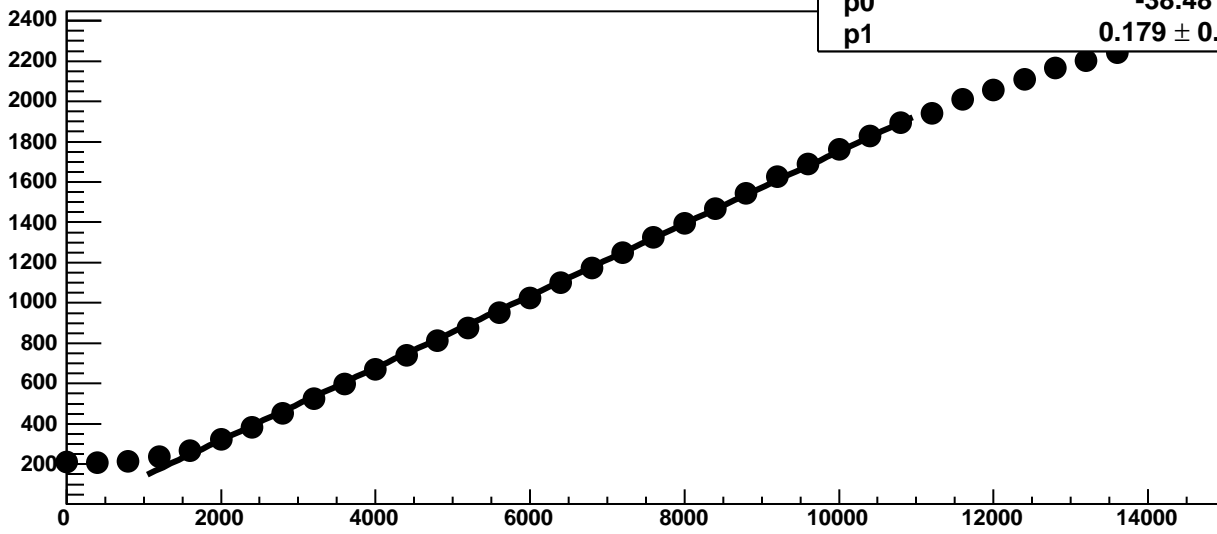
Chip 4, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



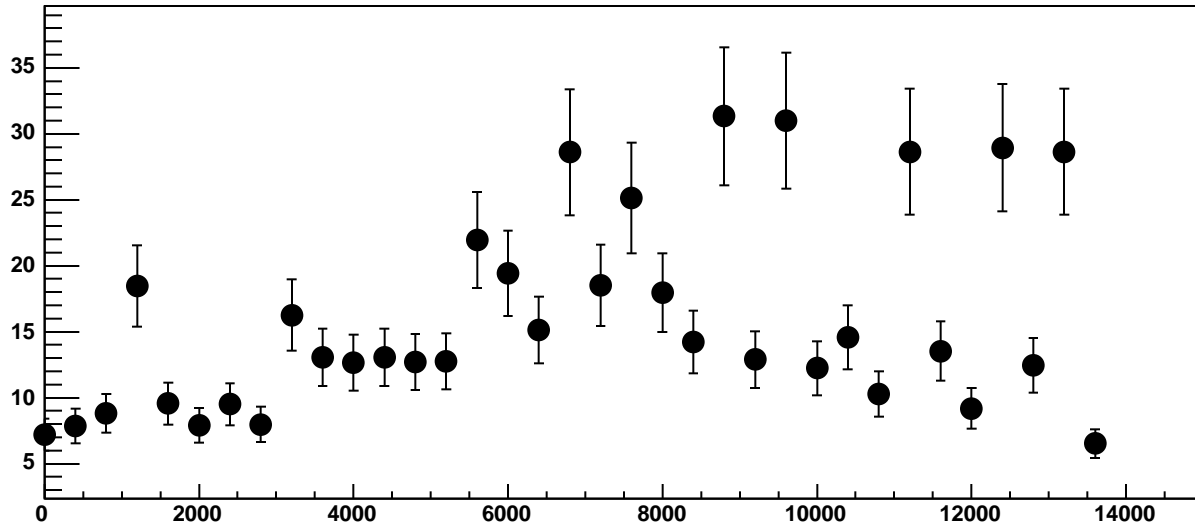
Chip 4, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



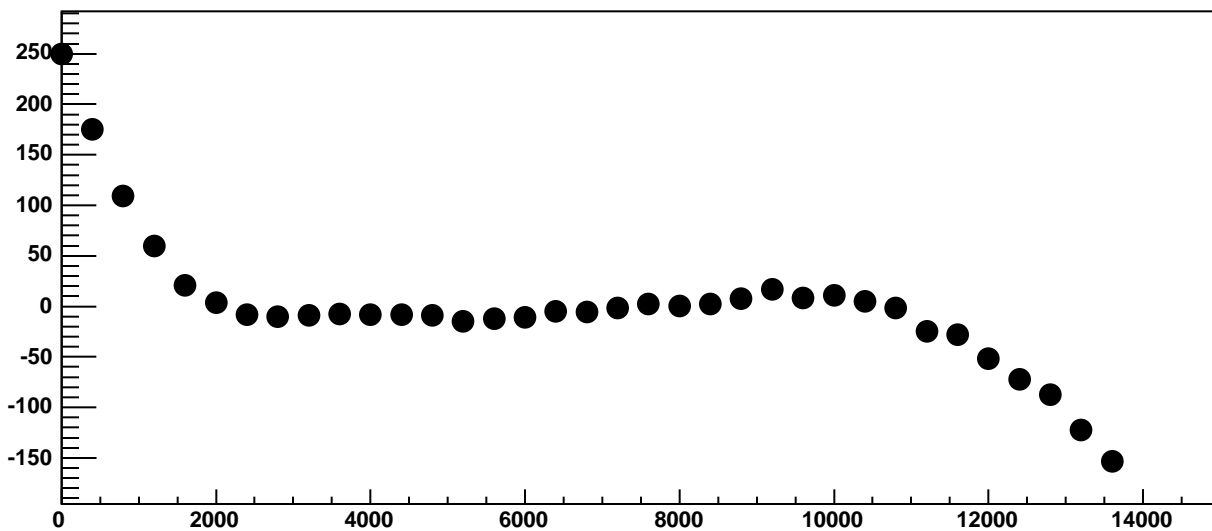
Chip 4, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



Chip 4, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

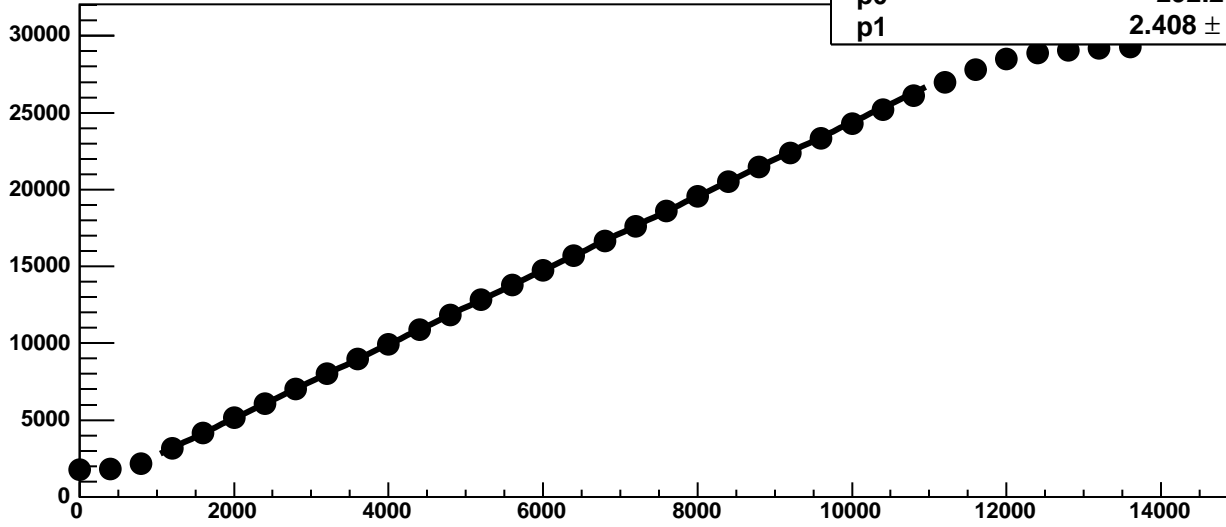


Chip 4, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

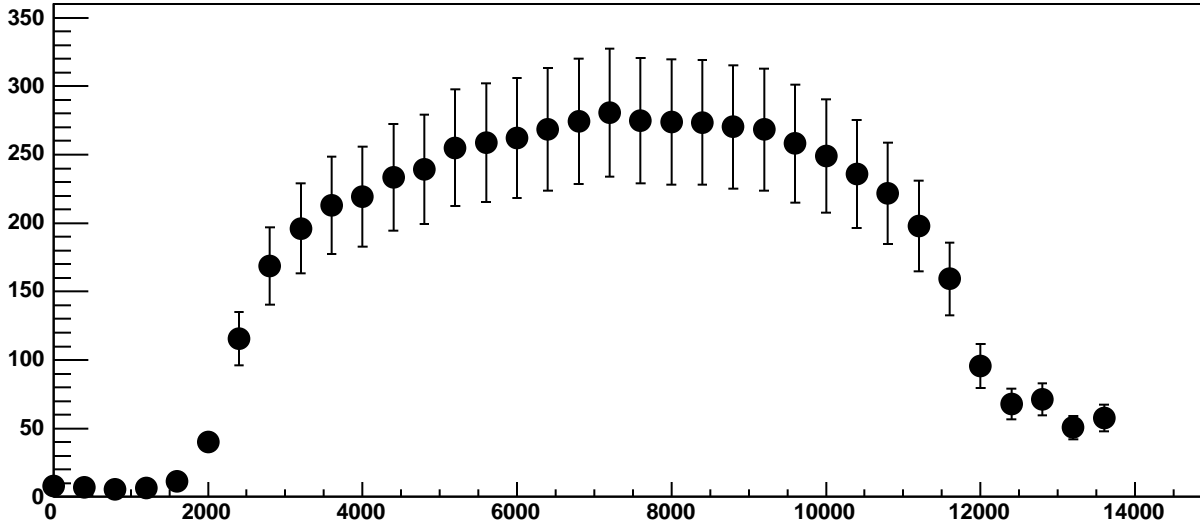


Chip 4, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC

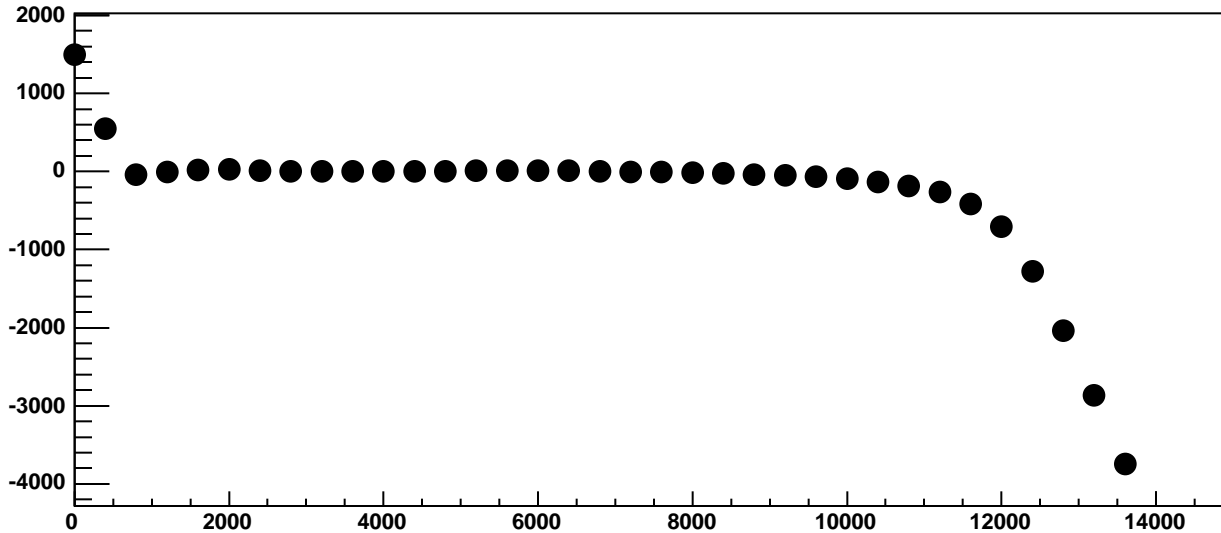
χ^2 / ndf 142 / 23
p0 292.2 ± 3.047
p1 2.408 ± 0.00201



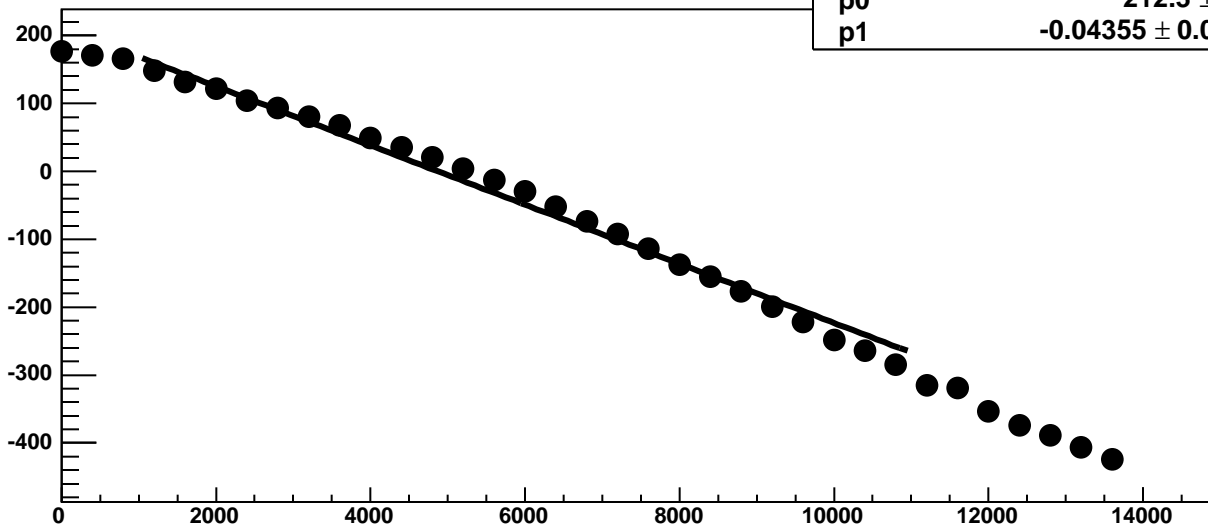
Chip 4, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC

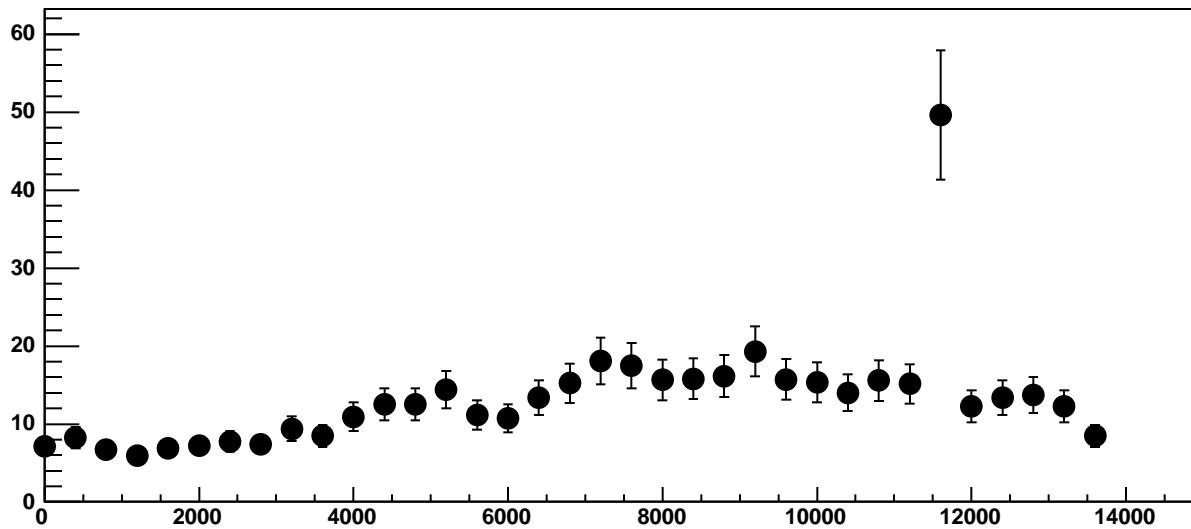


Chip 4, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

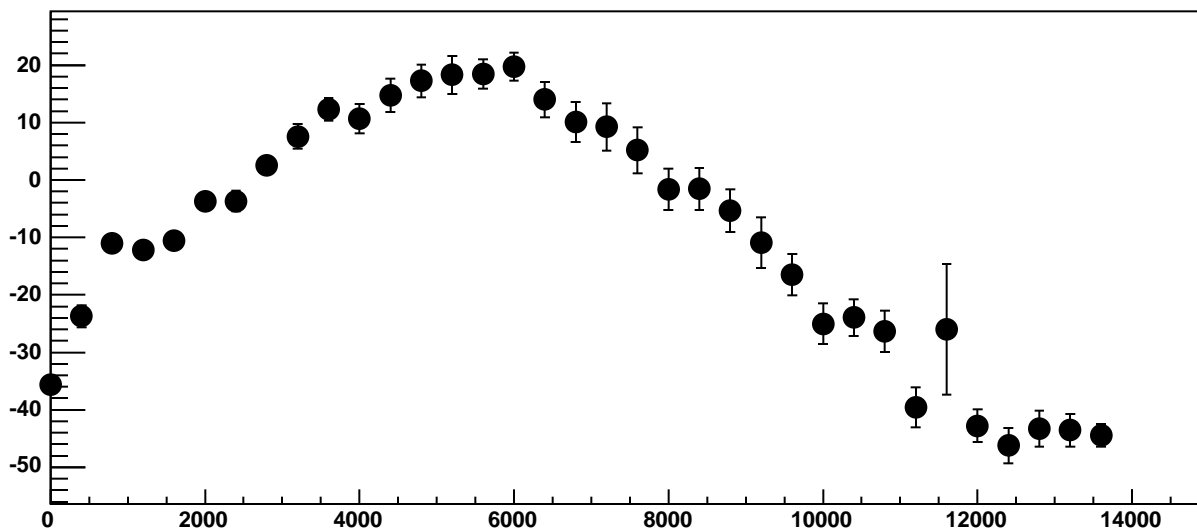


χ^2 / ndf 641.6 / 23
p0 212.3 ± 0.8819
p1 -0.04355 ± 0.0001767

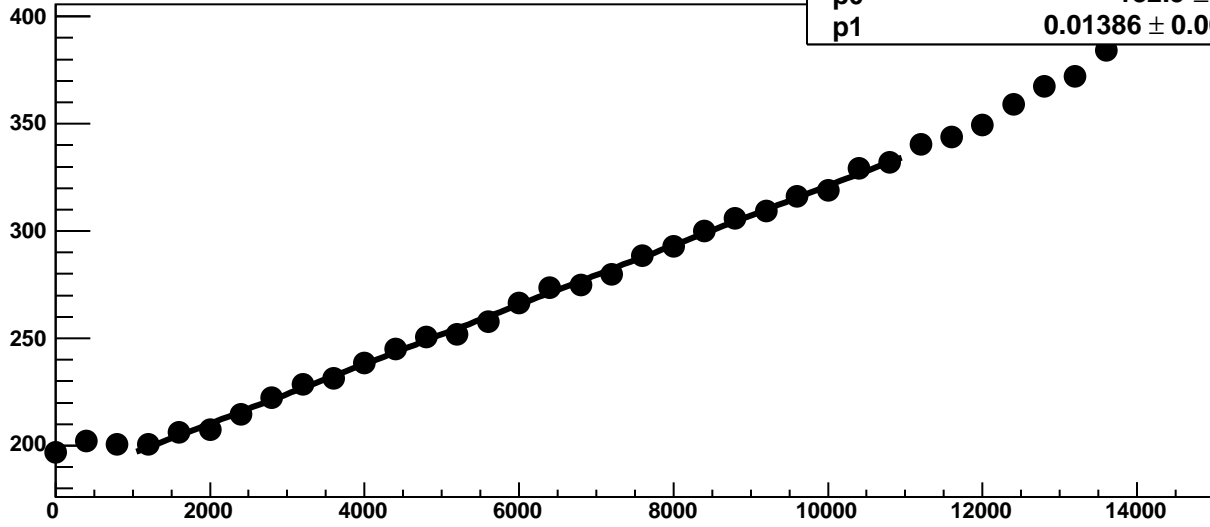
Chip 4, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



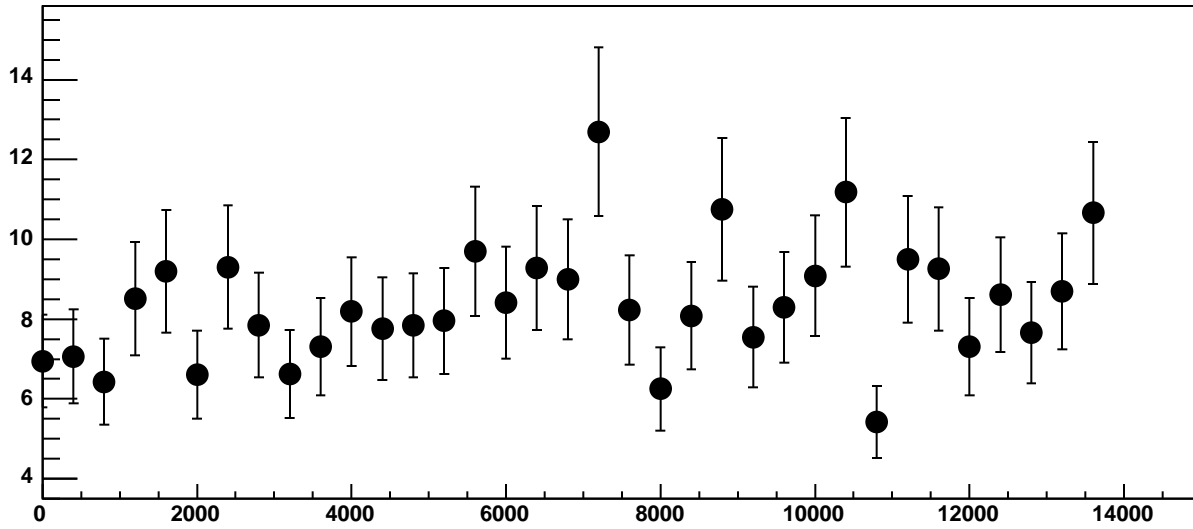
Chip 4, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



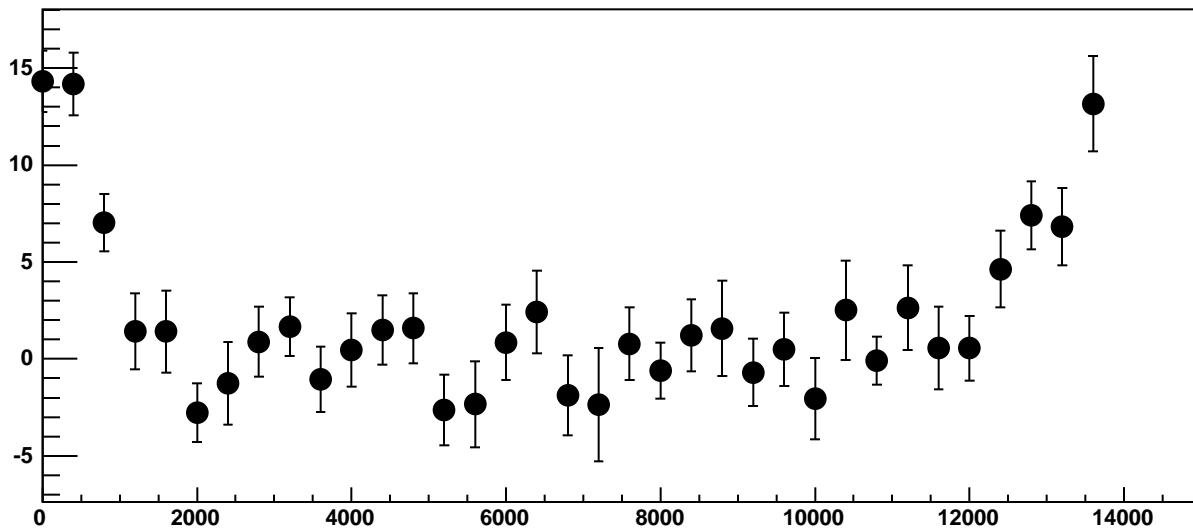
Chip 4, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



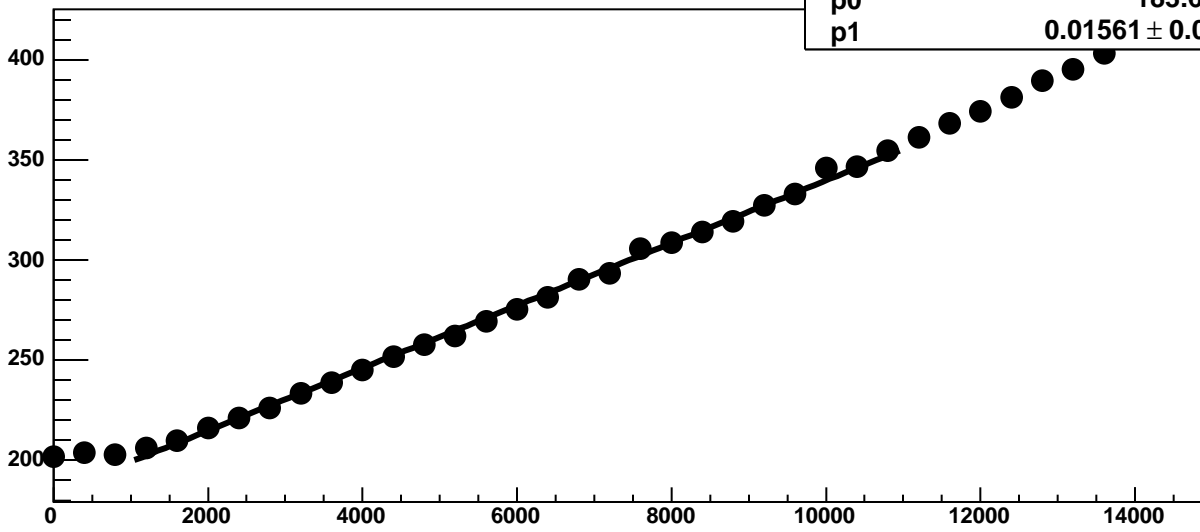
Chip 4, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

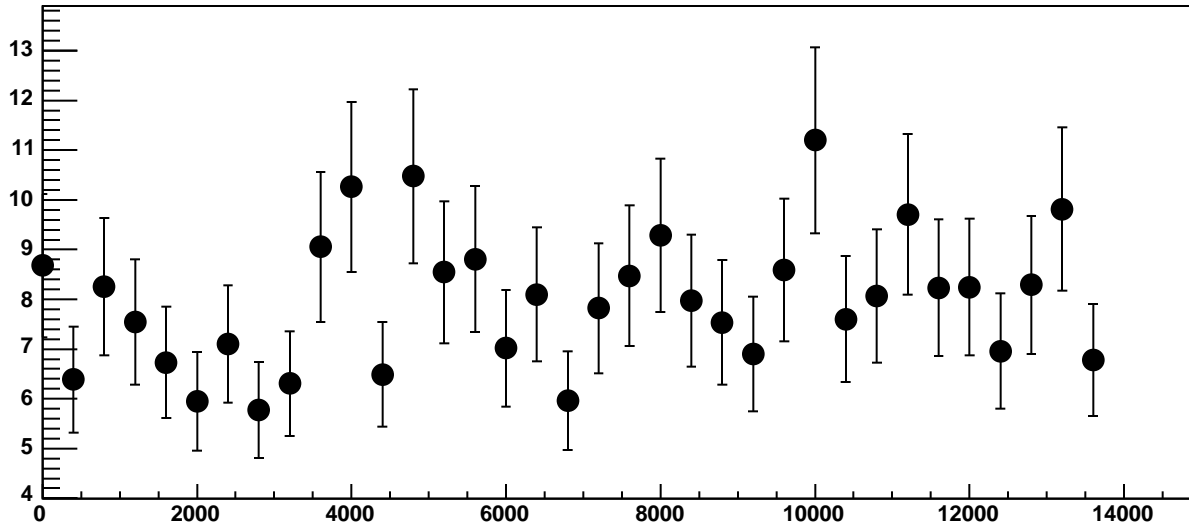


Chip 4, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

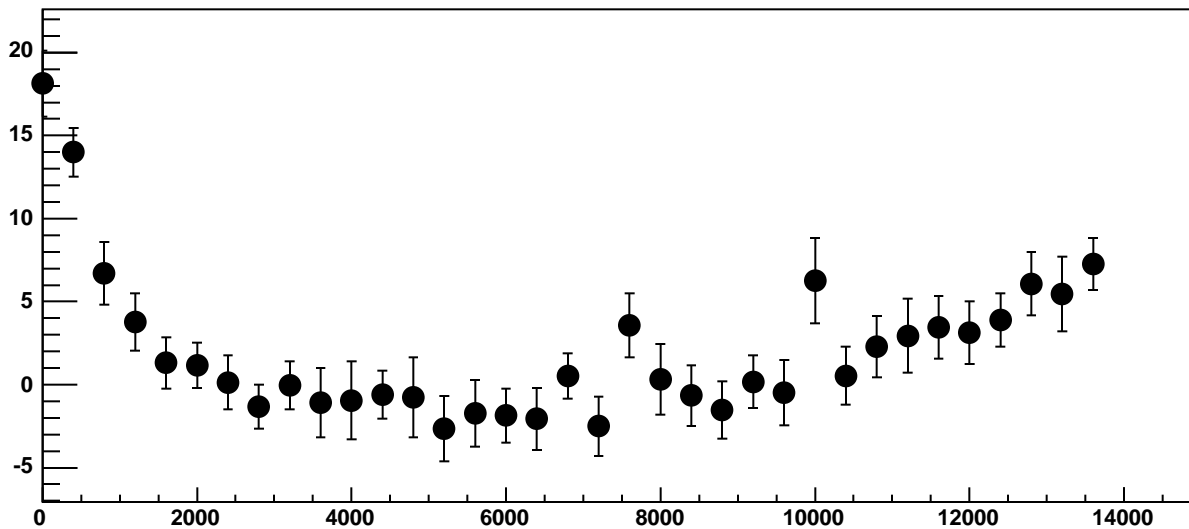


χ^2 / ndf 27.04 / 23
p0 183.6 ± 0.751
p1 0.01561 ± 0.0001187

Chip 4, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC

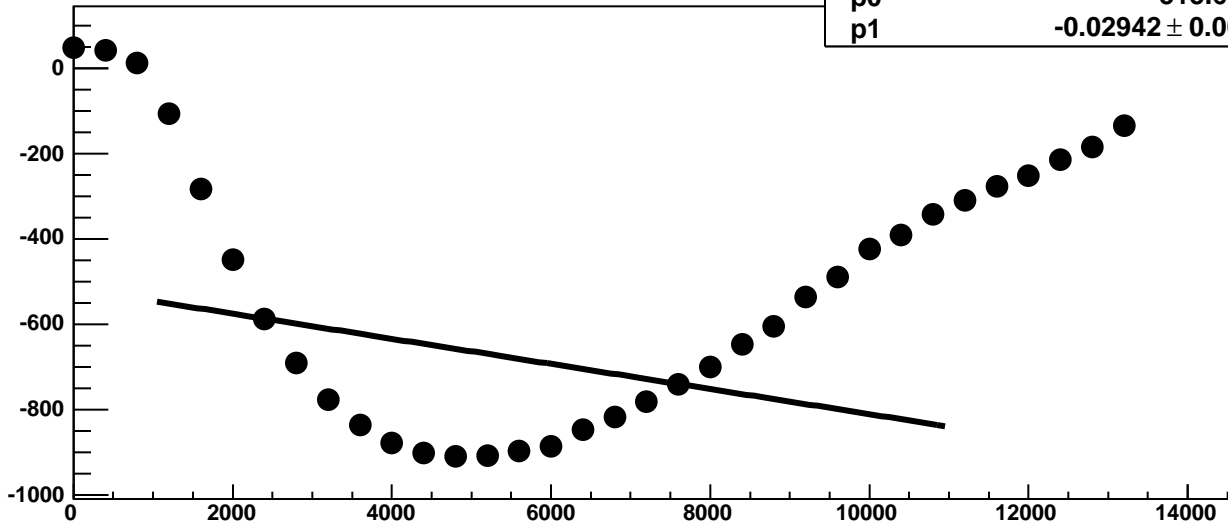


Chip 4, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC

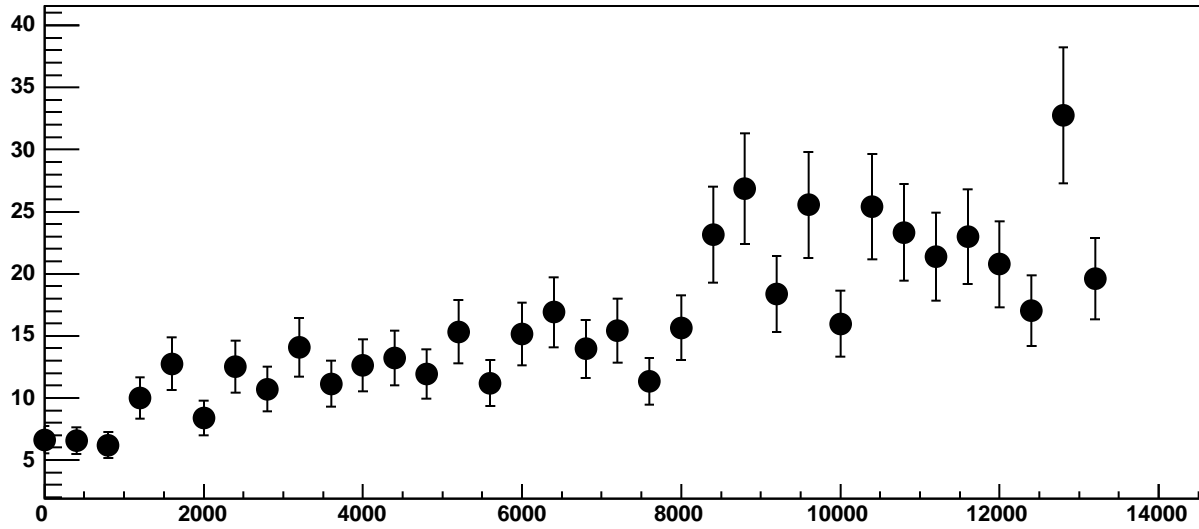


Chip 4, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

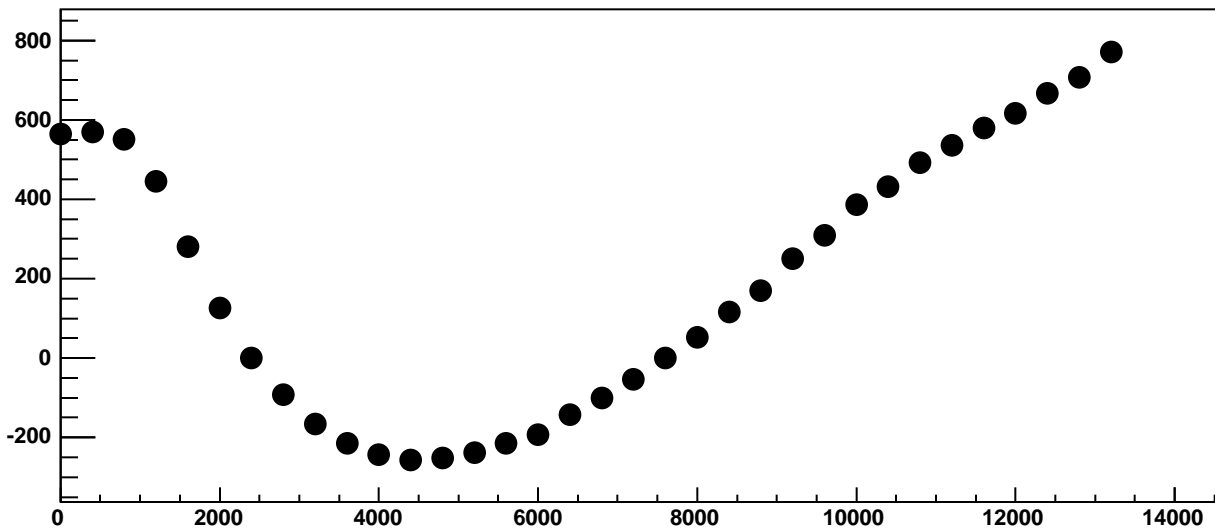
χ^2 / ndf 1.353e+05 / 23
p0 -515.6 ± 1.29
p1 -0.02942 ± 0.0002374



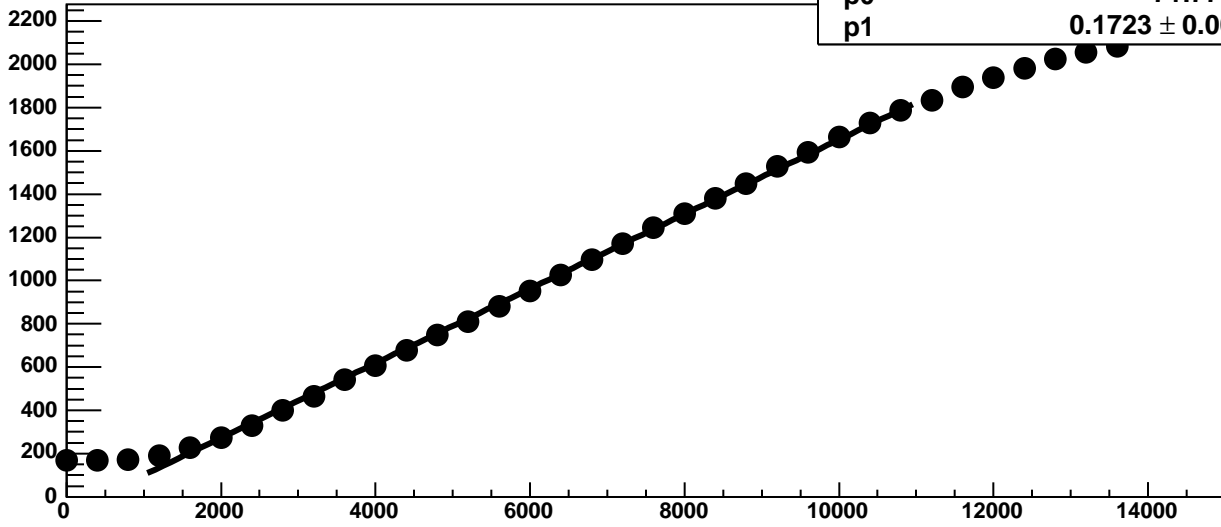
Chip 4, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



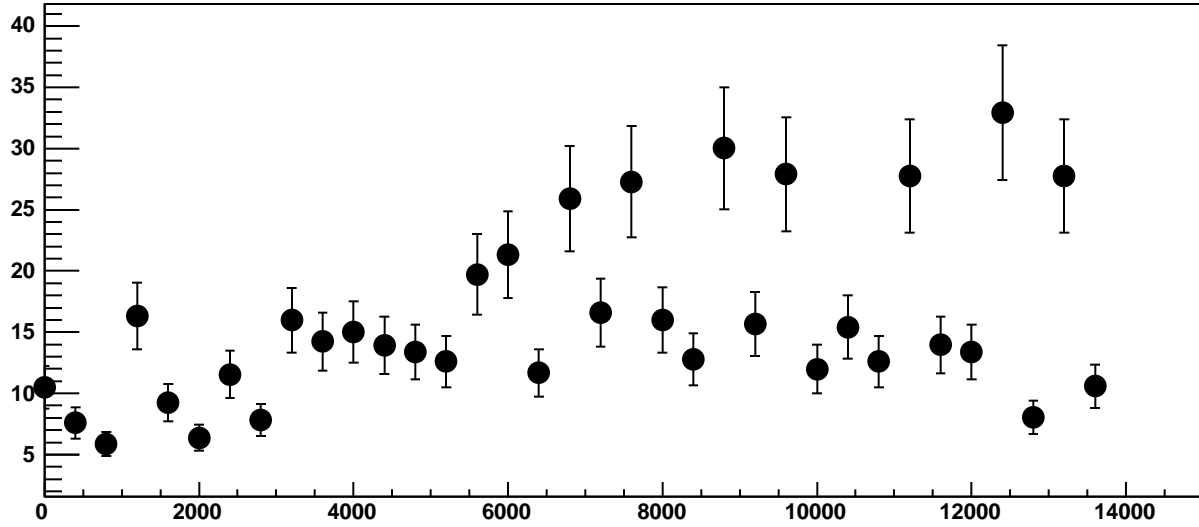
Chip 4, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



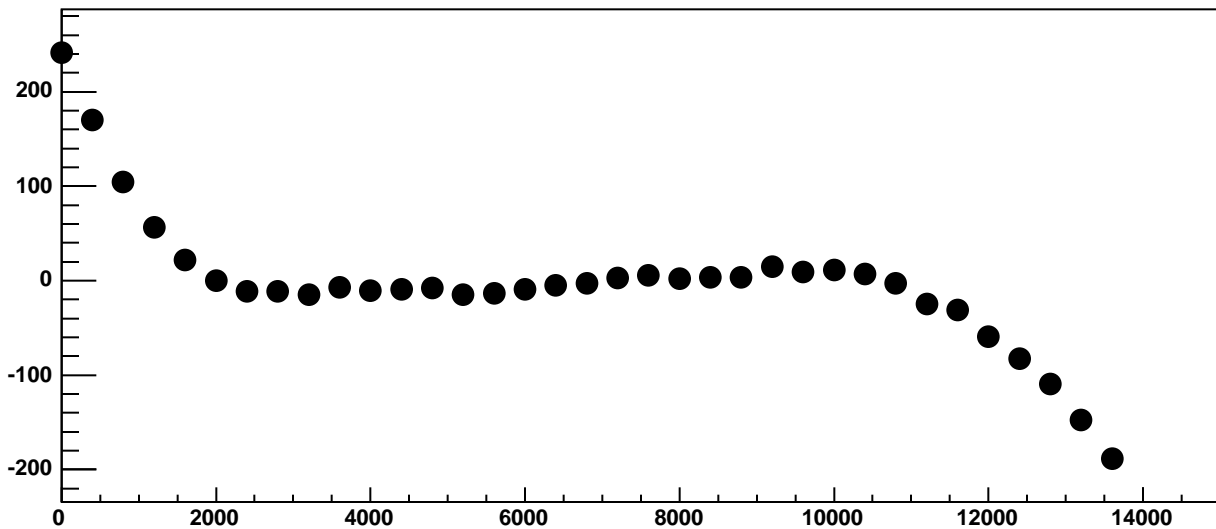
Chip 4, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



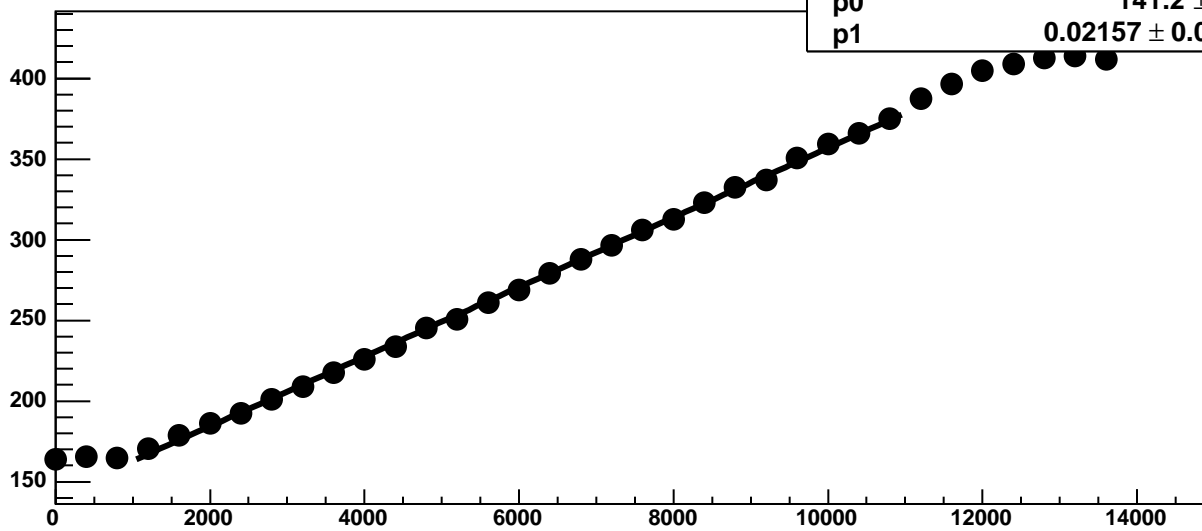
Chip 4, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



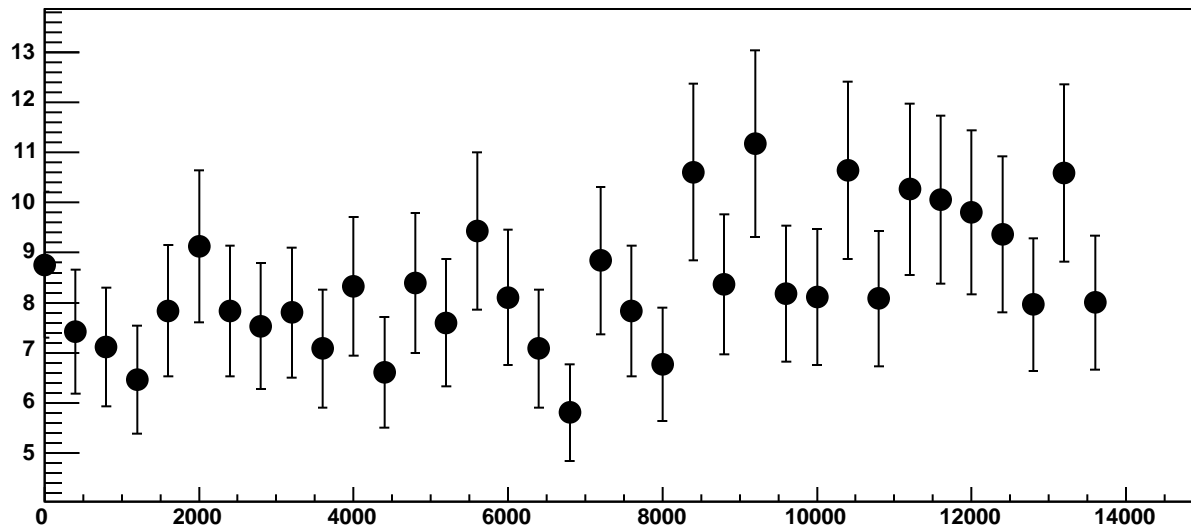
Chip 4, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



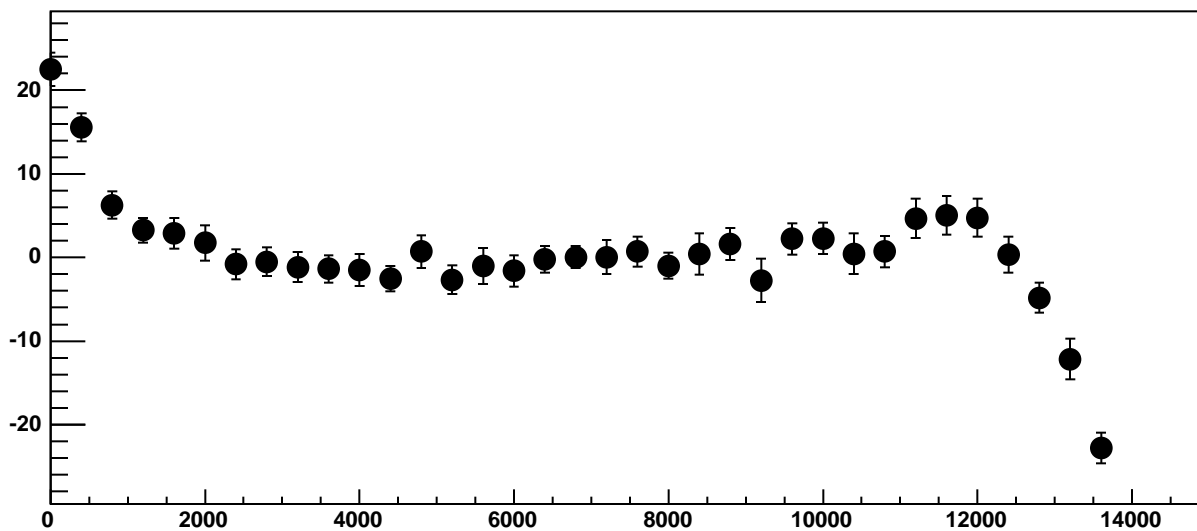
Chip 4, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



Chip 4, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

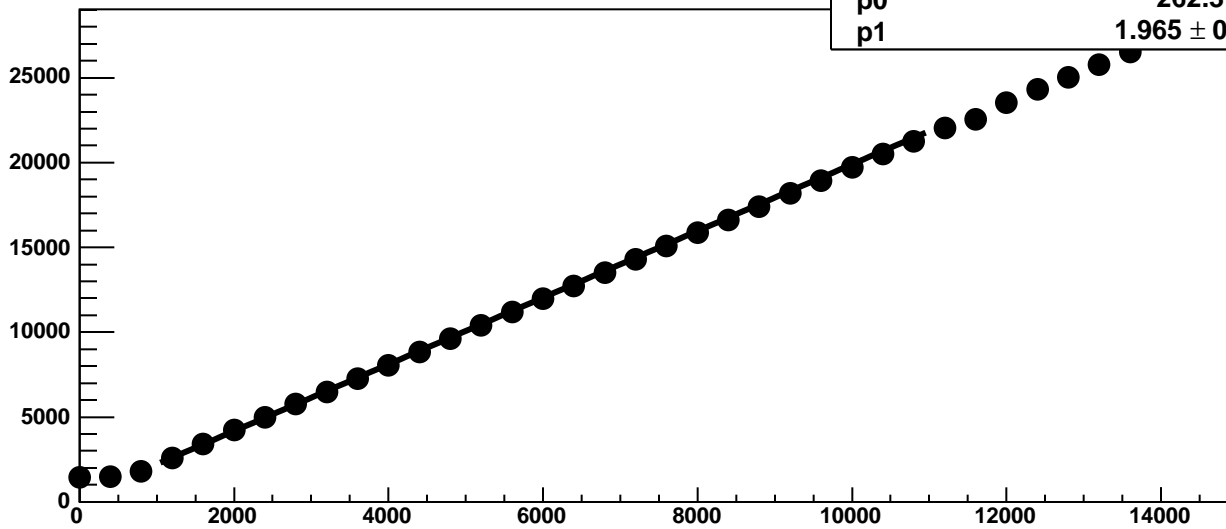


Chip 4, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

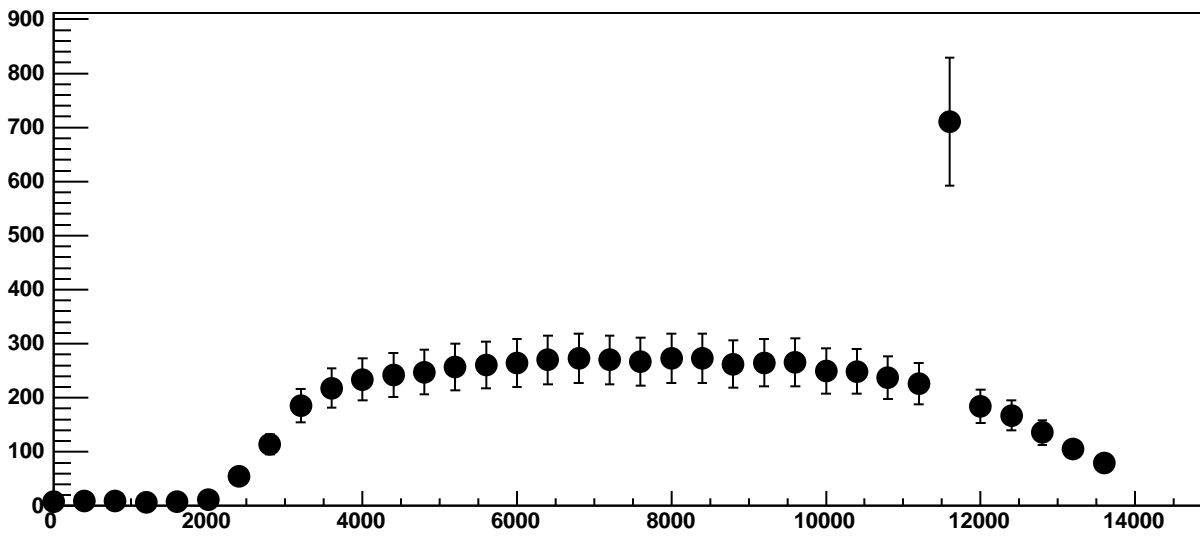


Chip 4, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC

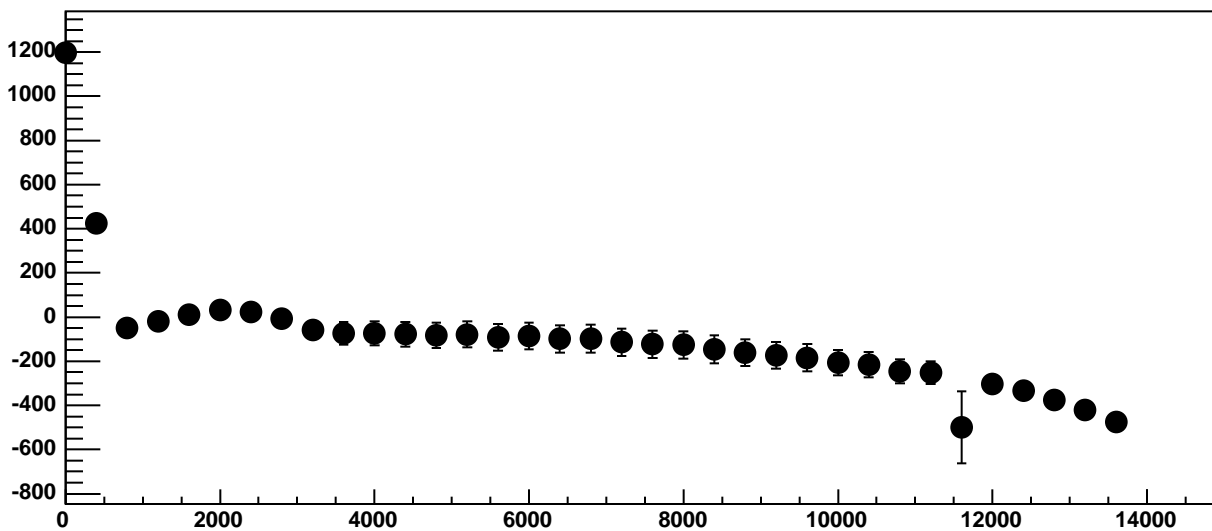
χ^2 / ndf 414.9 / 23
p0 262.3 ± 3.075
p1 1.965 ± 0.001881



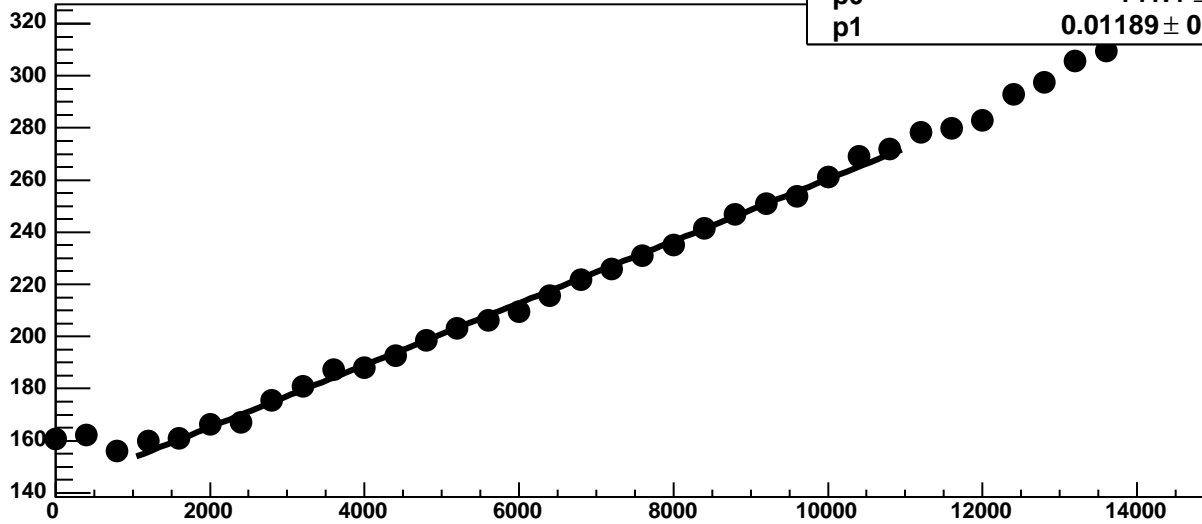
Chip 4, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC

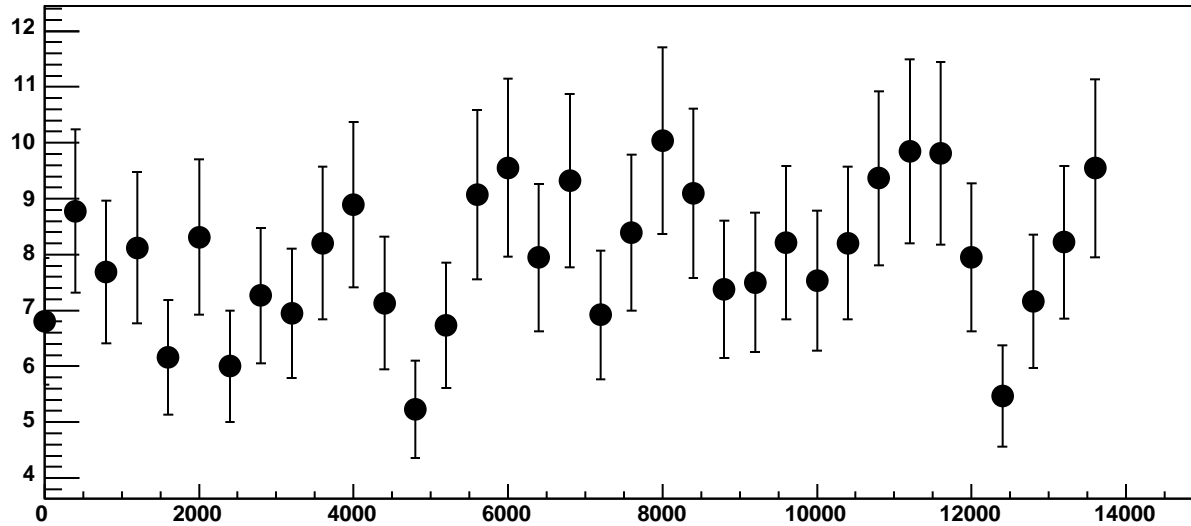


Chip 4, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

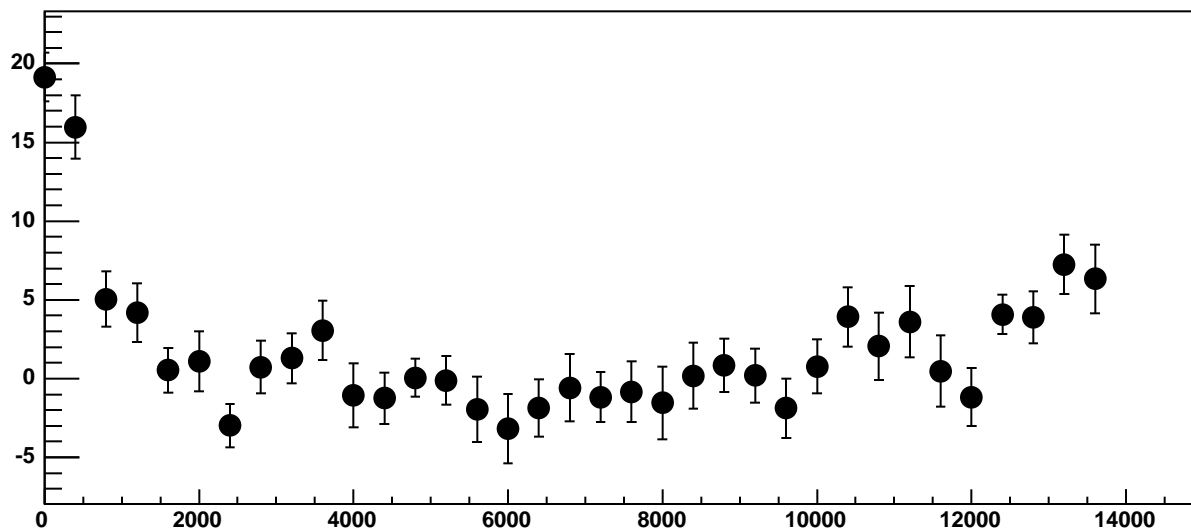


χ^2 / ndf 26.55 / 23
p0 141.4 ± 0.7704
p1 0.01189 ± 0.000122

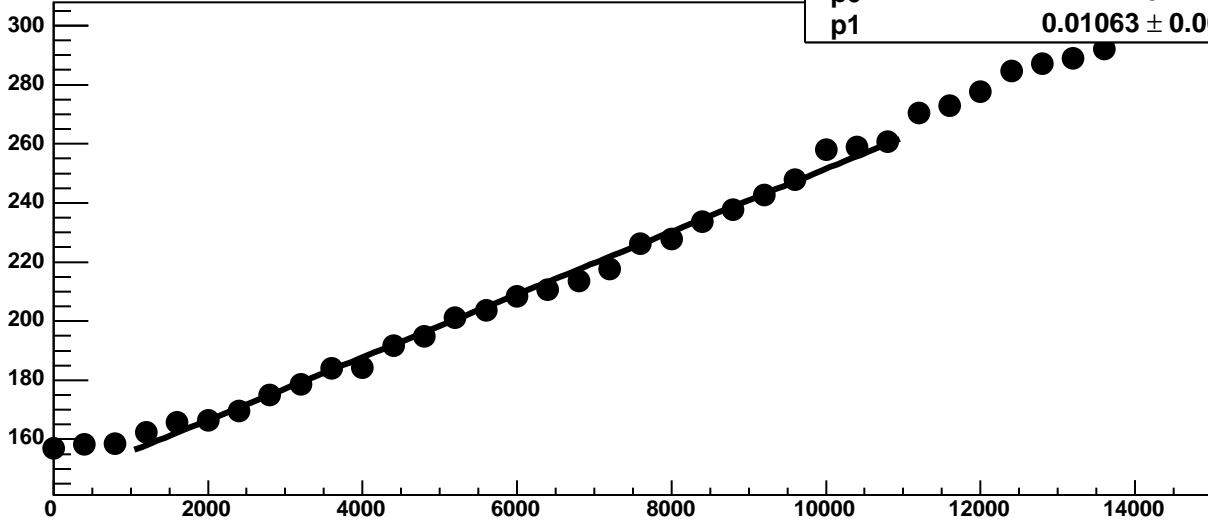
Chip 4, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

50.8 / 23

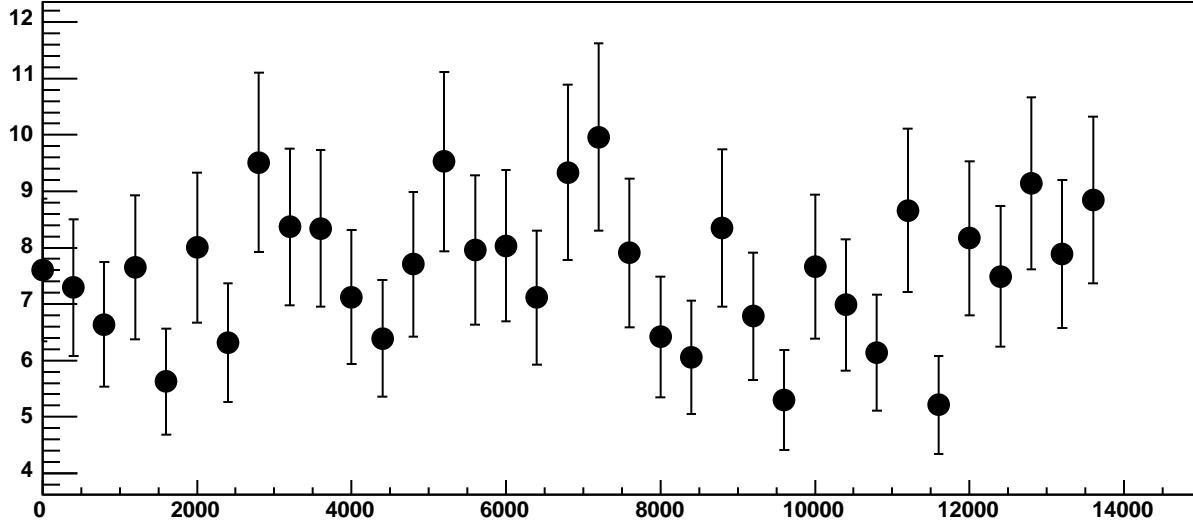
p0

145.2 ± 0.7512

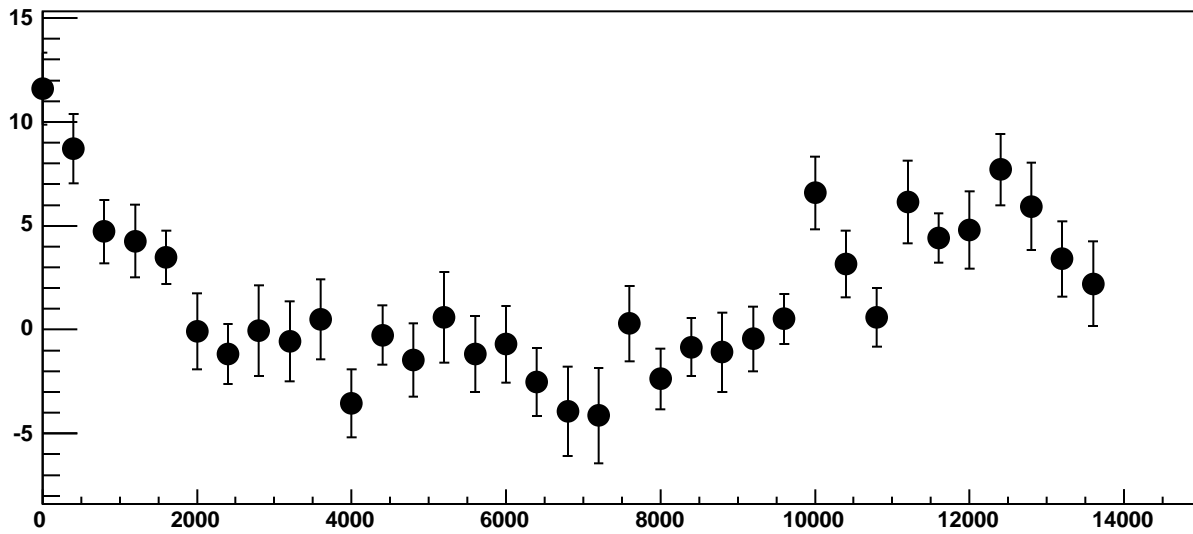
p1

0.01063 ± 0.0001088

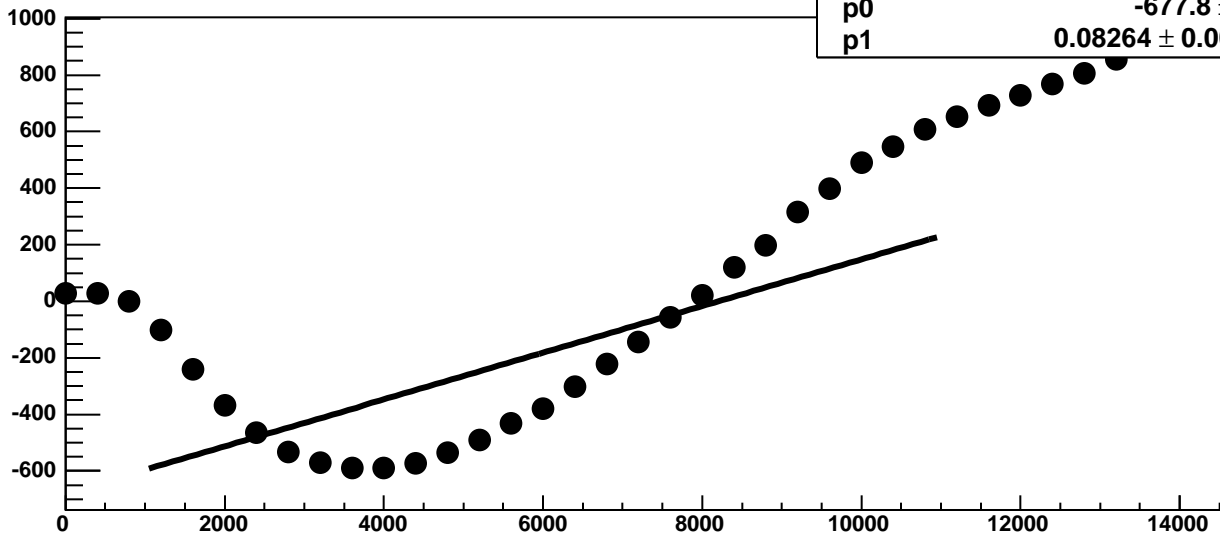
Chip 4, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

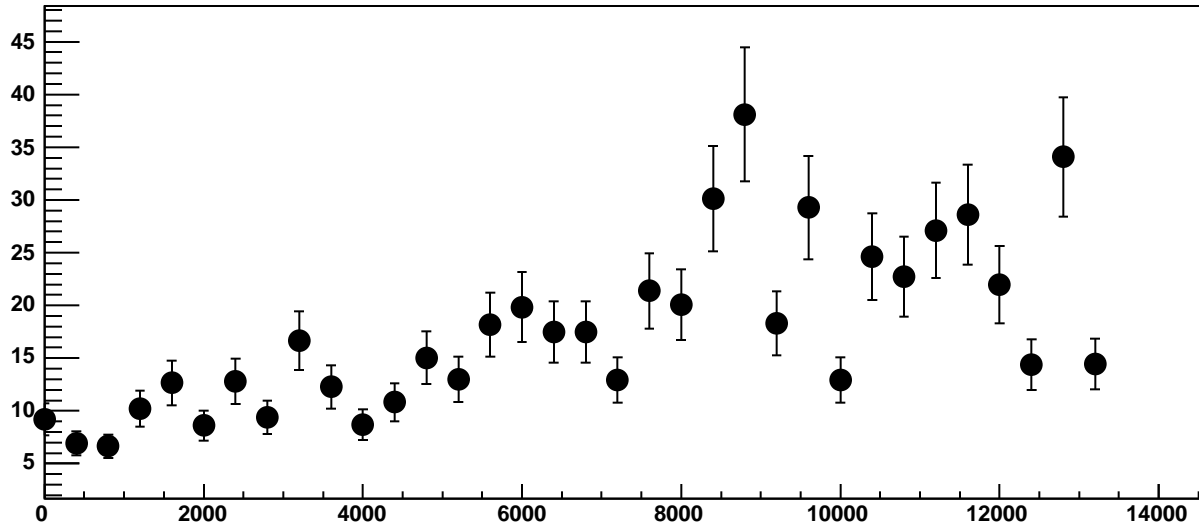


Chip 4, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC

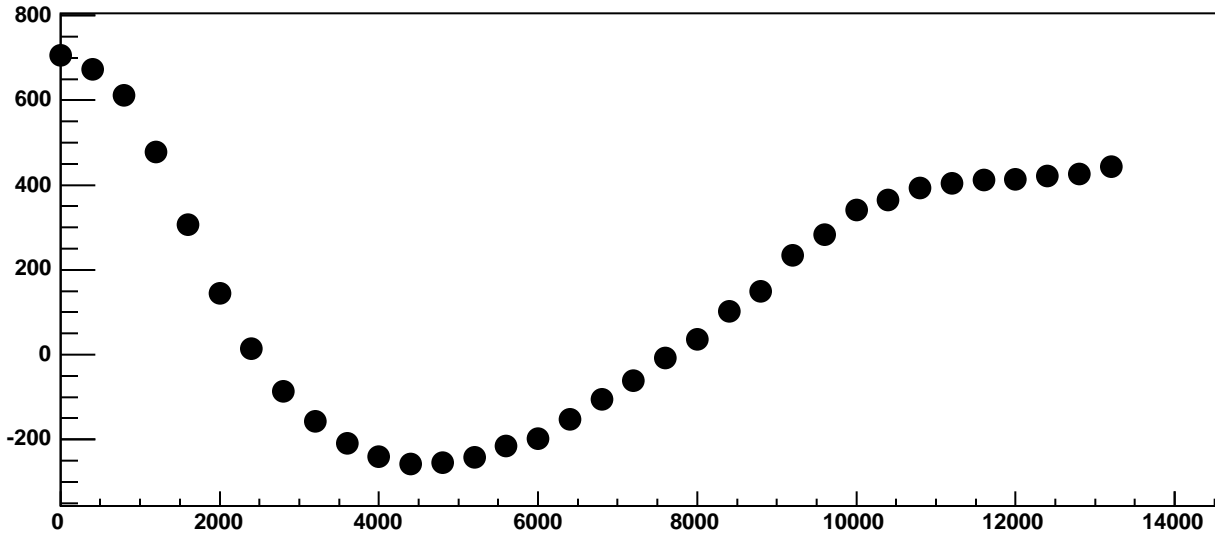


χ^2 / ndf 1.399e+05 / 23
p0 -677.8 ± 1.278
p1 0.08264 ± 0.0002428

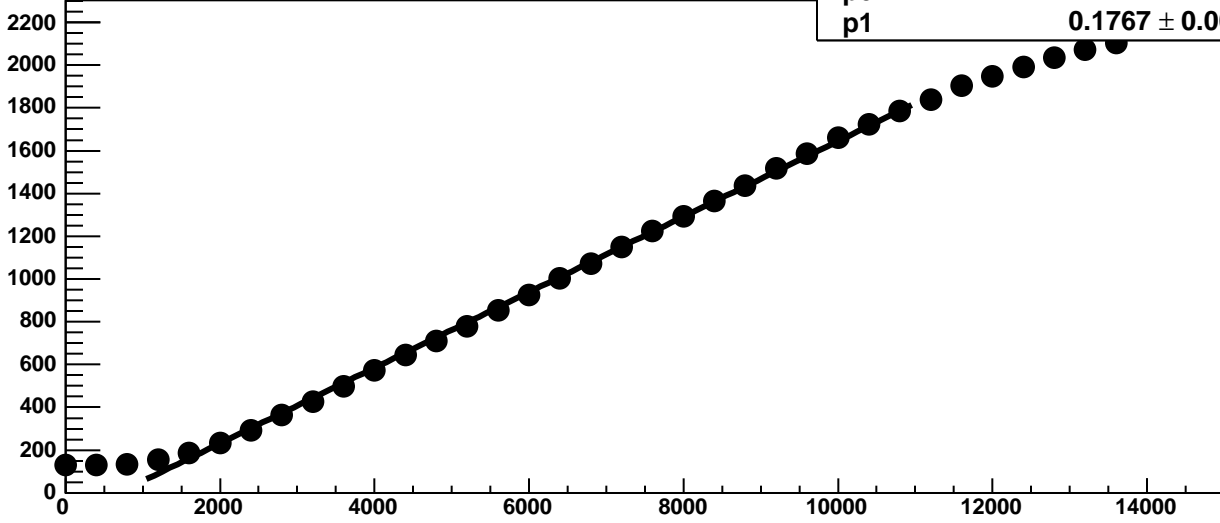
Chip 4, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



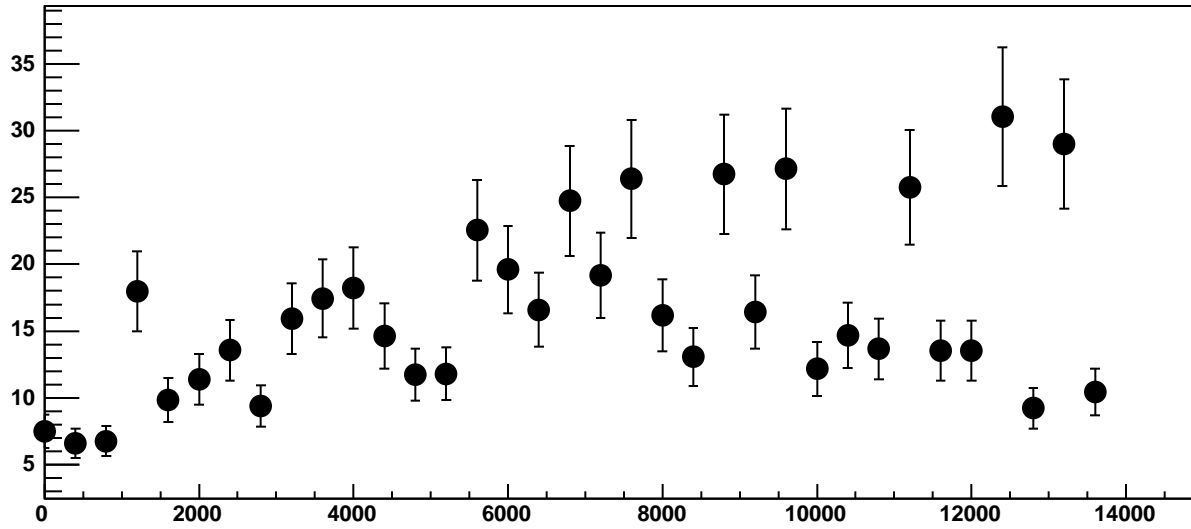
Chip 4, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC



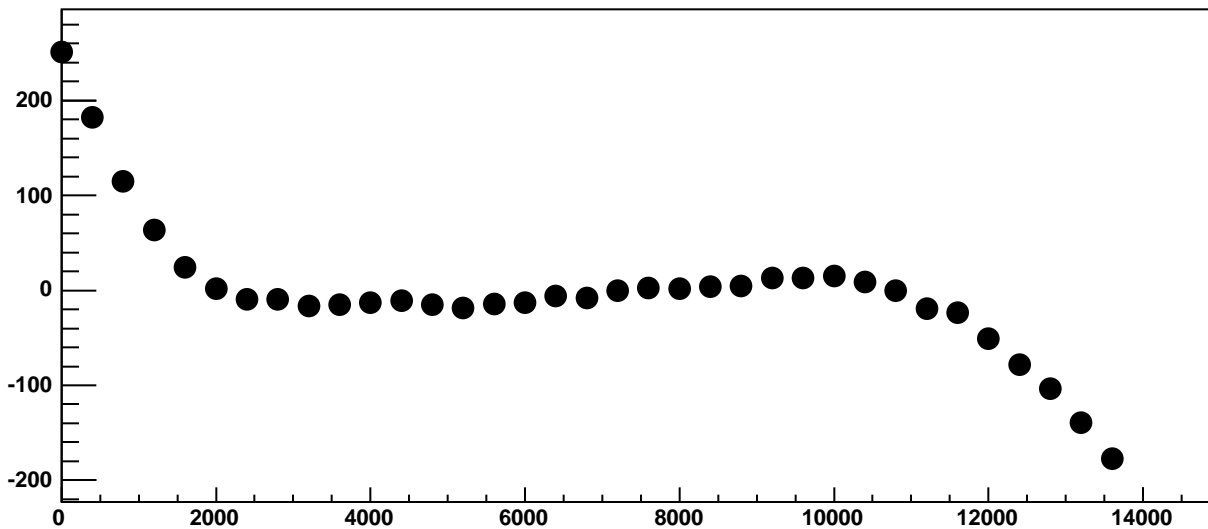
Chip 4, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



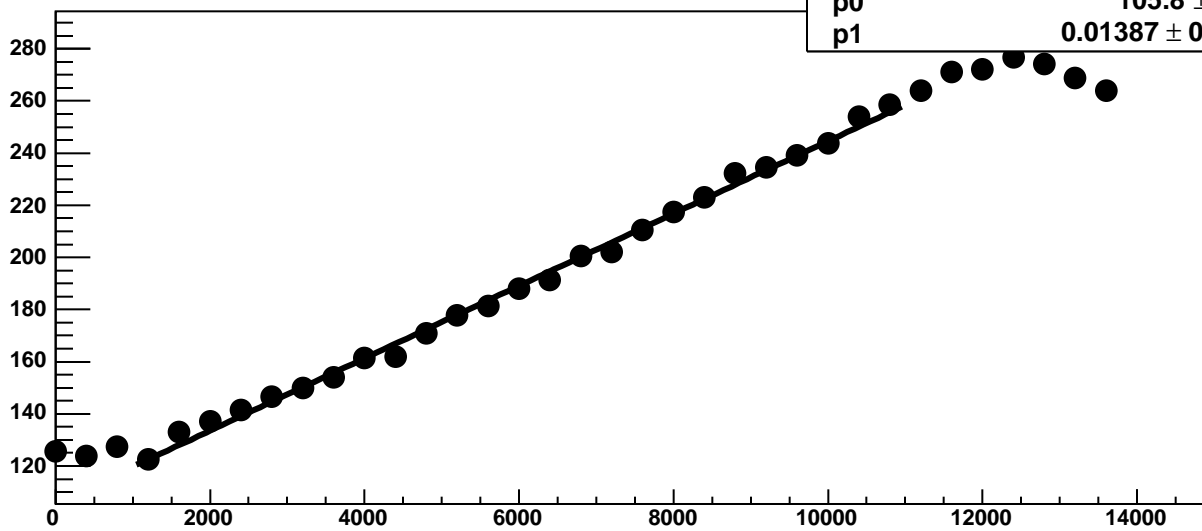
Chip 4, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



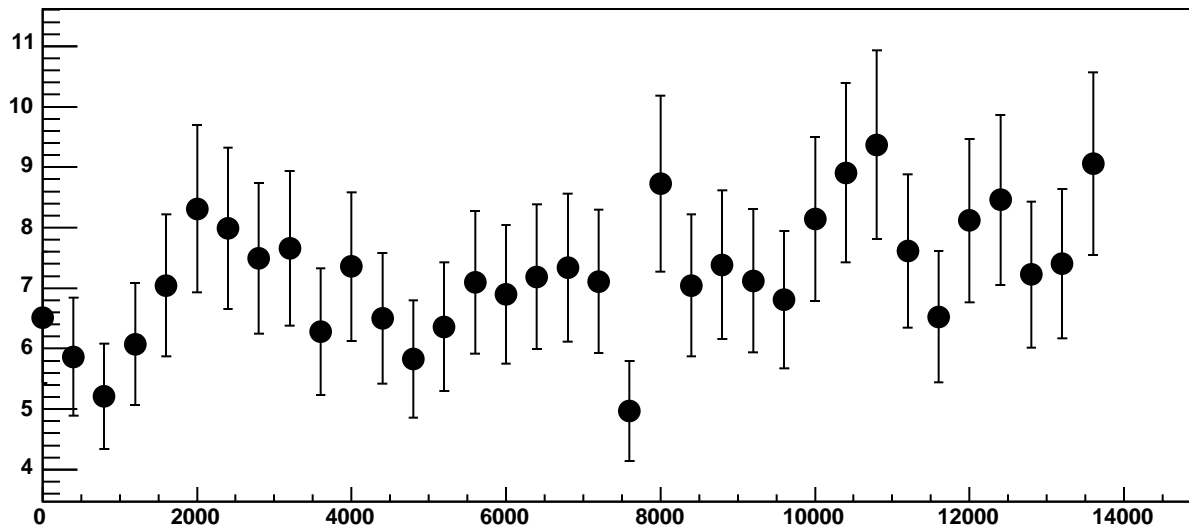
Chip 4, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



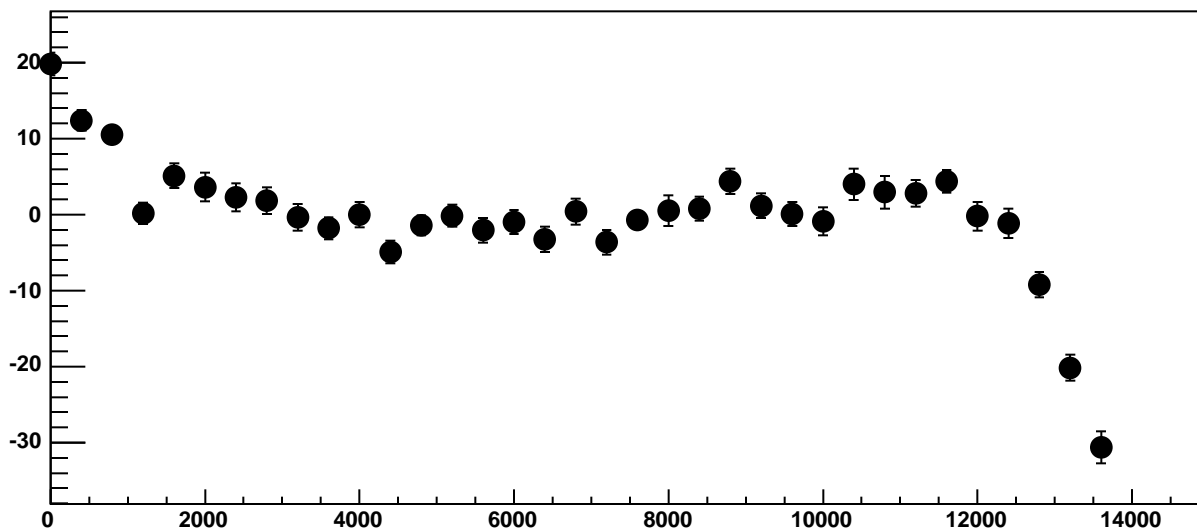
Chip 4, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



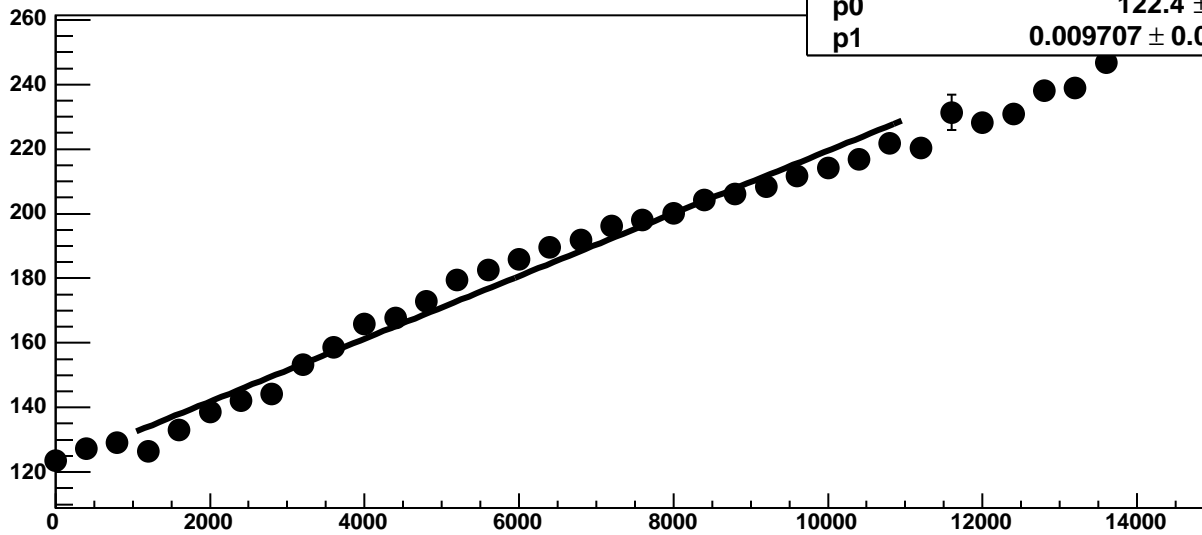
Chip 4, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

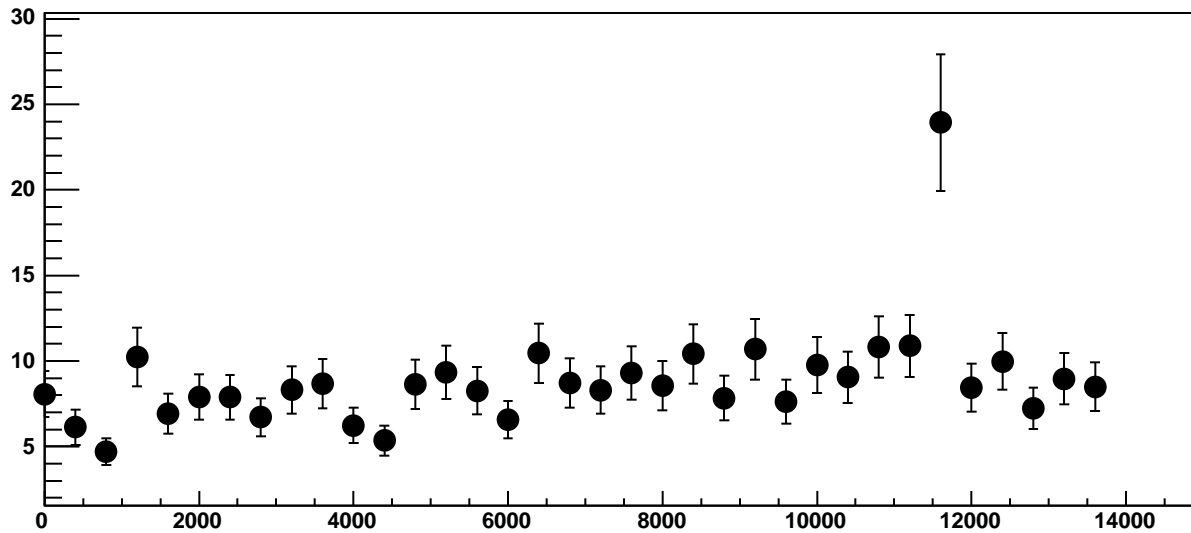


Chip 4, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

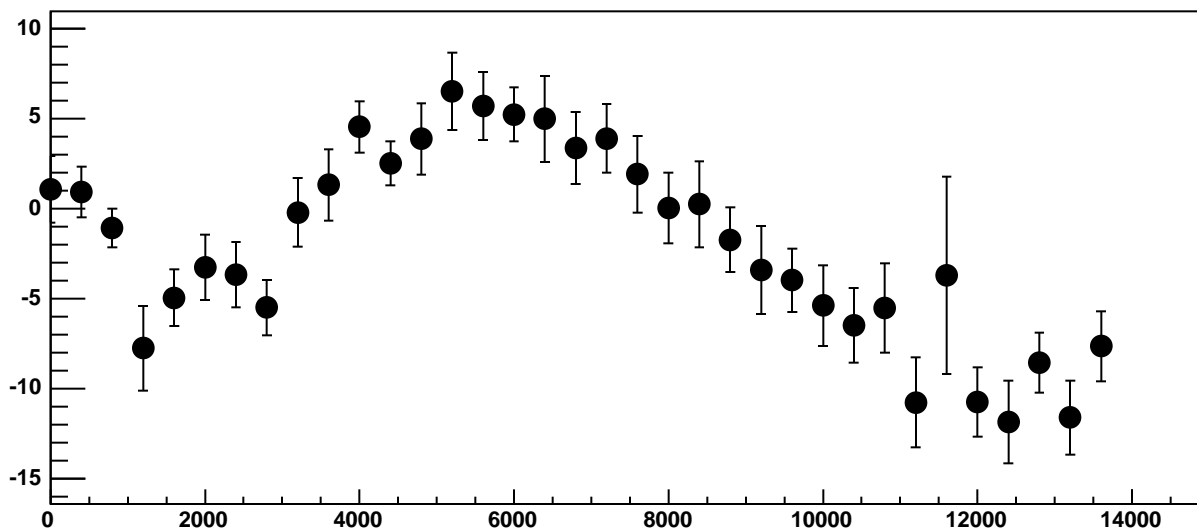


χ^2 / ndf 130.1 / 23
p0 122.4 ± 0.8403
p1 0.009707 ± 0.0001357

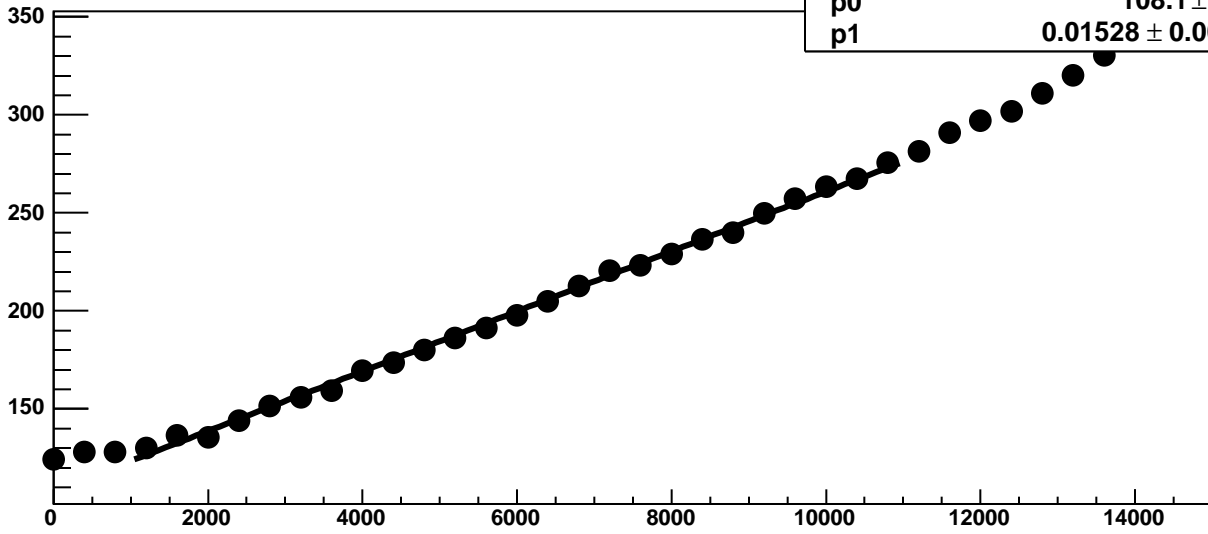
Chip 4, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

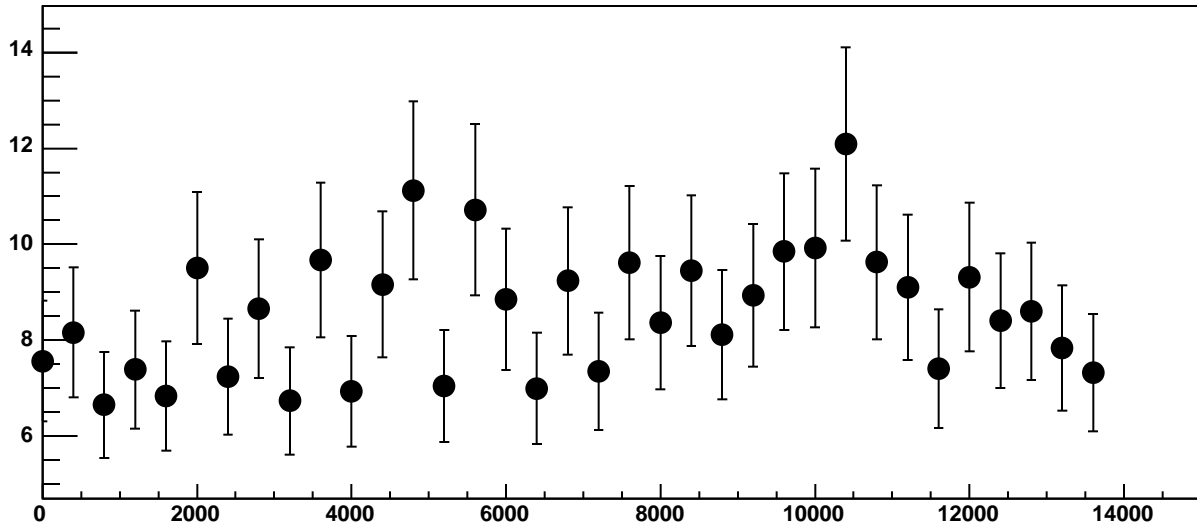


Chip 4, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

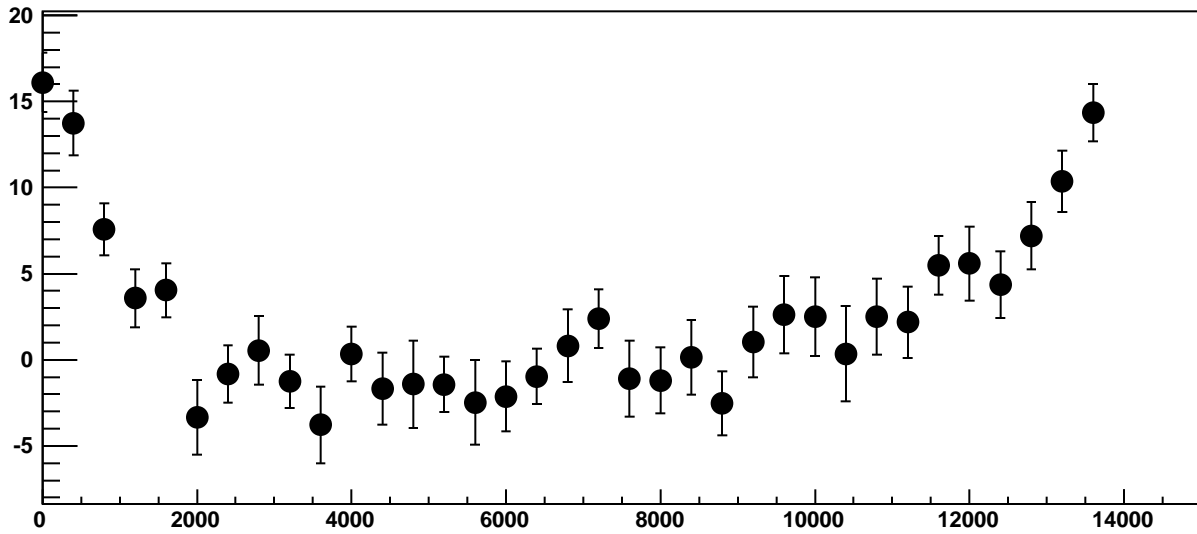


χ^2 / ndf 30.3 / 23
p0 108.1 ± 0.8513
p1 0.01528 ± 0.0001368

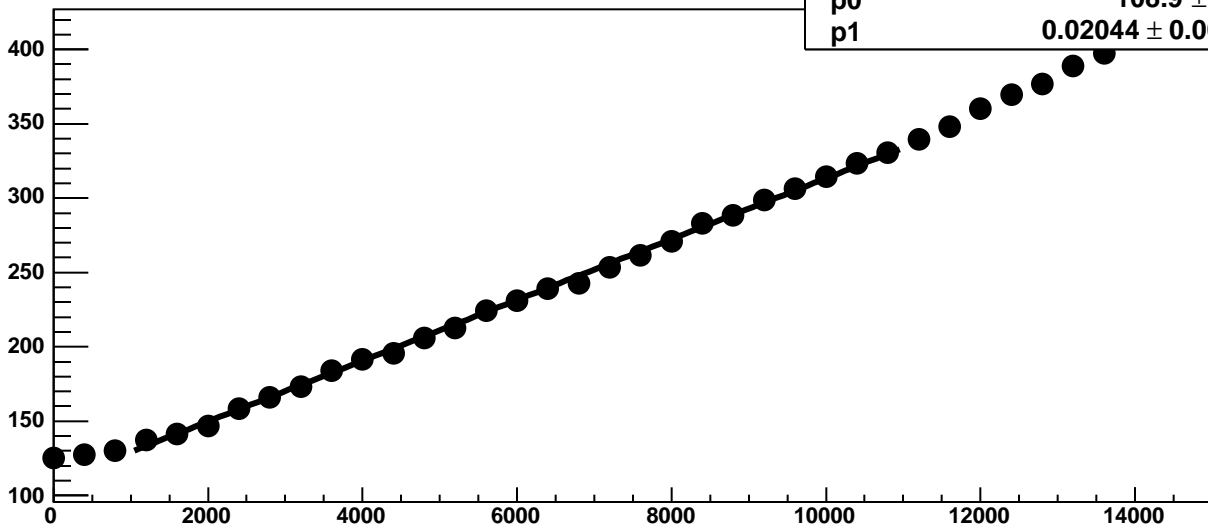
Chip 4, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

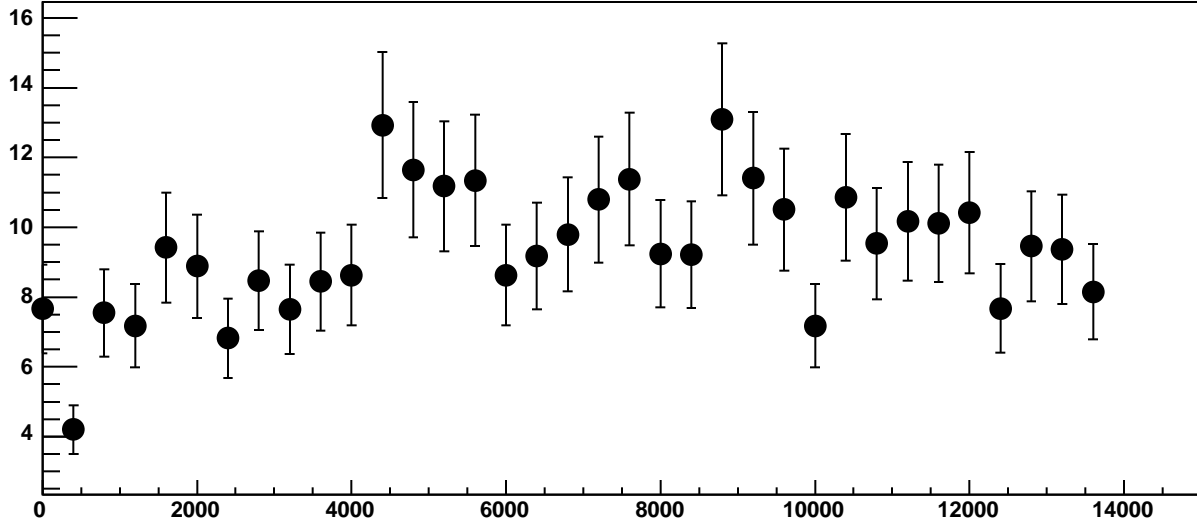


Chip 4, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

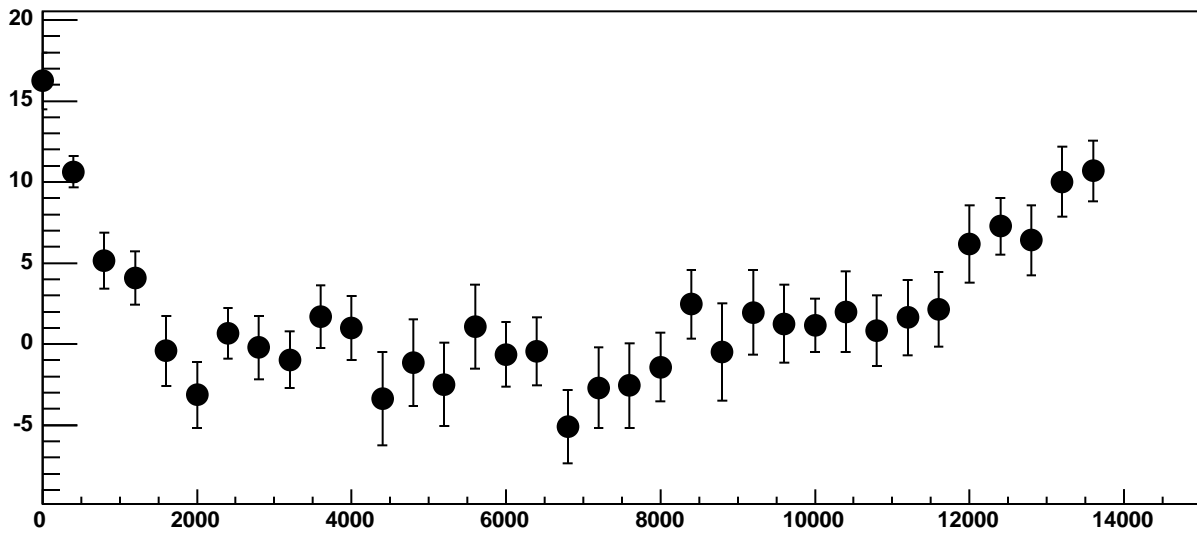


χ^2 / ndf 24.03 / 23
p0 108.9 ± 0.8917
p1 0.02044 ± 0.0001405

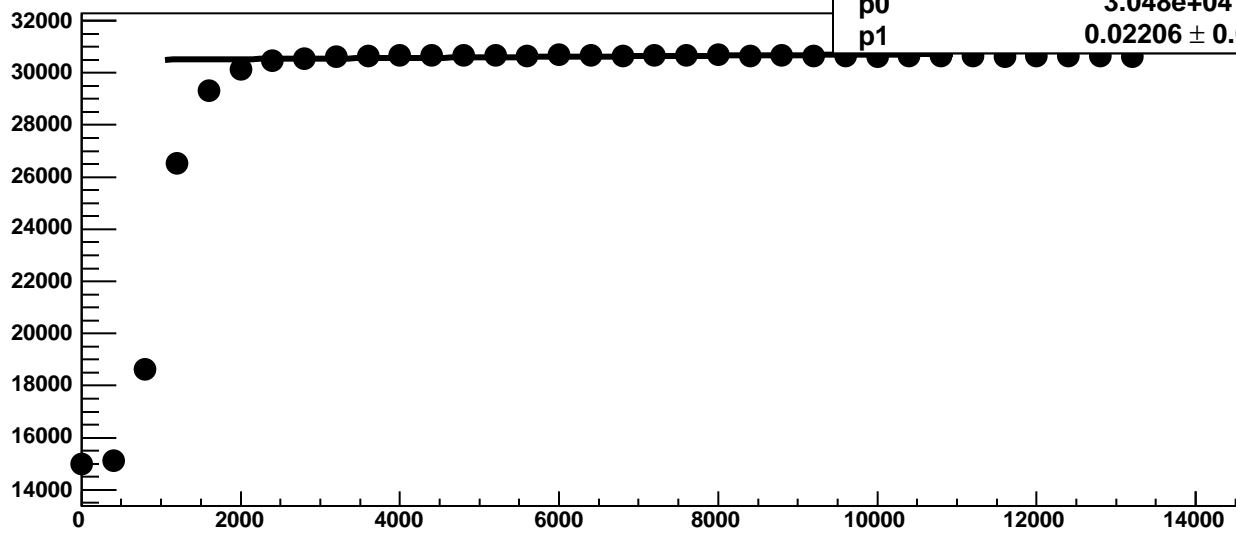
Chip 4, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



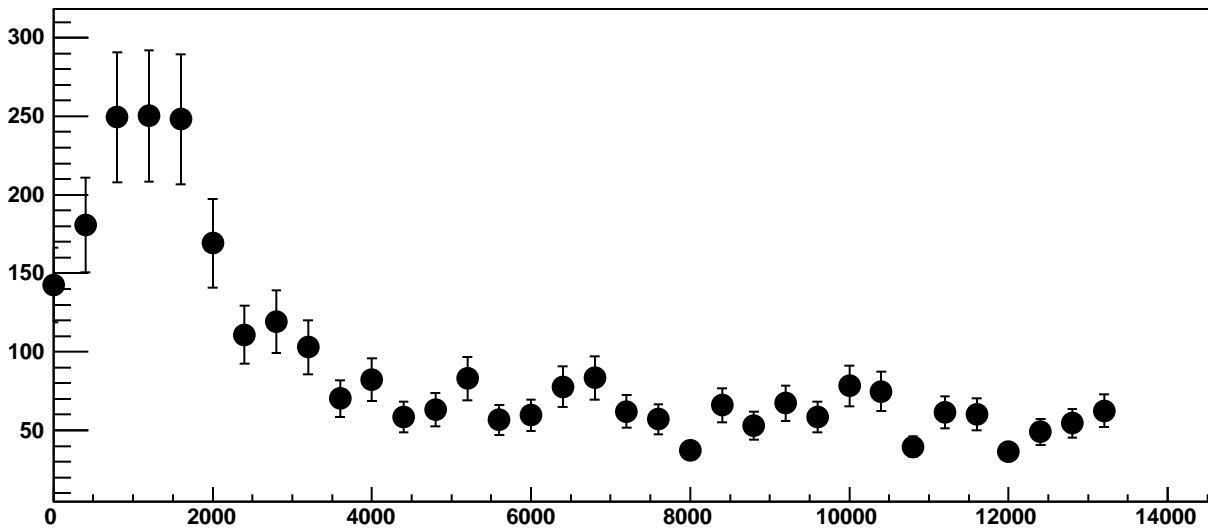
Chip 4, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



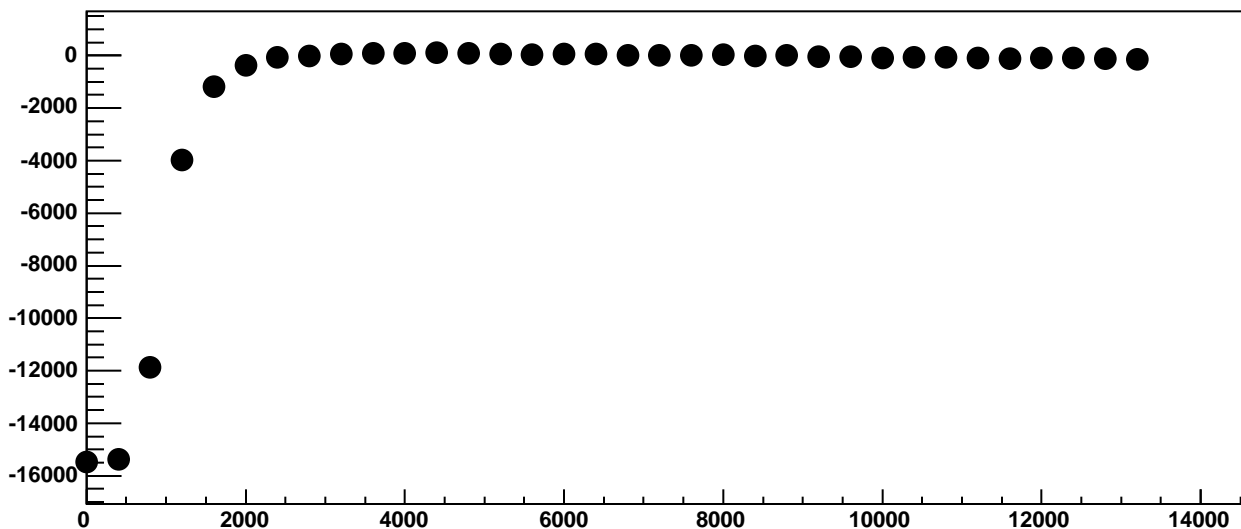
Chip 4, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC



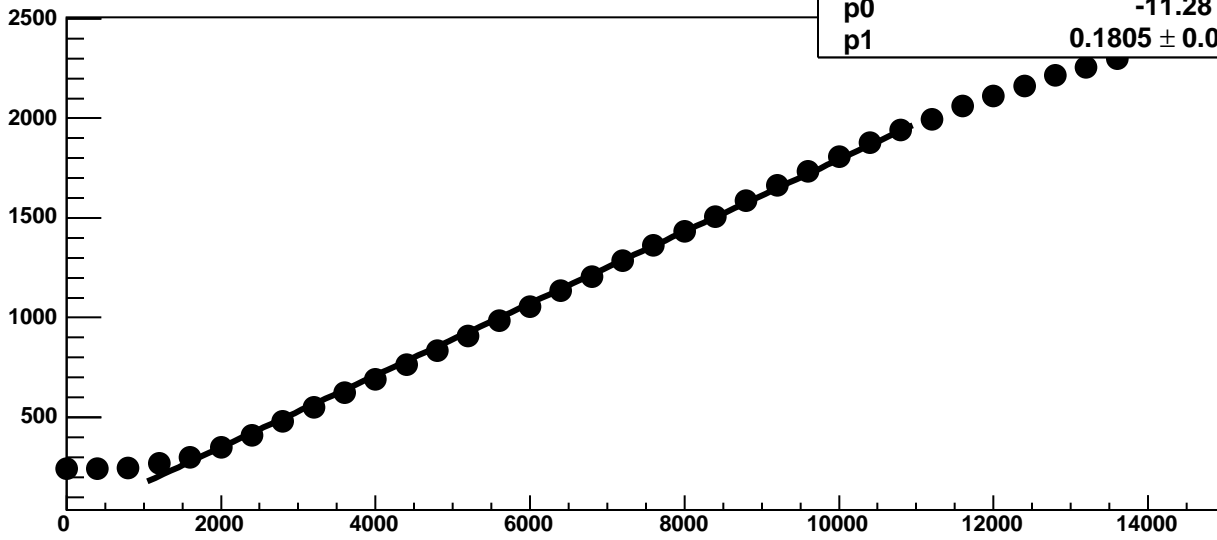
Chip 4, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

766.2 / 23

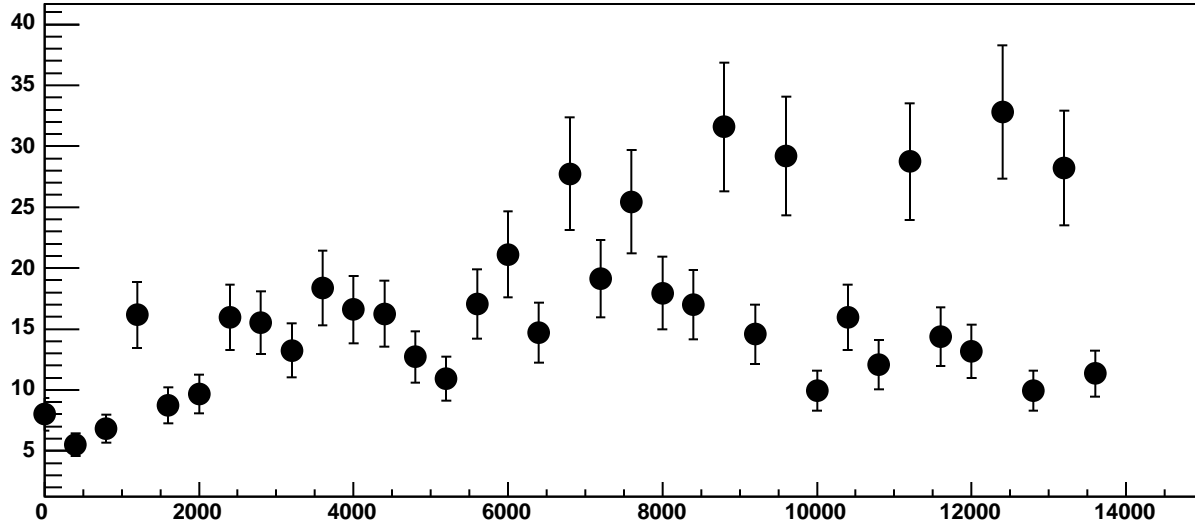
p0

-11.28 ± 1.334

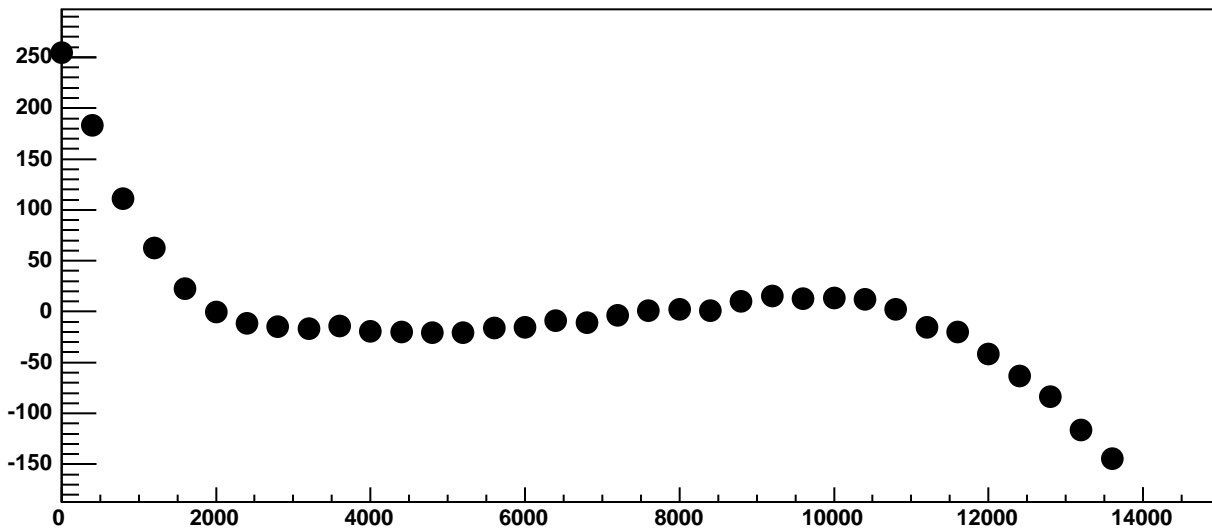
p1

0.1805 ± 0.0002109

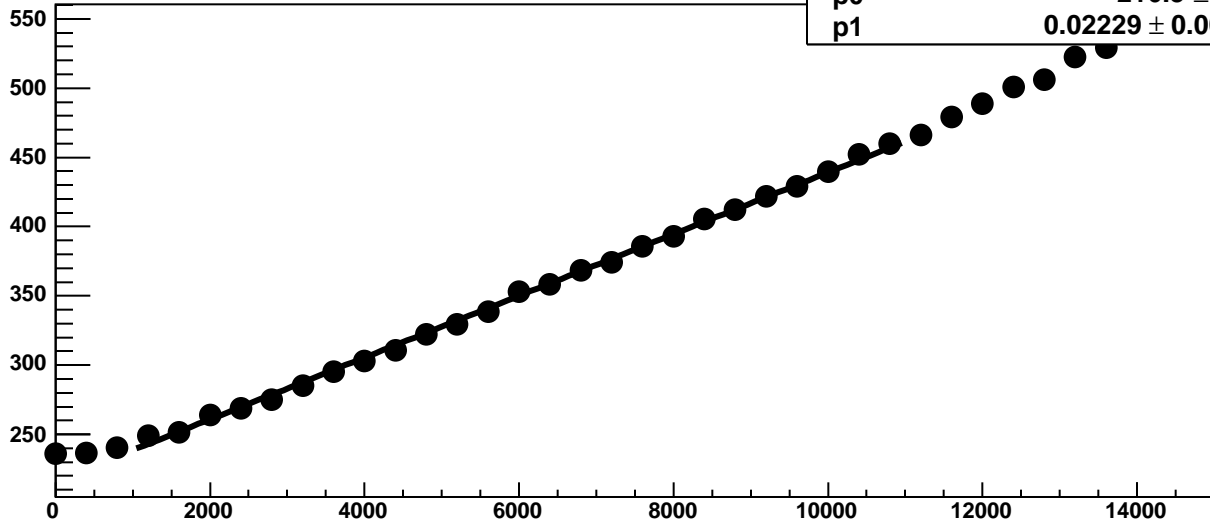
Chip 4, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

76.29 / 23

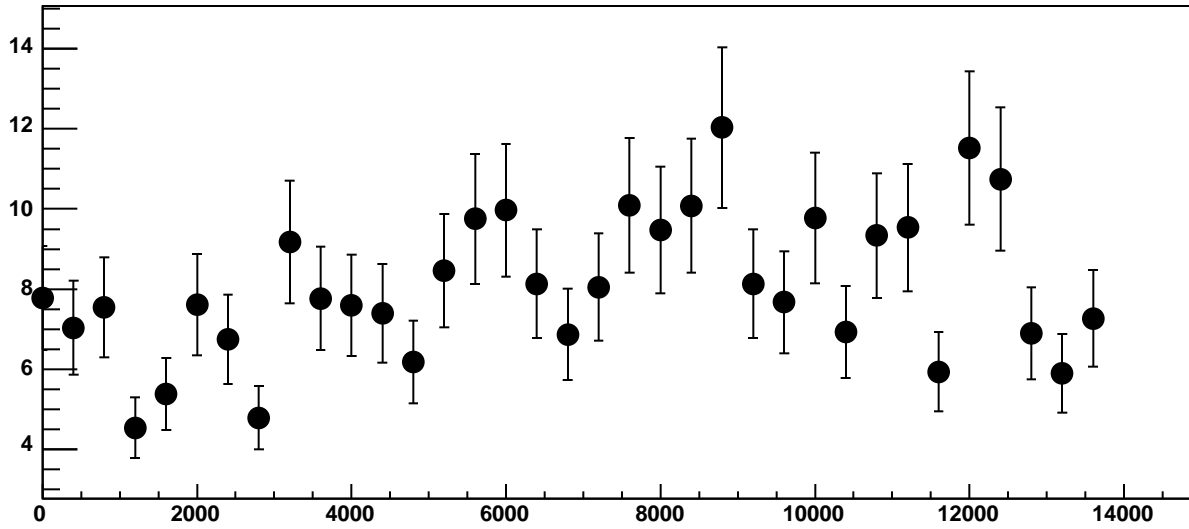
p0

216.3 ± 0.6609

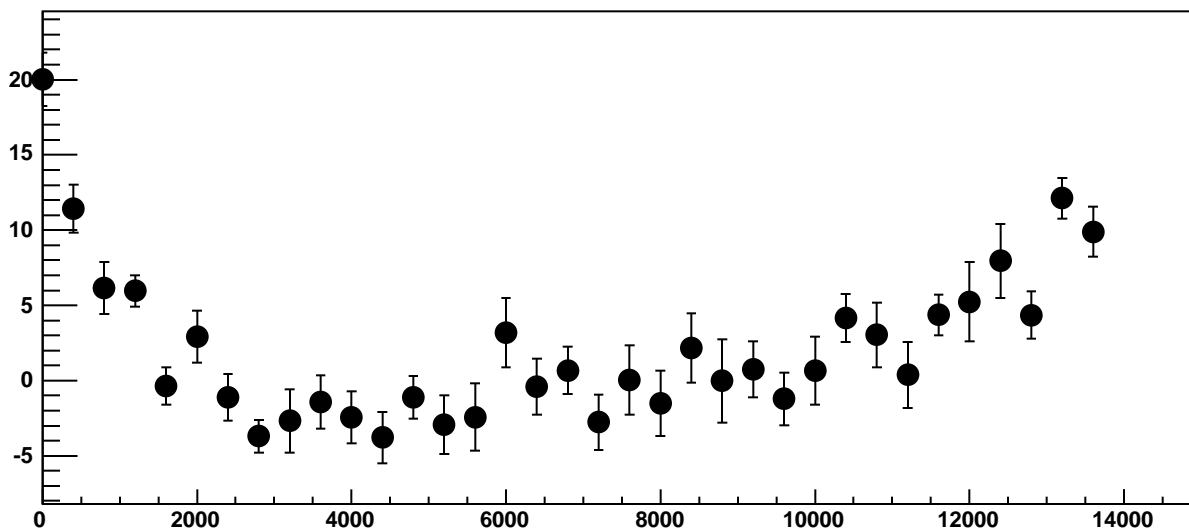
p1

0.02229 ± 0.0001124

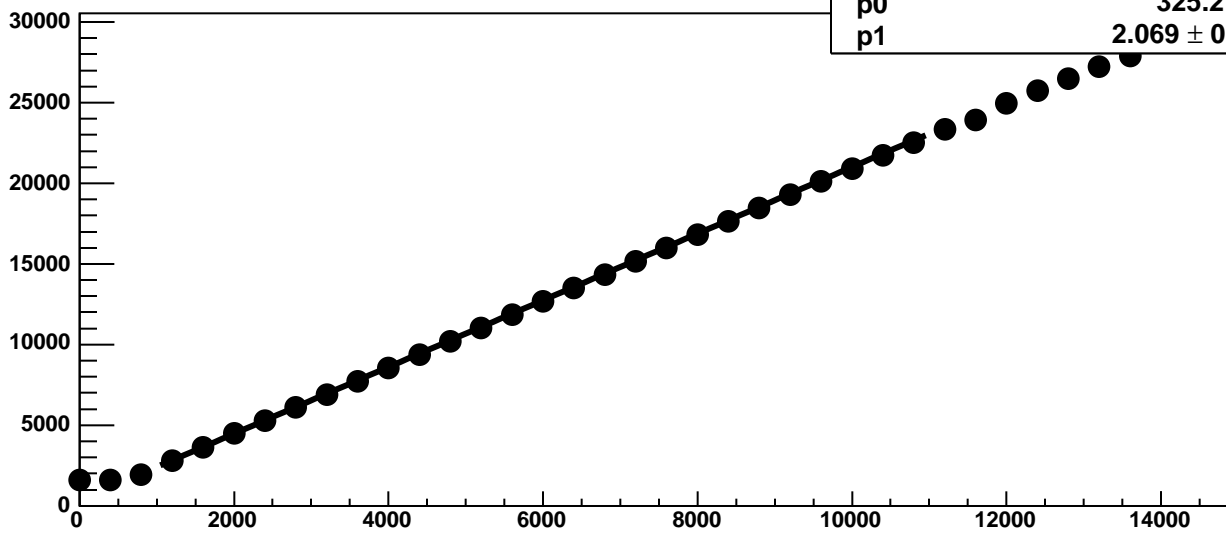
Chip 4, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



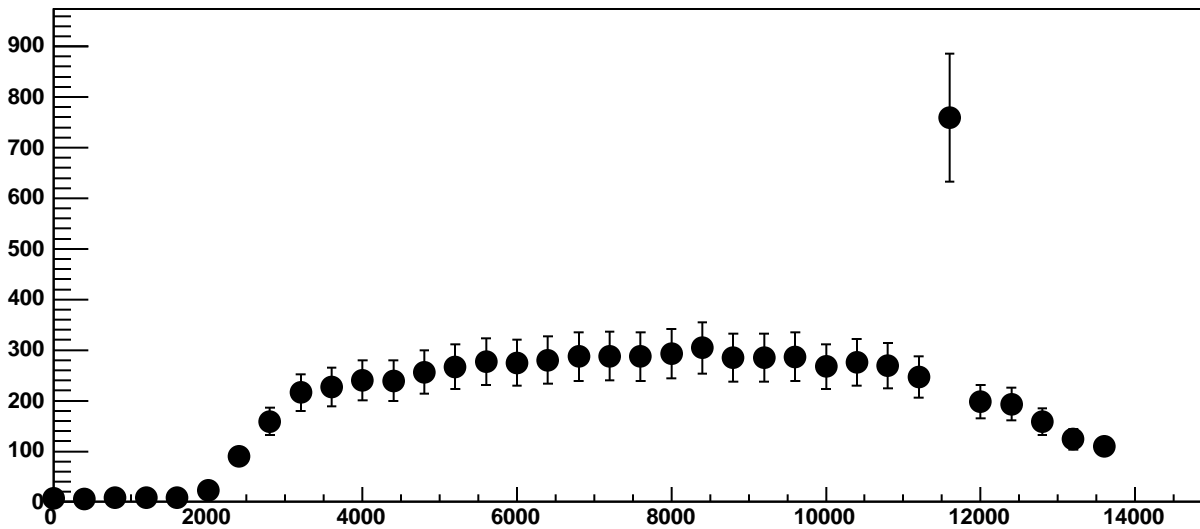
Chip 4, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



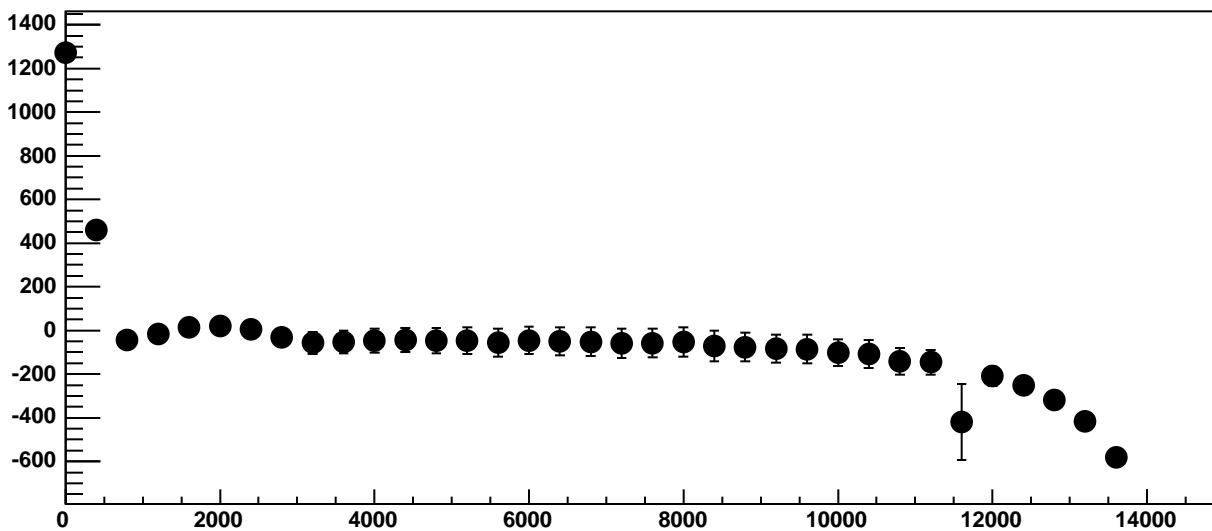
Chip 4, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC



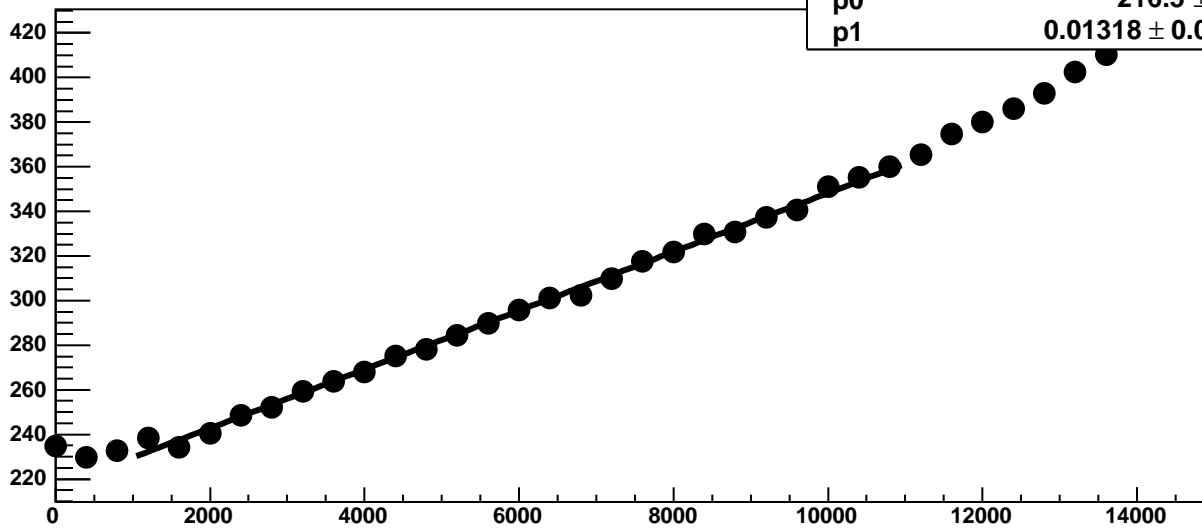
Chip 4, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

32.8 / 23

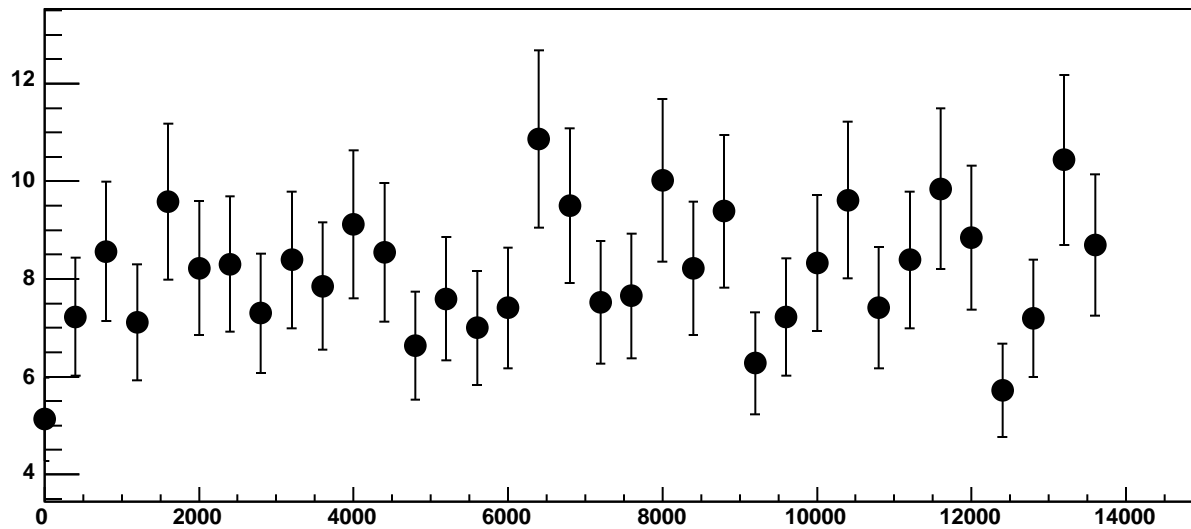
p0

216.5 ± 0.8449

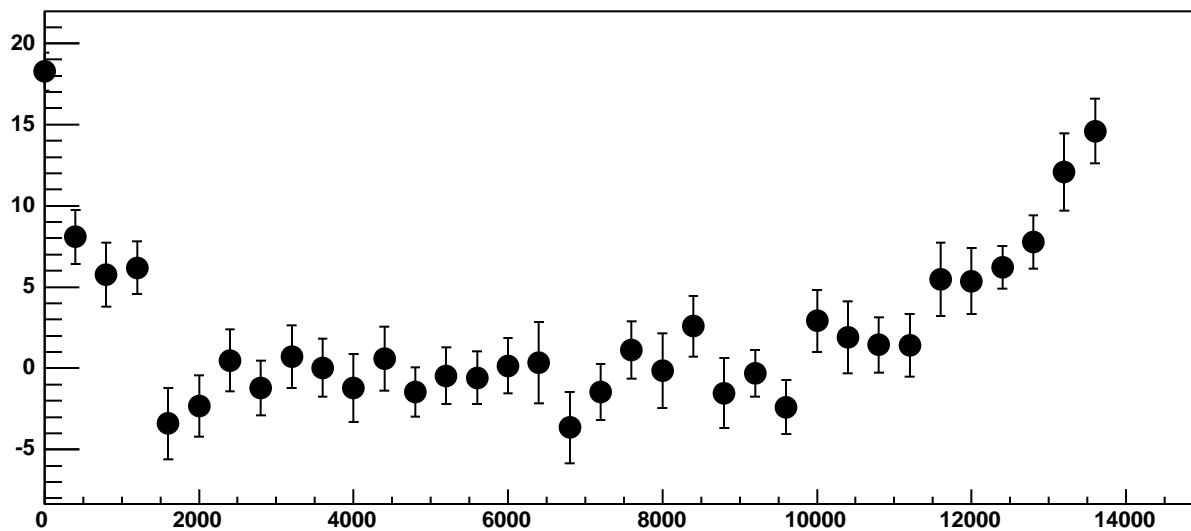
p1

0.01318 ± 0.0001267

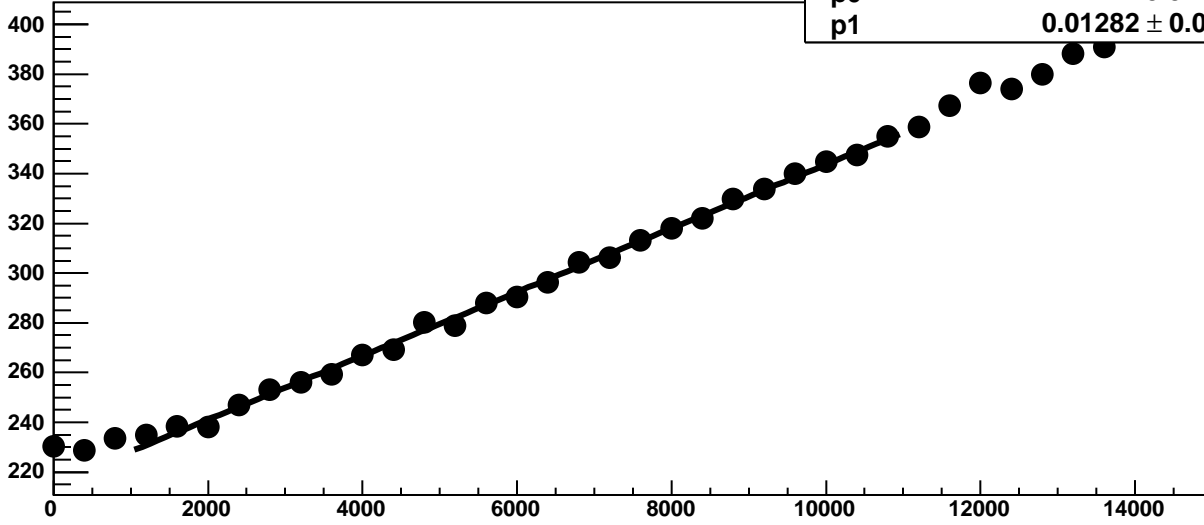
Chip 4, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

29.31 / 23

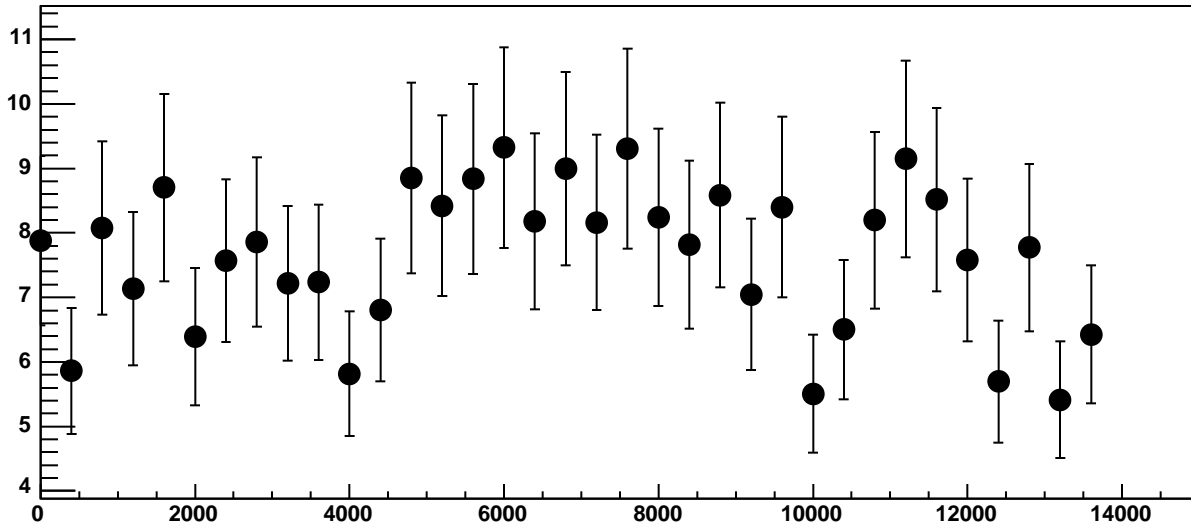
p0

215.5 ± 0.7687

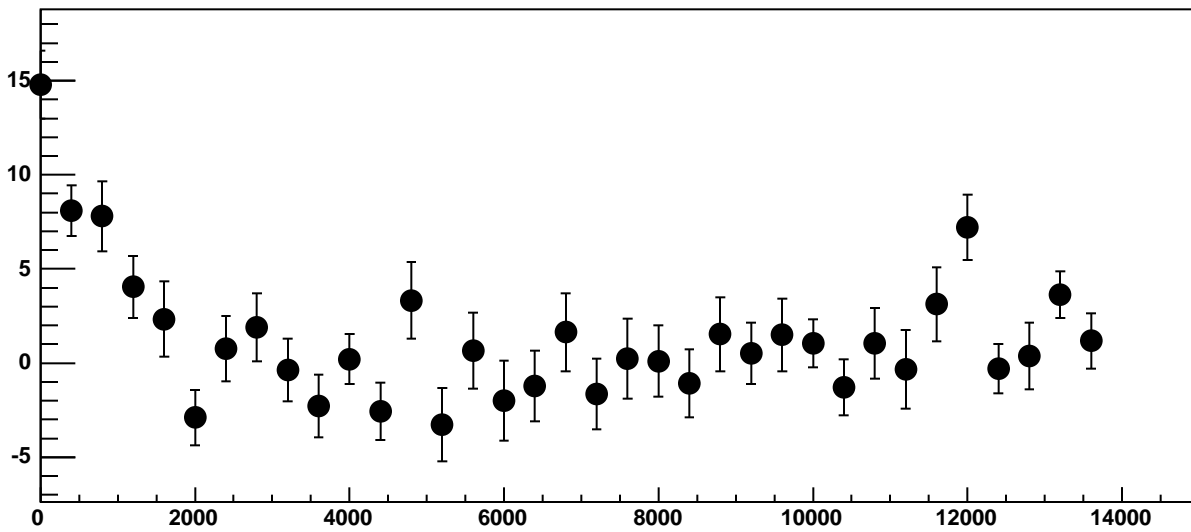
p1

0.01282 ± 0.0001147

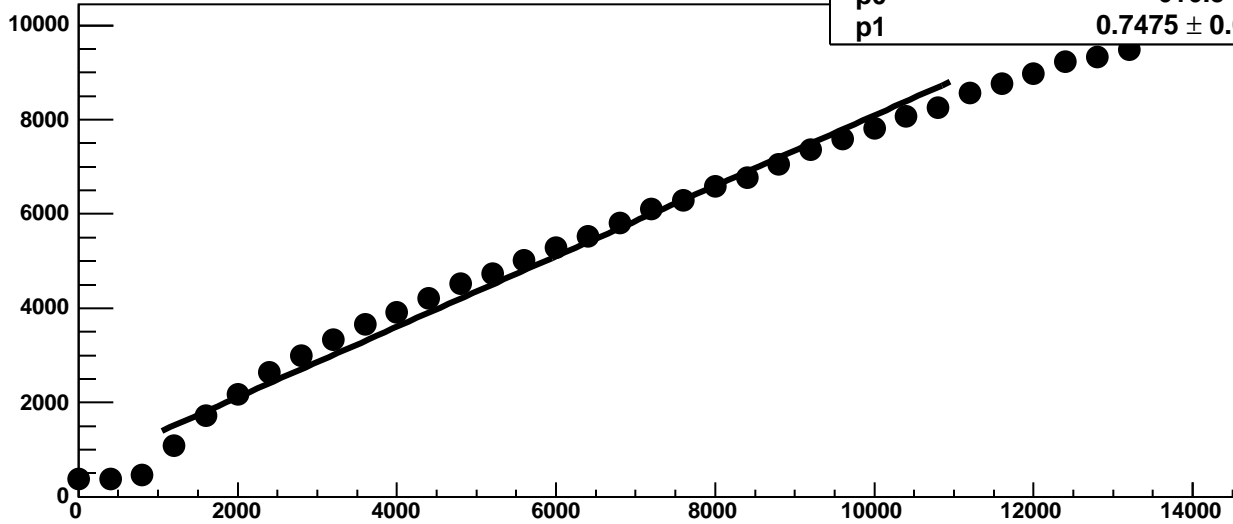
Chip 4, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

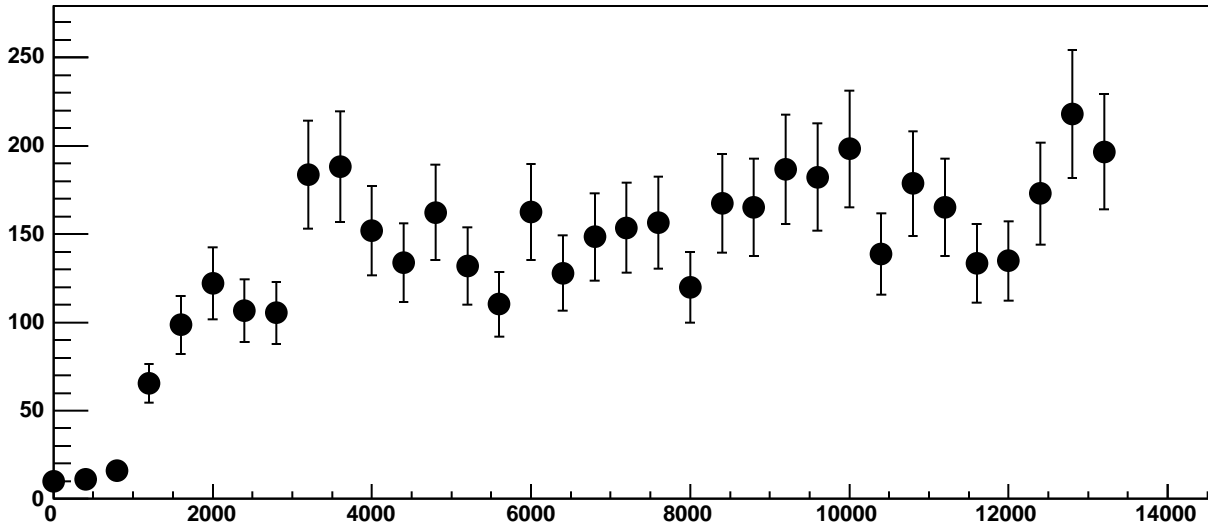


Chip 4, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC

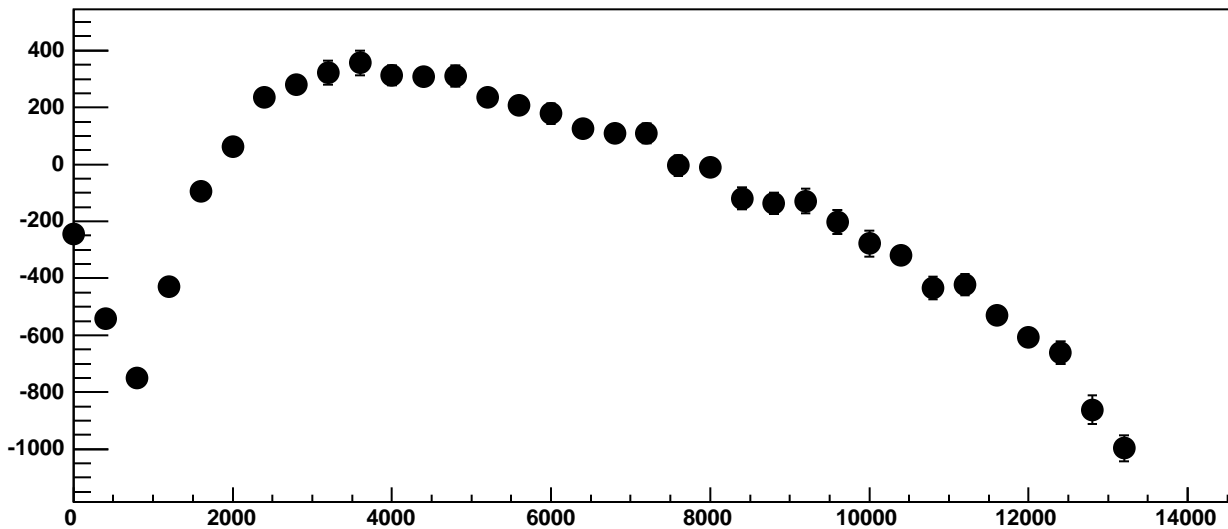


χ^2 / ndf 1938 / 23
p0 616.5 ± 11.39
p1 0.7475 ± 0.002004

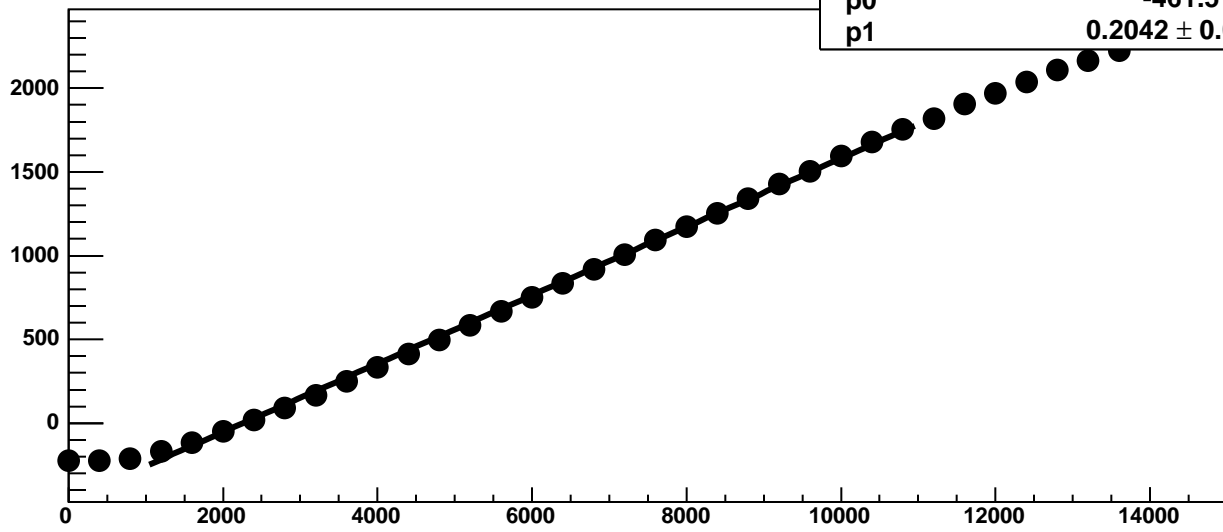
Chip 4, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC



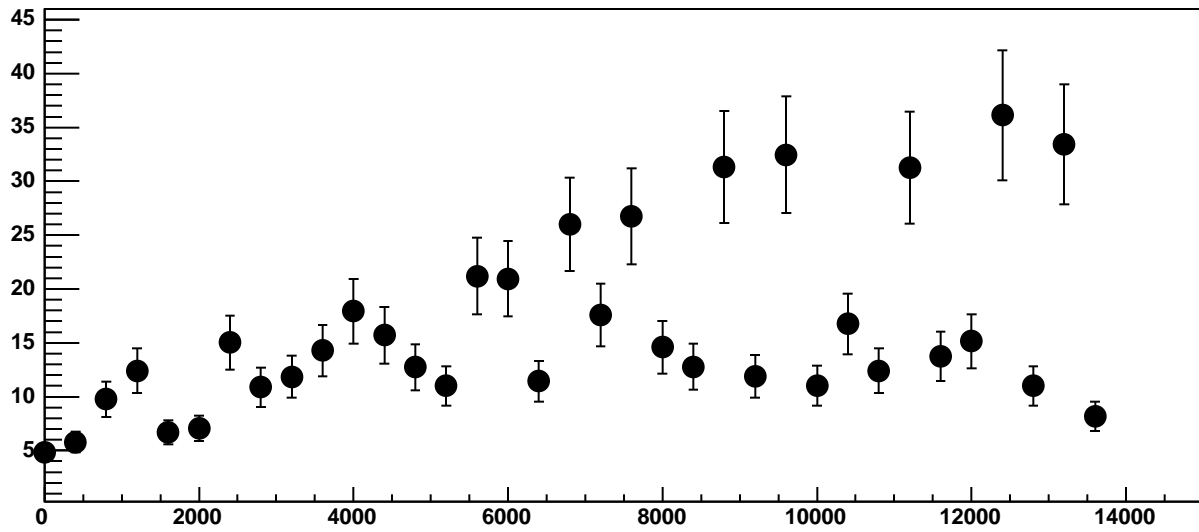
Chip 4, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC



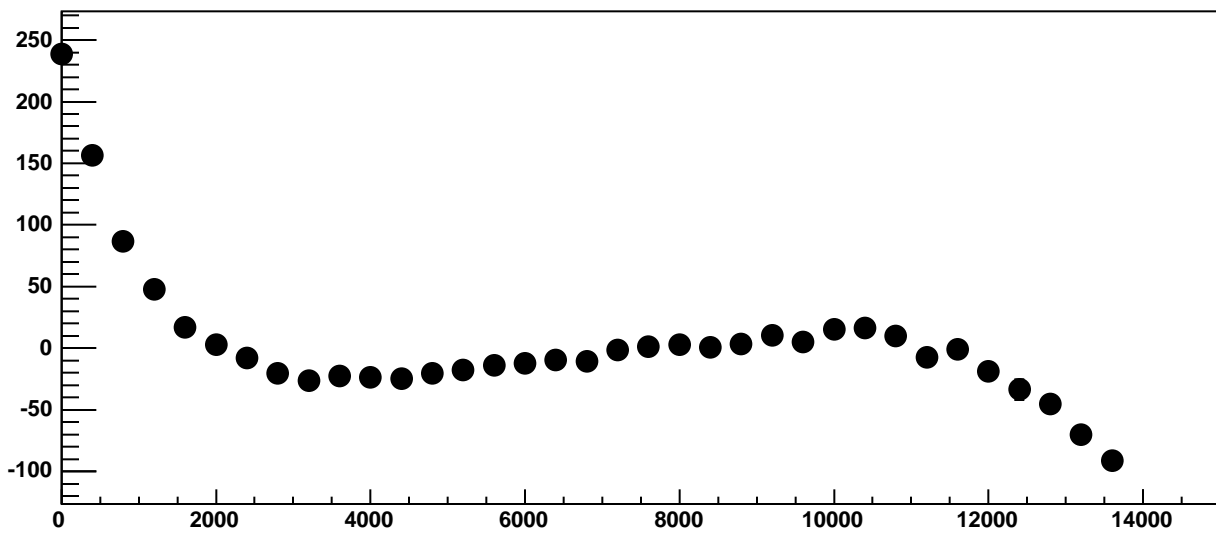
Chip 5, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC



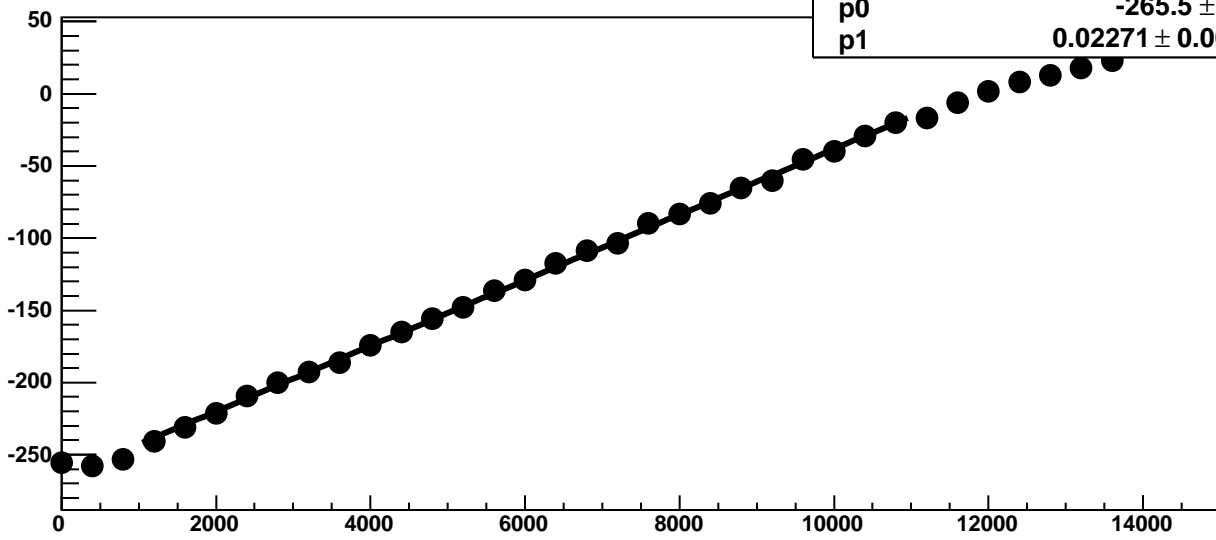
Chip 5, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

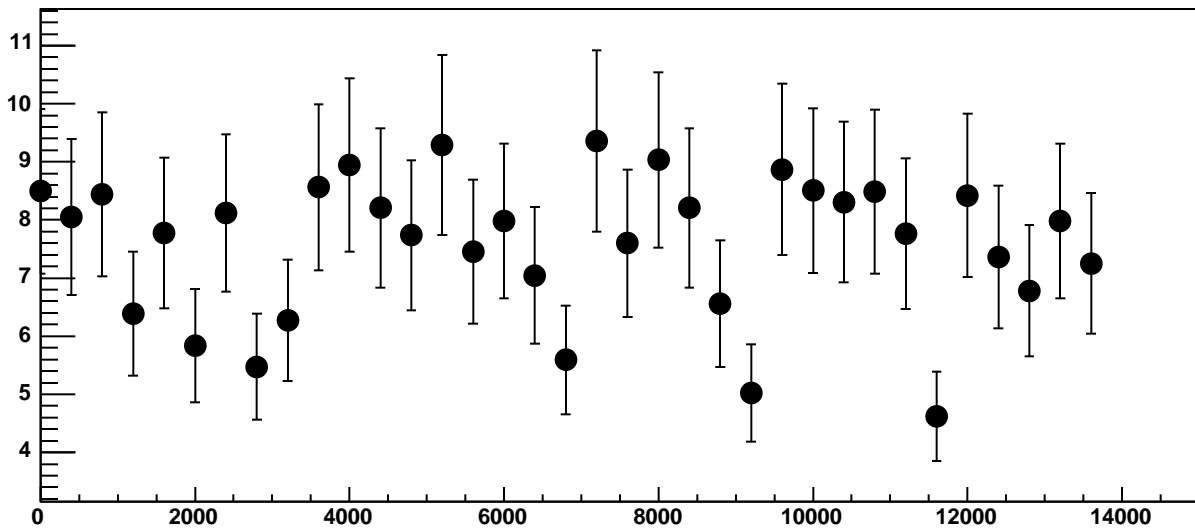


Chip 5, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

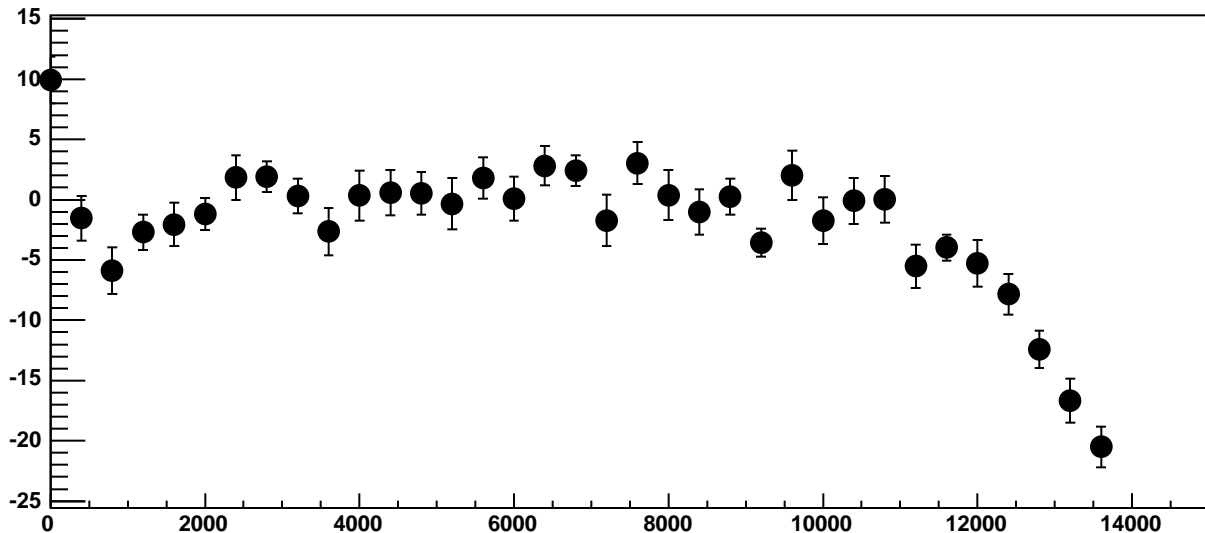


χ^2 / ndf 33.93 / 23
p0 -265.5 ± 0.7383
p1 0.02271 ± 0.0001138

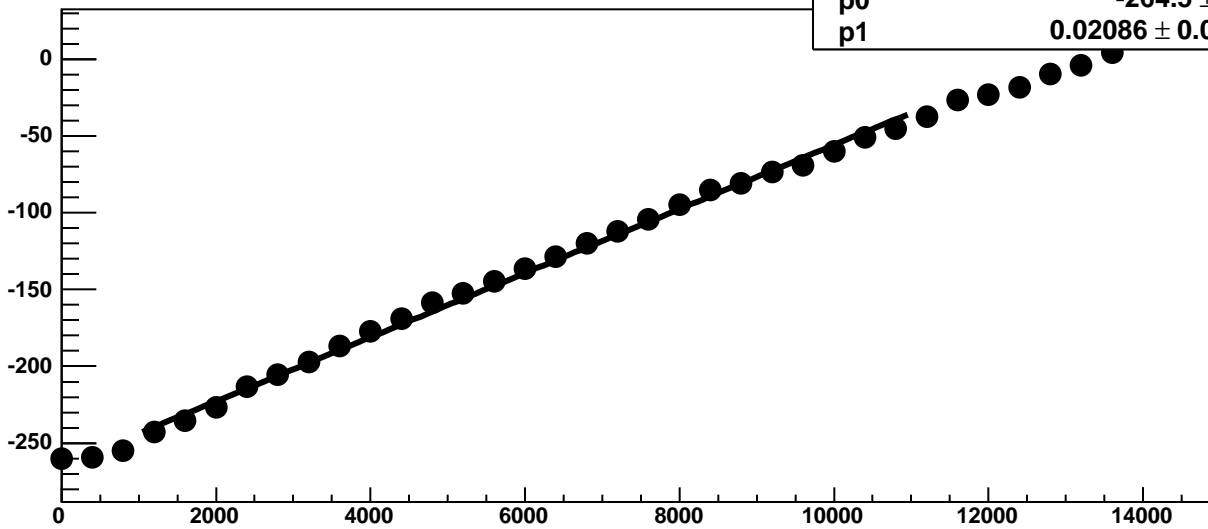
Chip 5, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

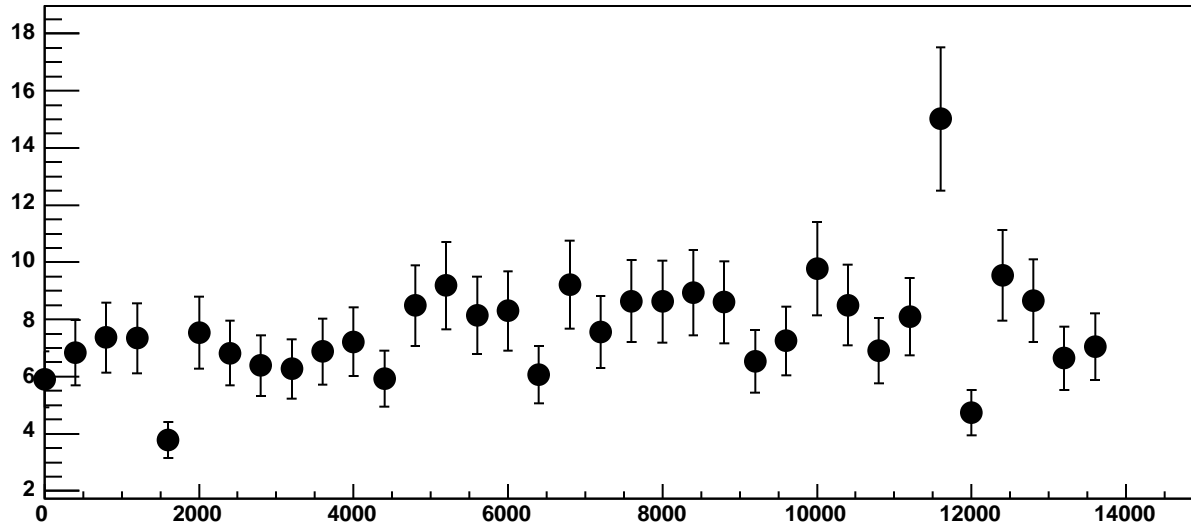


Chip 5, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

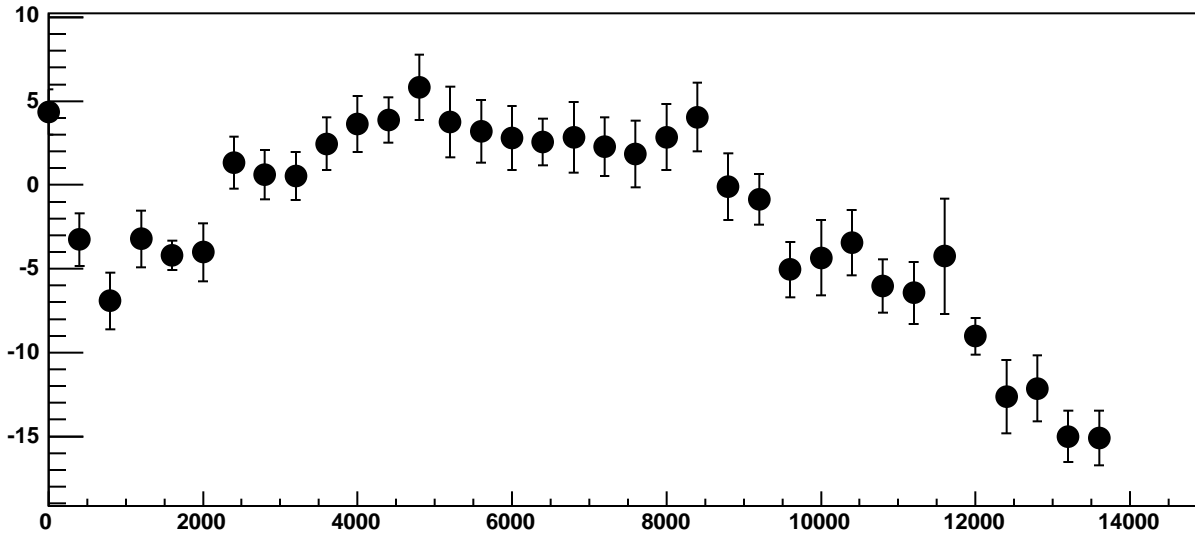


χ^2 / ndf 110.5 / 23
p0 -264.5 ± 0.6552
p1 0.02086 ± 0.0001076

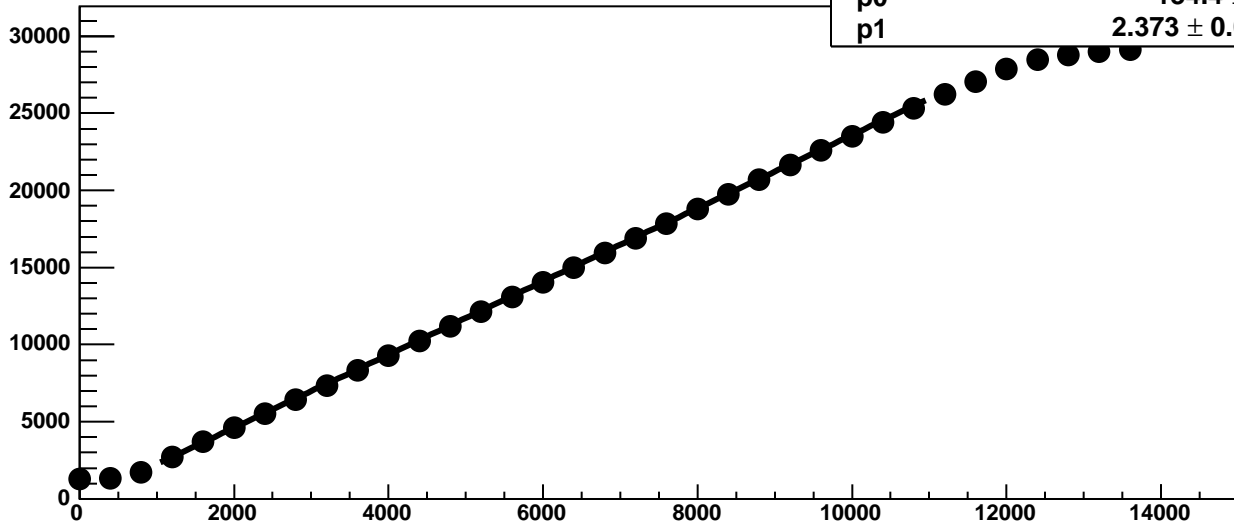
Chip 5, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

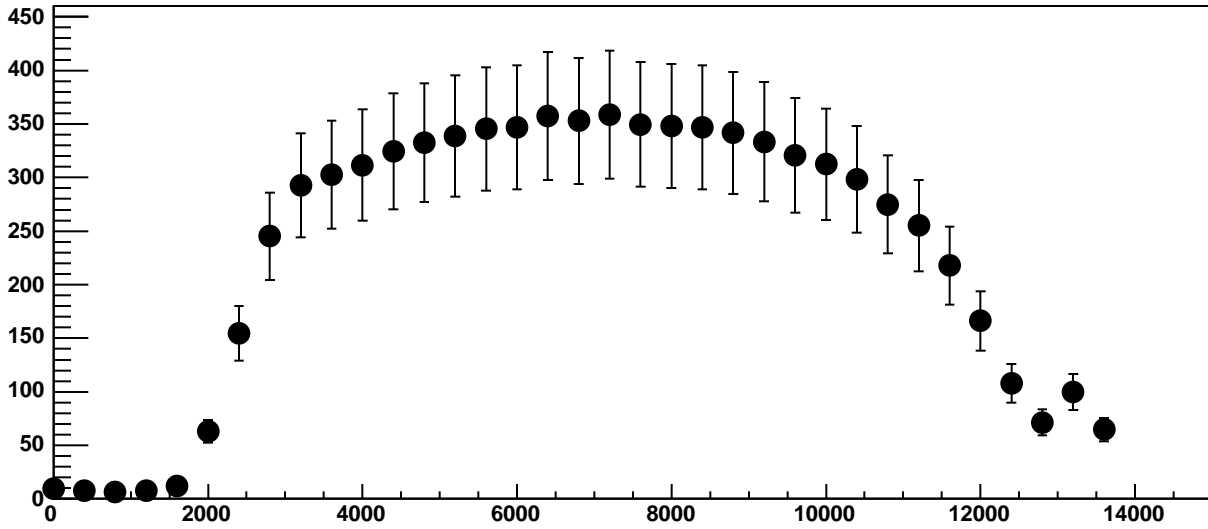


Chip 5, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC

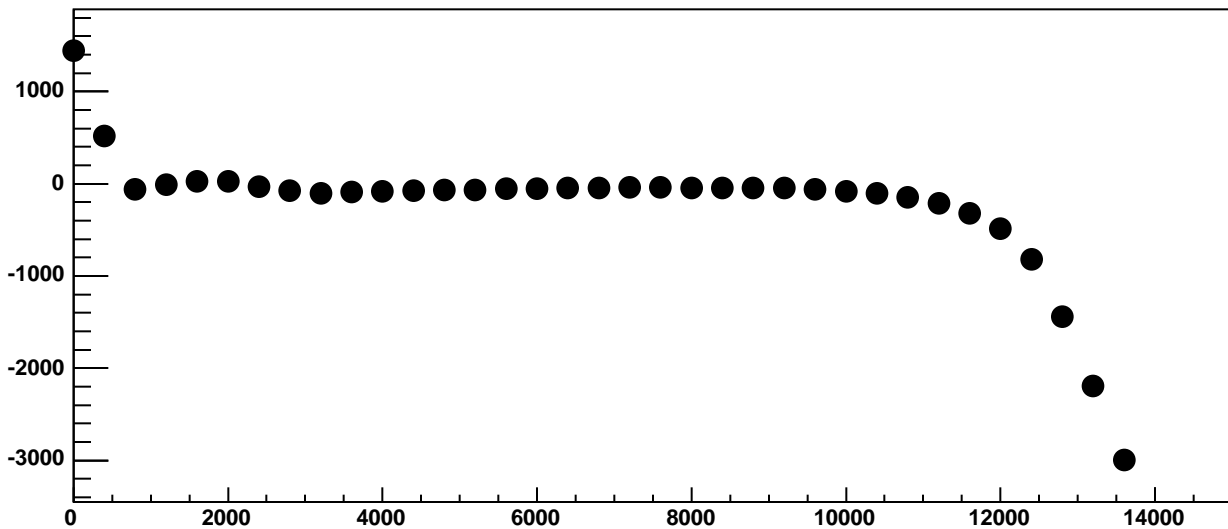


χ^2 / ndf 169.1 / 23
p0 -134.4 ± 3.757
p1 2.373 ± 0.002535

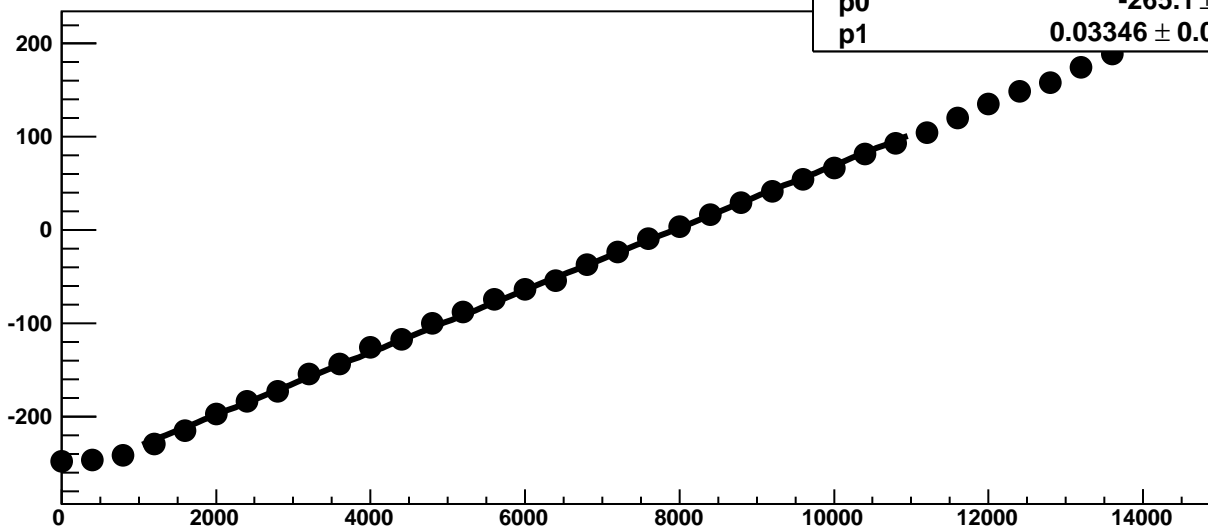
Chip 5, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC

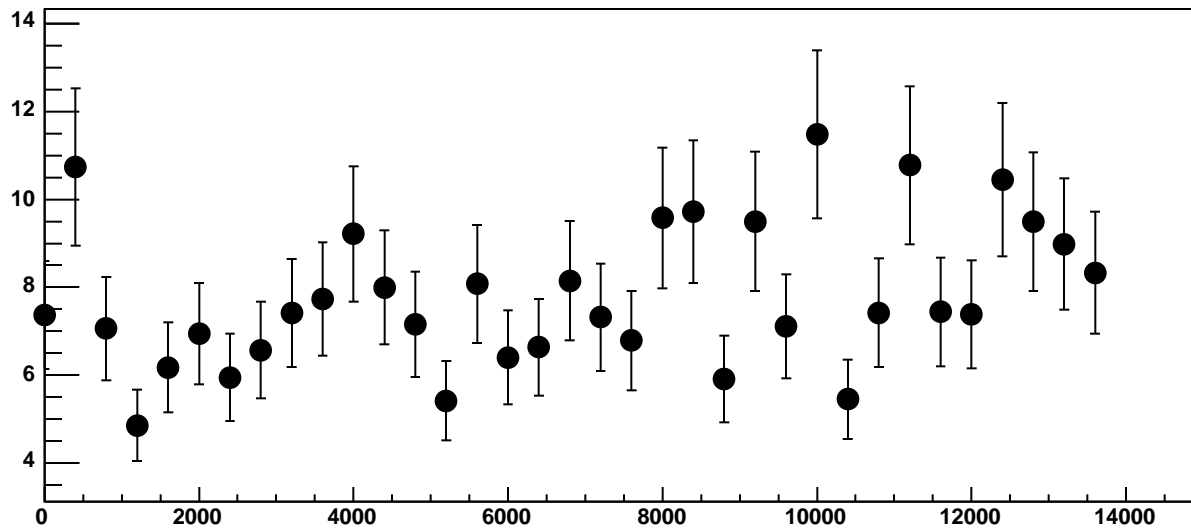


Chip 5, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

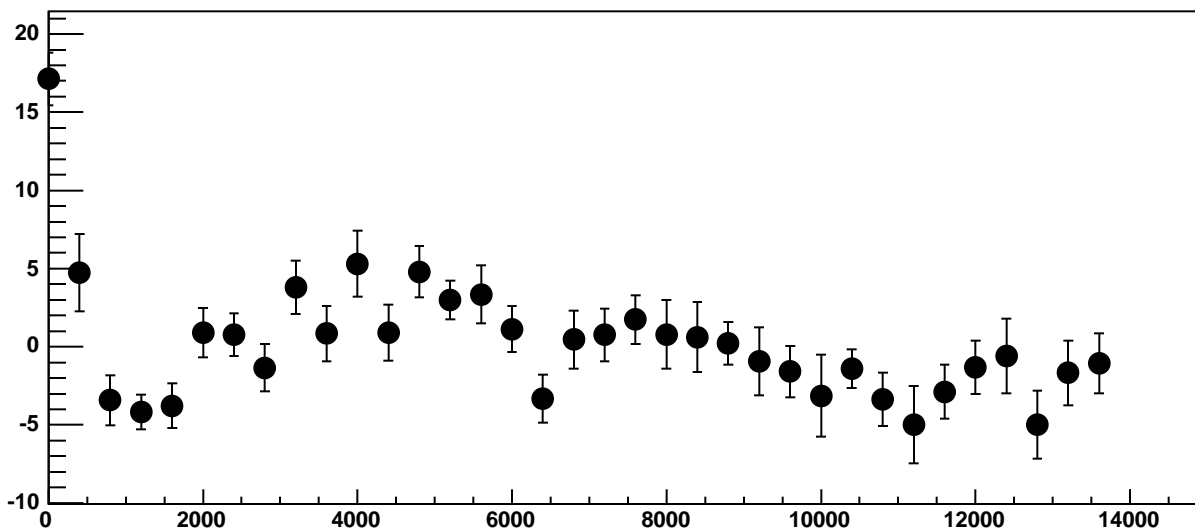


χ^2 / ndf 66.76 / 23
p0 -265.1 ± 0.6742
p1 0.03346 ± 0.0001057

Chip 5, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

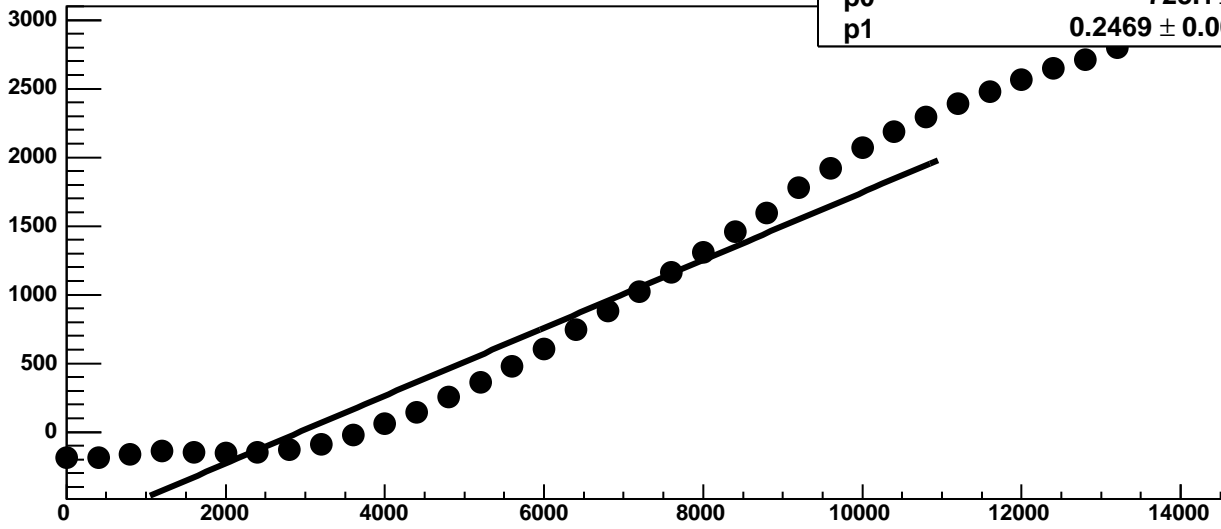


Chip 5, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

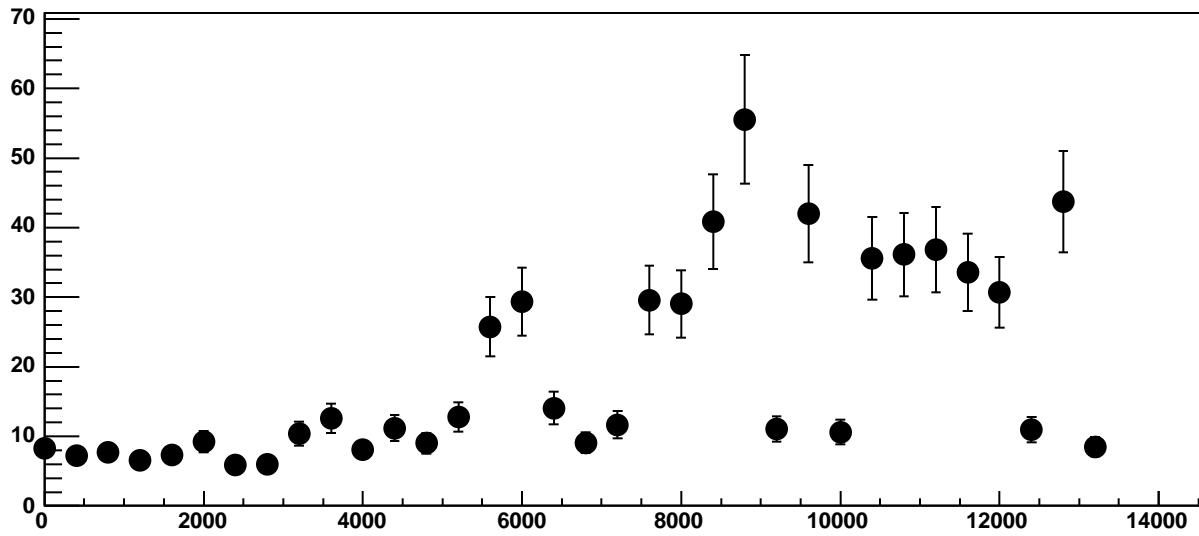


Chip 5, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

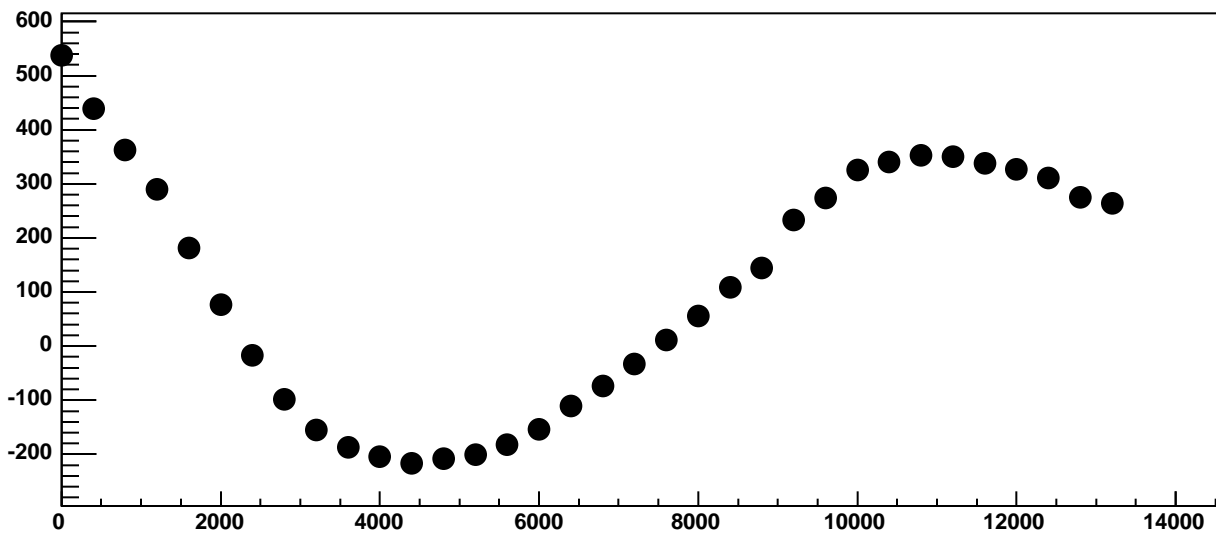
χ^2 / ndf 1.327e+05 / 23
p0 -723.1 ± 0.926
p1 0.2469 ± 0.0001968



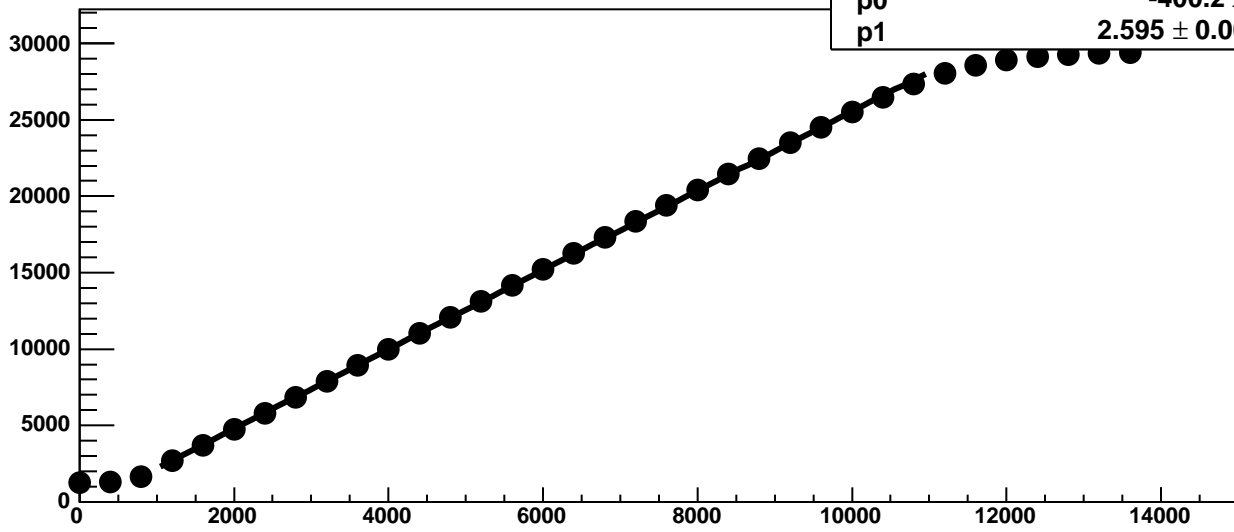
Chip 5, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



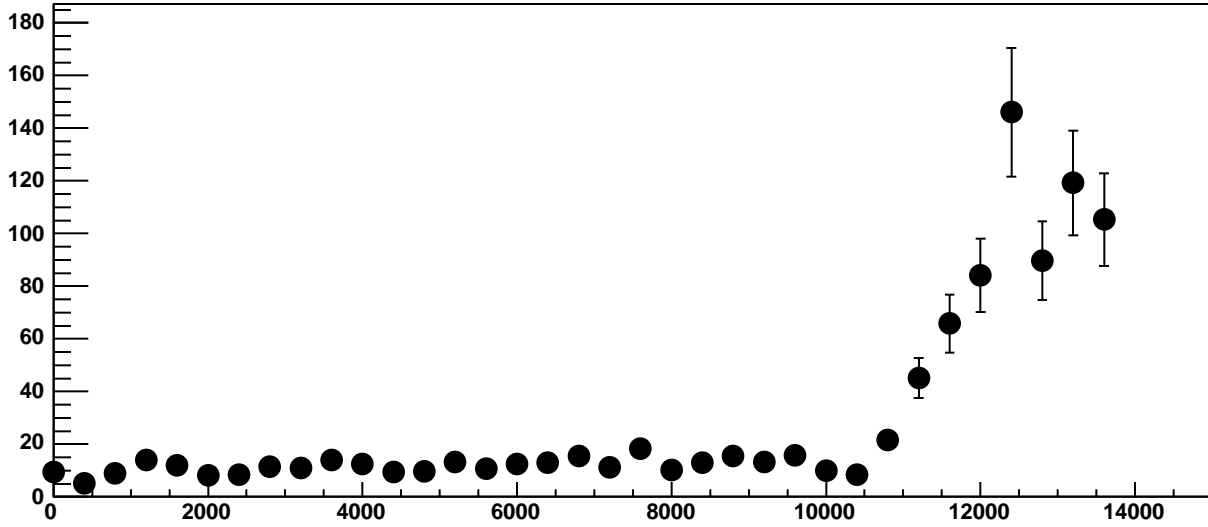
Chip 5, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



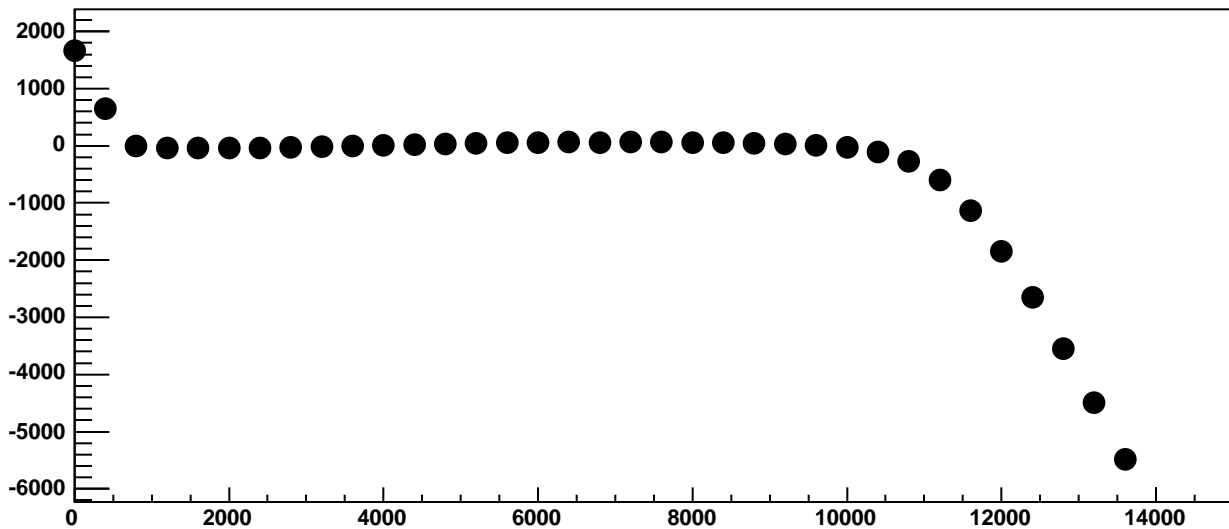
Chip 5, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



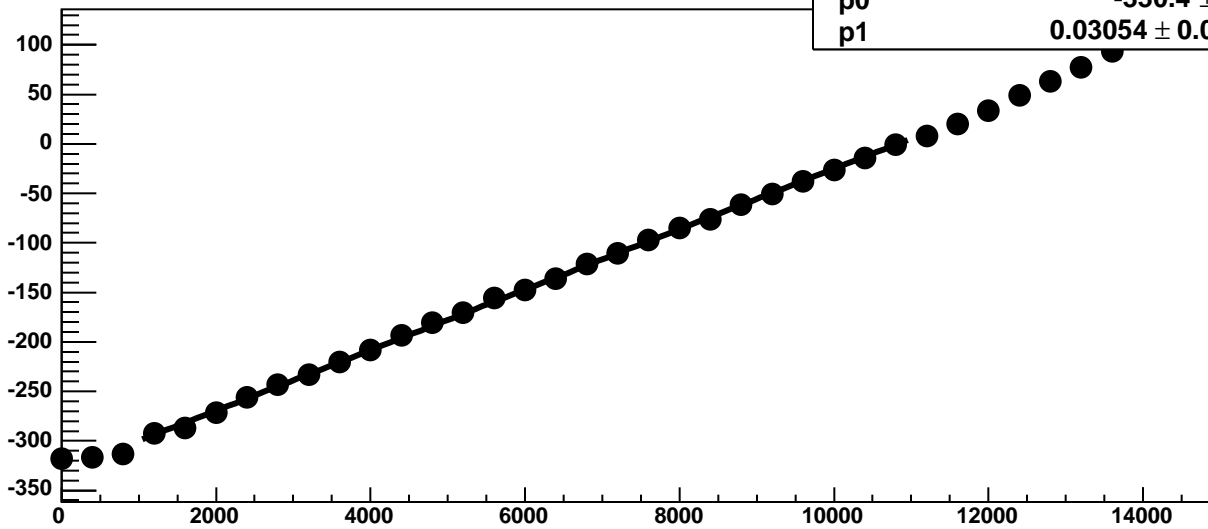
Chip 5, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC

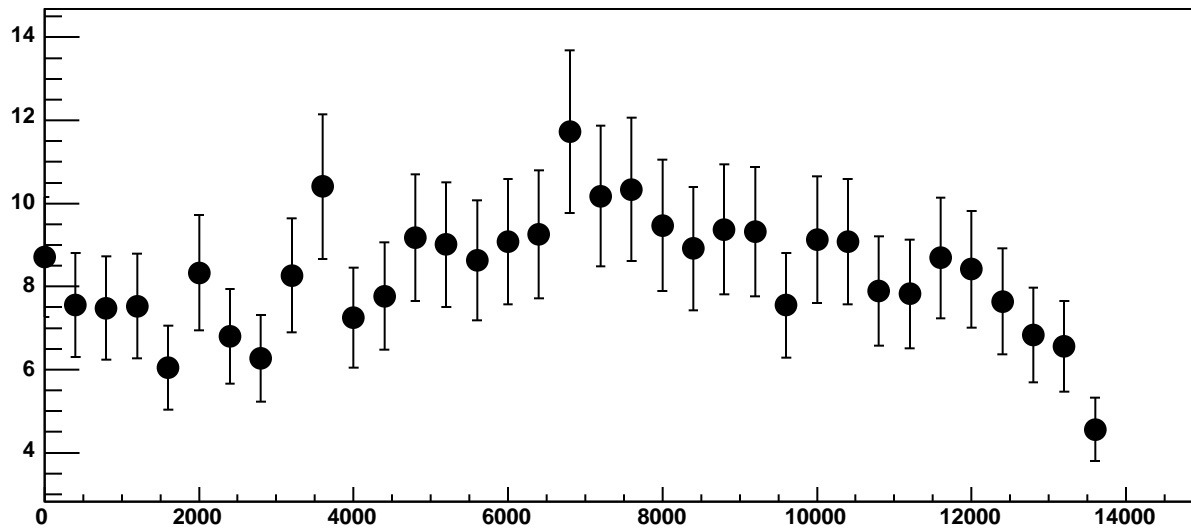


Chip 5, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

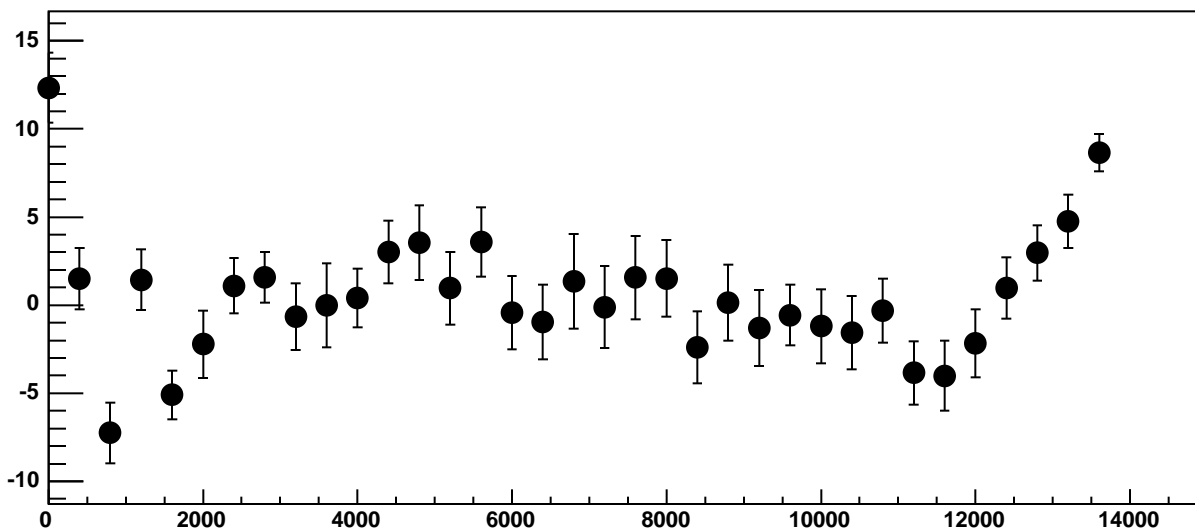


χ^2 / ndf 30.81 / 23
p0 -330.4 ± 0.7912
p1 0.03054 ± 0.0001259

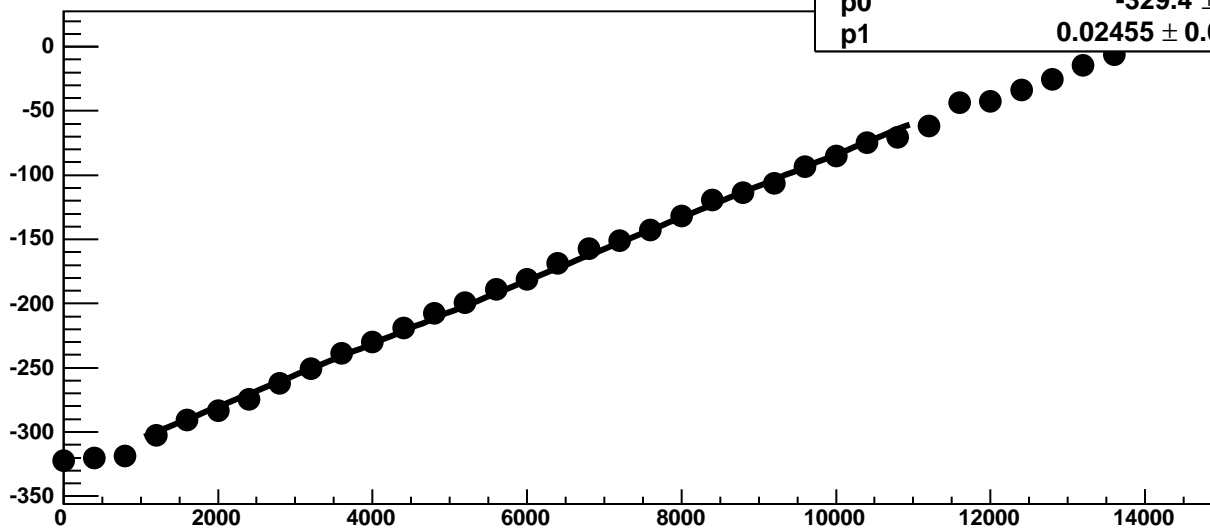
Chip 5, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



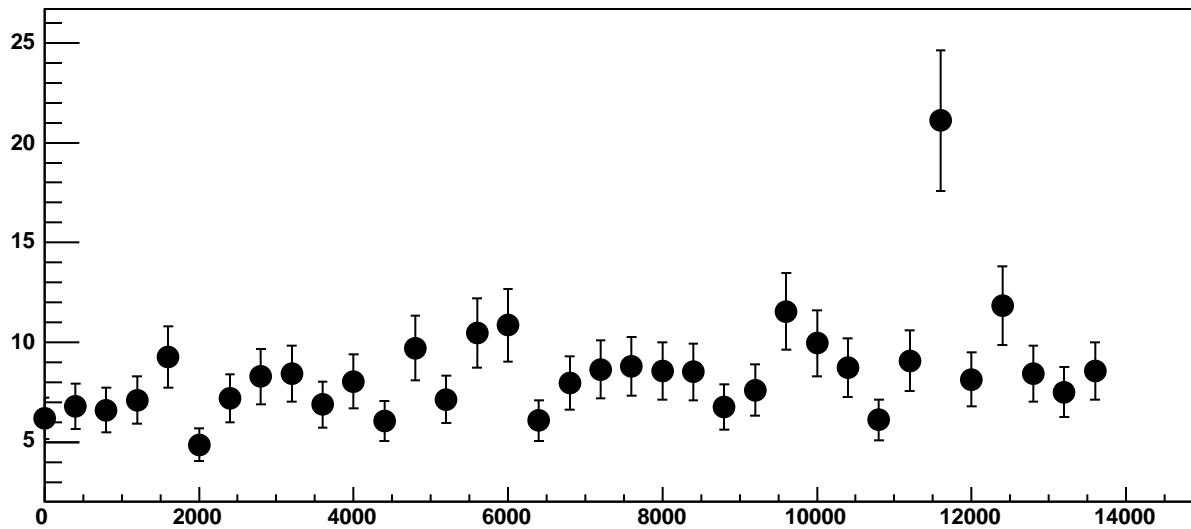
Chip 5, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



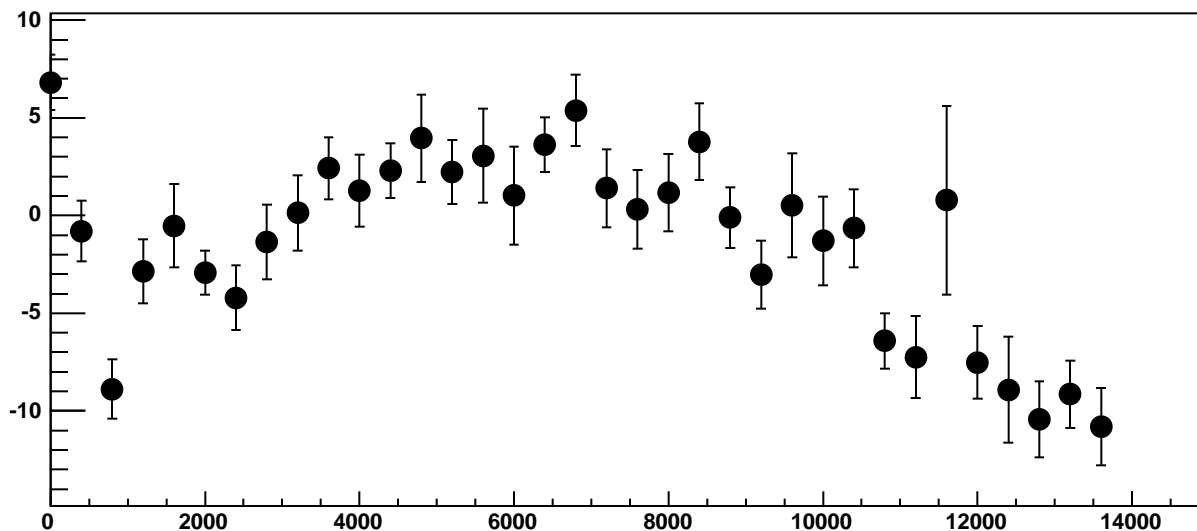
Chip 5, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



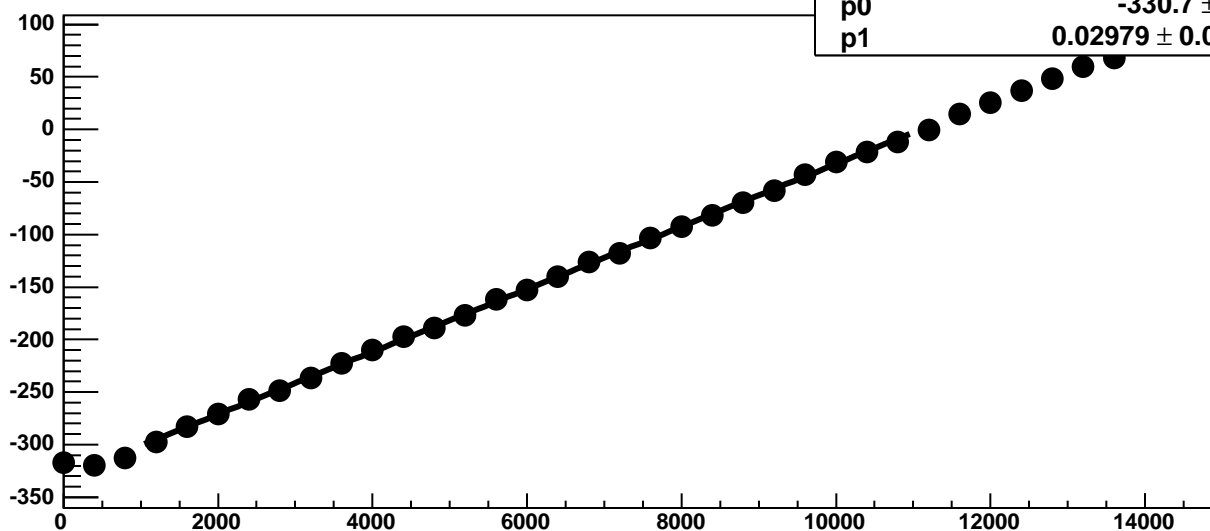
Chip 5, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



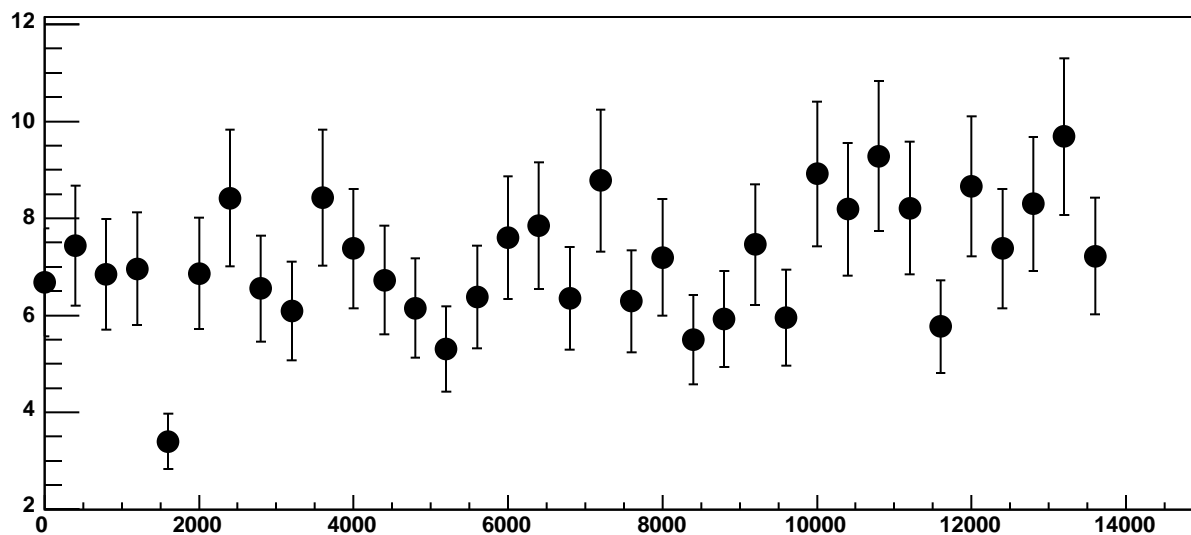
Chip 5, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



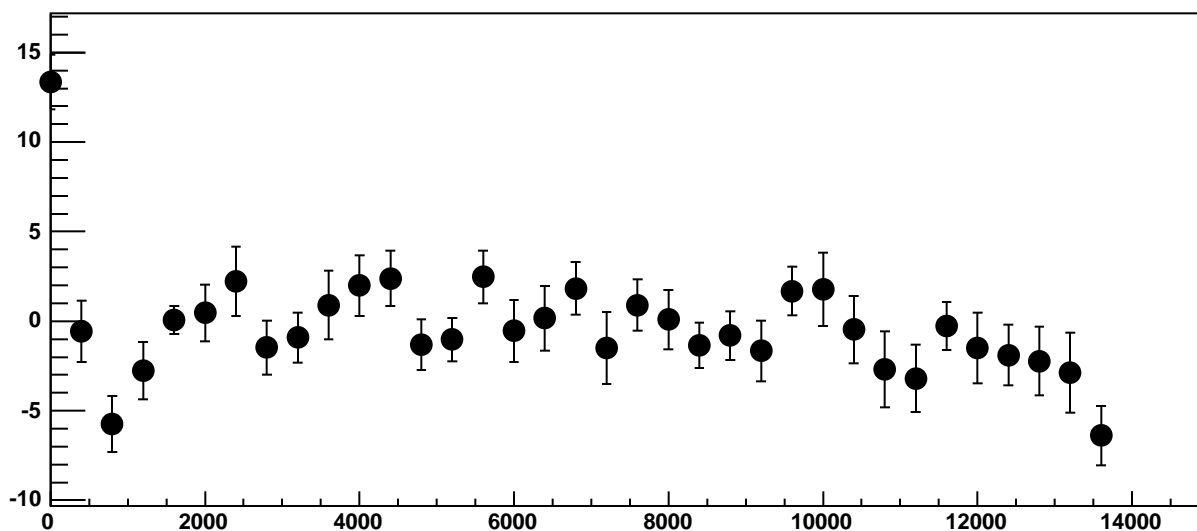
Chip 5, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC



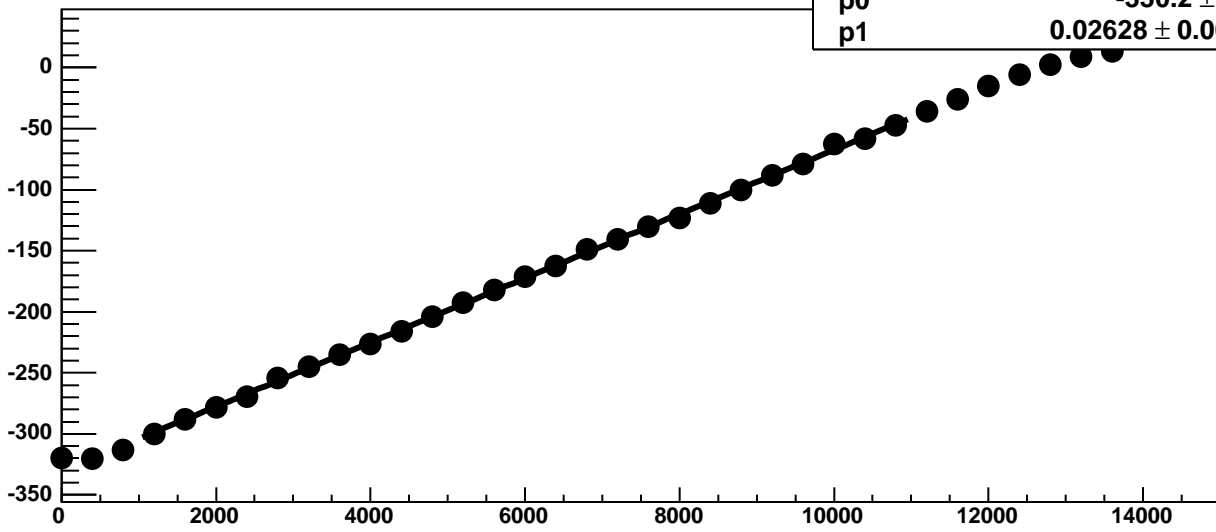
Chip 5, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC

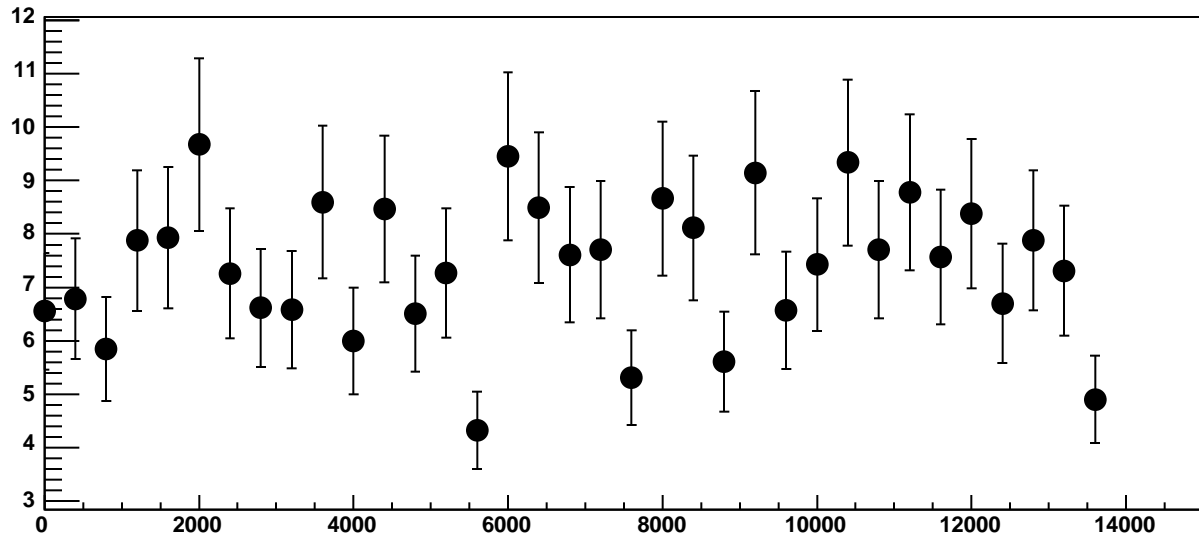


Chip 5, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

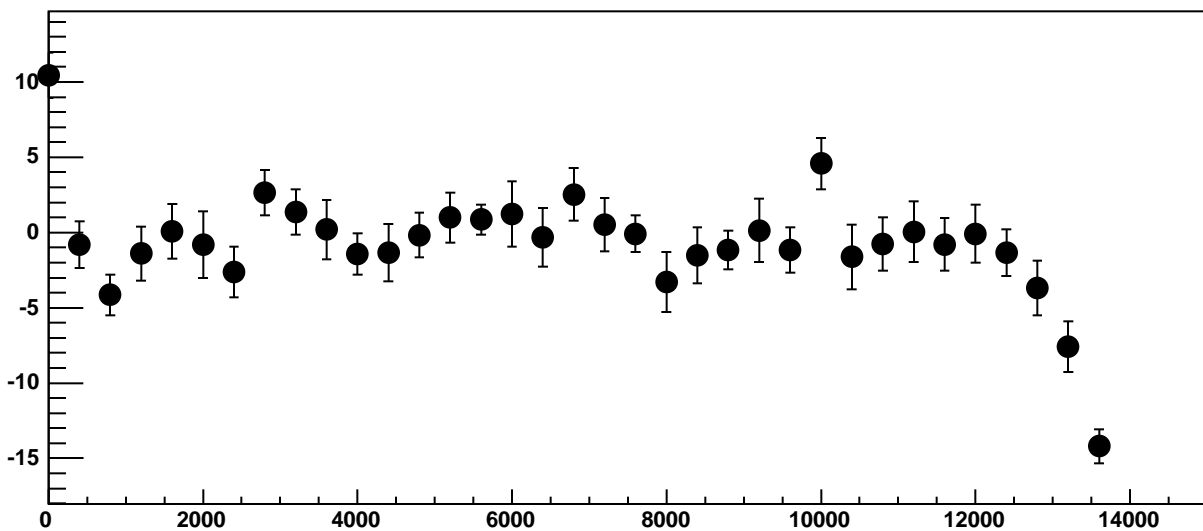


χ^2 / ndf 25.05 / 23
p0 -330.2 ± 0.7925
p1 0.02628 ± 0.0001204

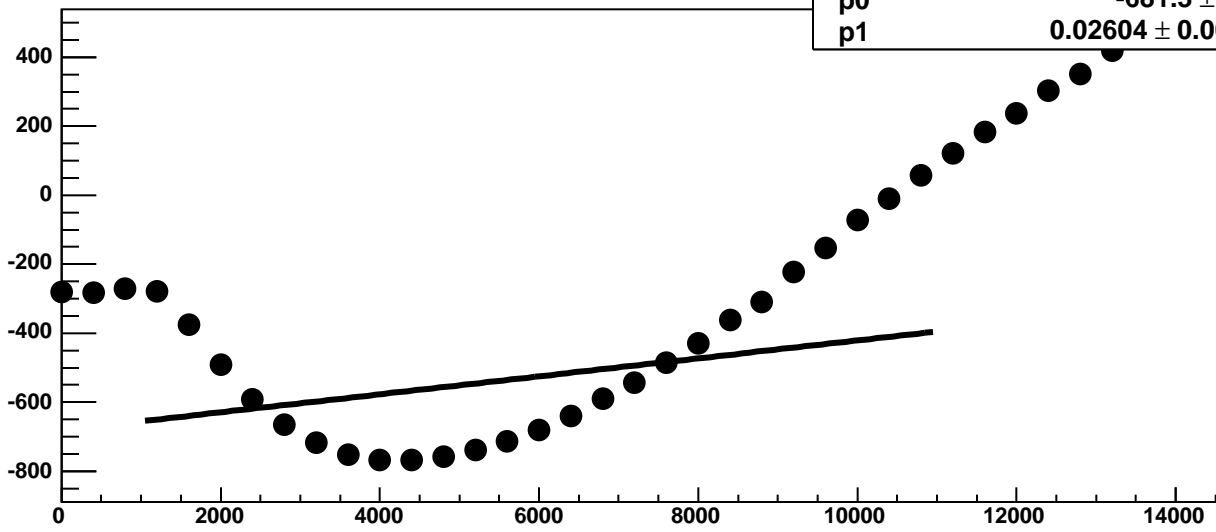
Chip 5, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

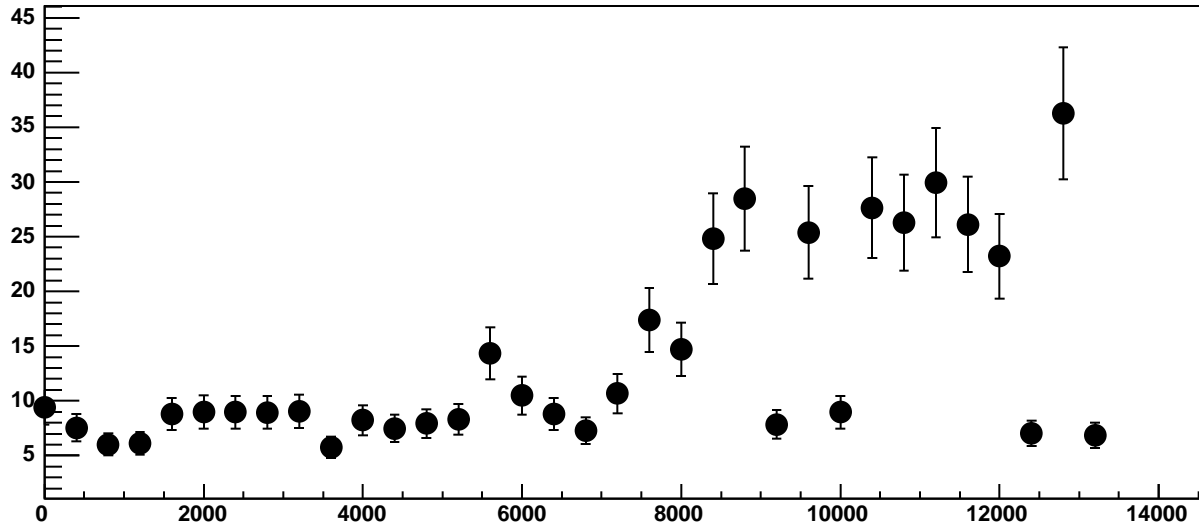


Chip 5, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

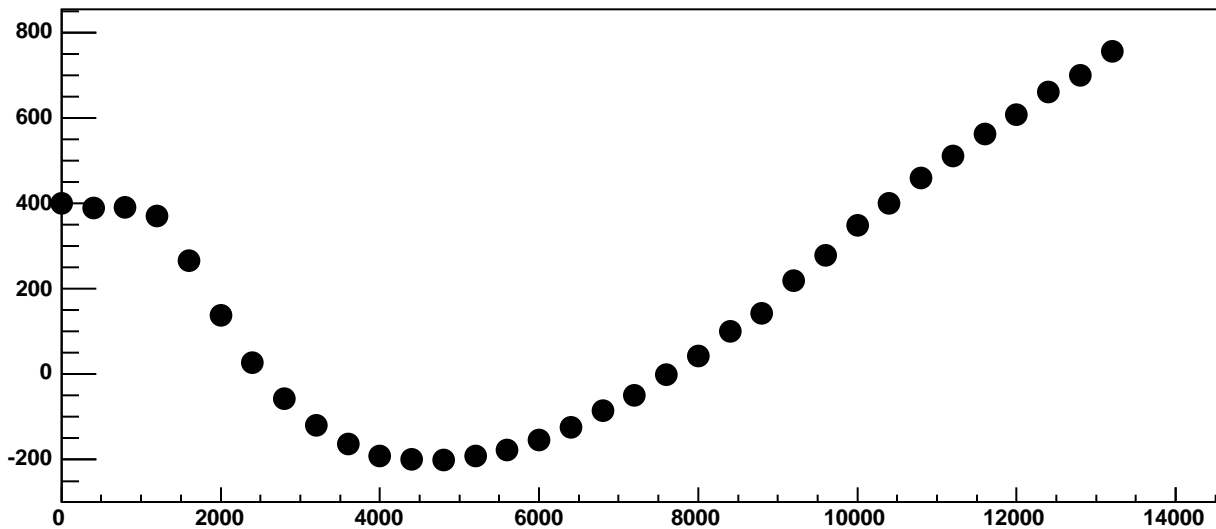


χ^2 / ndf 2.279e+05 / 23
p0 -681.3 ± 0.9134
p1 0.02604 ± 0.0001684

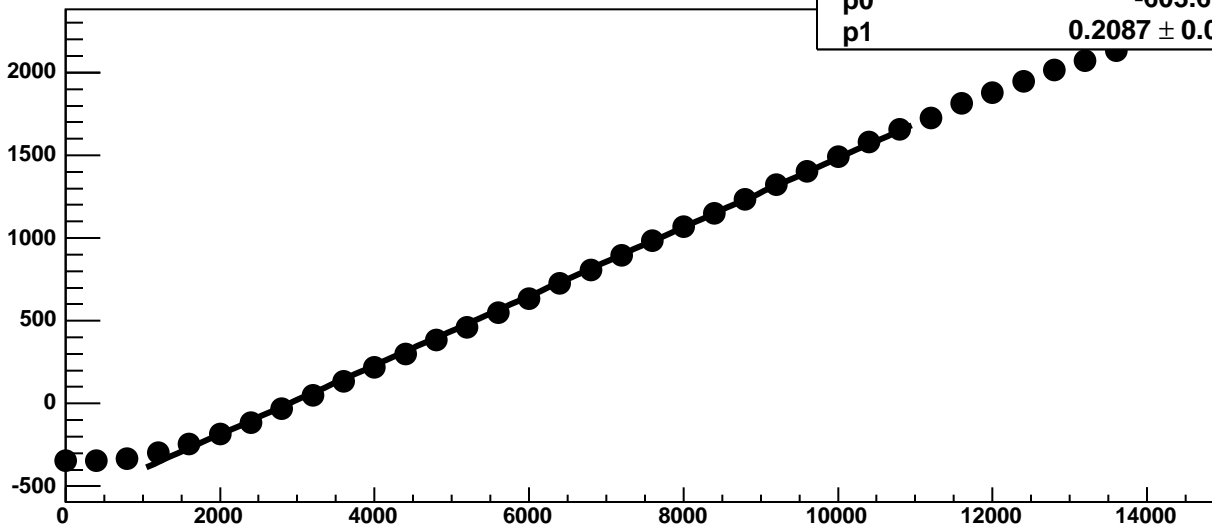
Chip 5, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



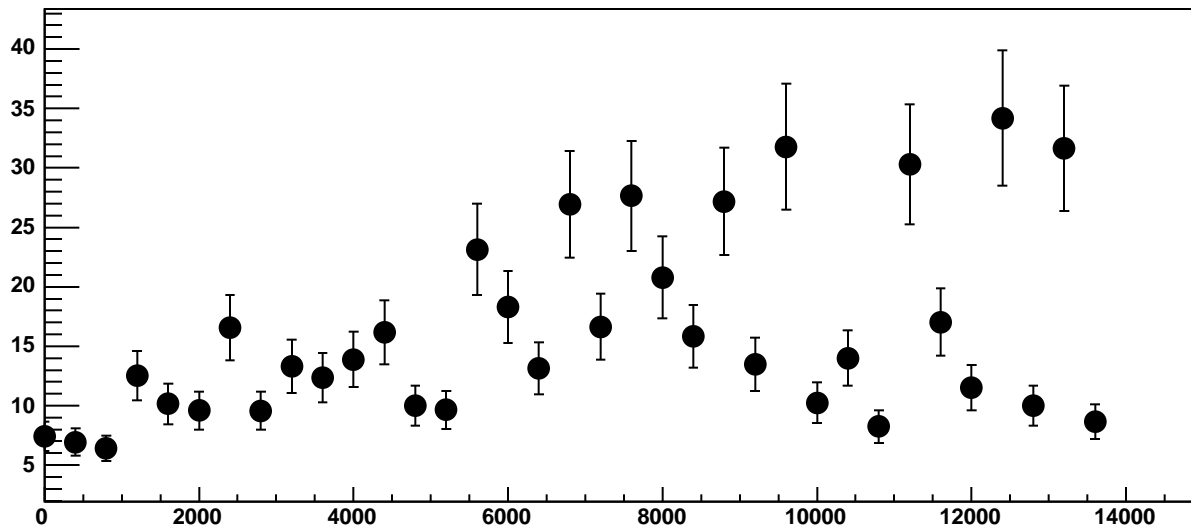
Chip 5, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



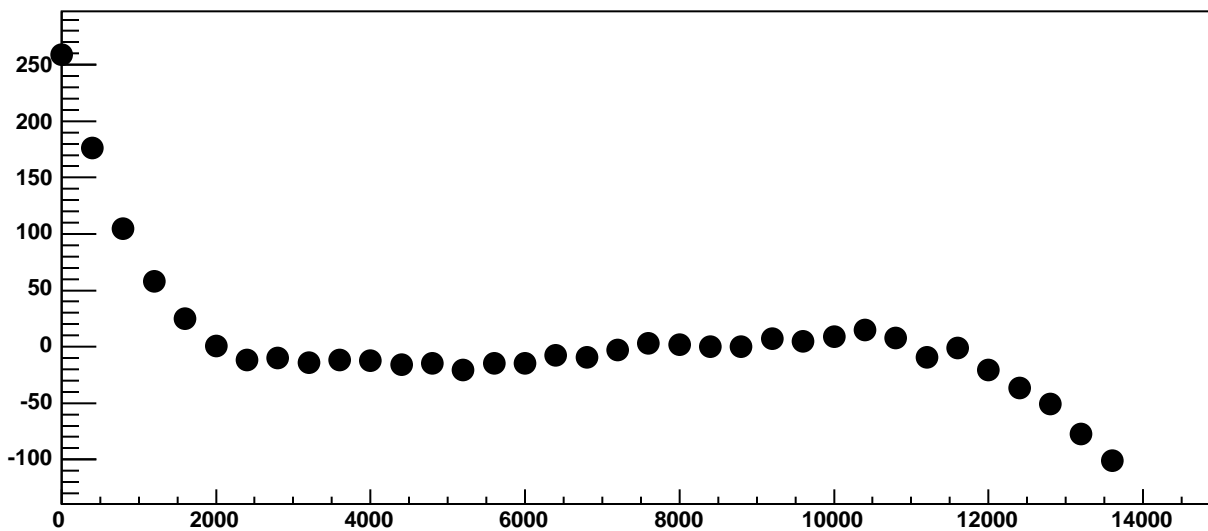
Chip 5, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC



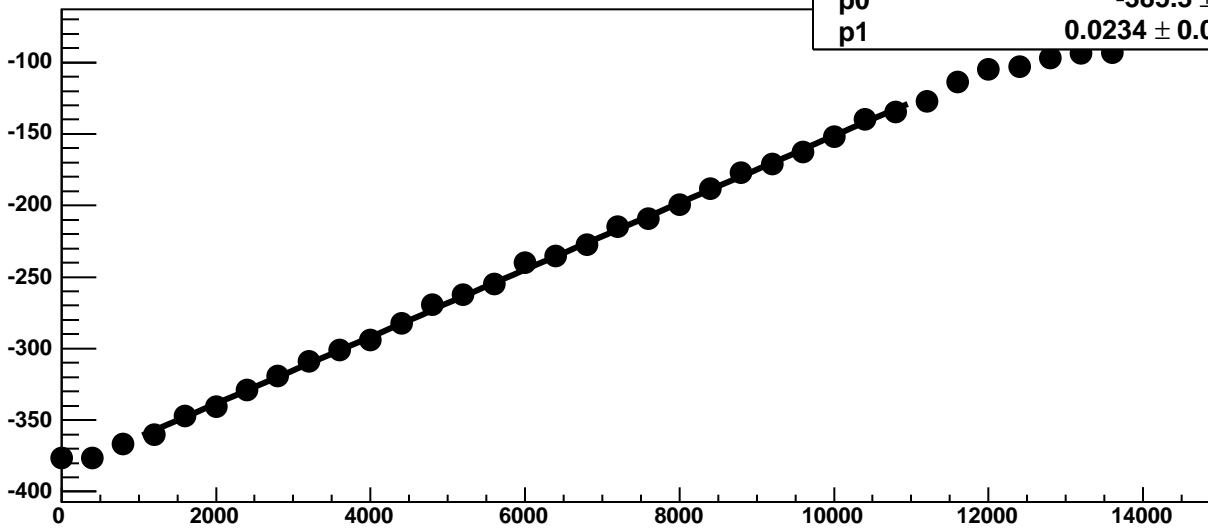
Chip 5, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

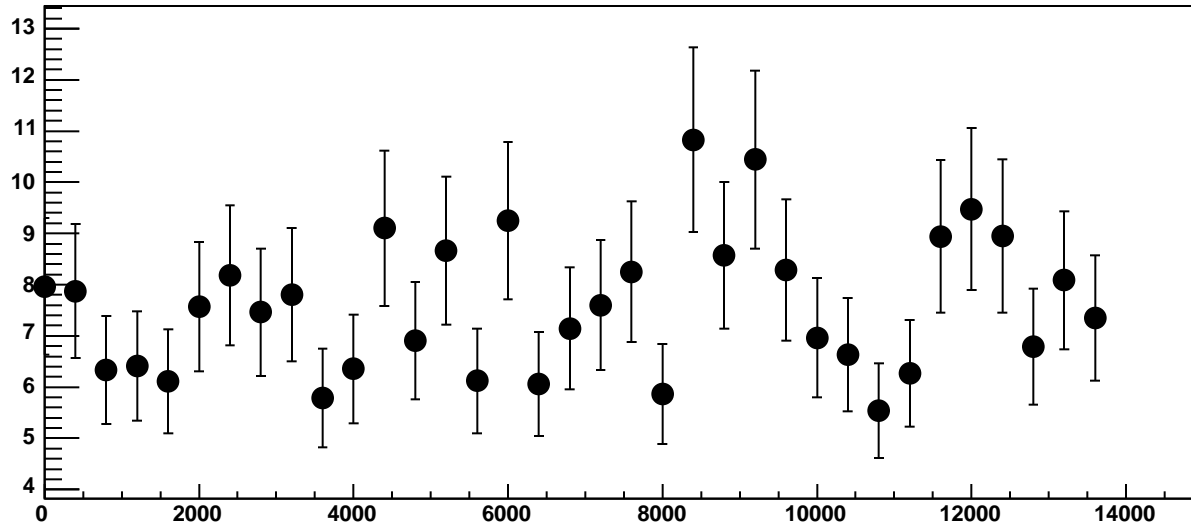


Chip 5, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

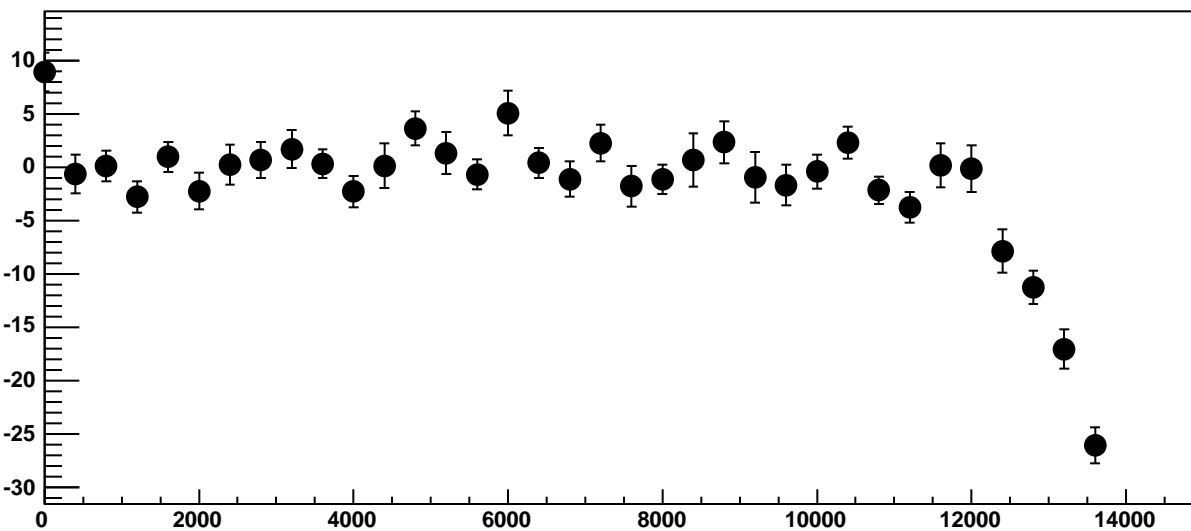


χ^2 / ndf 32.45 / 23
p0 -385.3 ± 0.7285
p1 0.0234 ± 0.0001099

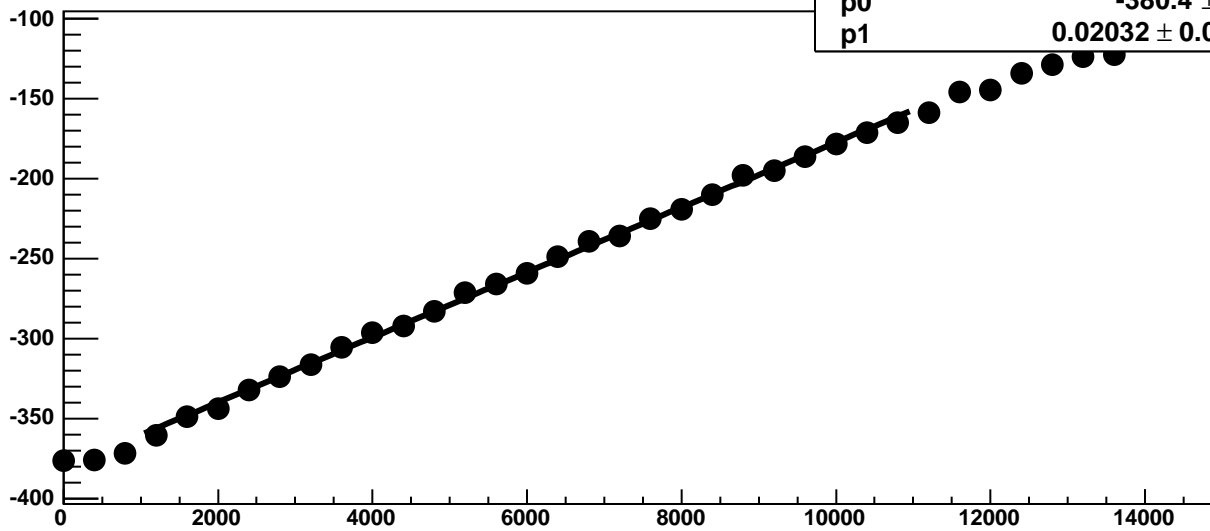
Chip 5, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

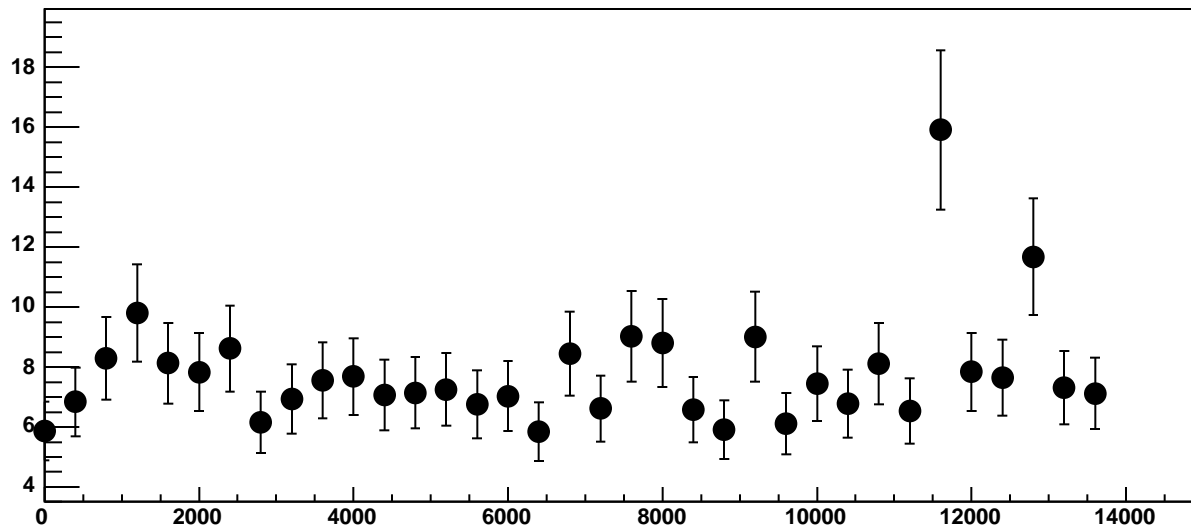


Chip 5, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

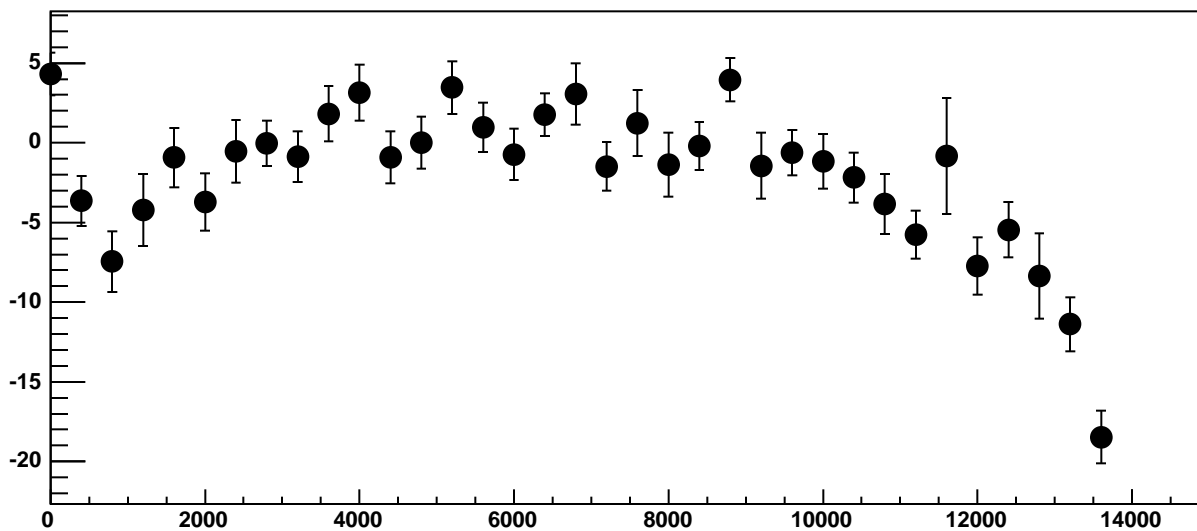


χ^2 / ndf 39.89 / 23
p0 -380.4 ± 0.8099
p1 0.02032 ± 0.0001197

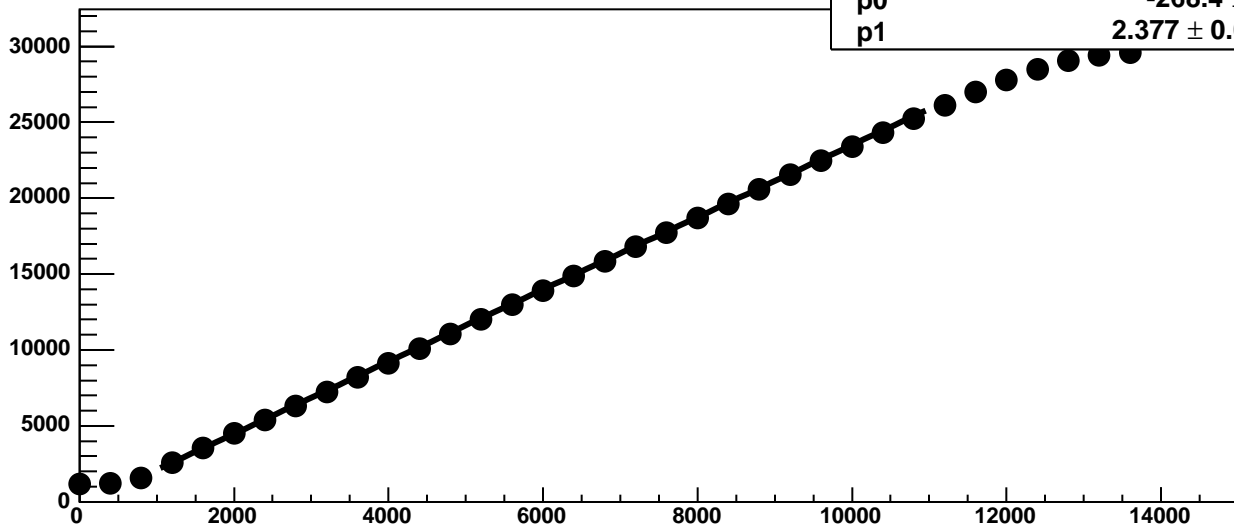
Chip 5, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

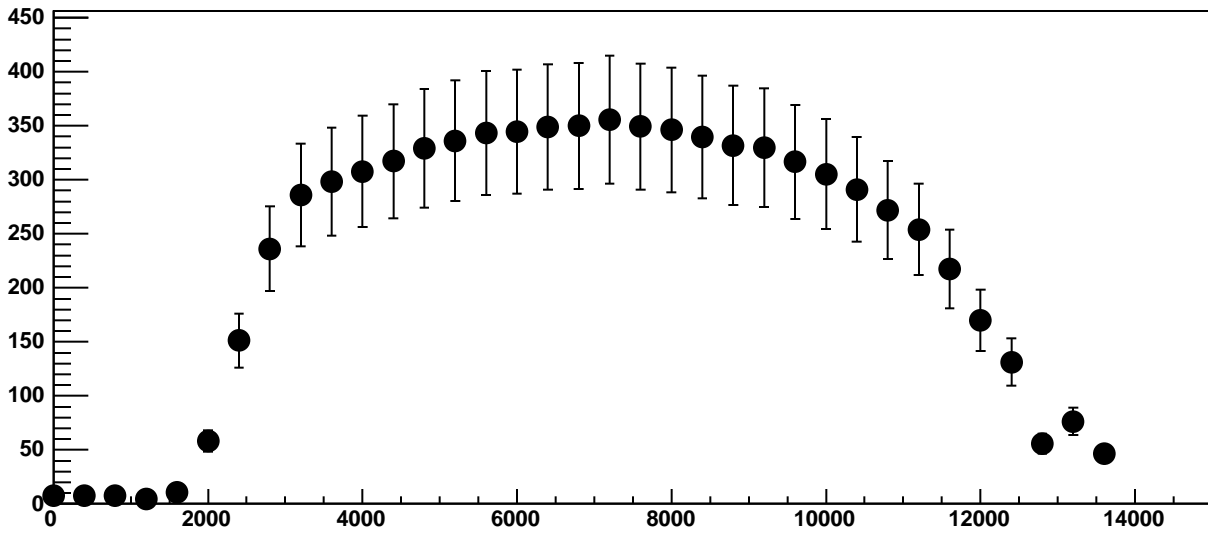


Chip 5, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC

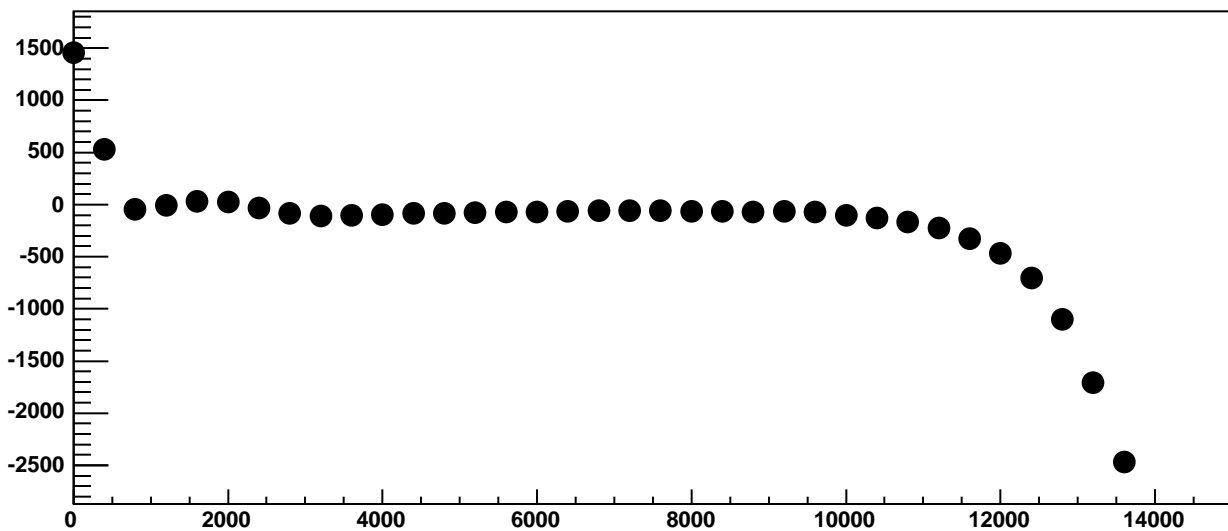


χ^2 / ndf 203.8 / 23
p0 -268.4 ± 3.305
p1 2.377 ± 0.002427

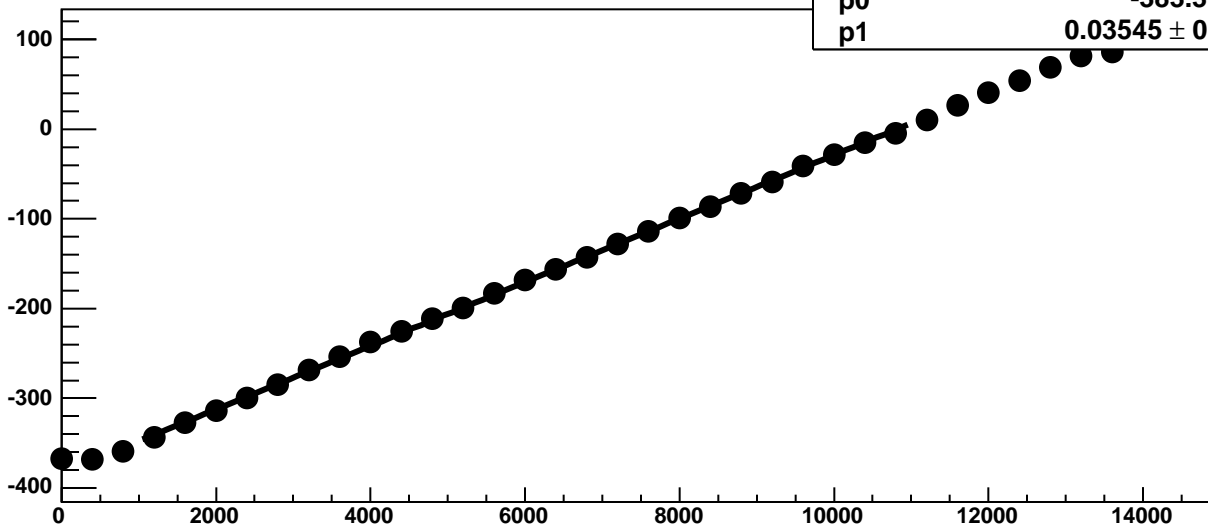
Chip 5, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC

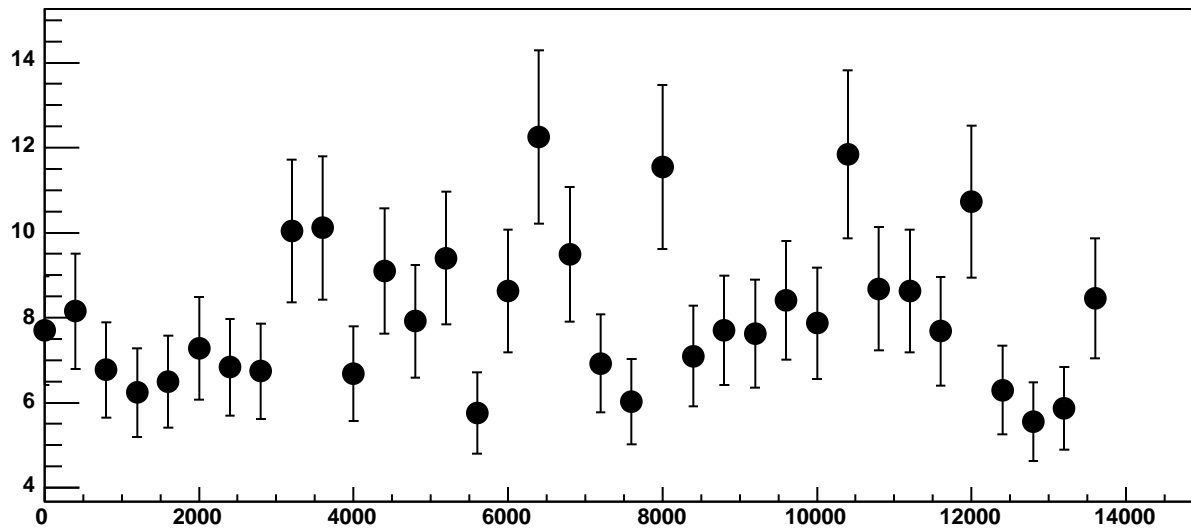


Chip 5, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

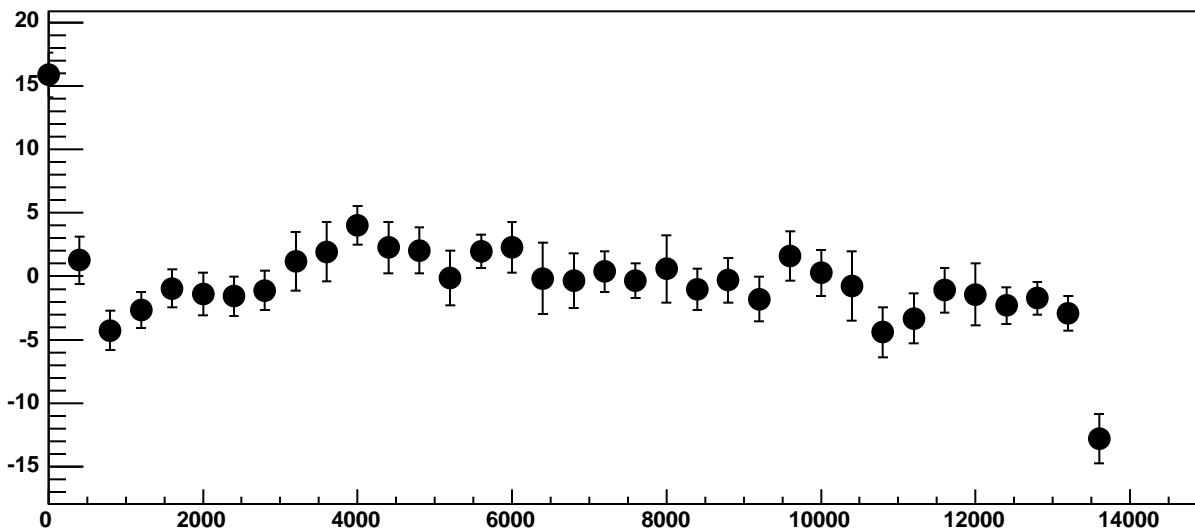


χ^2 / ndf 27.38 / 23
p0 -383.3 ± 0.774
p1 0.03545 ± 0.000122

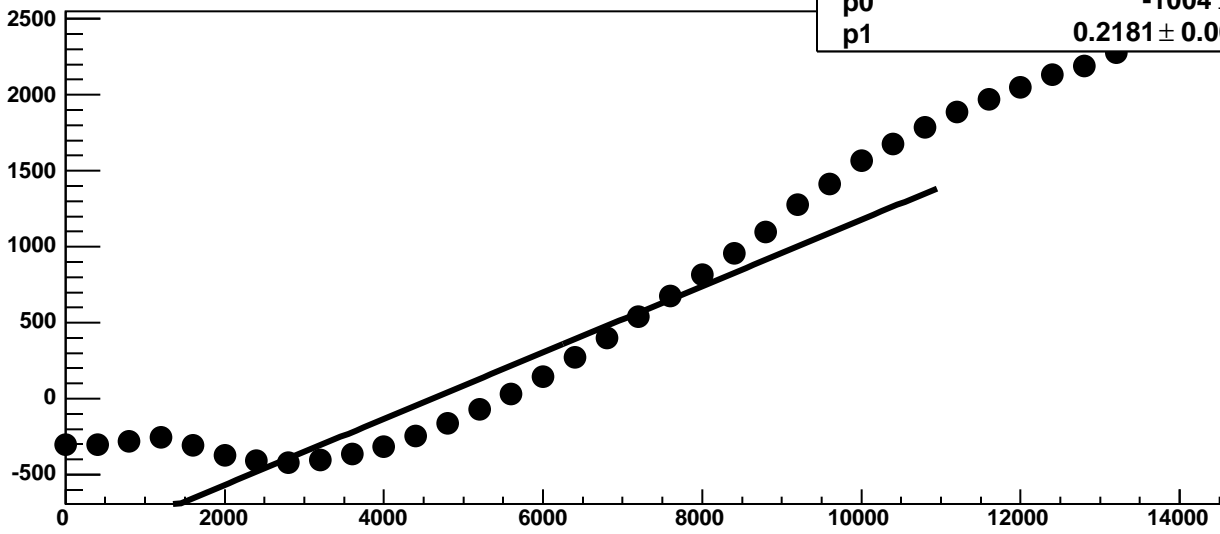
Chip 5, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



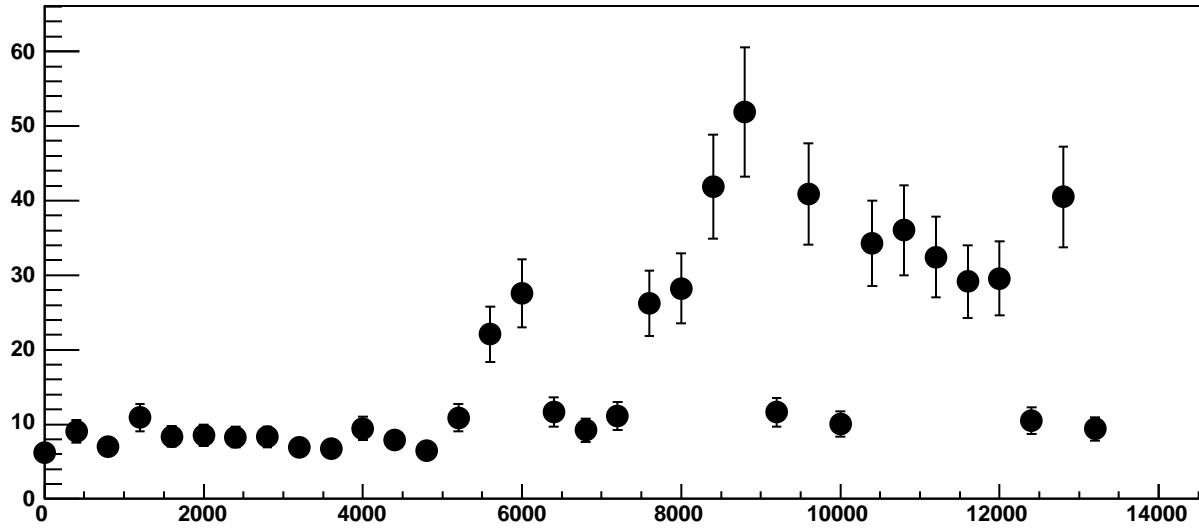
Chip 5, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



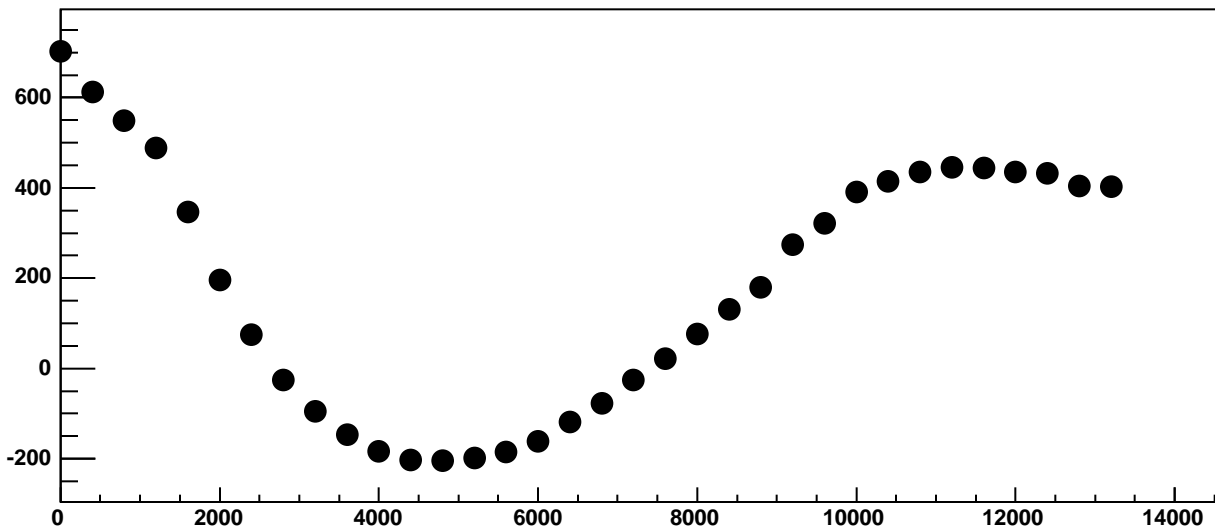
Chip 5, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



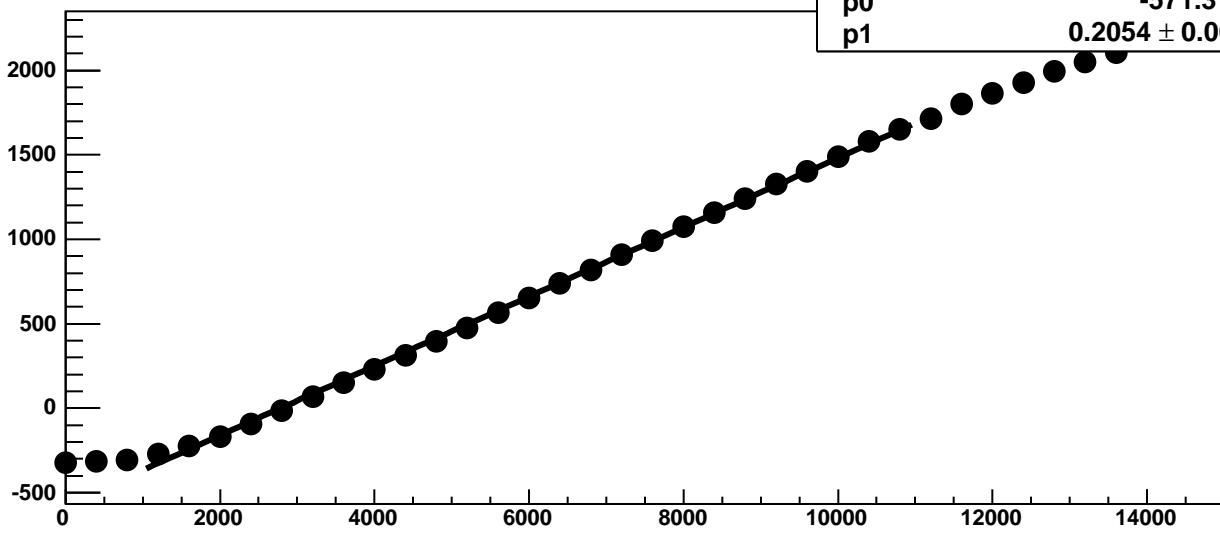
Chip 5, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



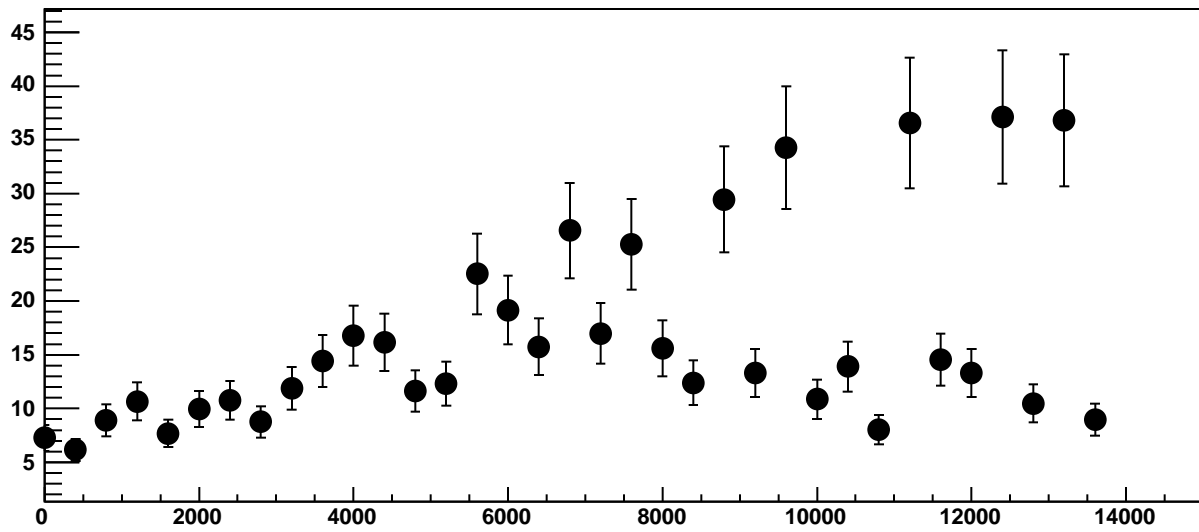
Chip 5, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



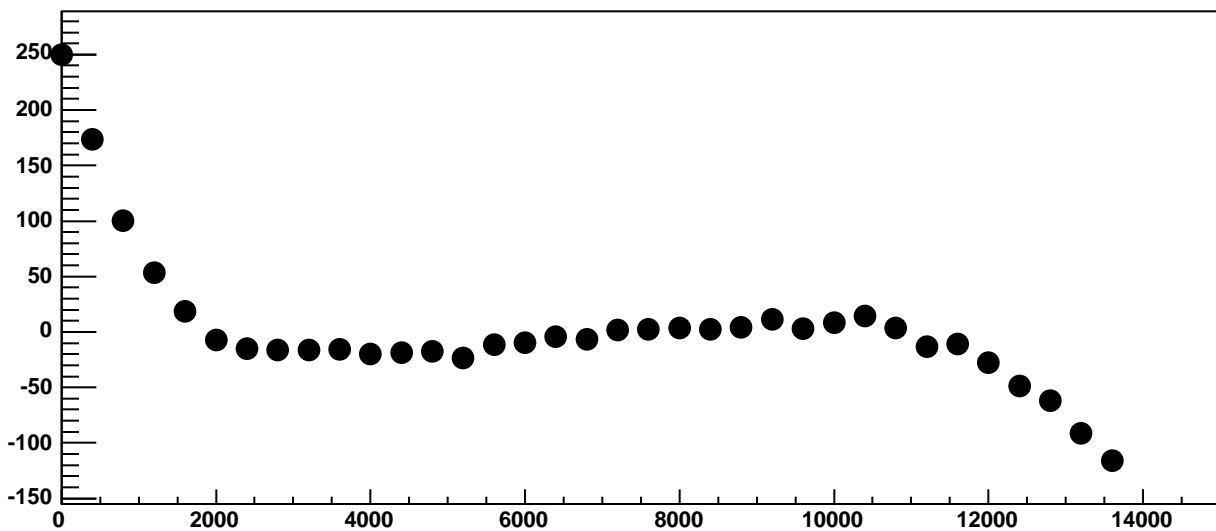
Chip 5, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



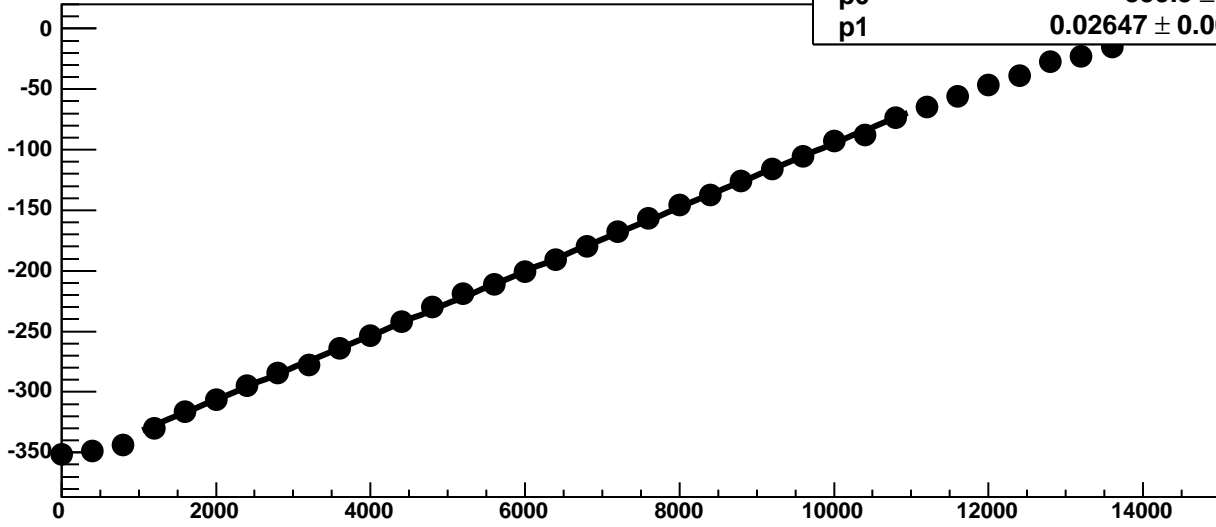
Chip 5, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



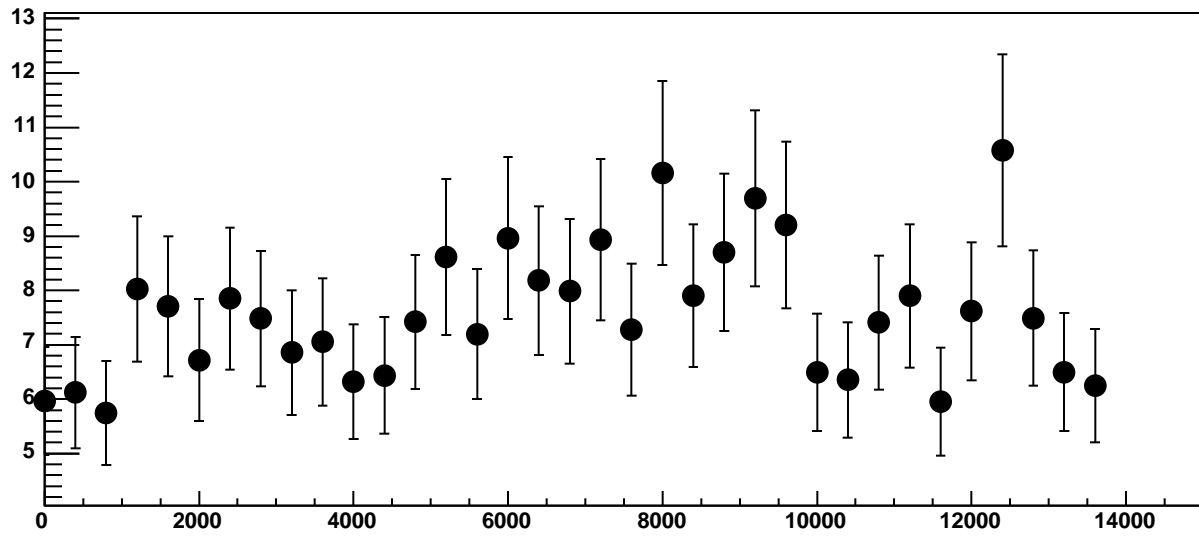
Chip 5, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



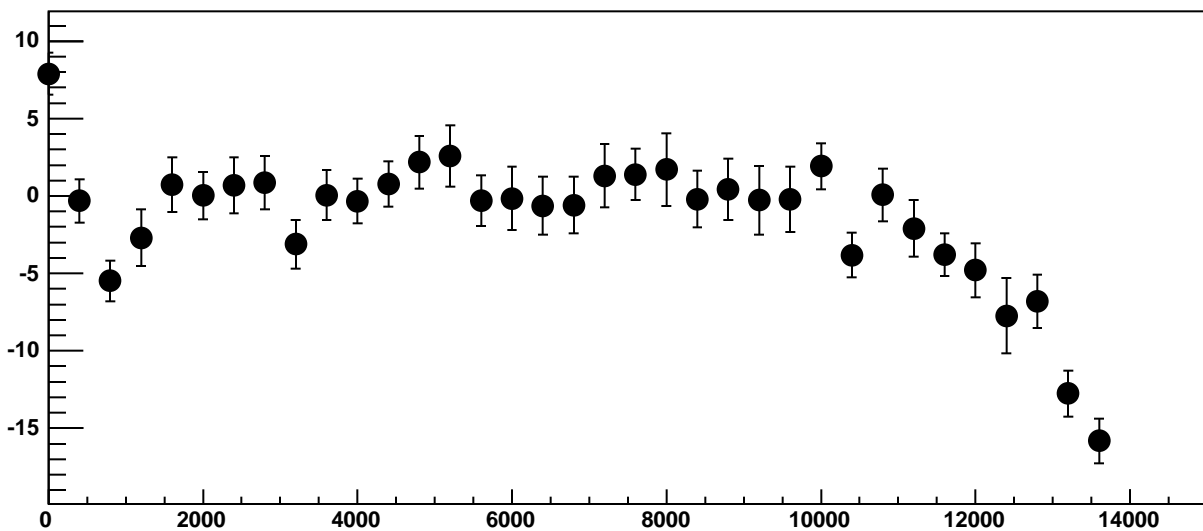
Chip 5, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC



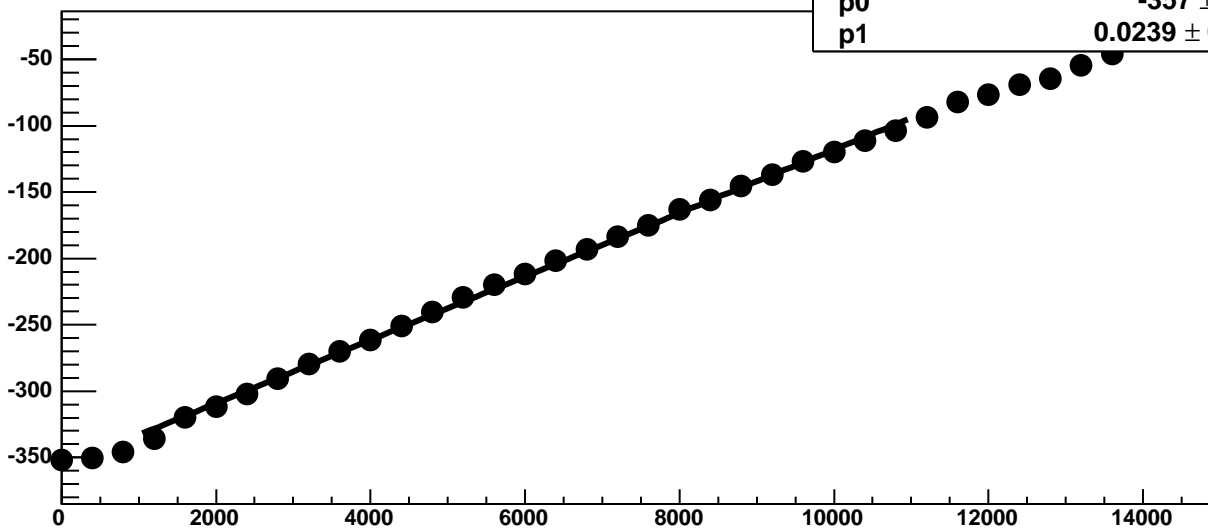
Chip 5, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



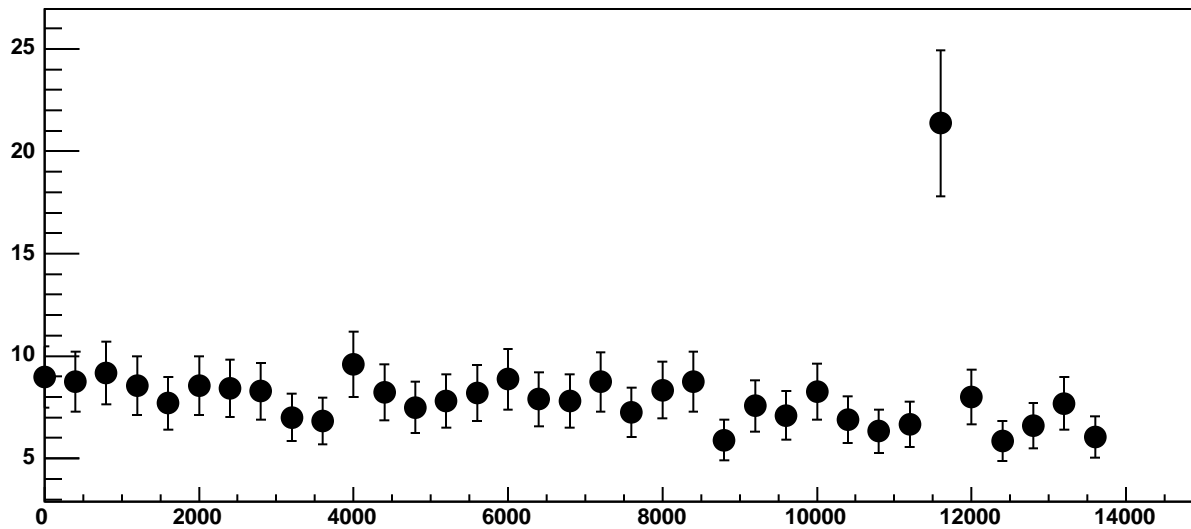
Chip 5, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC



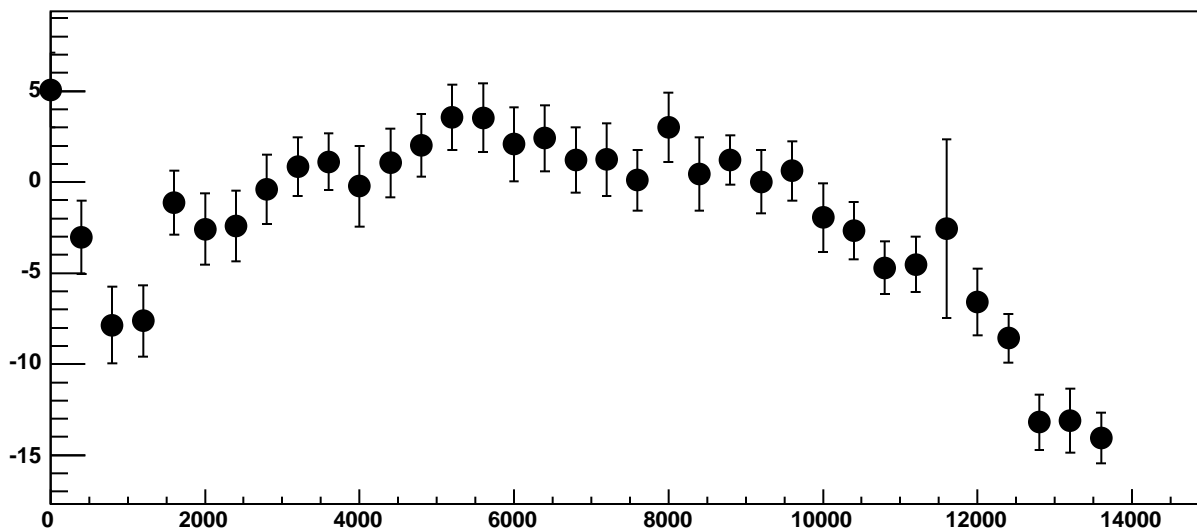
Chip 5, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



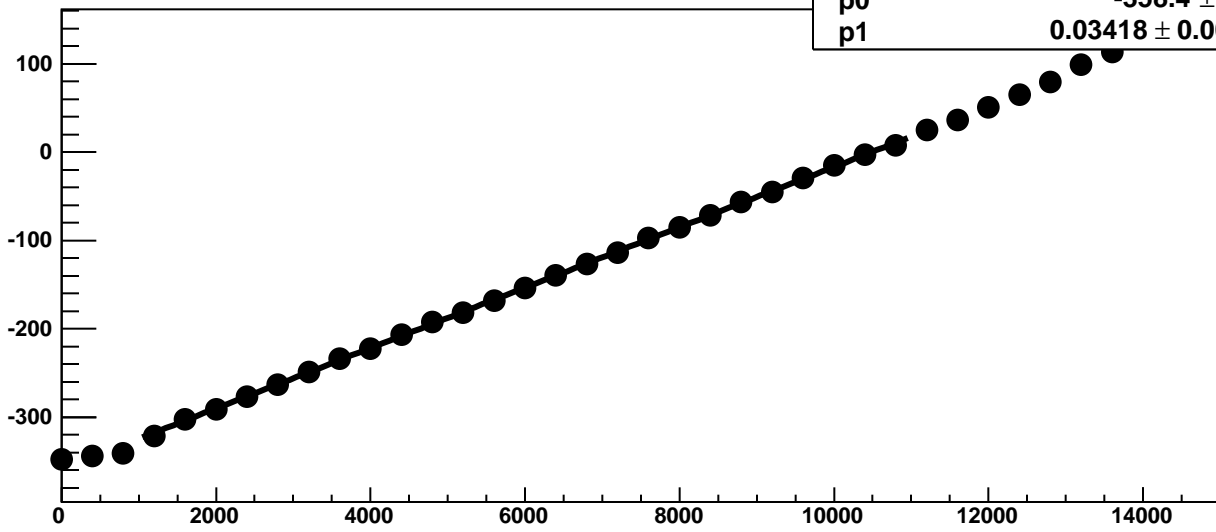
Chip 5, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

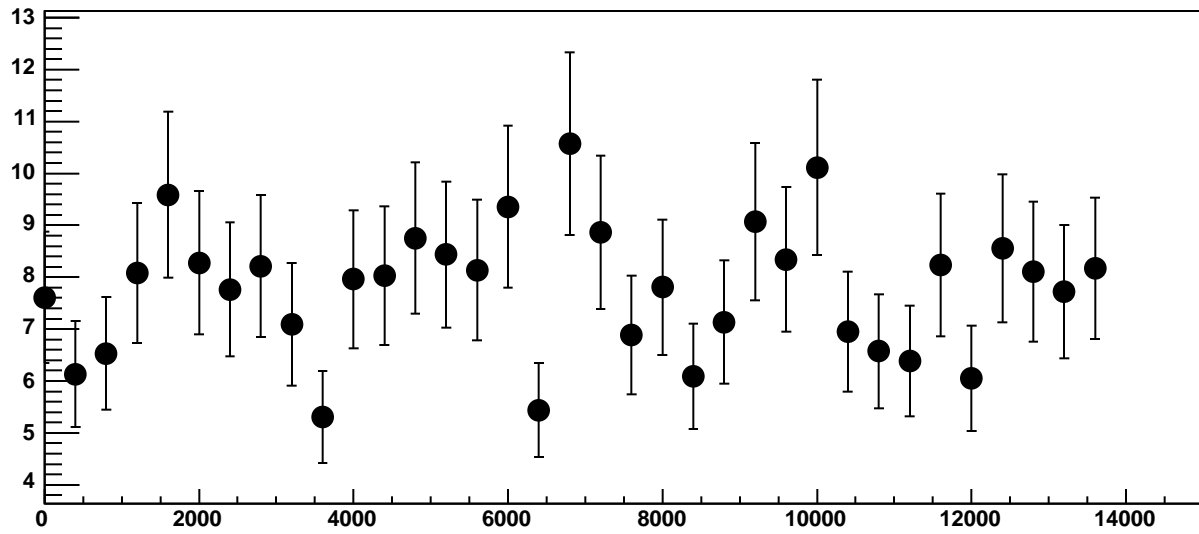


Chip 5, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

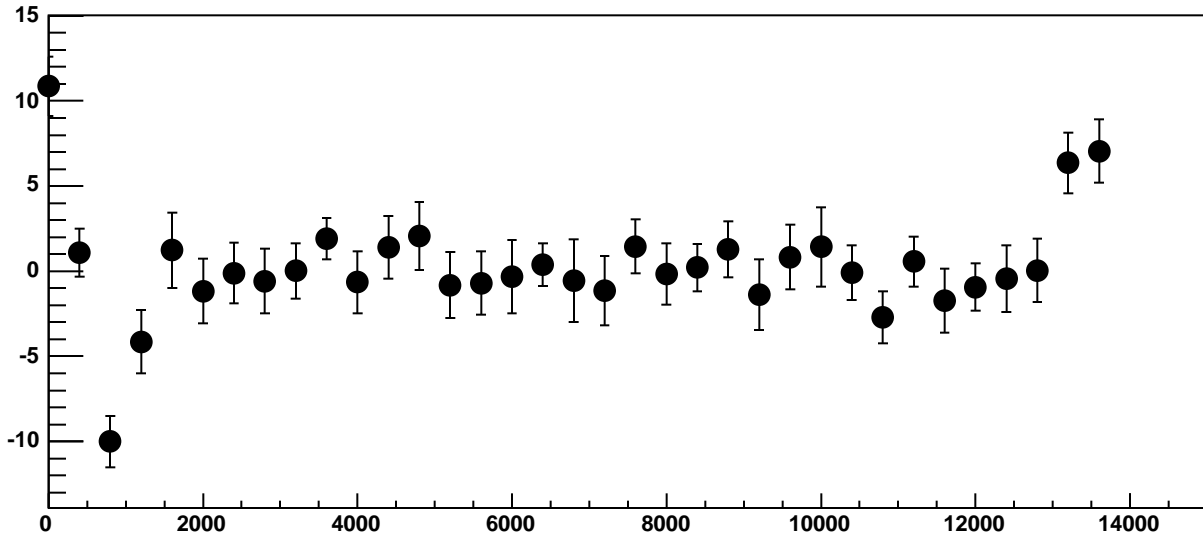


χ^2 / ndf 16.61 / 23
p0 -358.4 ± 0.8224
p1 0.03418 ± 0.0001224

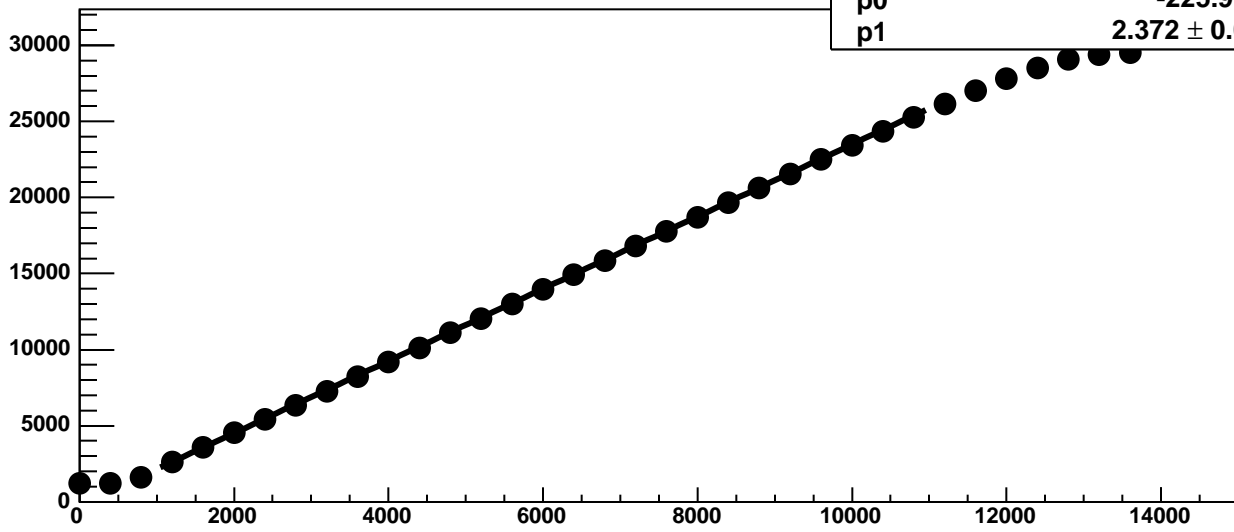
Chip 5, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

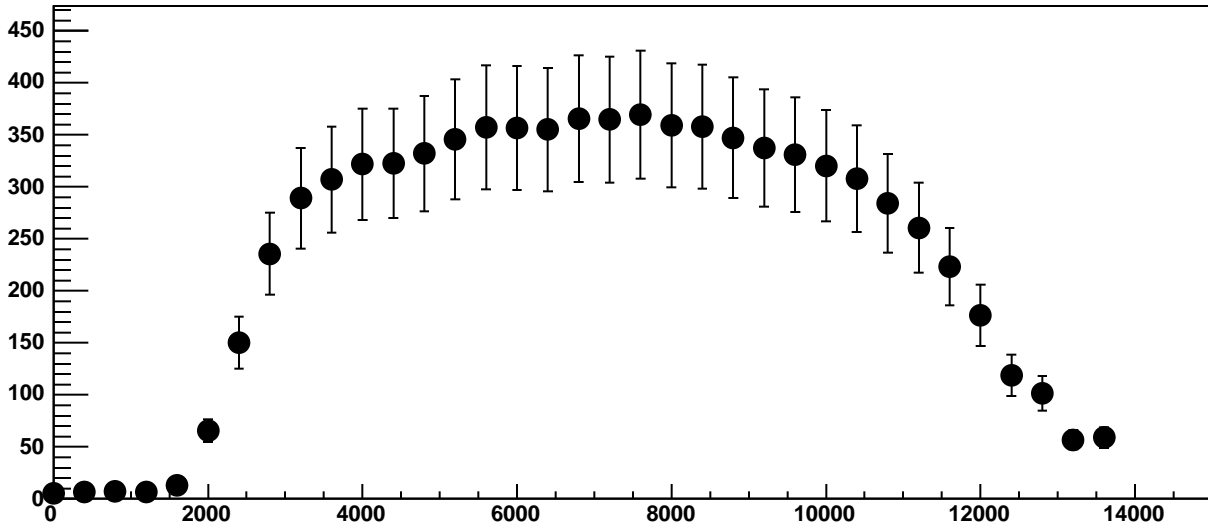


Chip 5, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC

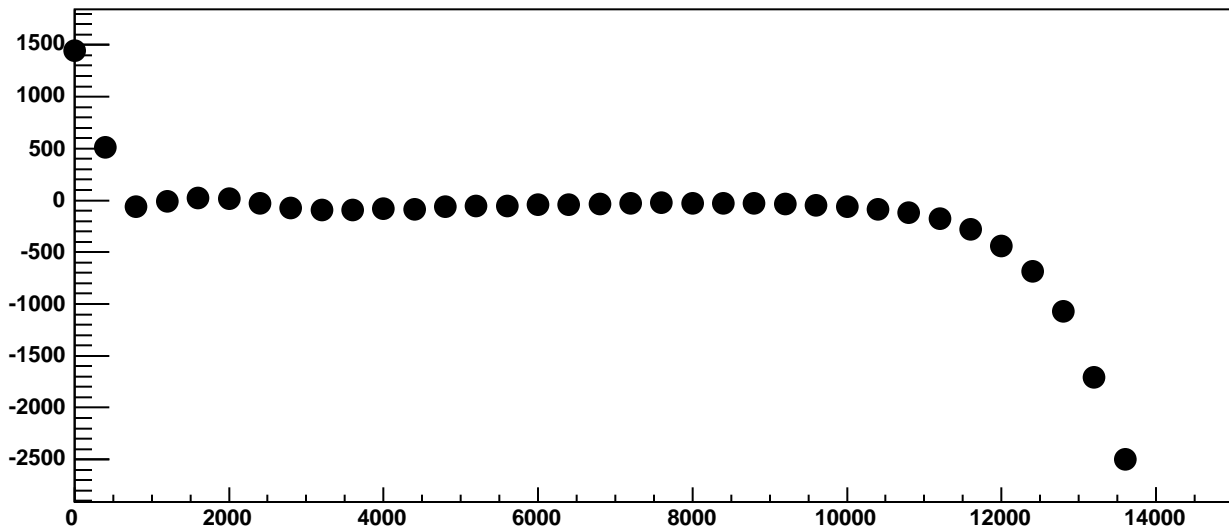


χ^2 / ndf 110.7 / 23
p0 -225.9 ± 3.69
p1 2.372 ± 0.002586

Chip 5, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC

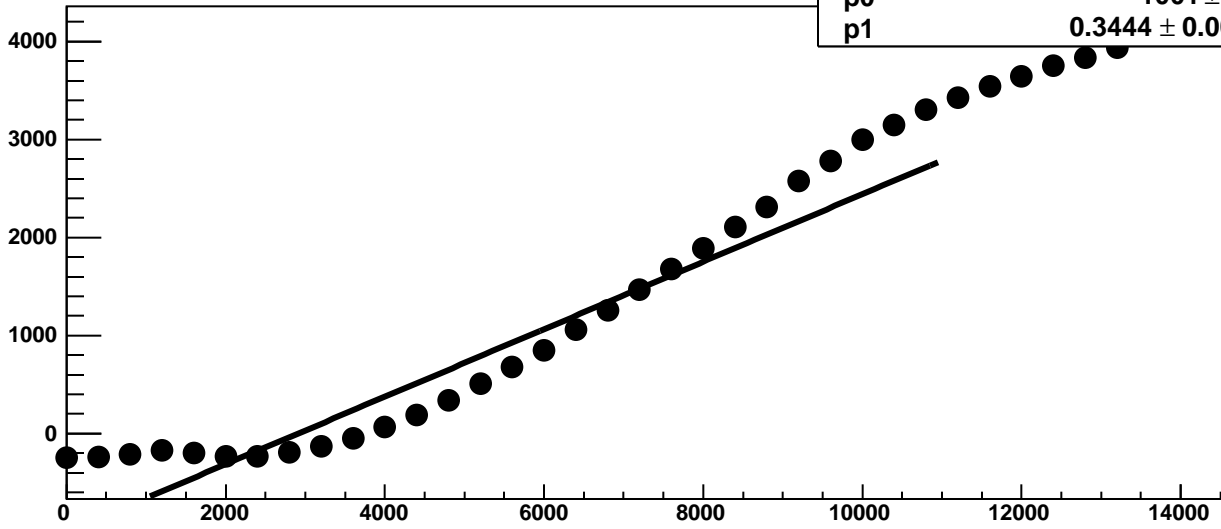


Chip 5, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC

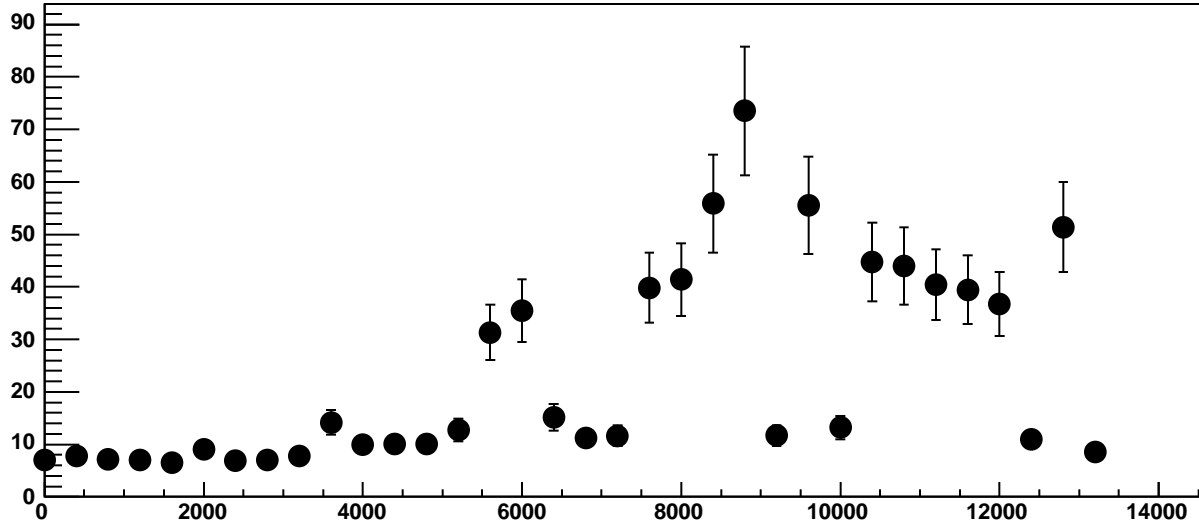


Chip 5, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

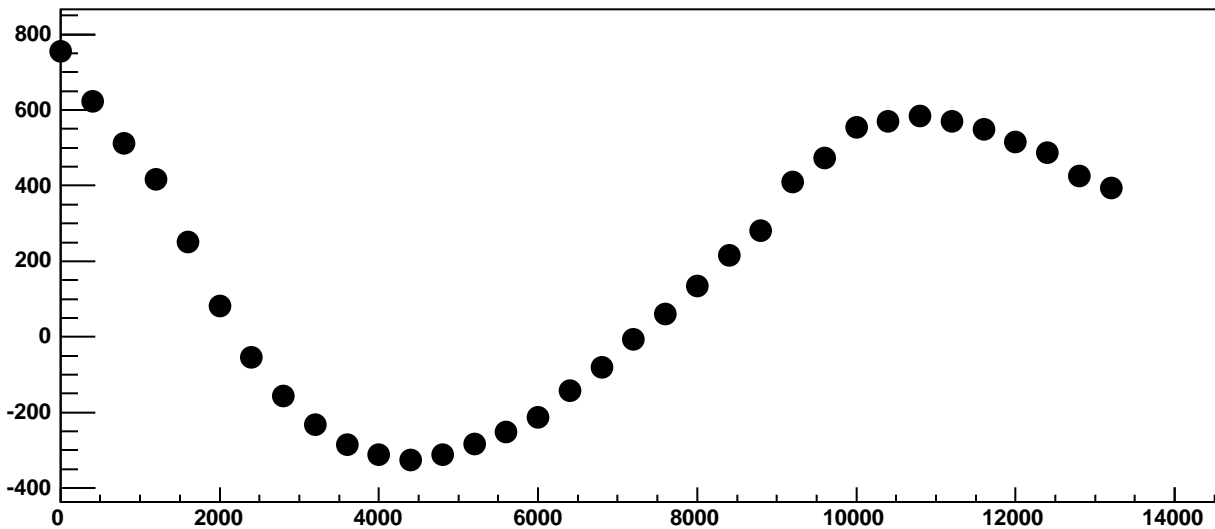
χ^2 / ndf 2.663e+05 / 23
p0 -1001 ± 0.9796
p1 0.3444 ± 0.0002192



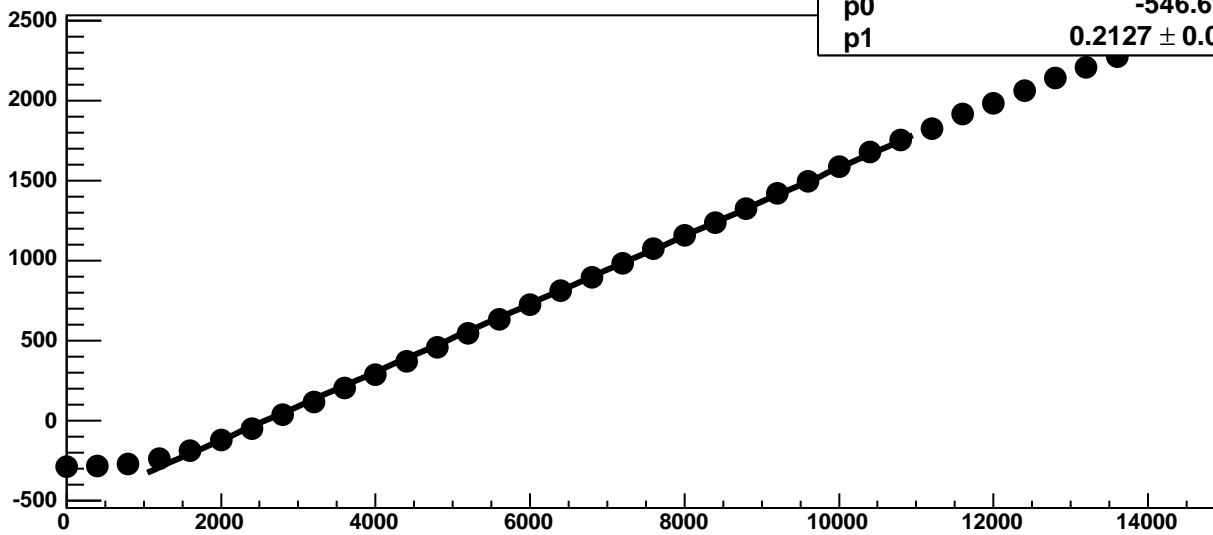
Chip 5, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



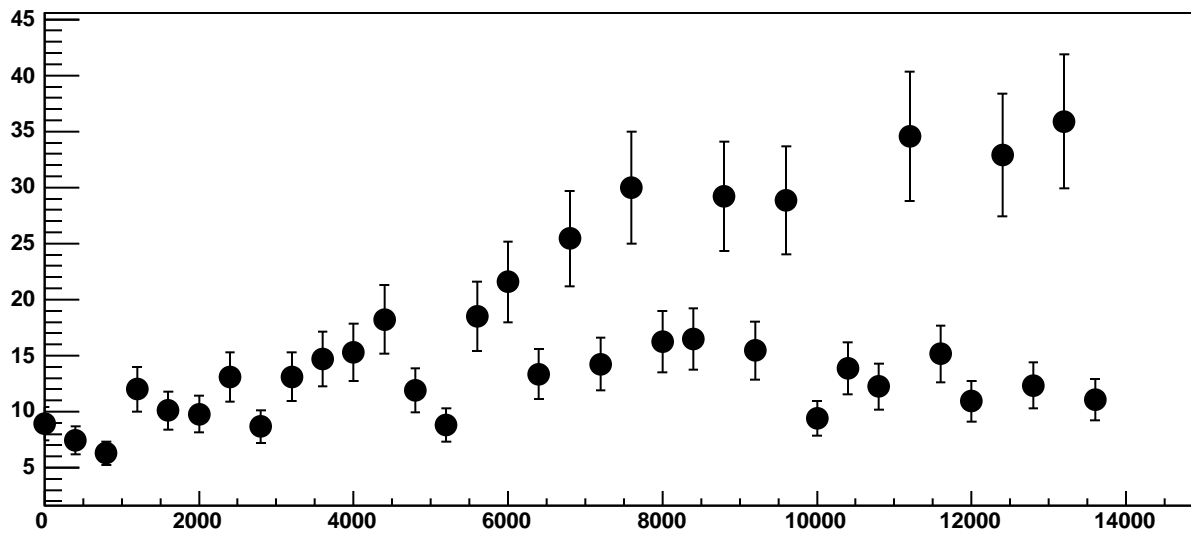
Chip 5, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



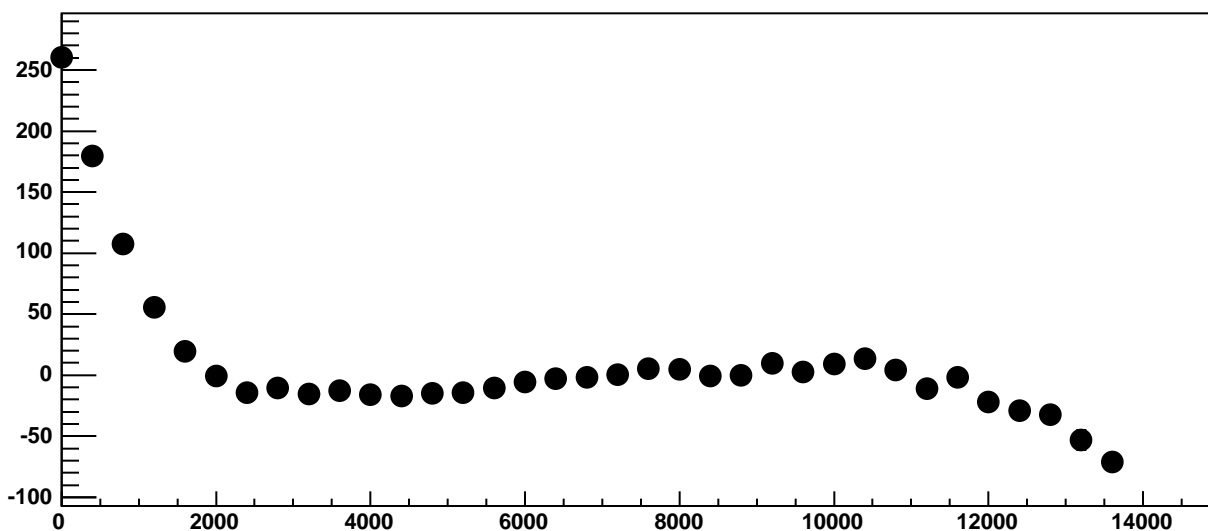
Chip 5, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



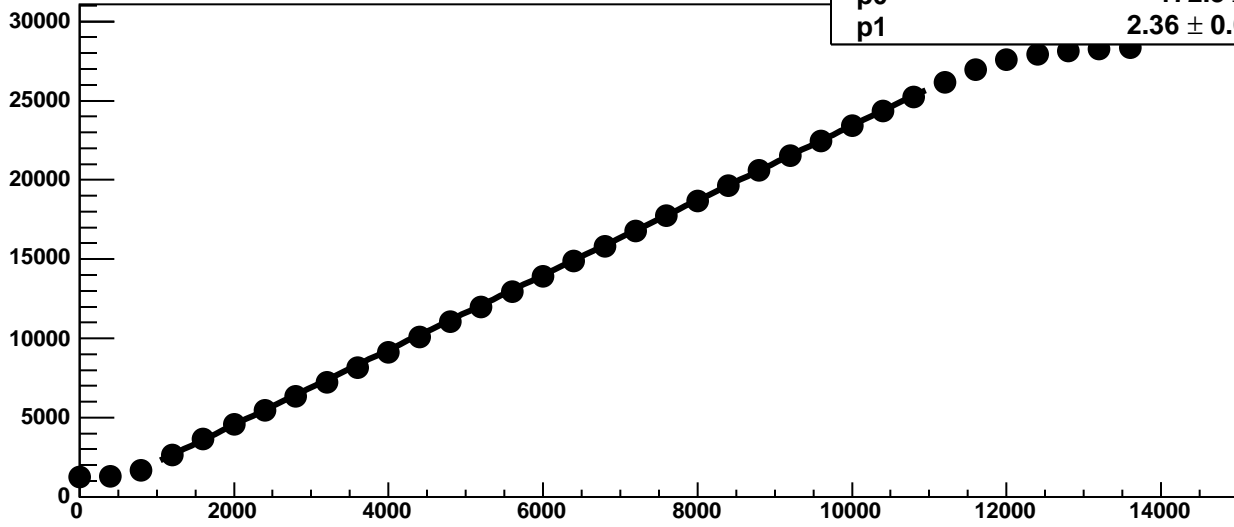
Chip 5, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



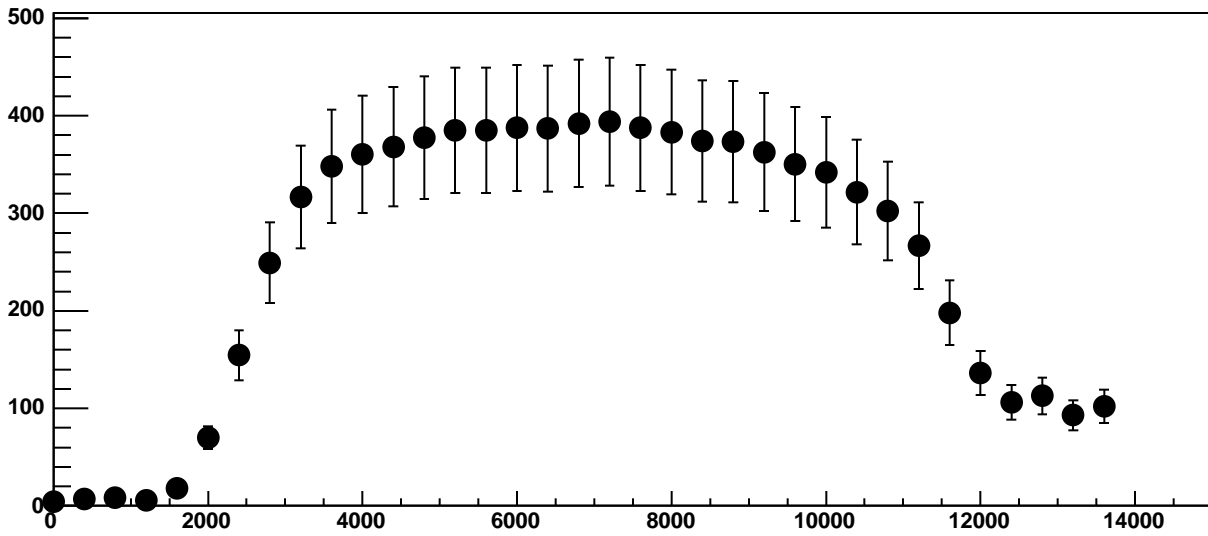
Chip 5, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



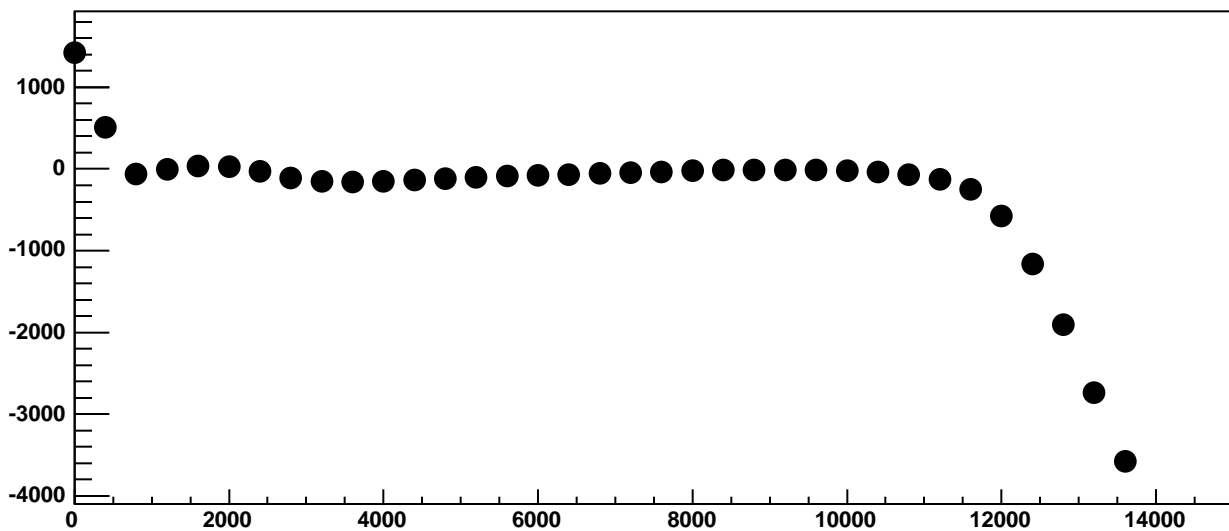
Chip 5, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC



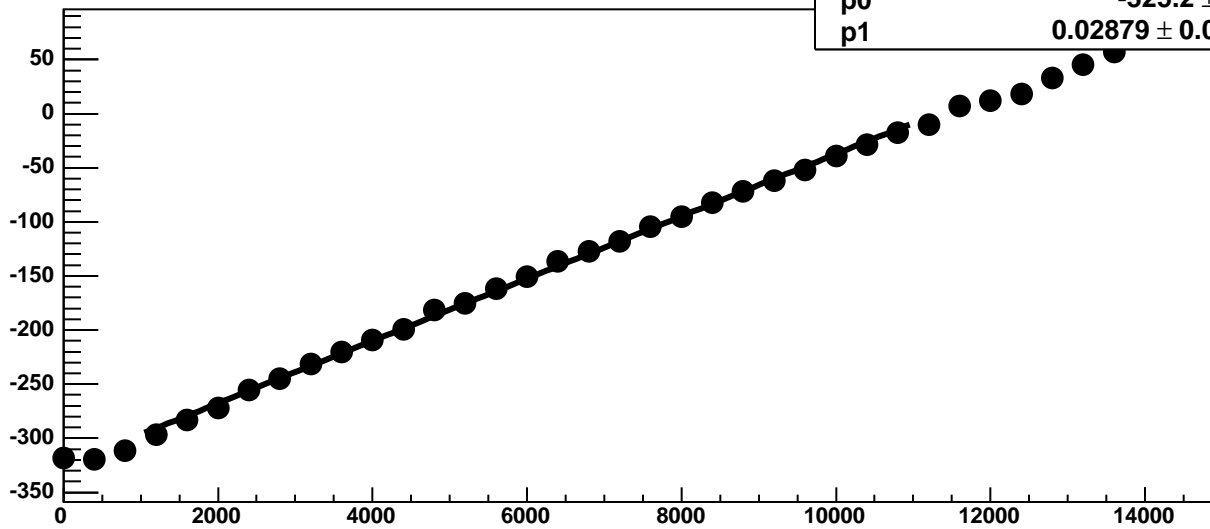
Chip 5, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



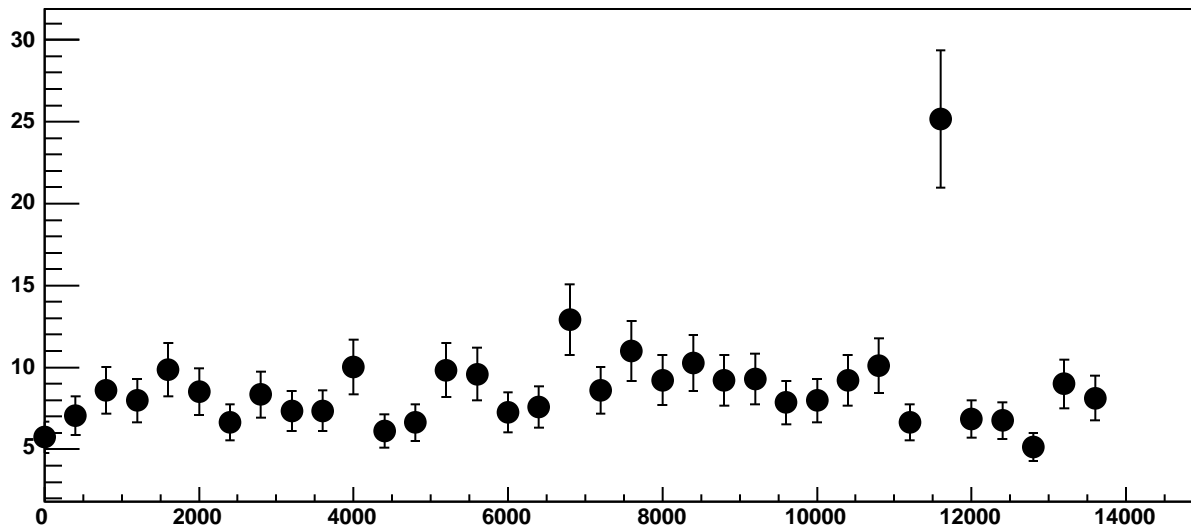
Chip 5, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC



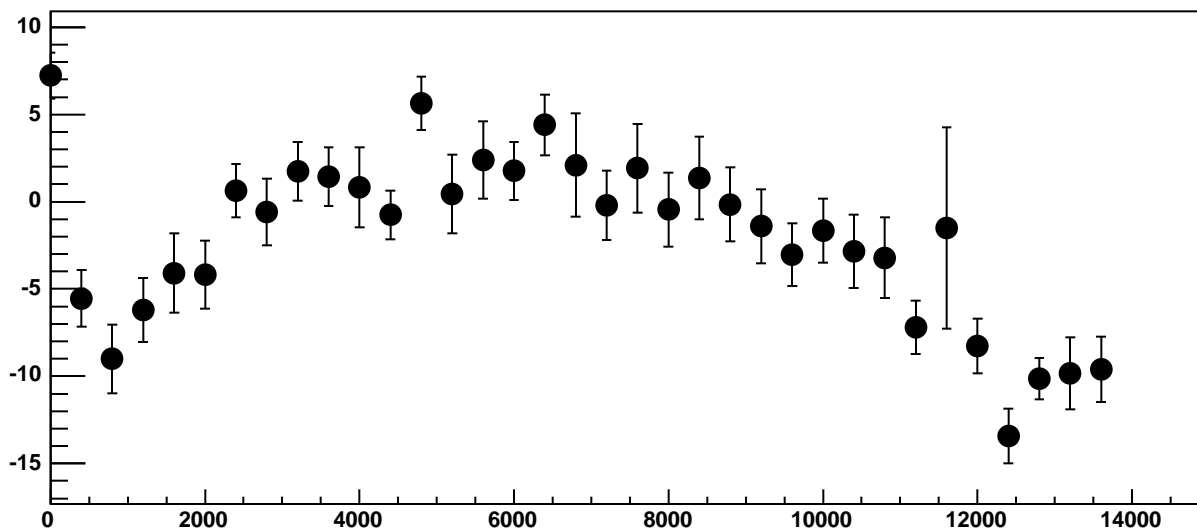
Chip 5, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



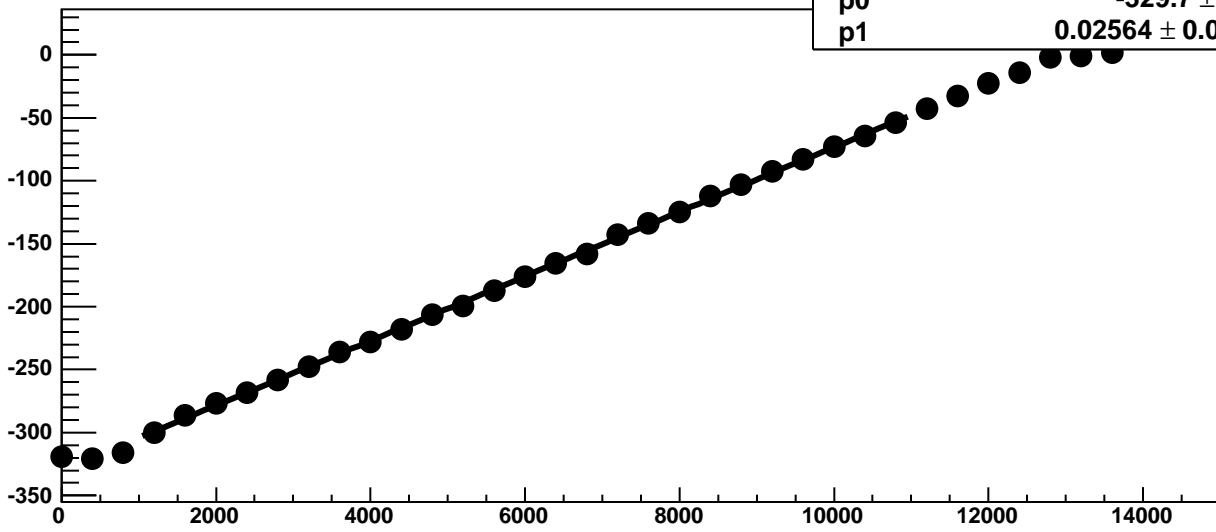
Chip 5, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

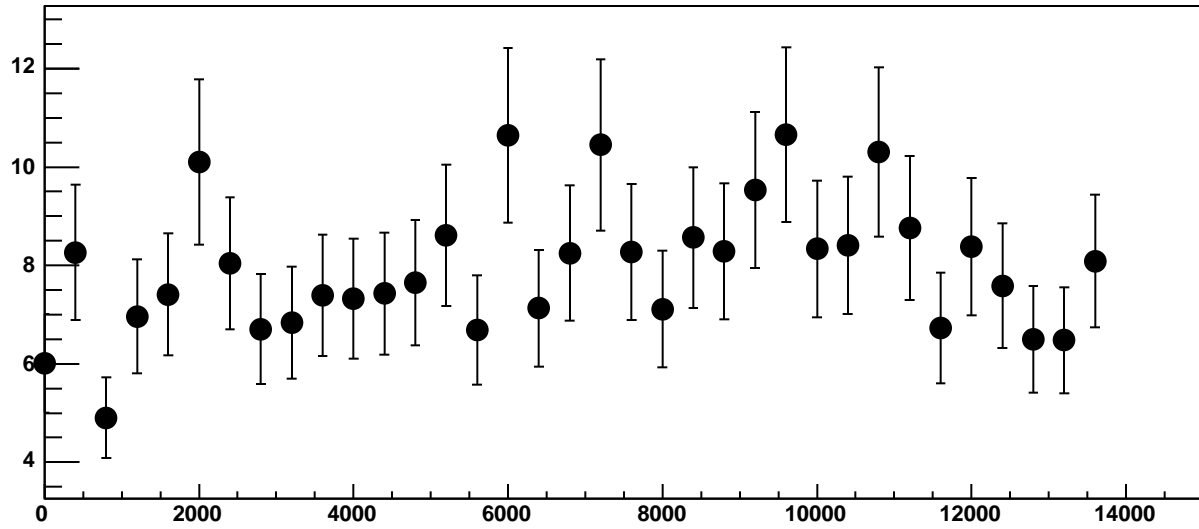


Chip 5, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

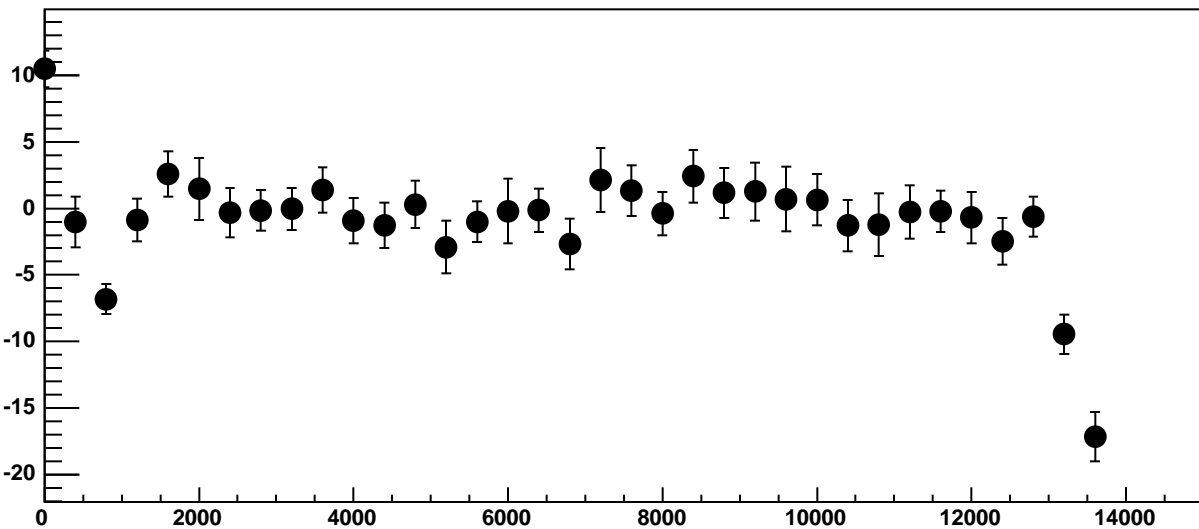


χ^2 / ndf 13.63 / 23
p0 -329.7 ± 0.8208
p1 0.02564 ± 0.0001311

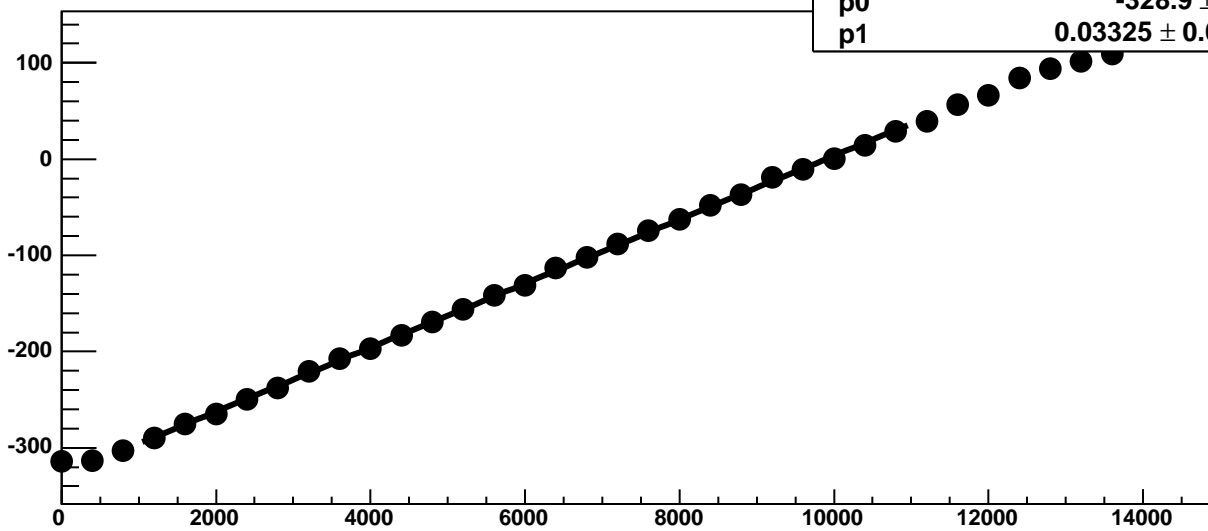
Chip 5, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

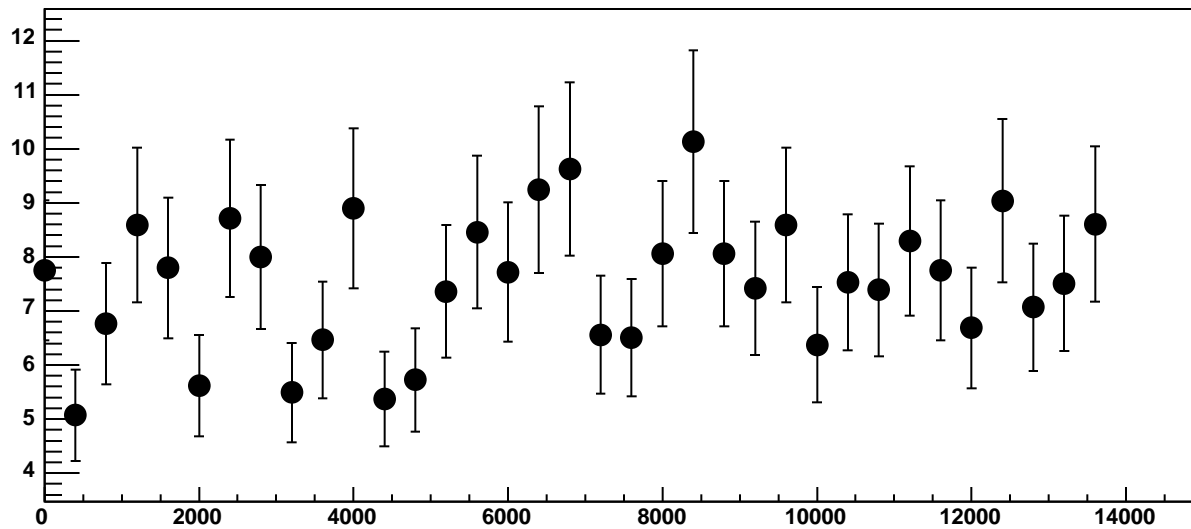


Chip 5, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

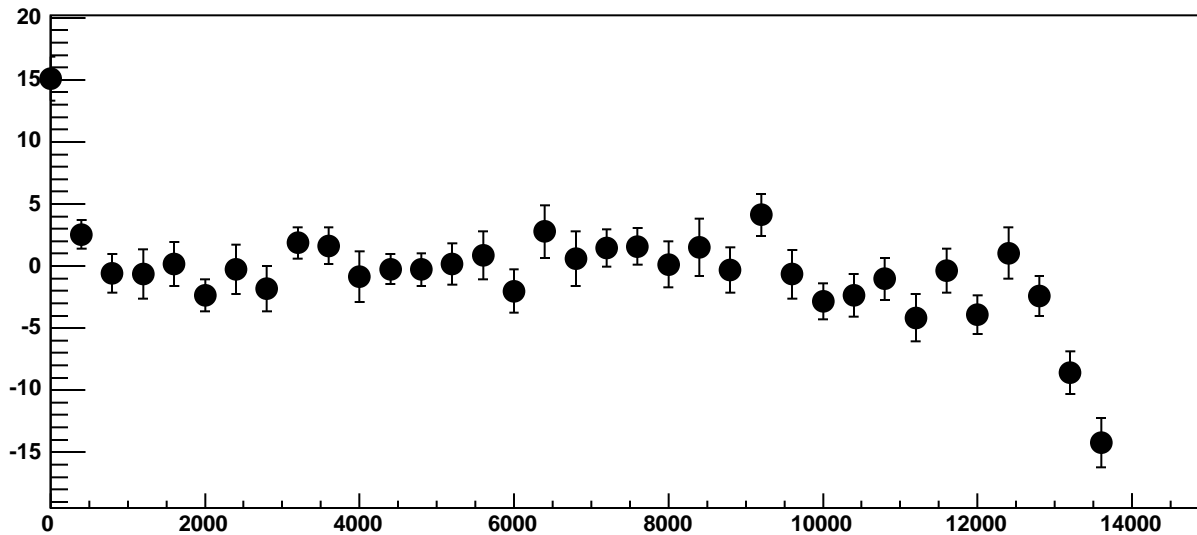


χ^2 / ndf 25.88 / 23
p0 -328.9 ± 0.7485
p1 0.03325 ± 0.0001161

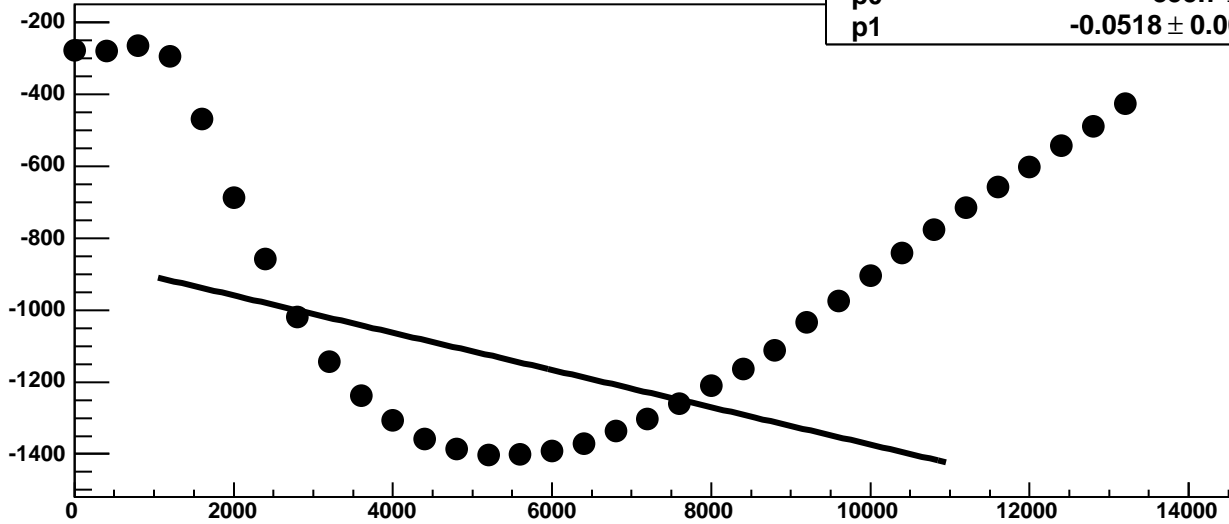
Chip 5, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



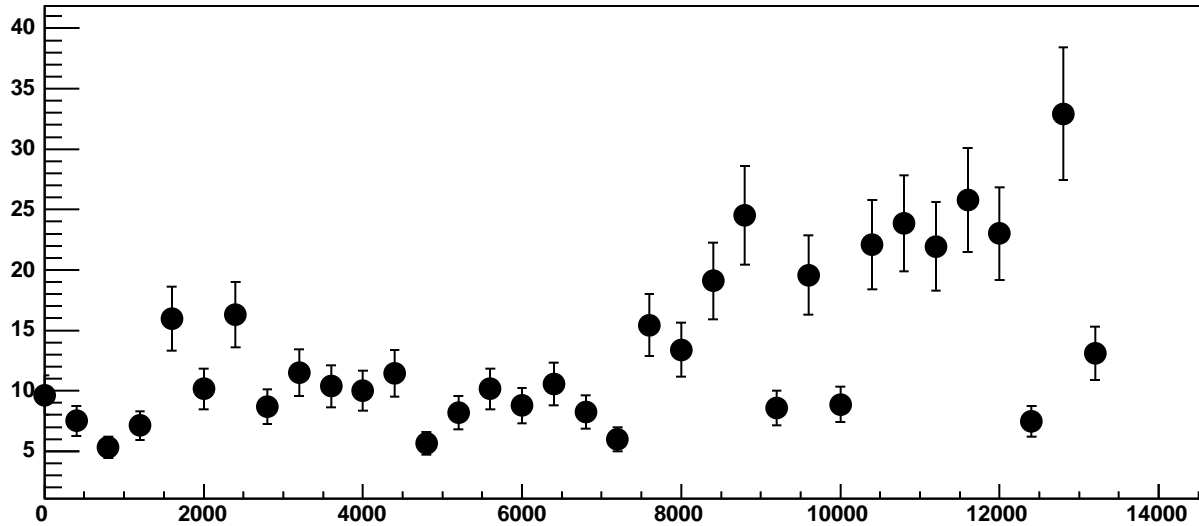
Chip 5, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC



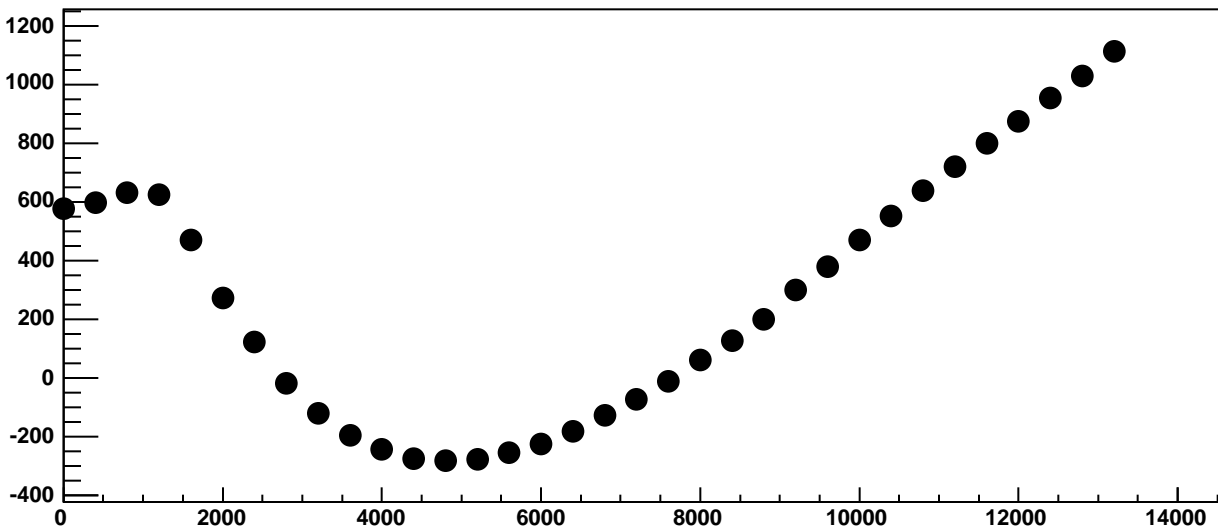
Chip 5, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



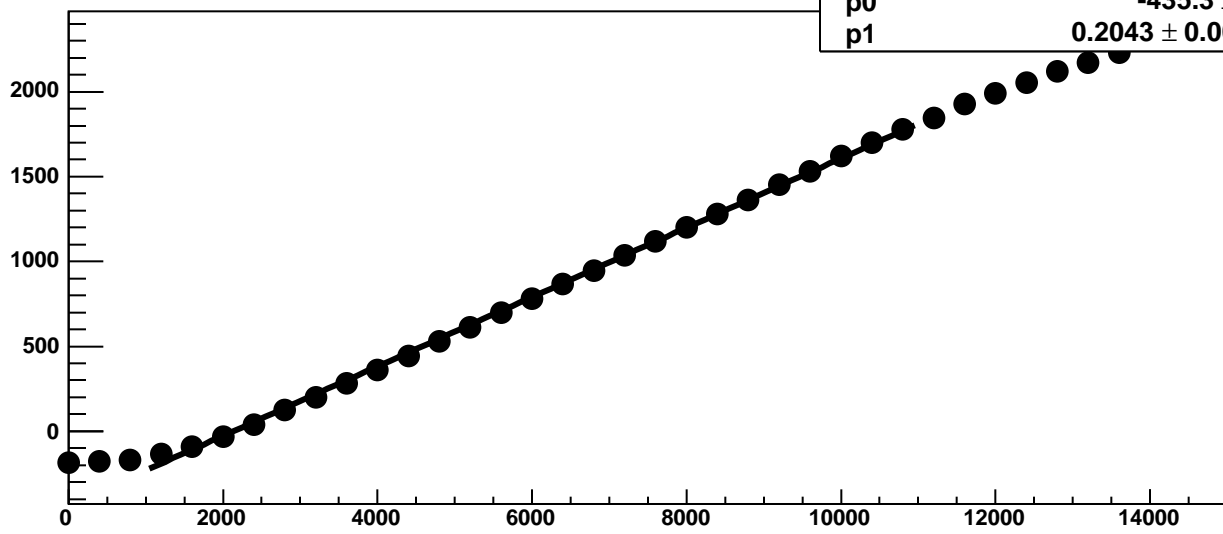
Chip 5, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 5, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

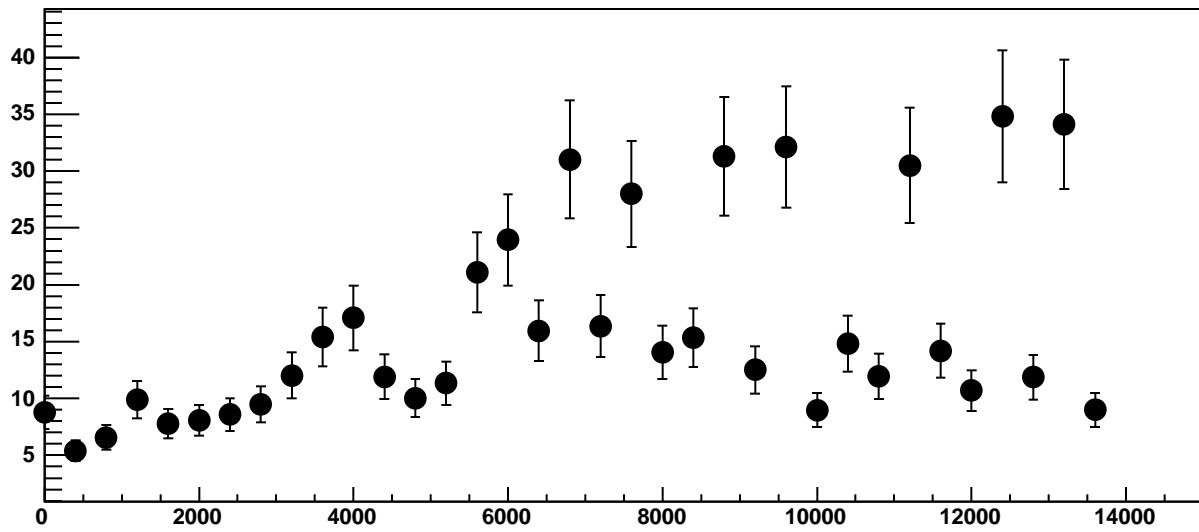


Chip 5, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

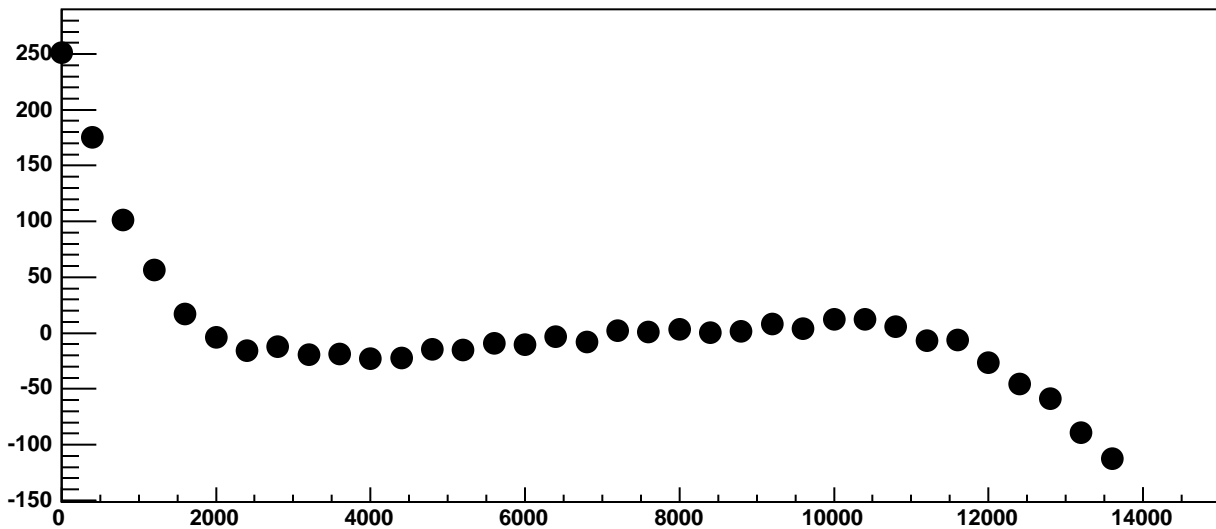


χ^2 / ndf 1139 / 23
p0 -435.3 ± 1.054
p1 0.2043 ± 0.0001802

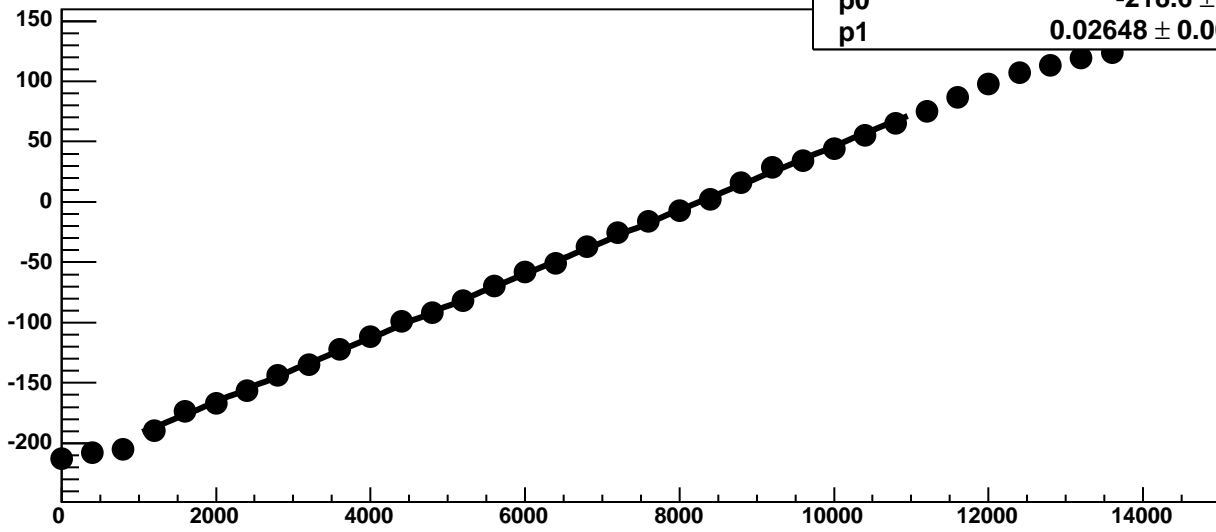
Chip 5, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

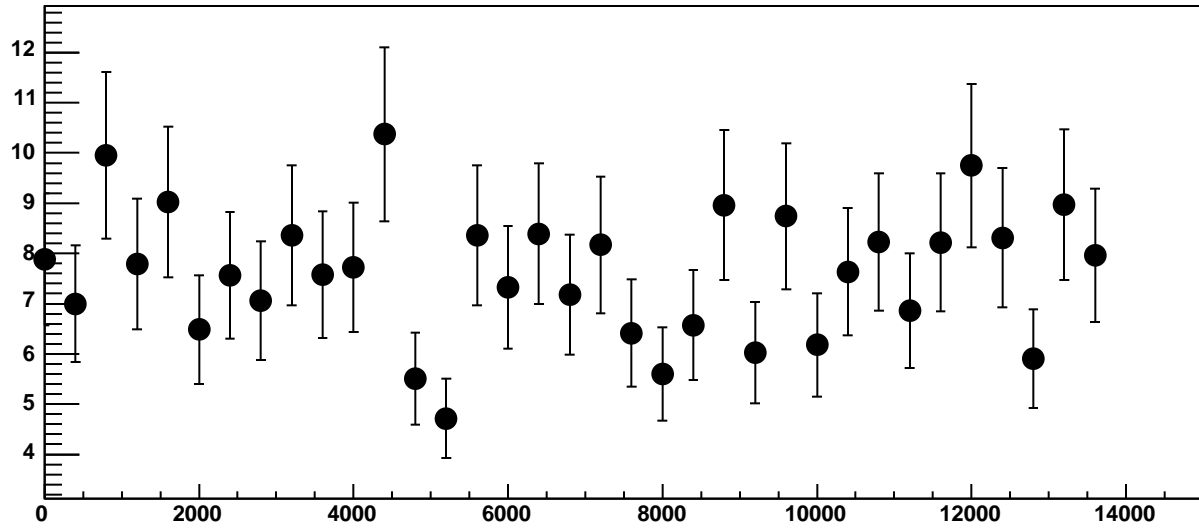


Chip 5, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

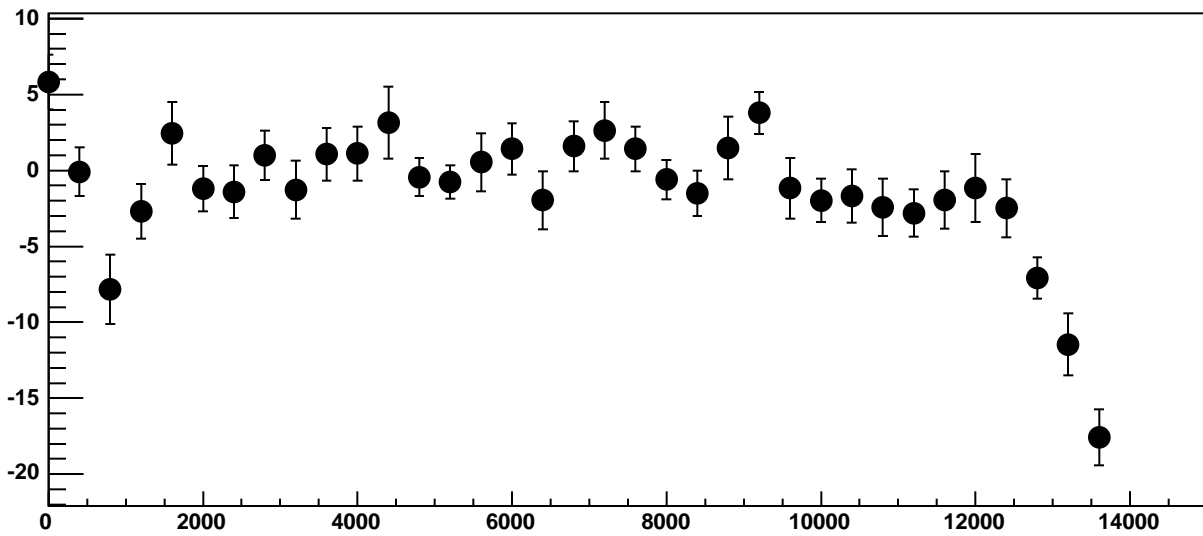


χ^2 / ndf 28.73 / 23
p0 -218.6 ± 0.7878
p1 0.02648 ± 0.0001176

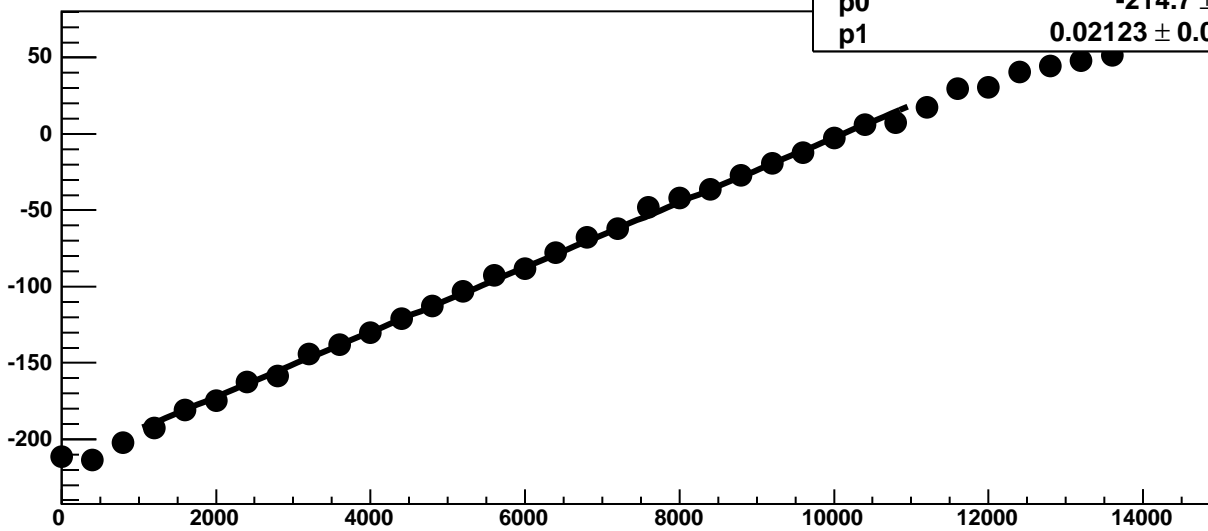
Chip 5, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

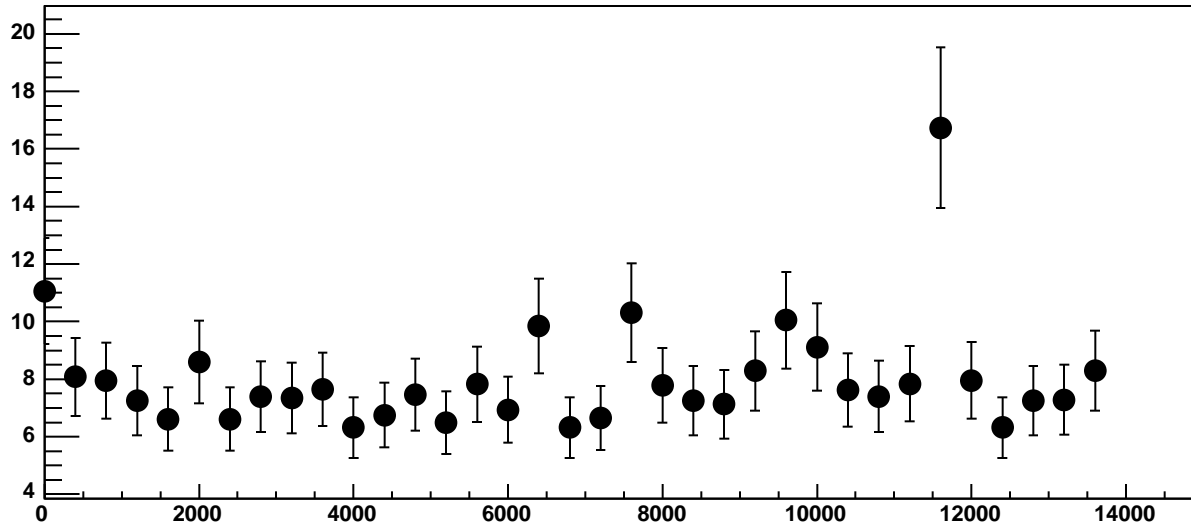


Chip 5, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

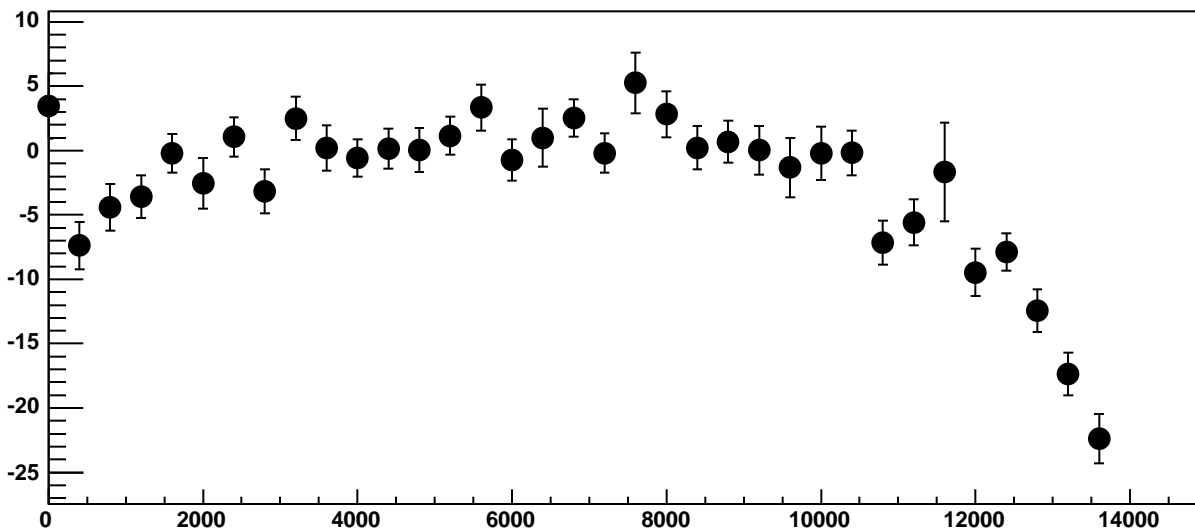


χ^2 / ndf 45.92 / 23
p0 -214.7 ± 0.7713
p1 0.02123 ± 0.0001205

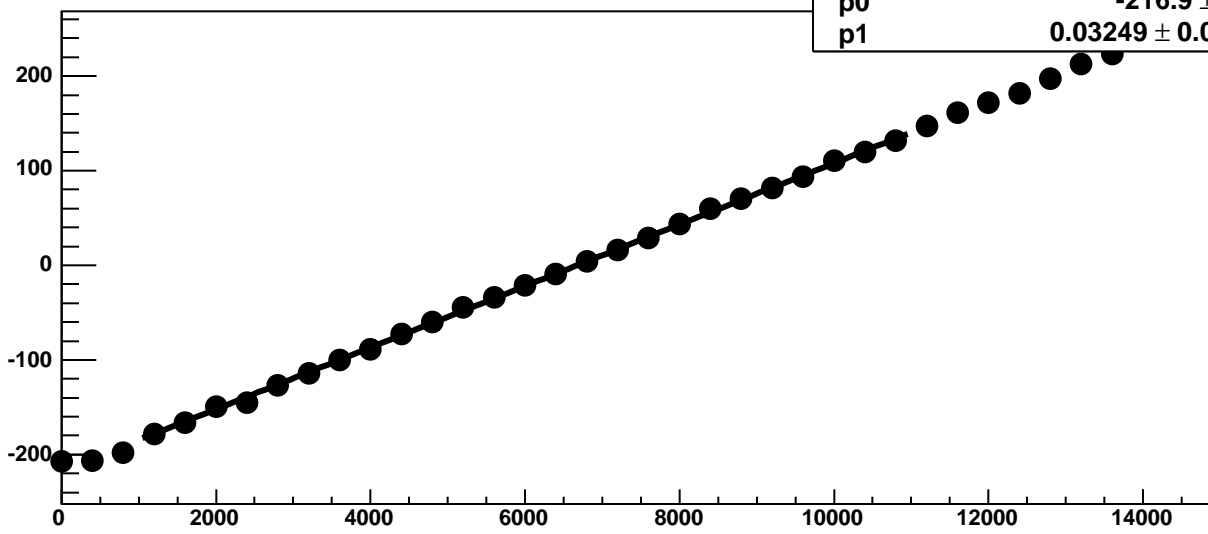
Chip 5, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

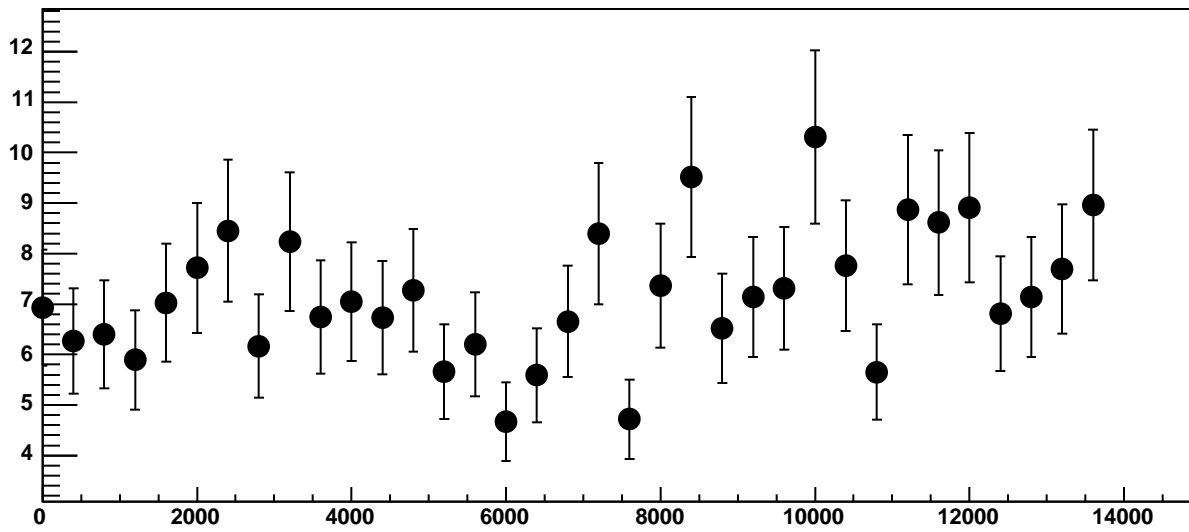


Chip 5, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

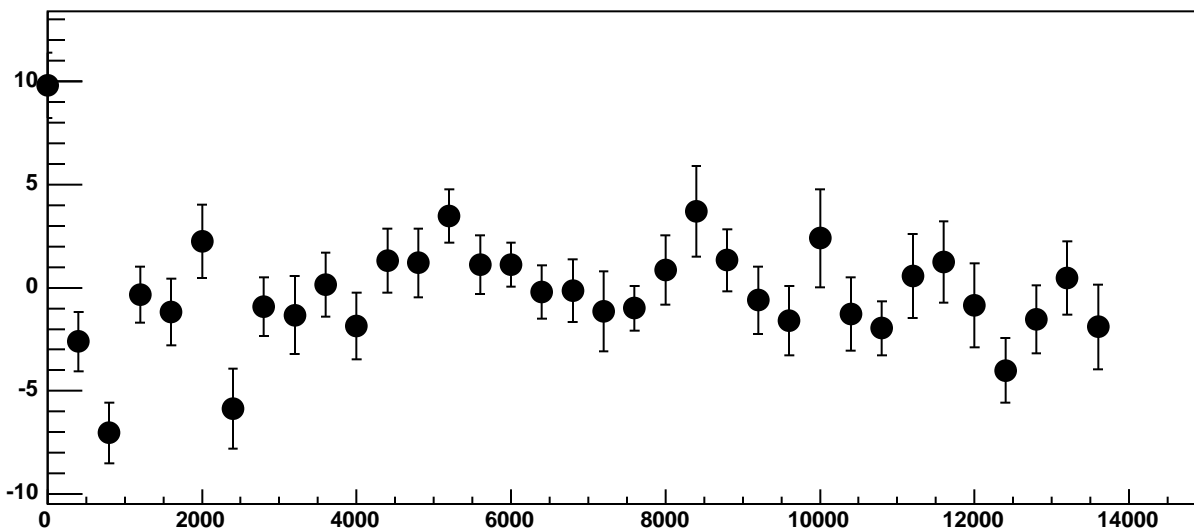


χ^2 / ndf 33.7 / 23
p0 -216.9 ± 0.7259
p1 0.03249 ± 0.0001104

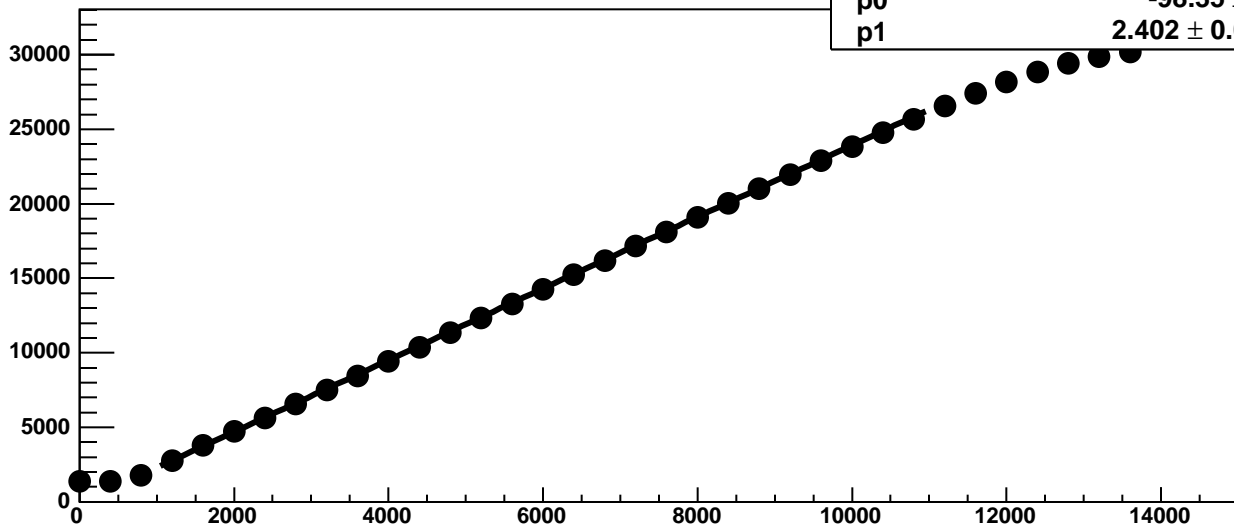
Chip 5, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

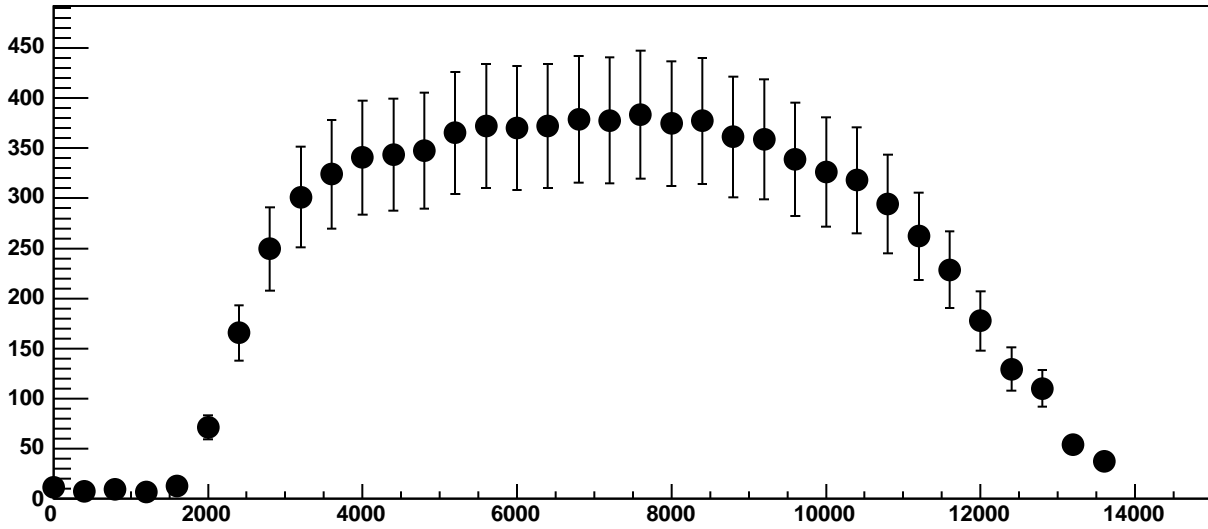


Chip 5, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC

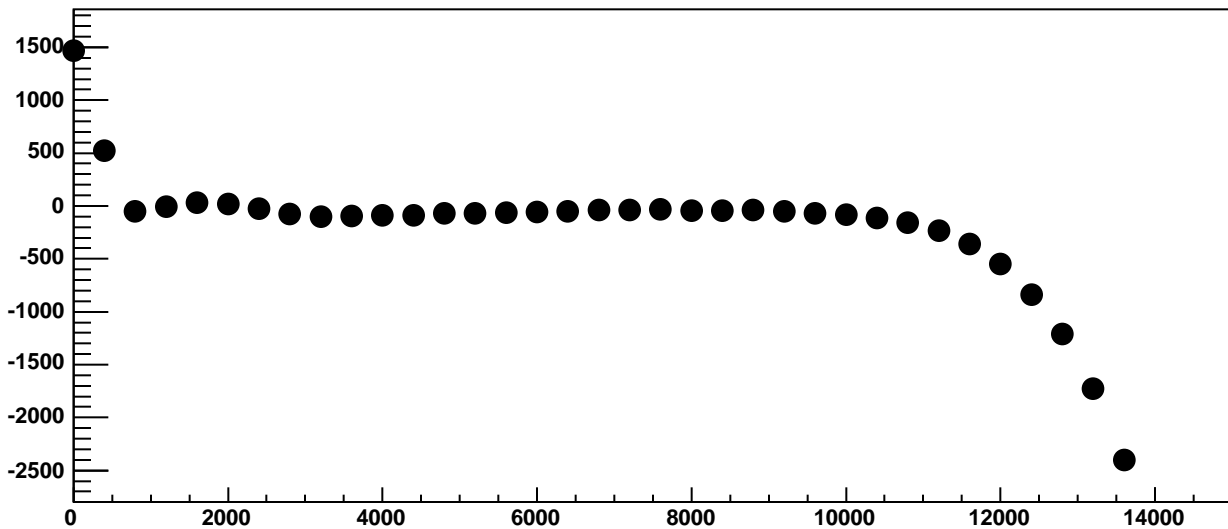


χ^2 / ndf 157.1 / 23
p0 -98.35 ± 3.845
p1 2.402 ± 0.002685

Chip 5, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC

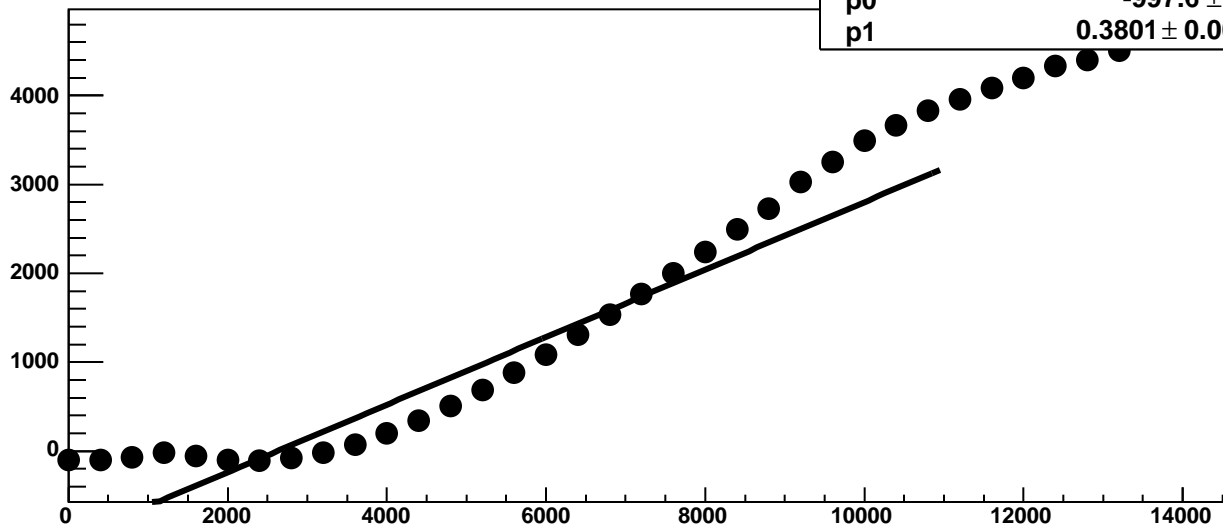


Chip 5, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC

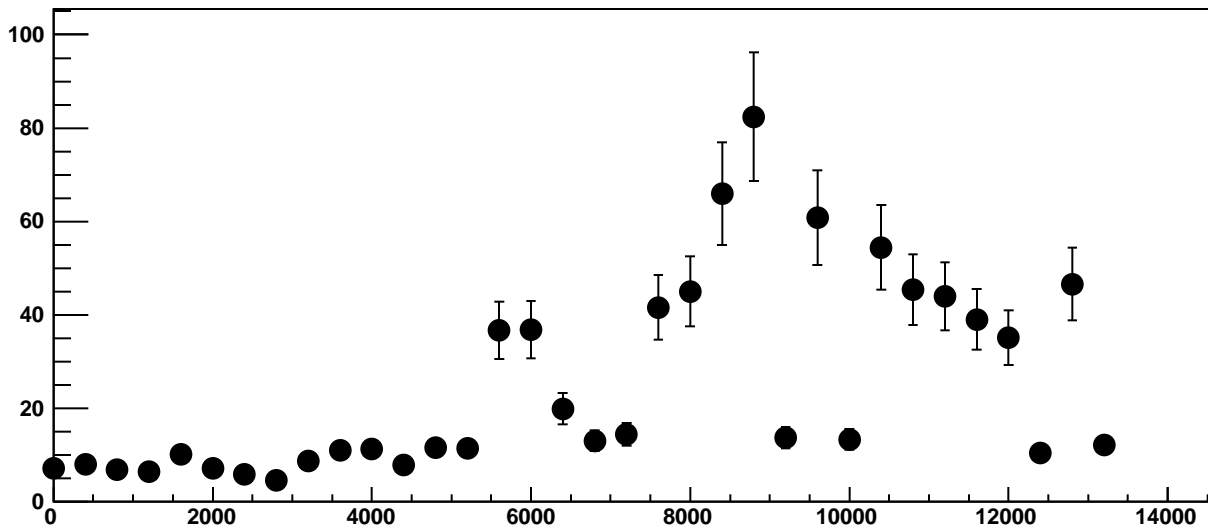


Chip 5, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

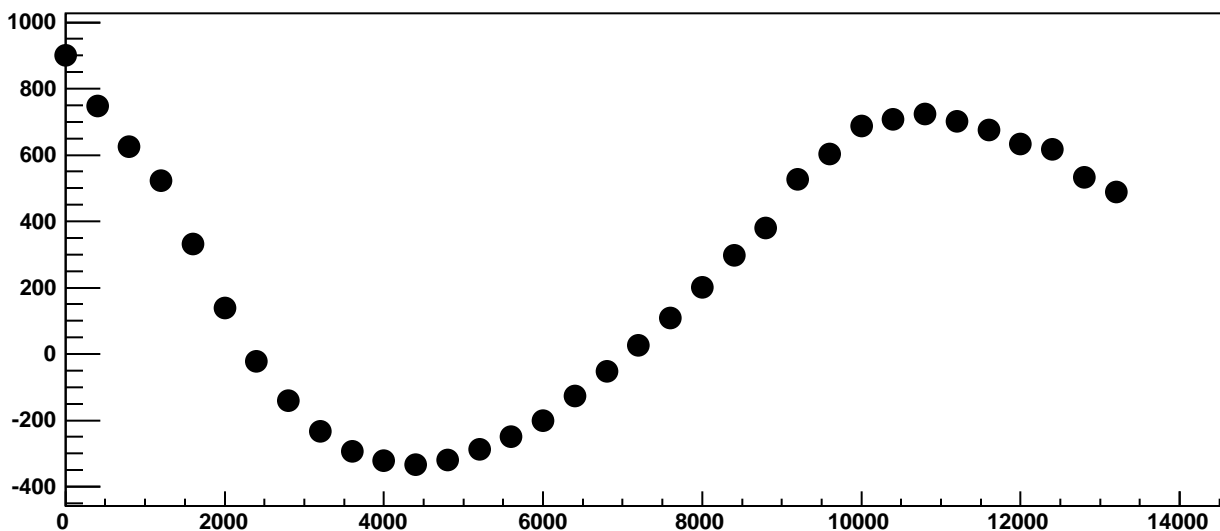
χ^2 / ndf 3.603e+05 / 23
p0 -997.6 ± 0.9593
p1 0.3801 ± 0.0002346



Chip 5, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC

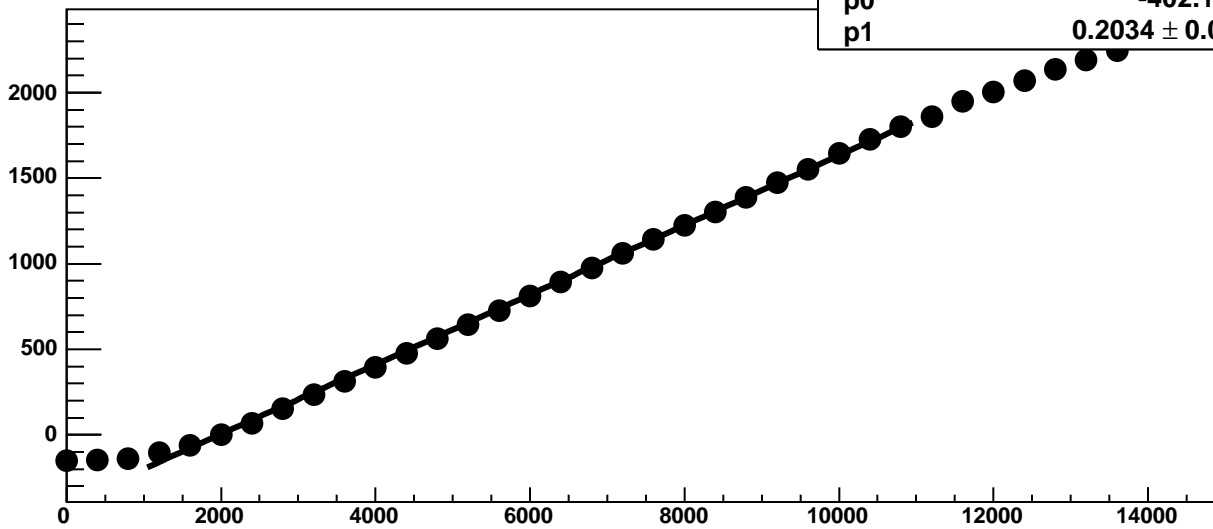


Chip 5, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

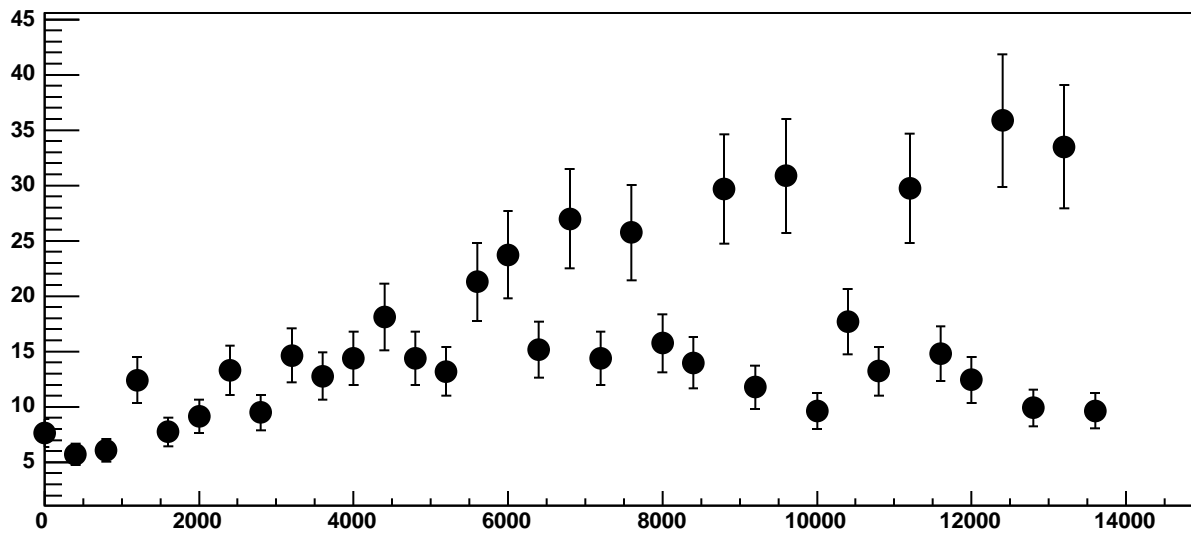


Chip 5, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

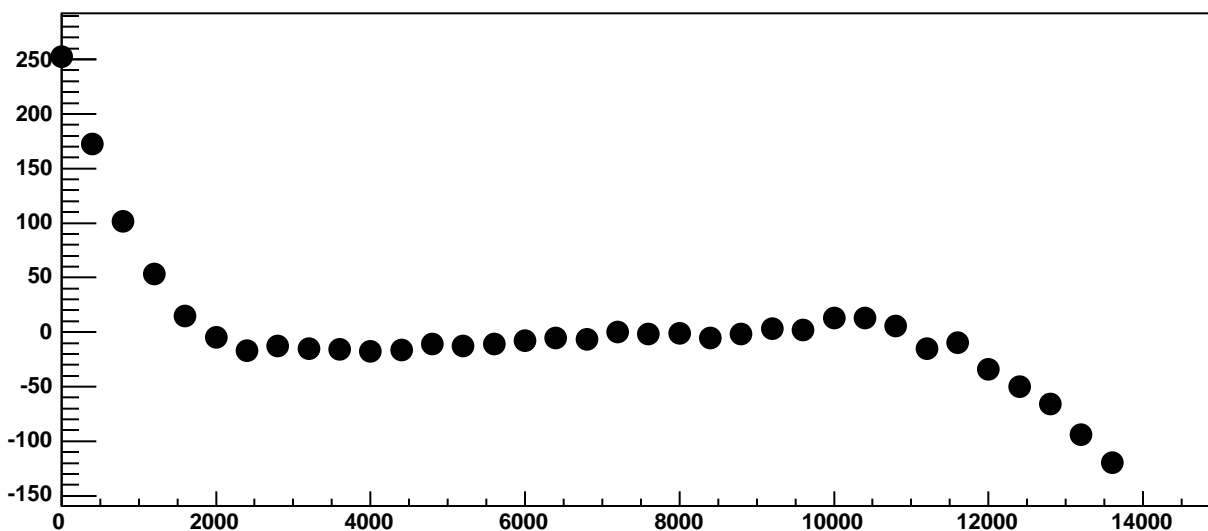
χ^2 / ndf 667.6 / 23
p0 -402.1 ± 1.182
p1 0.2034 ± 0.0001951



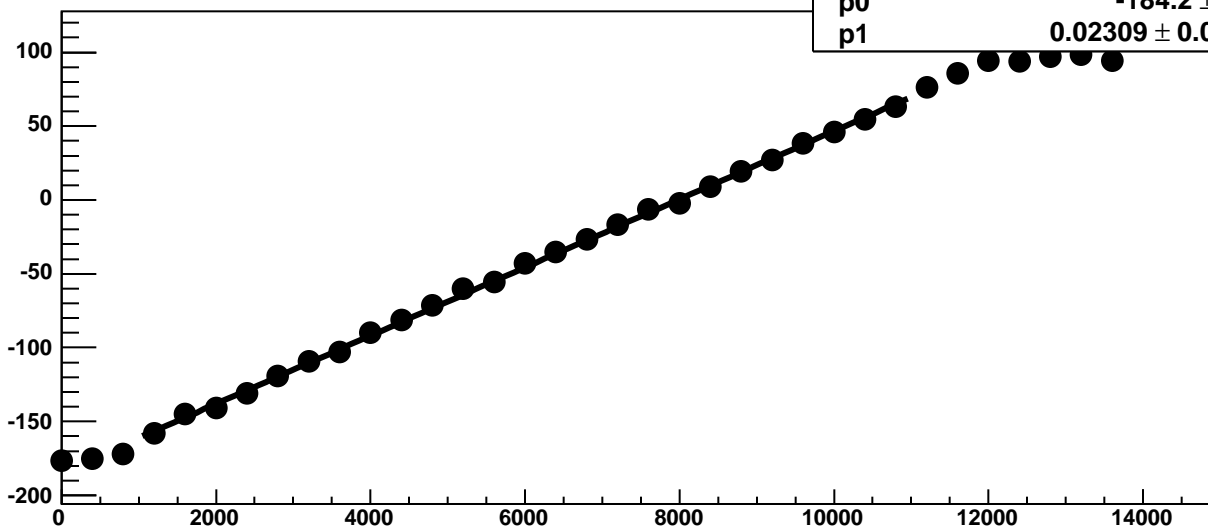
Chip 5, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



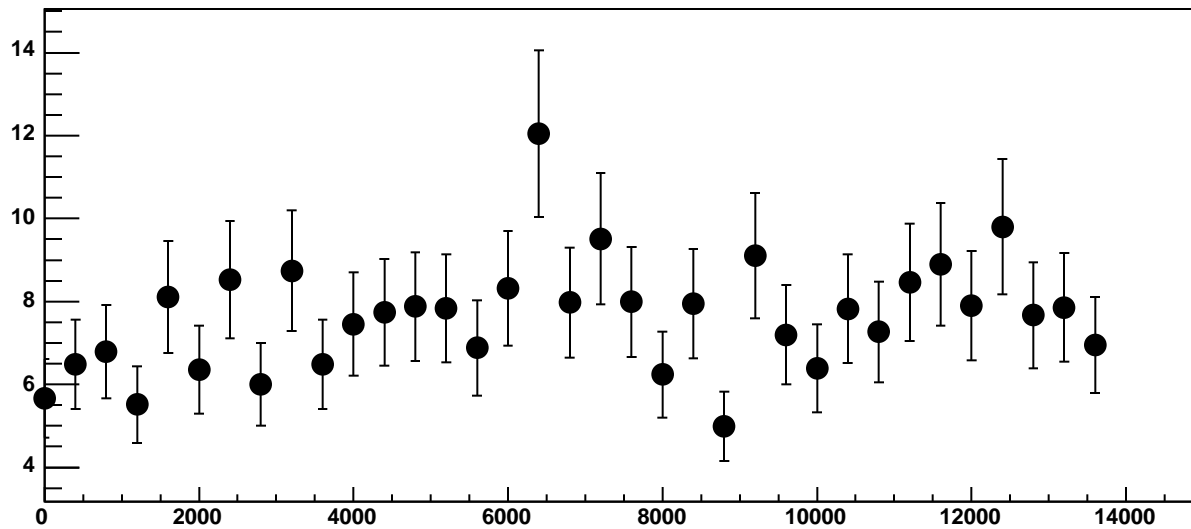
Chip 5, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



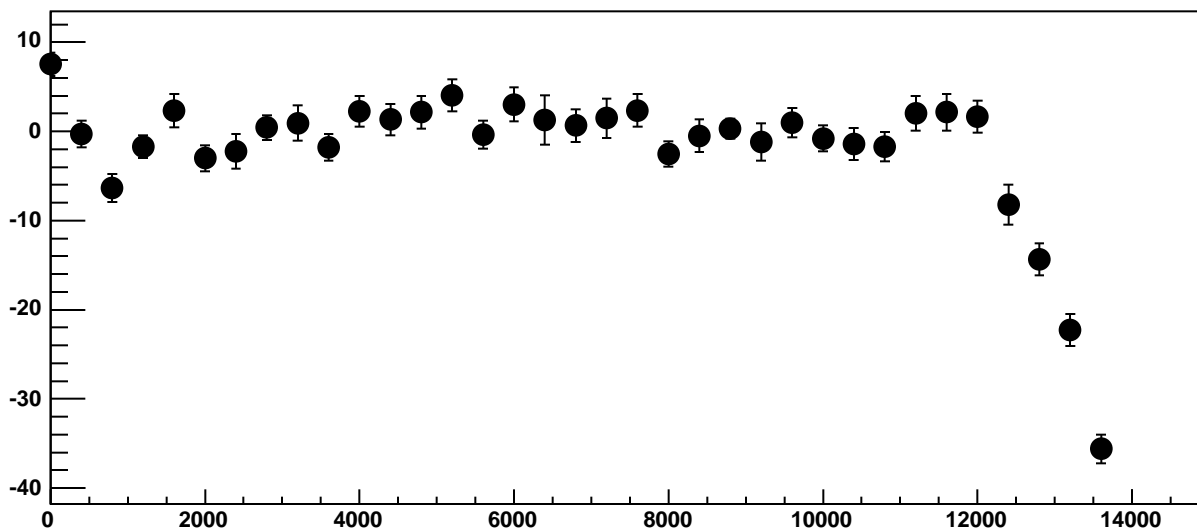
Chip 5, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



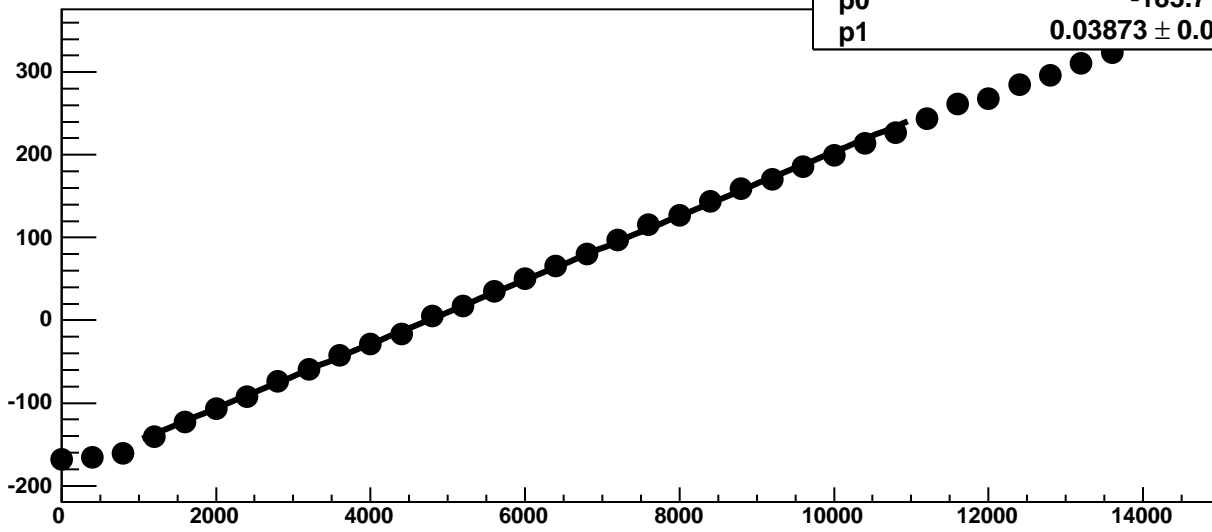
Chip 5, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



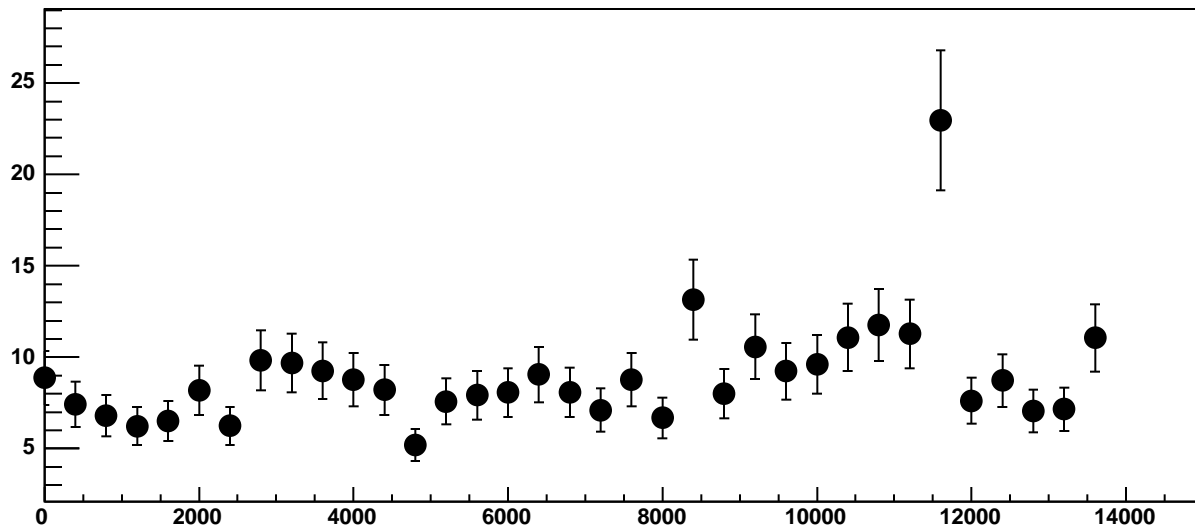
Chip 5, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



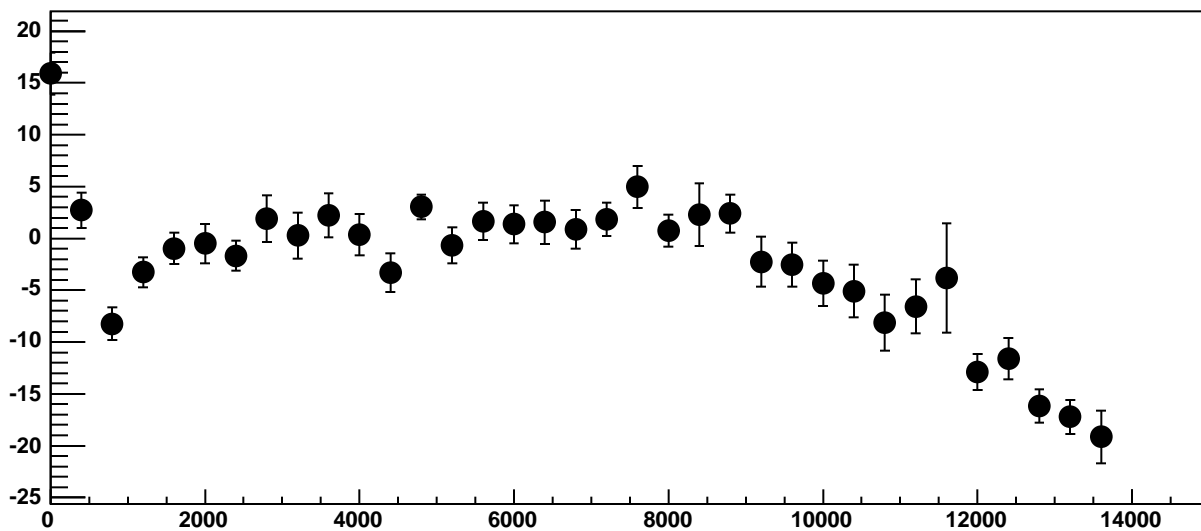
Chip 5, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC



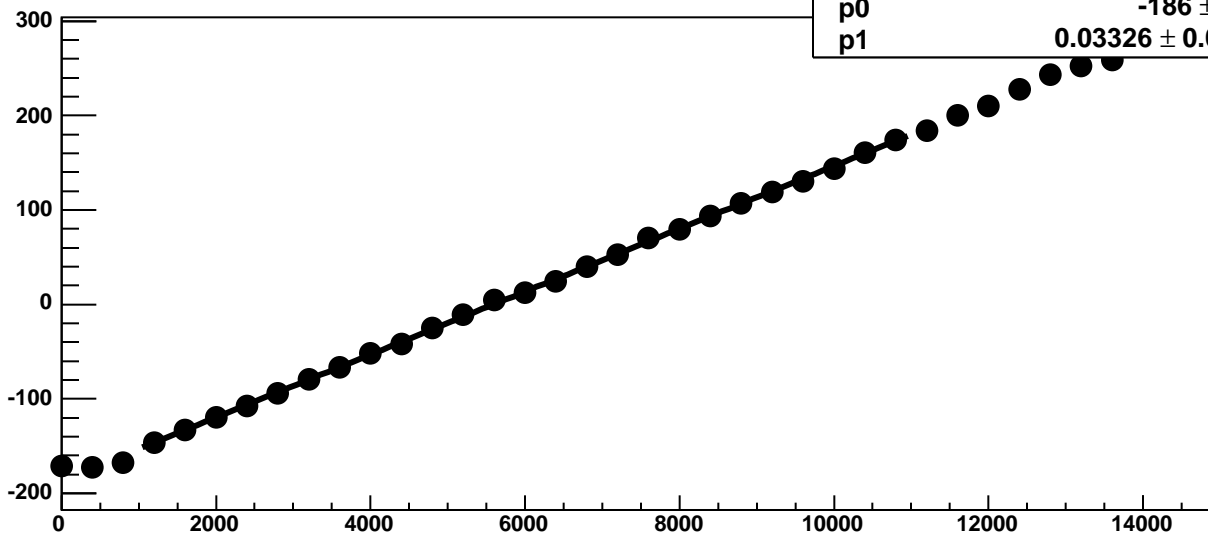
Chip 5, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC

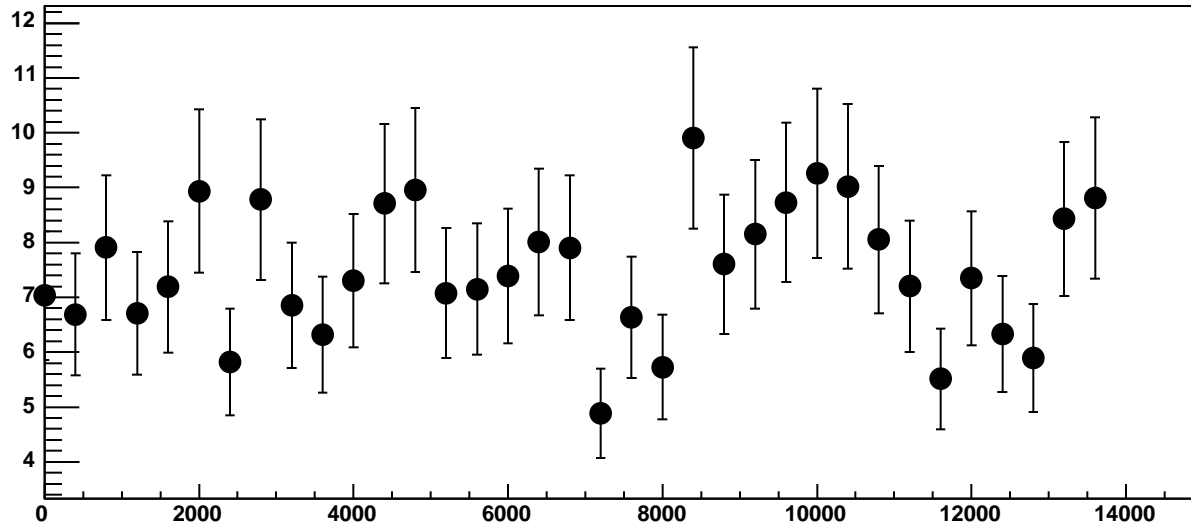


Chip 5, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

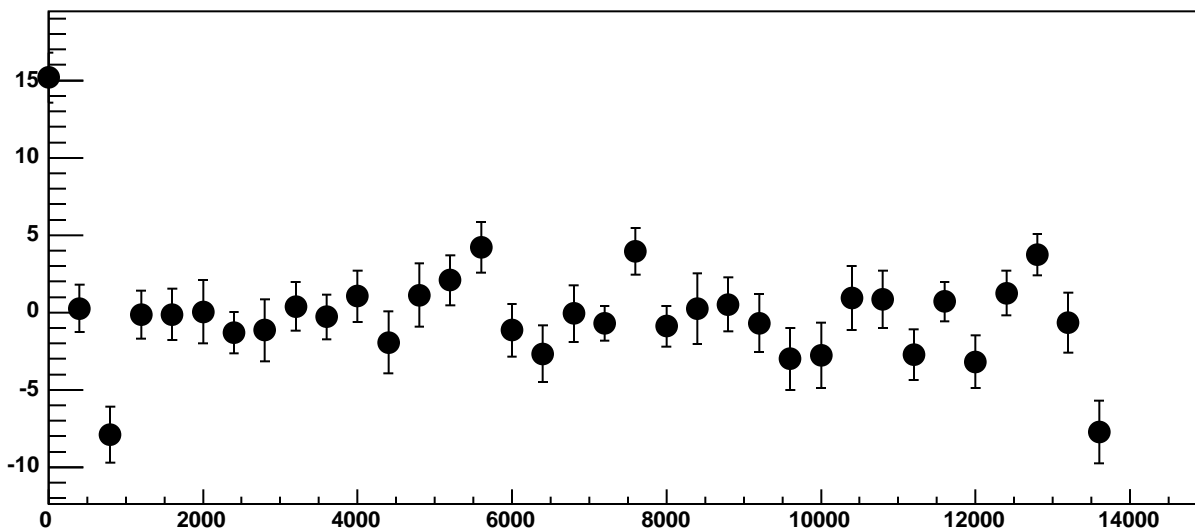


χ^2 / ndf 25.95 / 23
p0 -186 ± 0.7779
p1 0.03326 ± 0.0001211

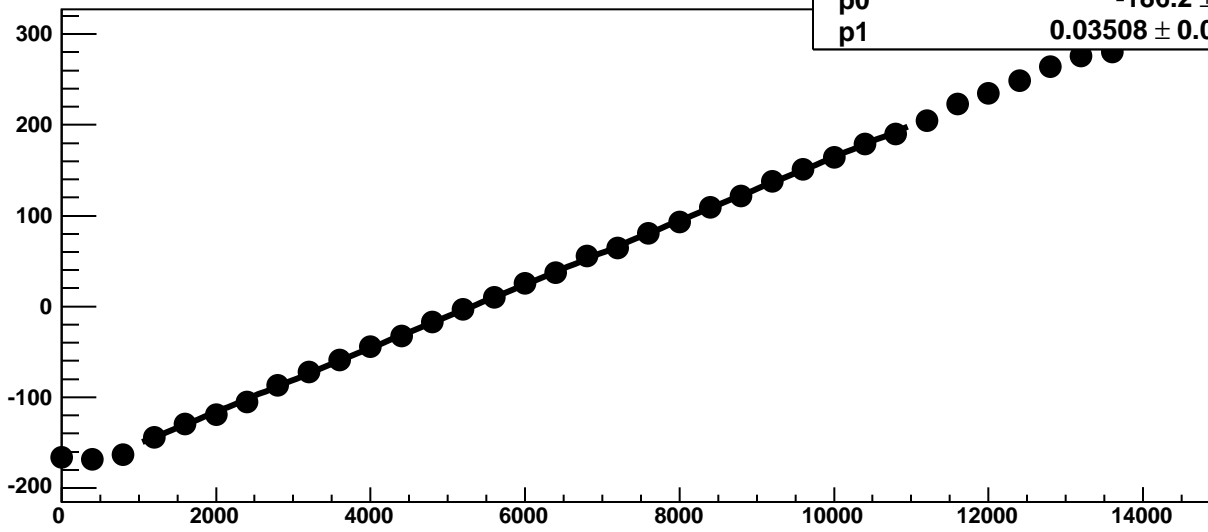
Chip 5, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

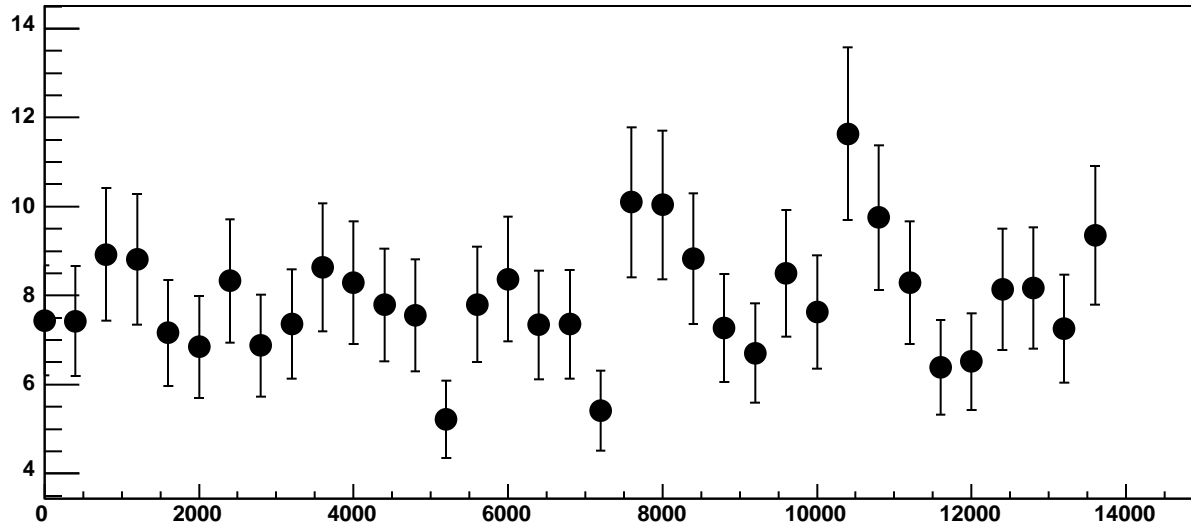


Chip 5, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

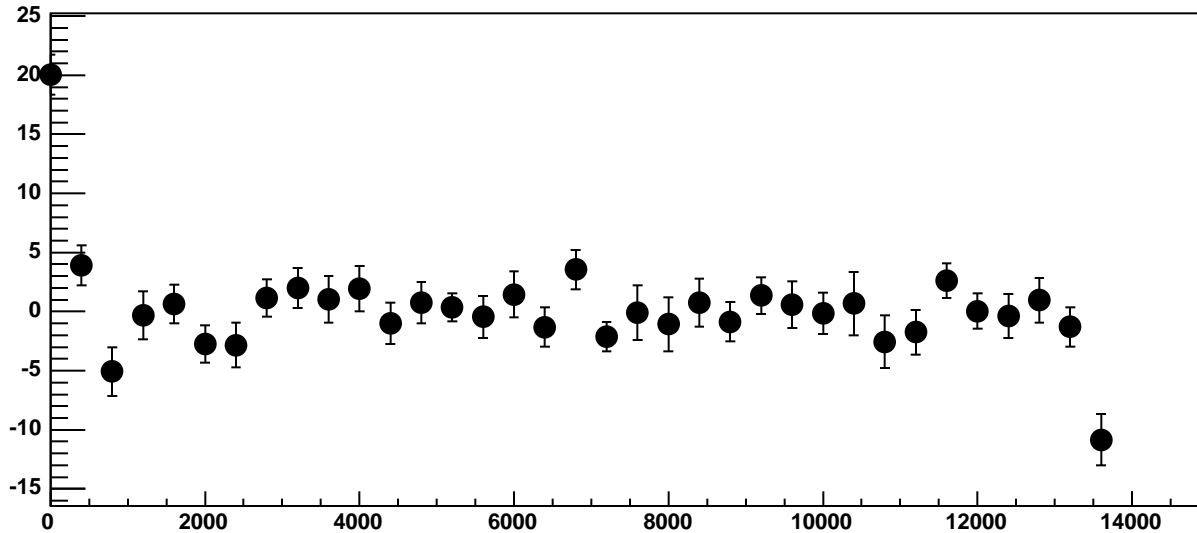


χ^2 / ndf 20.72 / 23
p0 -186.2 ± 0.8202
p1 0.03508 ± 0.0001284

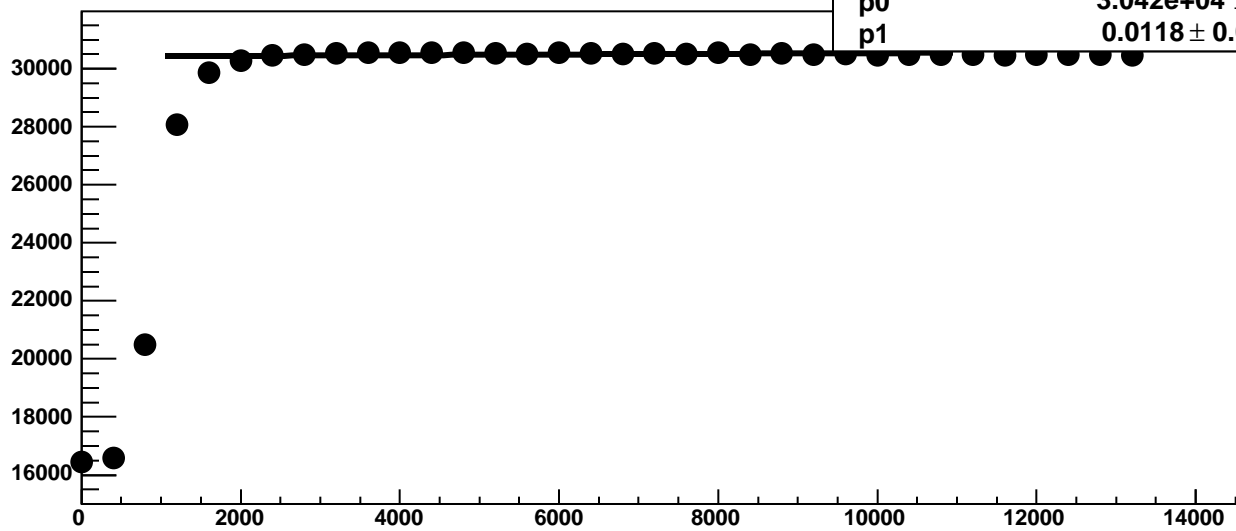
Chip 5, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



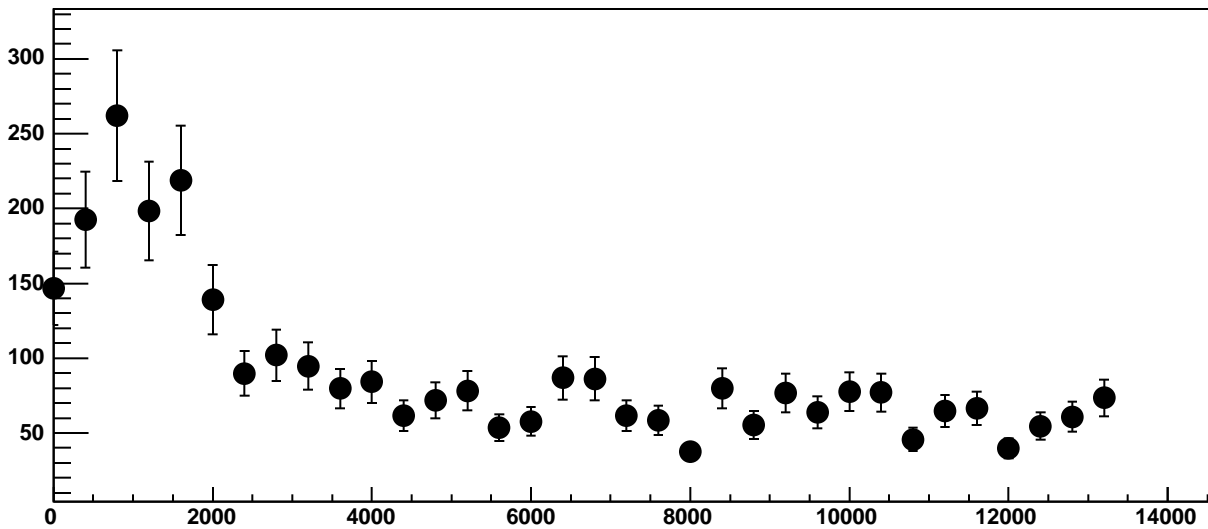
Chip 5, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



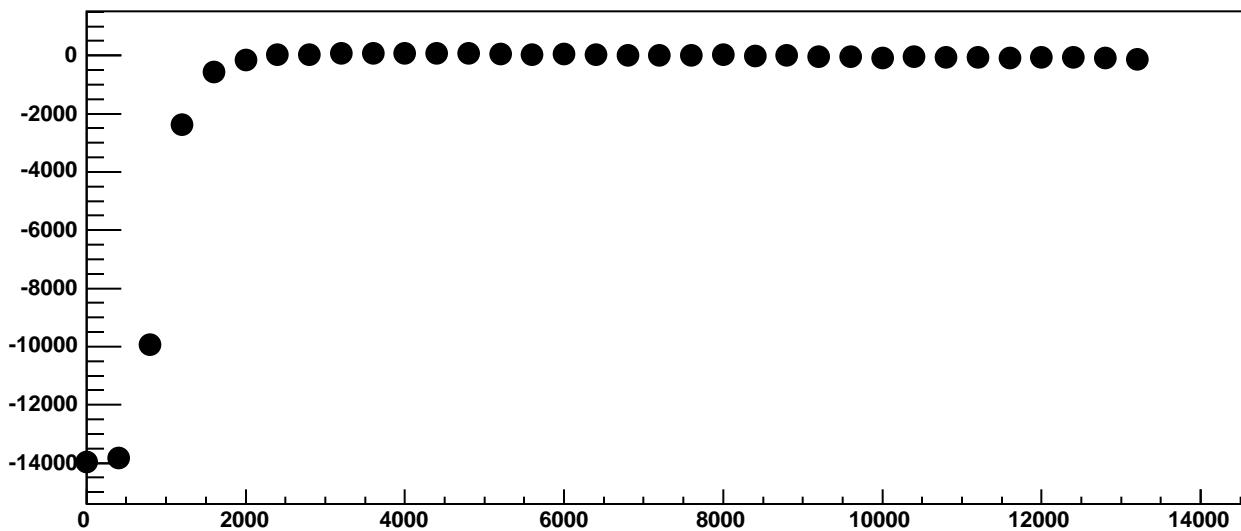
Chip 5, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC



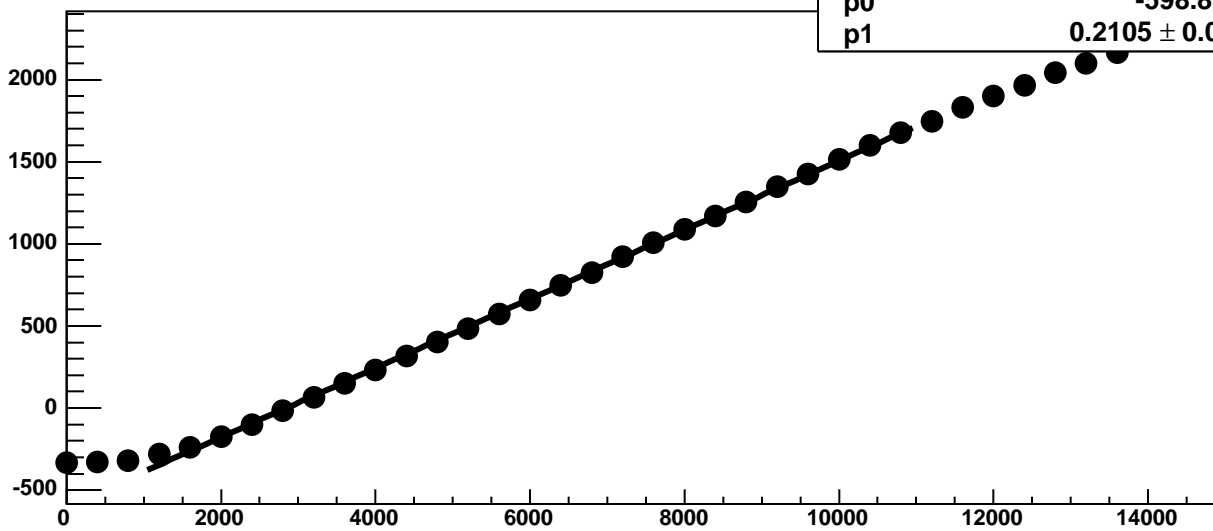
Chip 5, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC



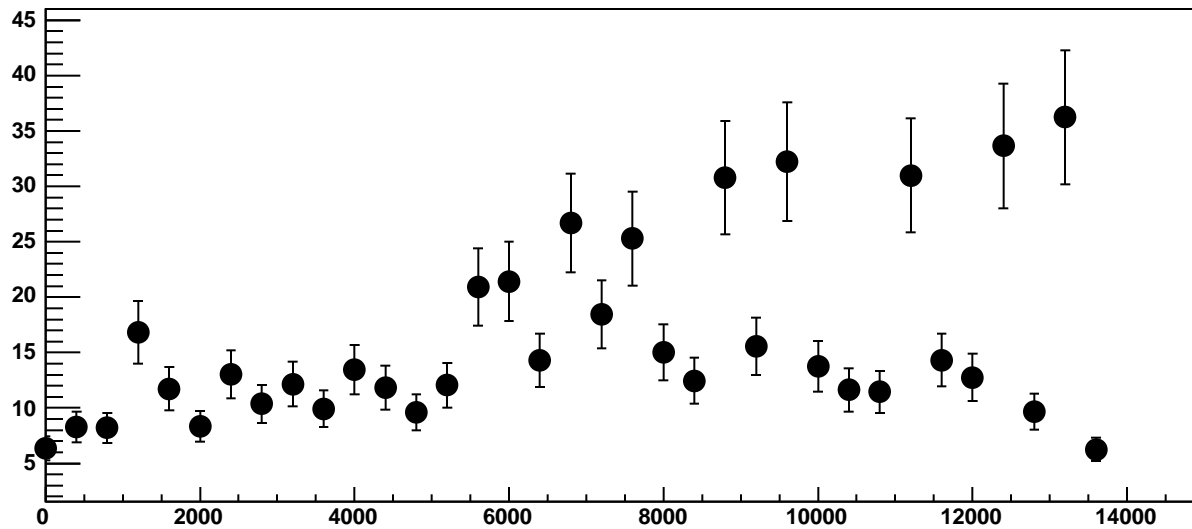
Chip 5, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC



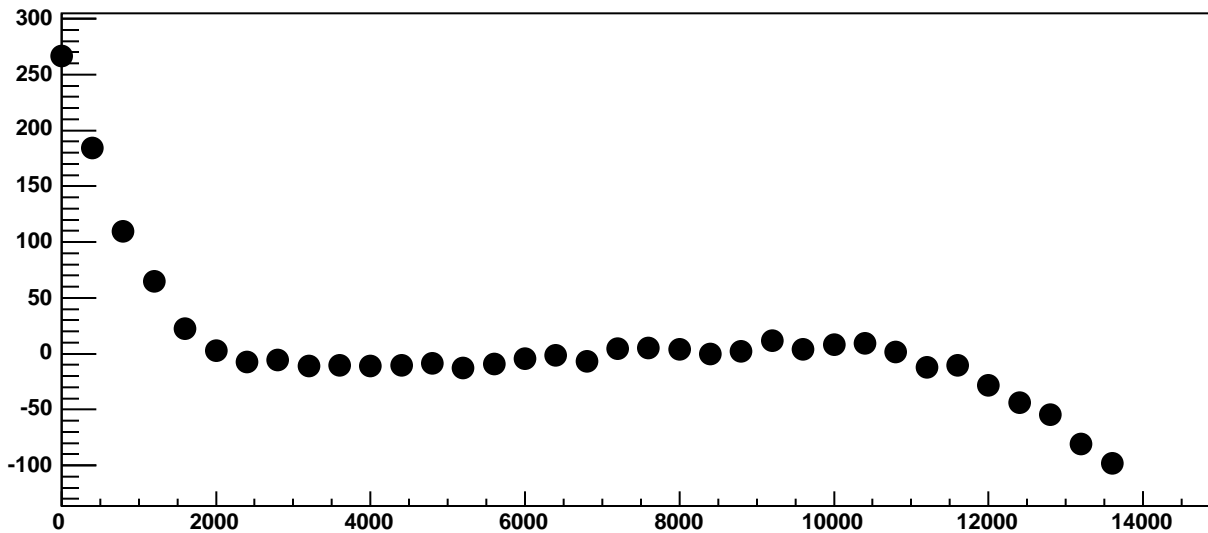
Chip 5, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



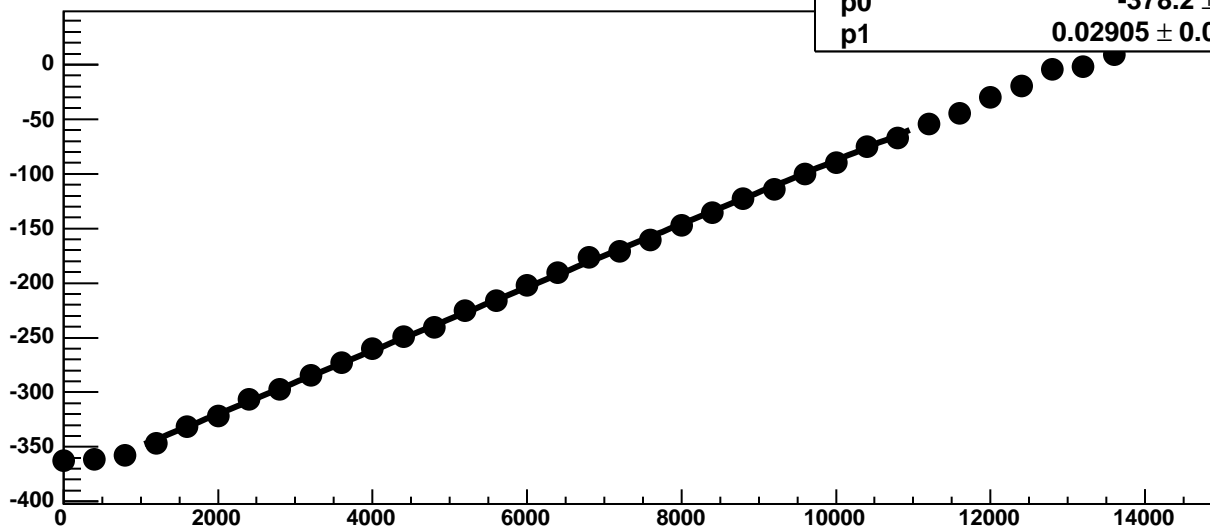
Chip 5, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

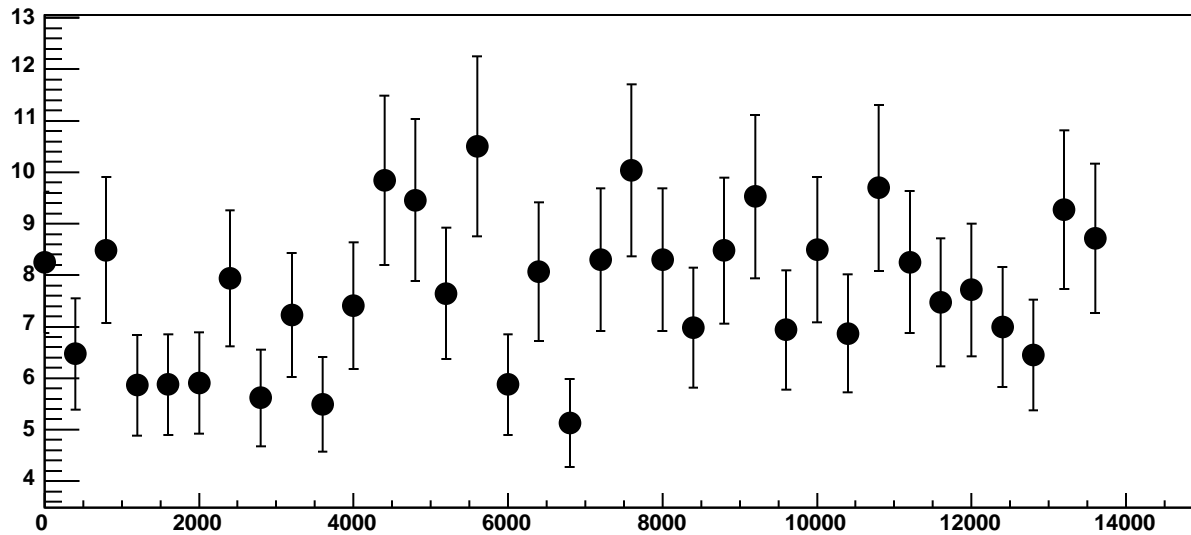


Chip 5, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

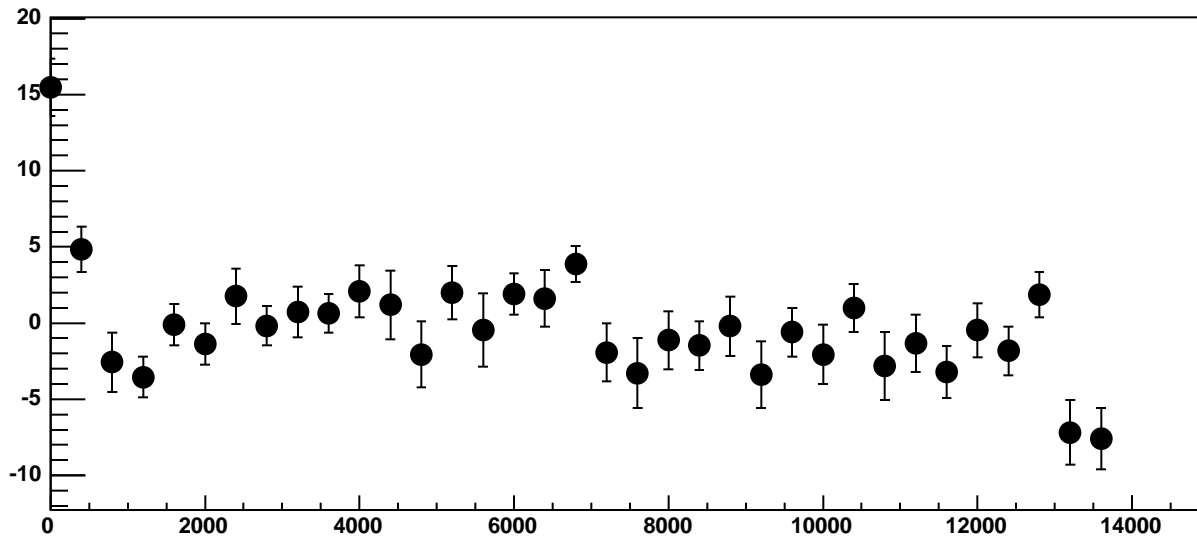


χ^2 / ndf 37.09 / 23
p0 -378.2 ± 0.6966
p1 0.02905 ± 0.0001124

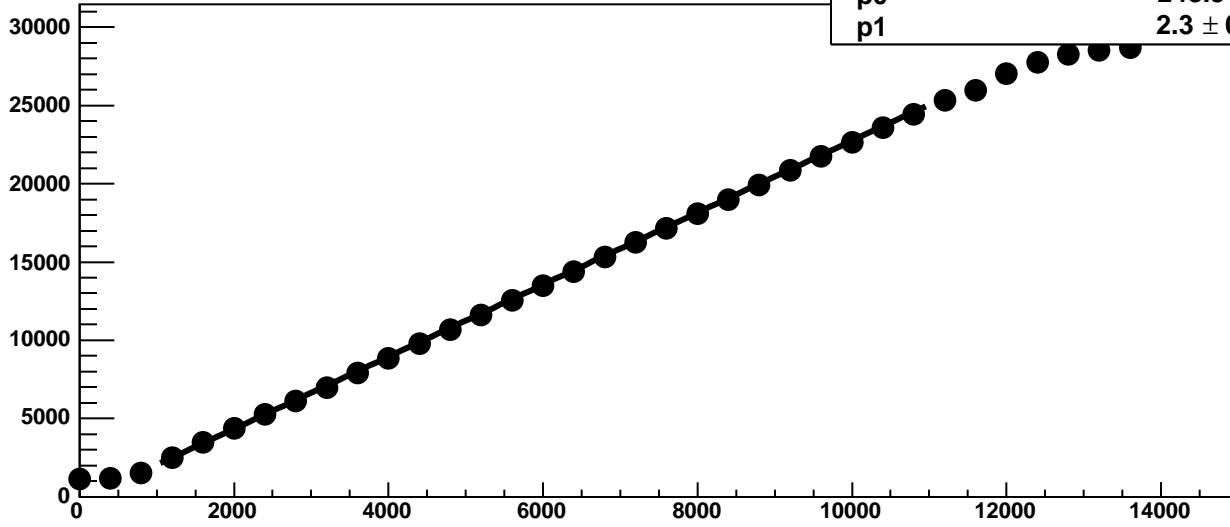
Chip 5, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

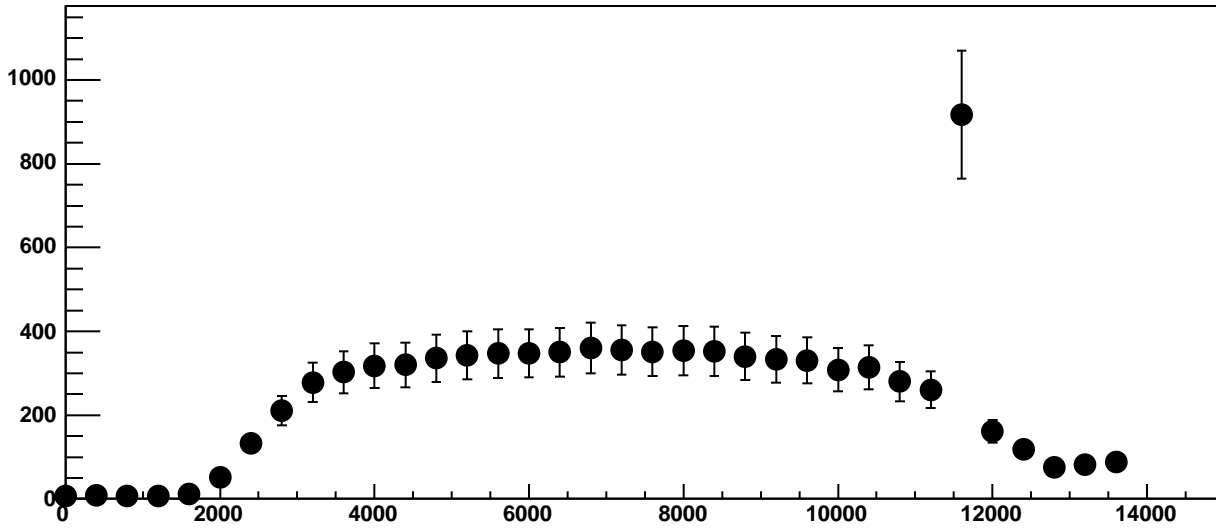


Chip 5, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC

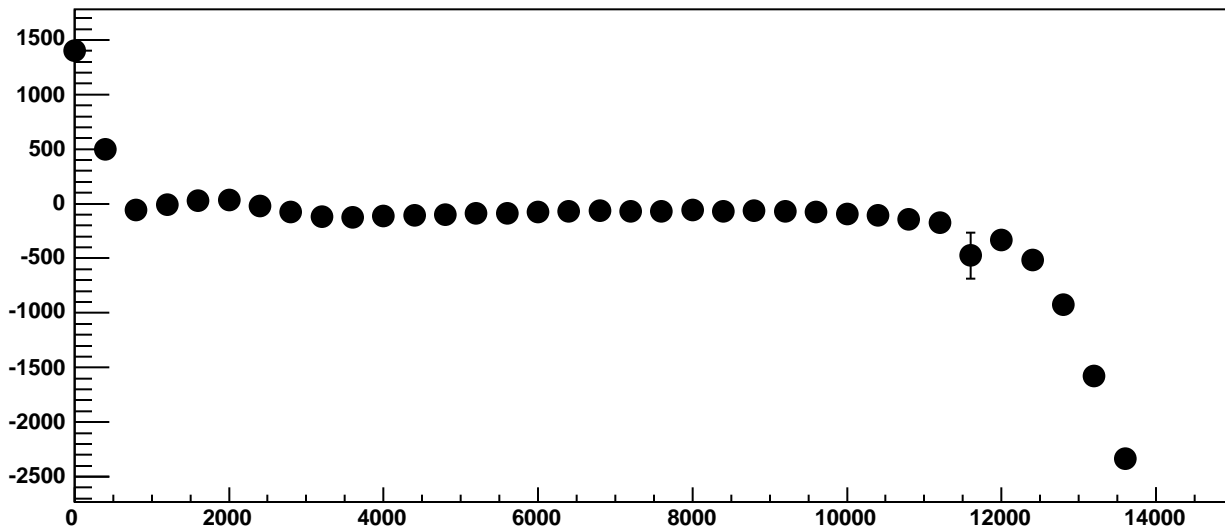


χ^2 / ndf	214 / 23
p0	-248.3 ± 3.655
p1	2.3 ± 0.00253

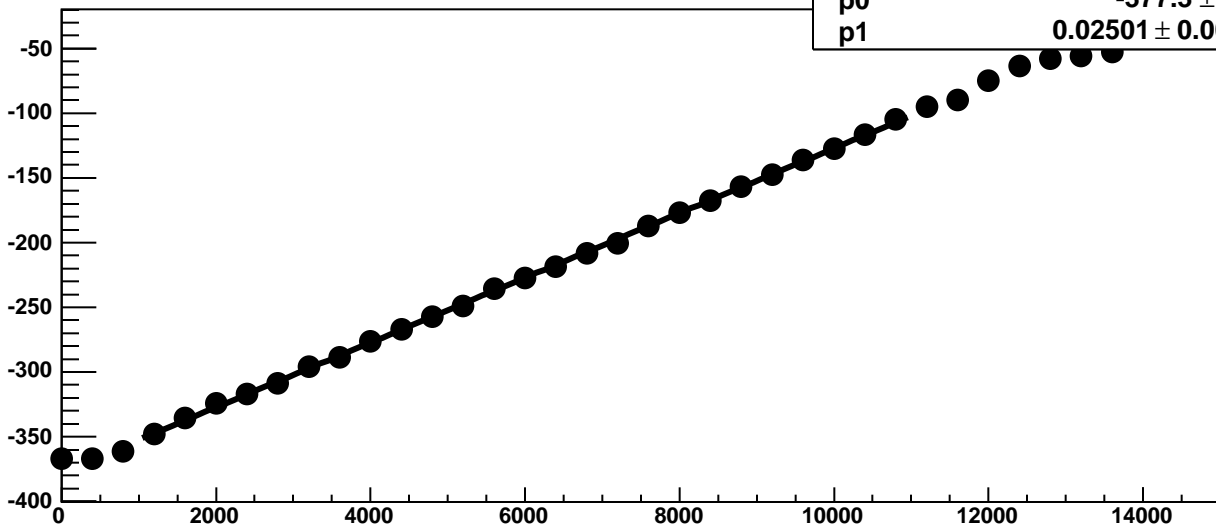
Chip 5, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC

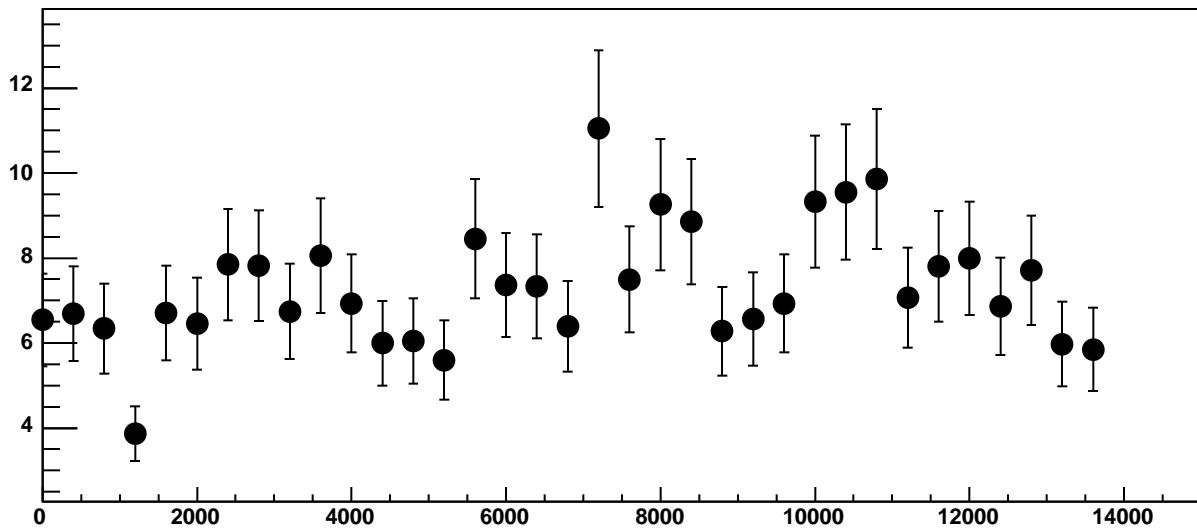


Chip 5, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

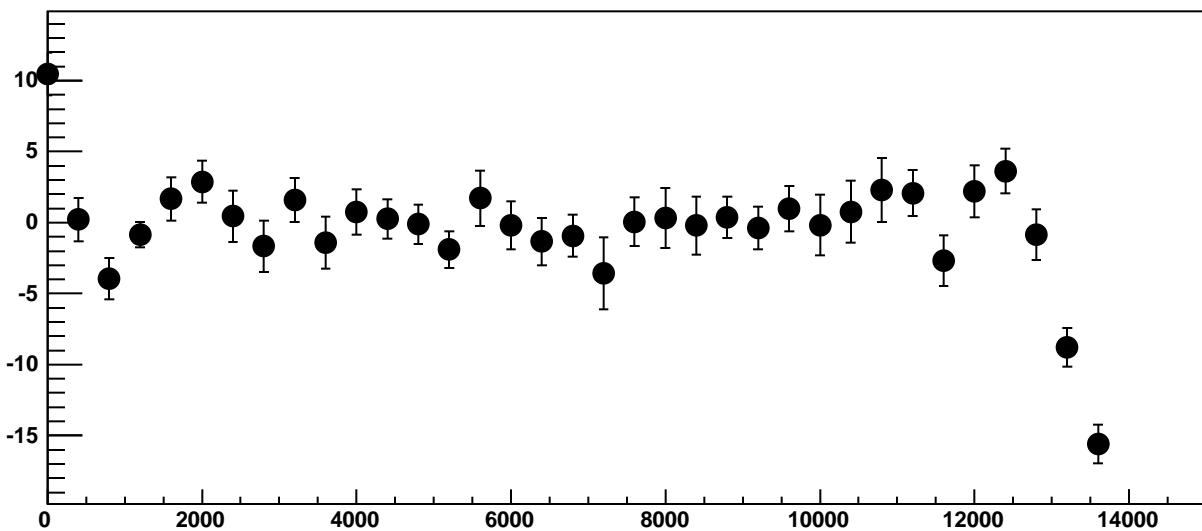


χ^2 / ndf 16.4 / 23
p0 -377.3 ± 0.6536
p1 0.02501 ± 0.0001088

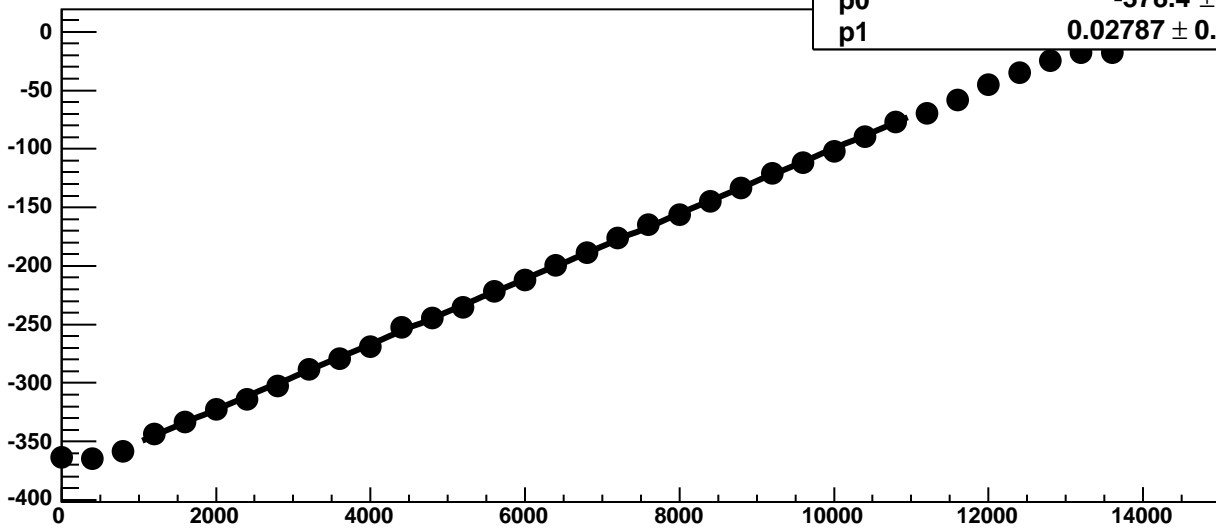
Chip 5, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



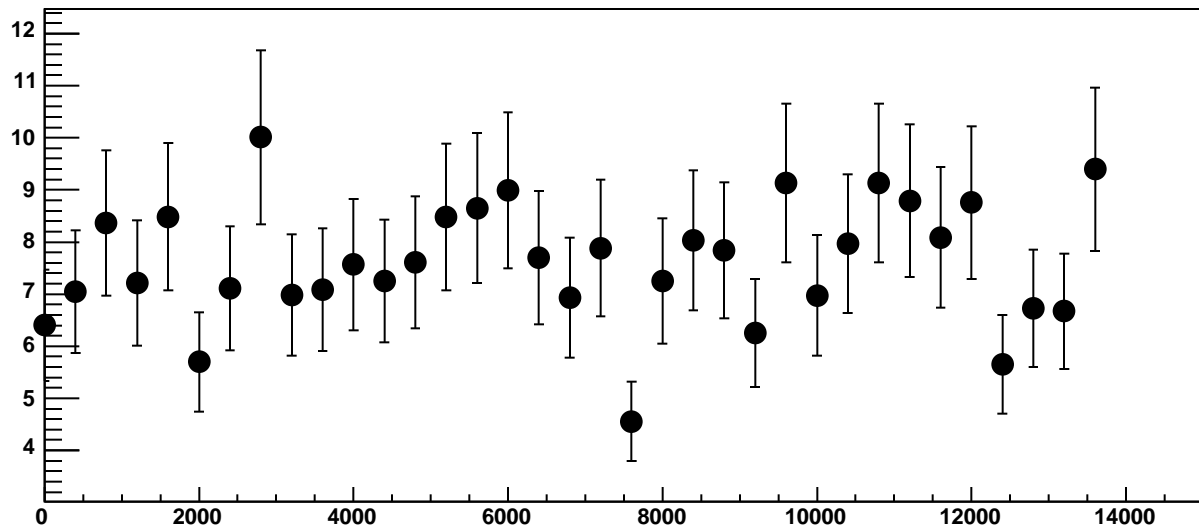
Chip 5, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



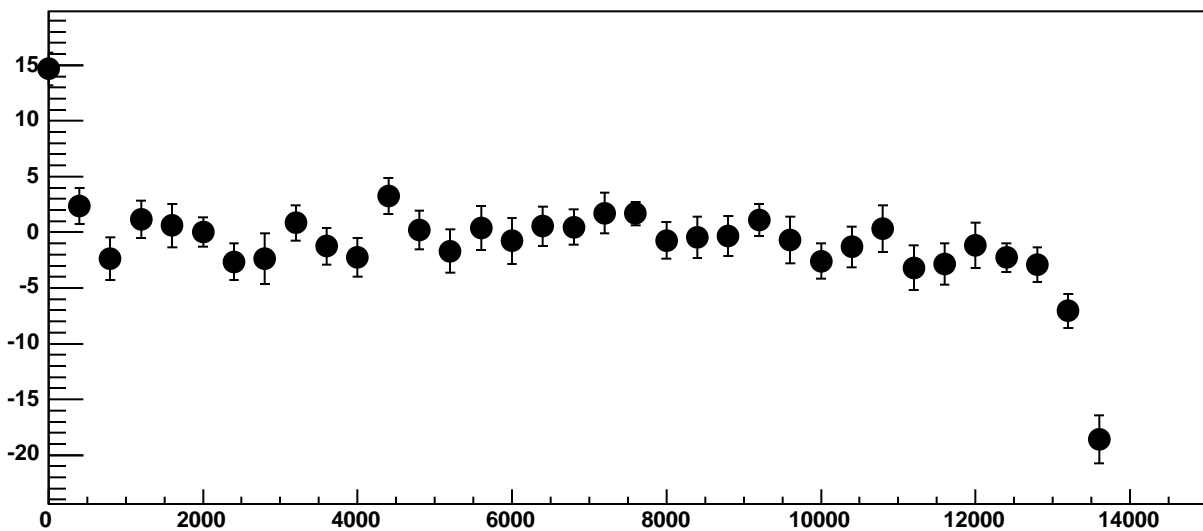
Chip 5, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



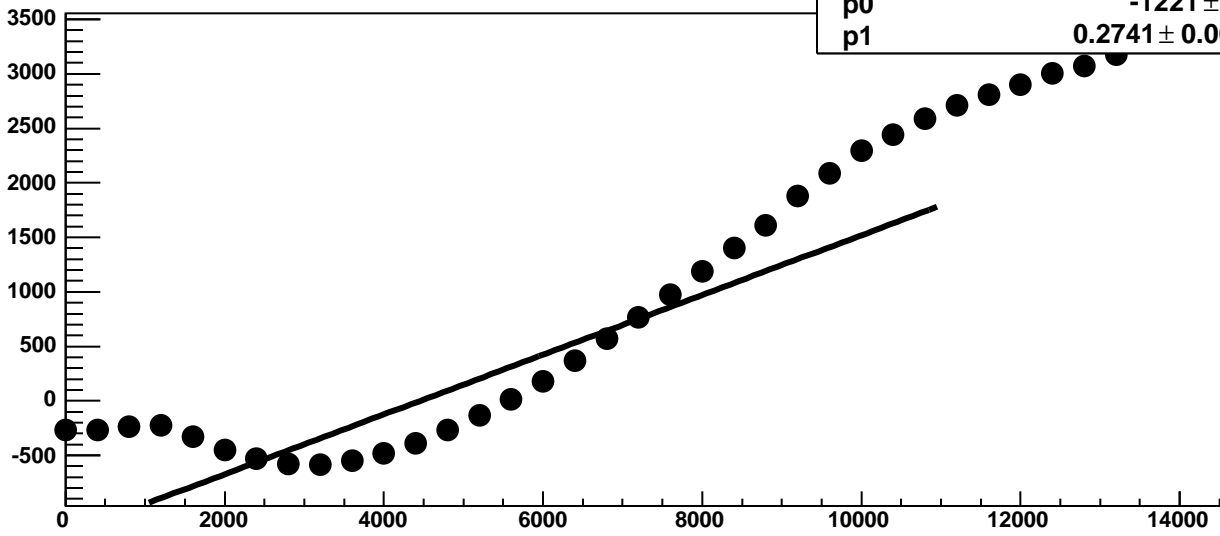
Chip 5, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

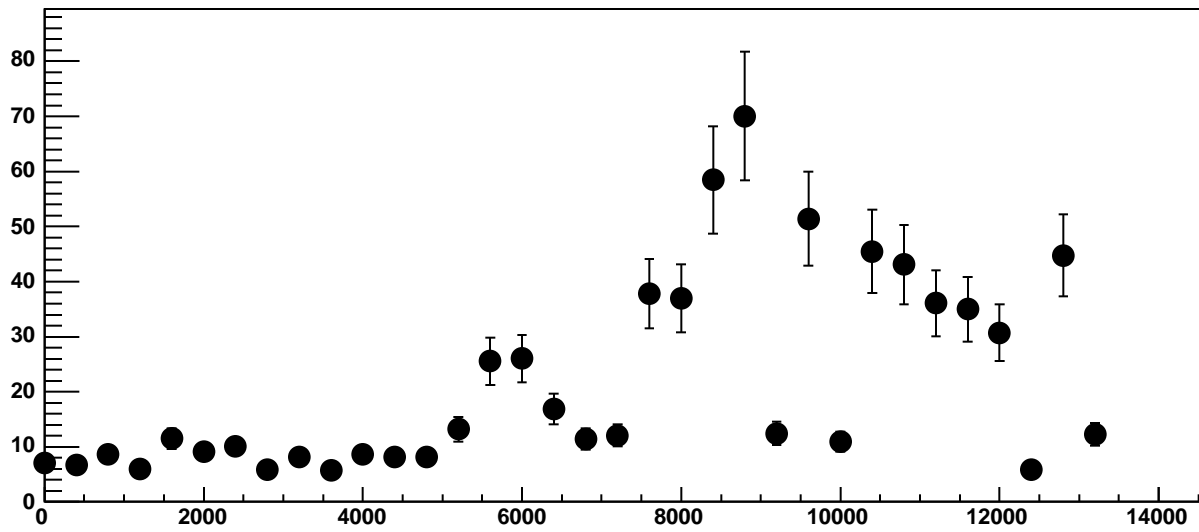


Chip 5, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC

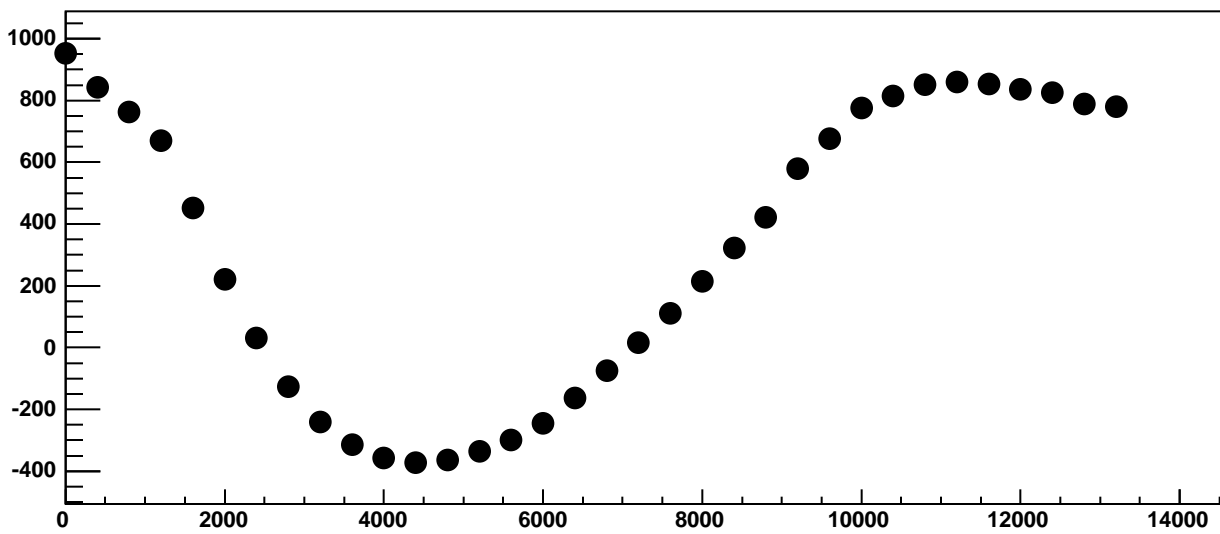


χ^2 / ndf 6.417e+05 / 23
p0 -1221 ± 0.9823
p1 0.2741 ± 0.0002157

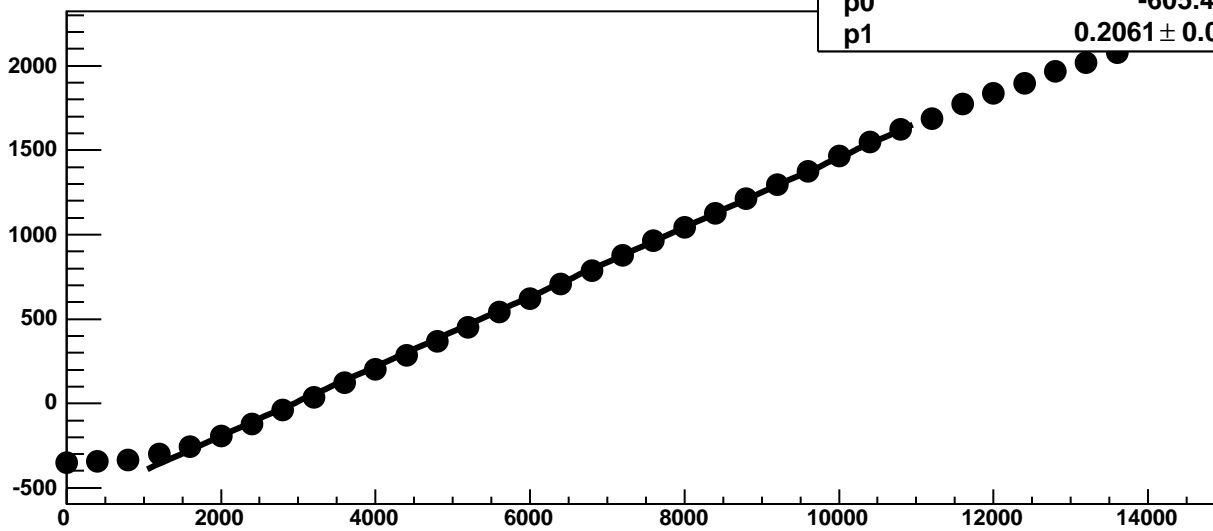
Chip 5, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



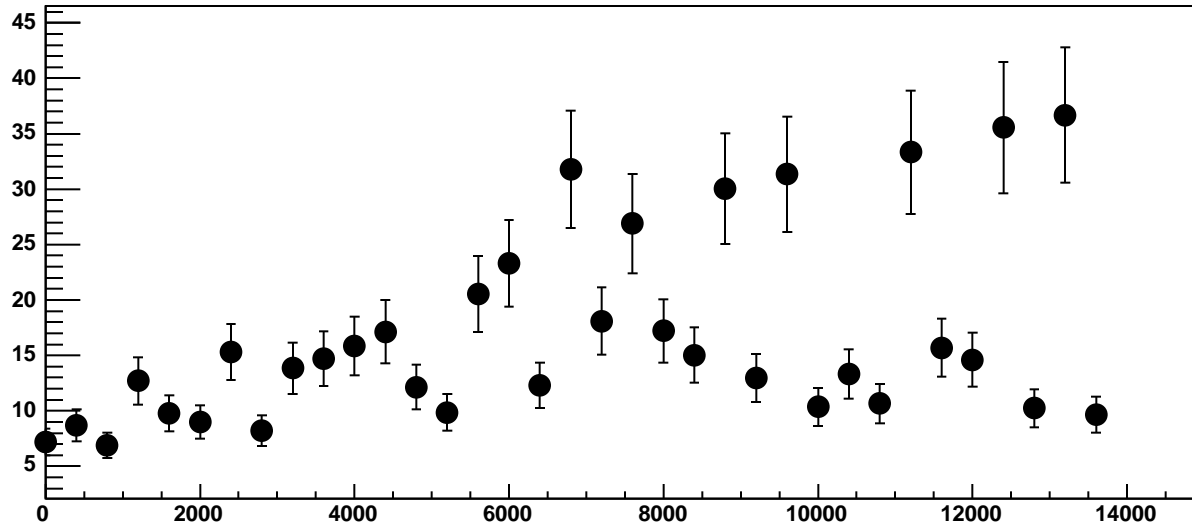
Chip 5, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC



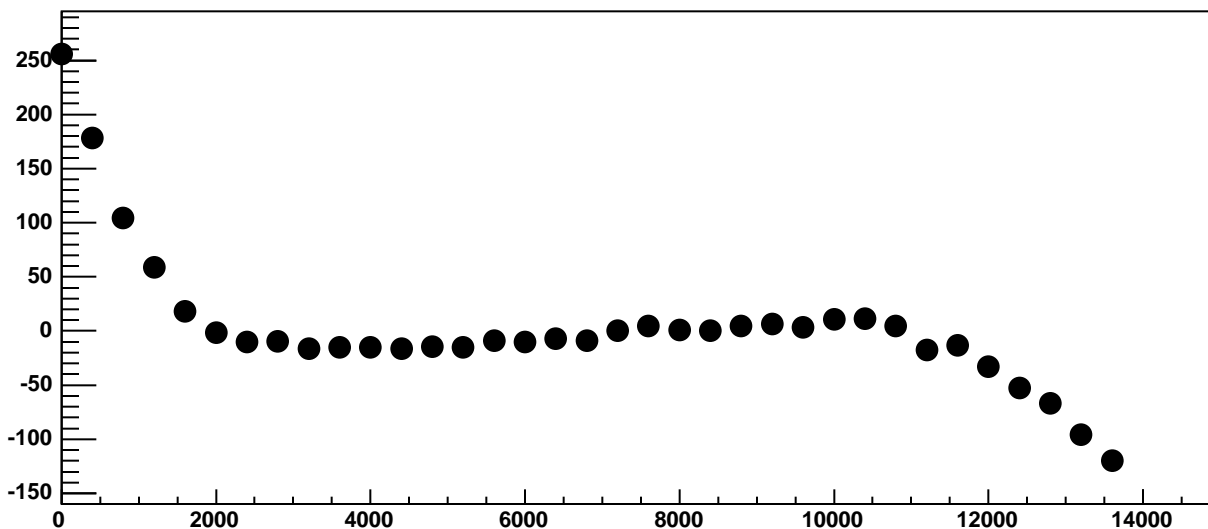
Chip 5, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



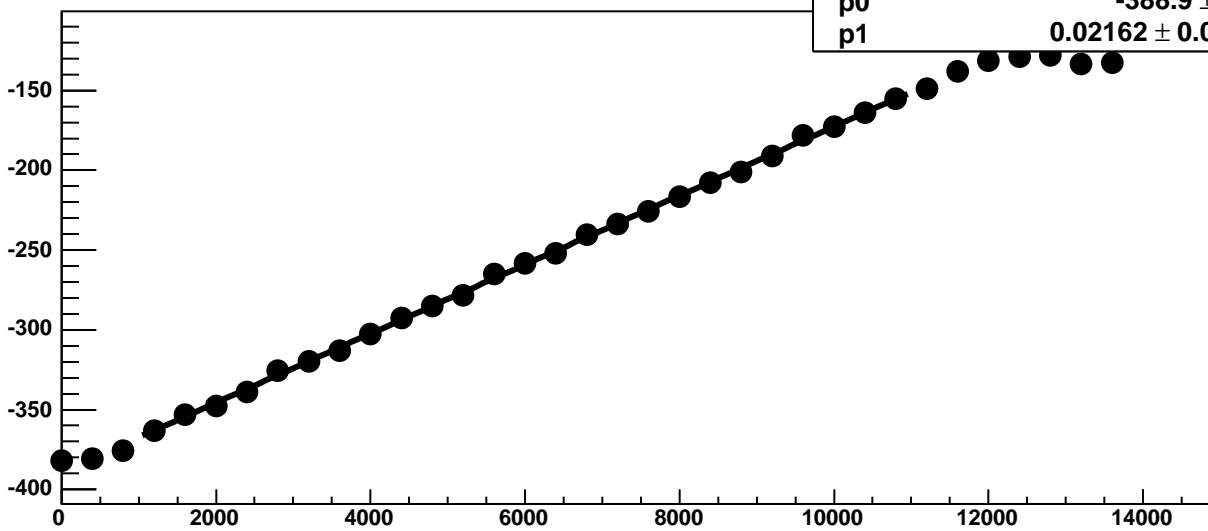
Chip 5, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



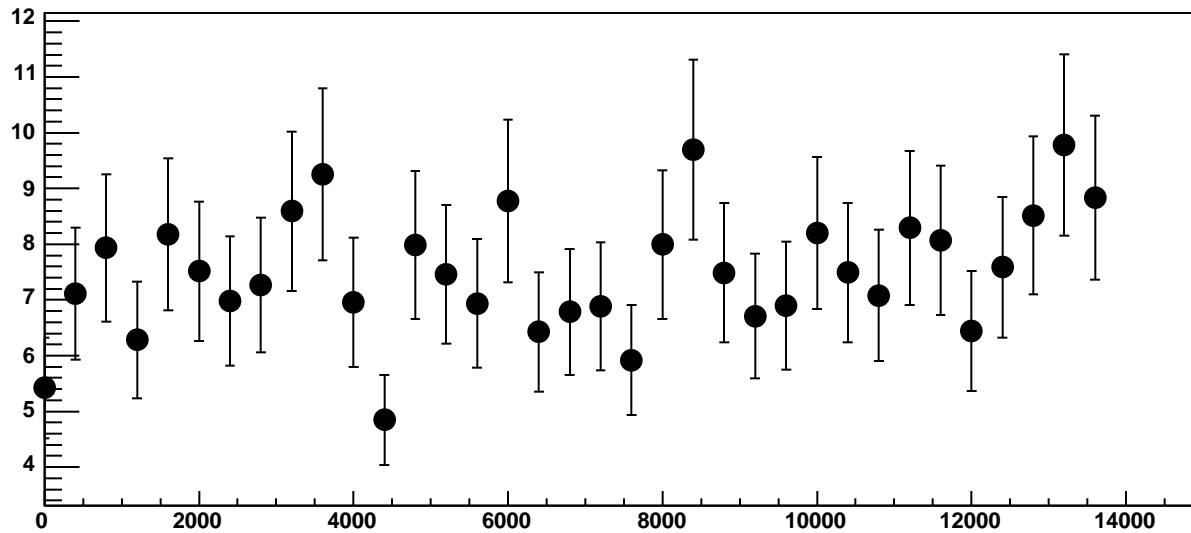
Chip 5, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



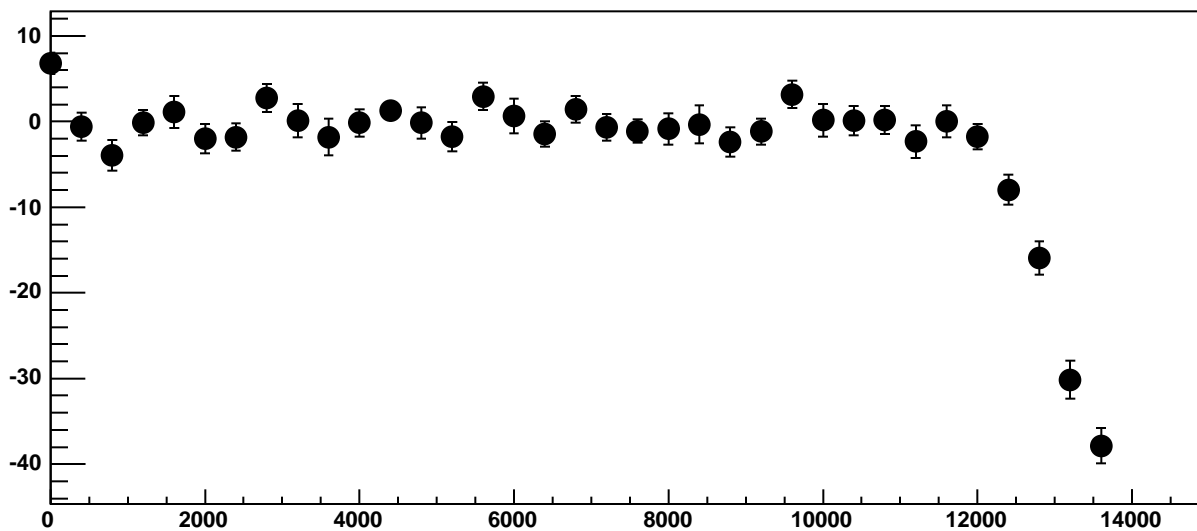
Chip 5, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



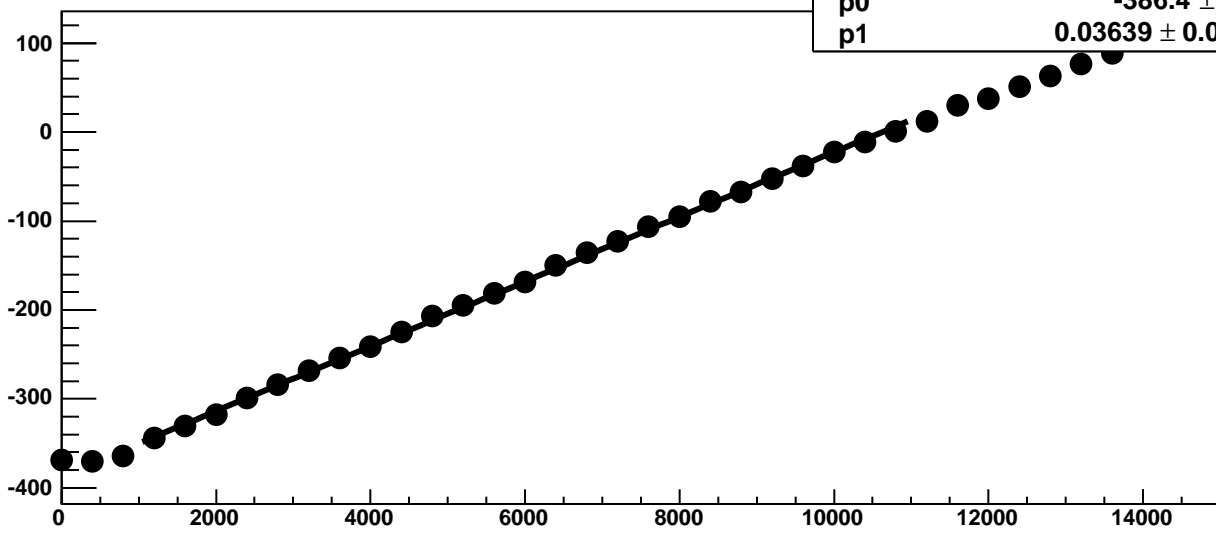
Chip 5, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



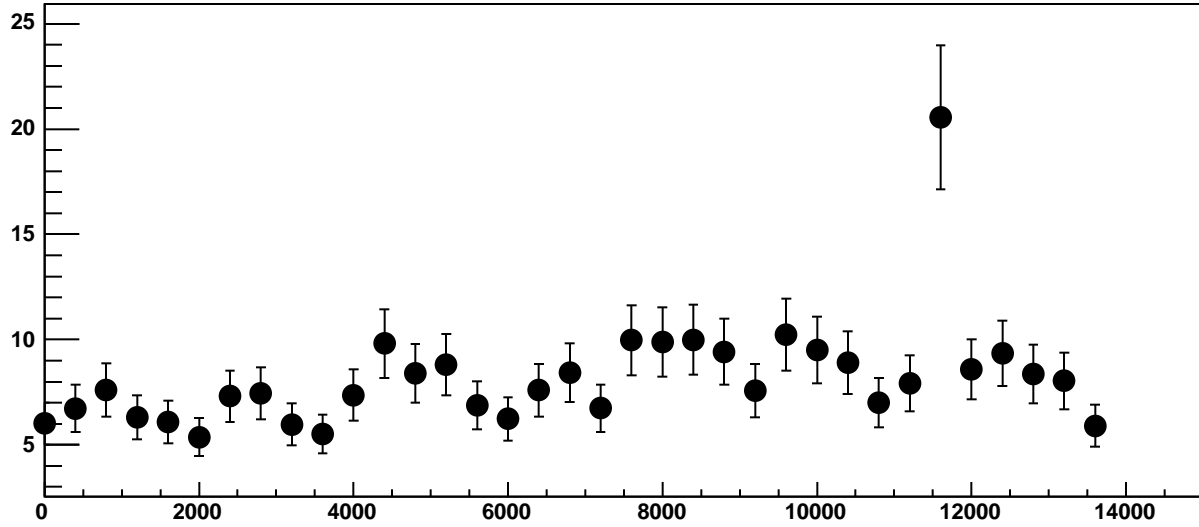
Chip 5, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



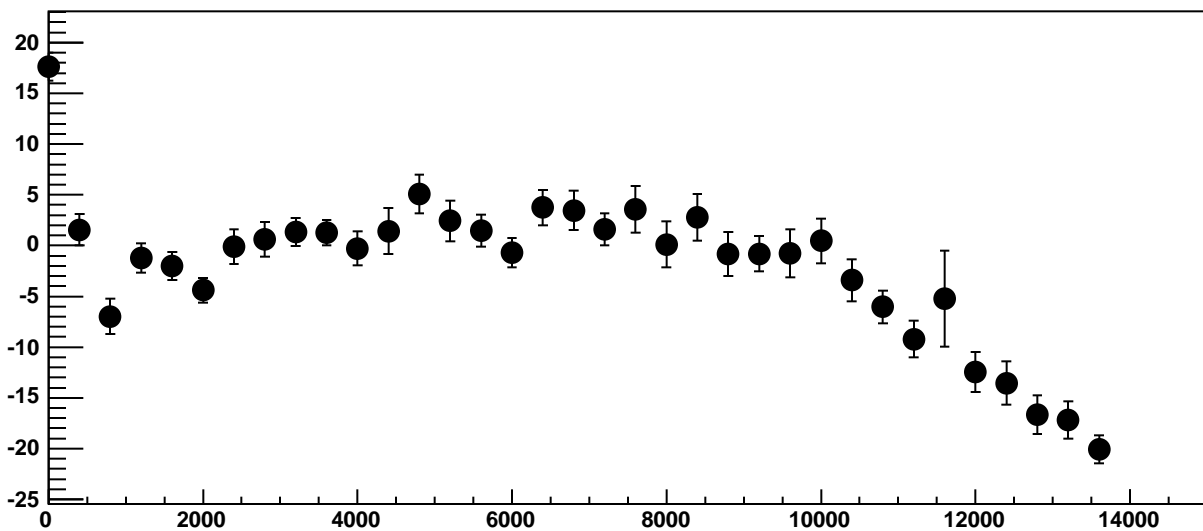
Chip 5, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC



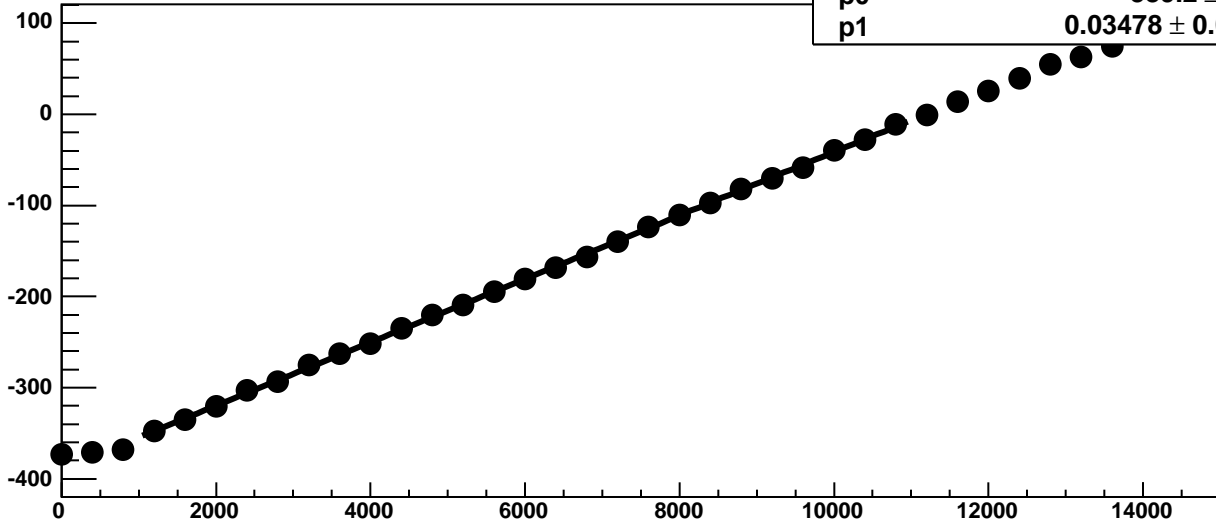
Chip 5, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC

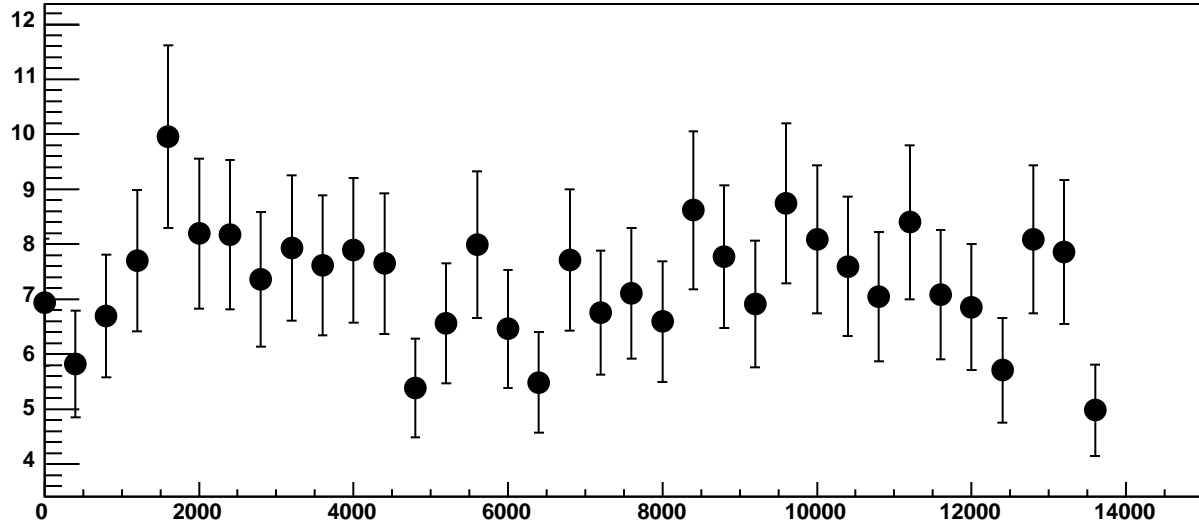


Chip 5, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

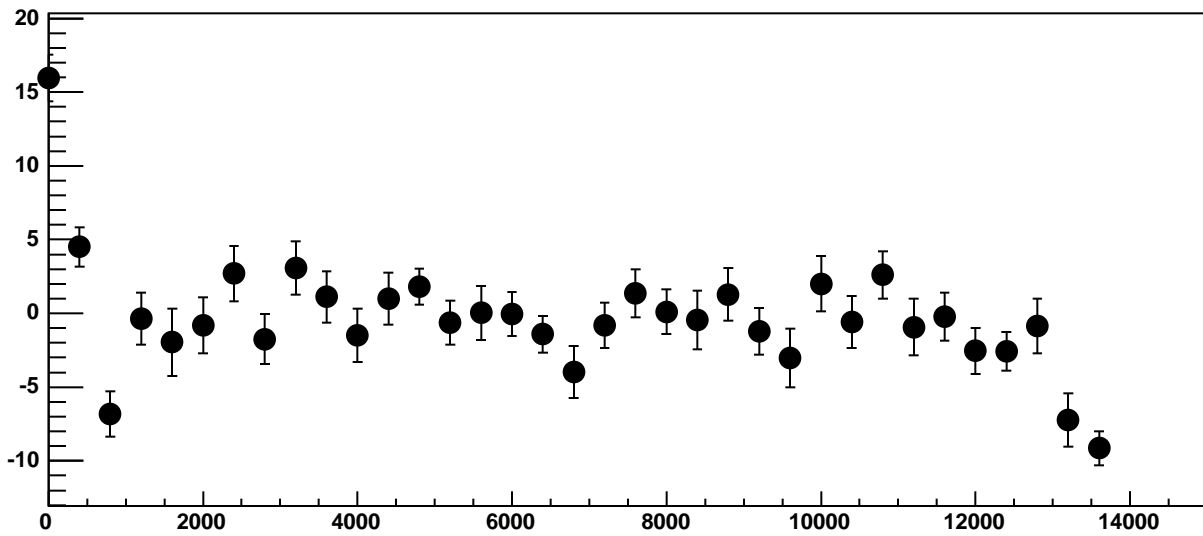


χ^2 / ndf 25.26 / 23
p0 -389.2 ± 0.8271
p1 0.03478 ± 0.000124

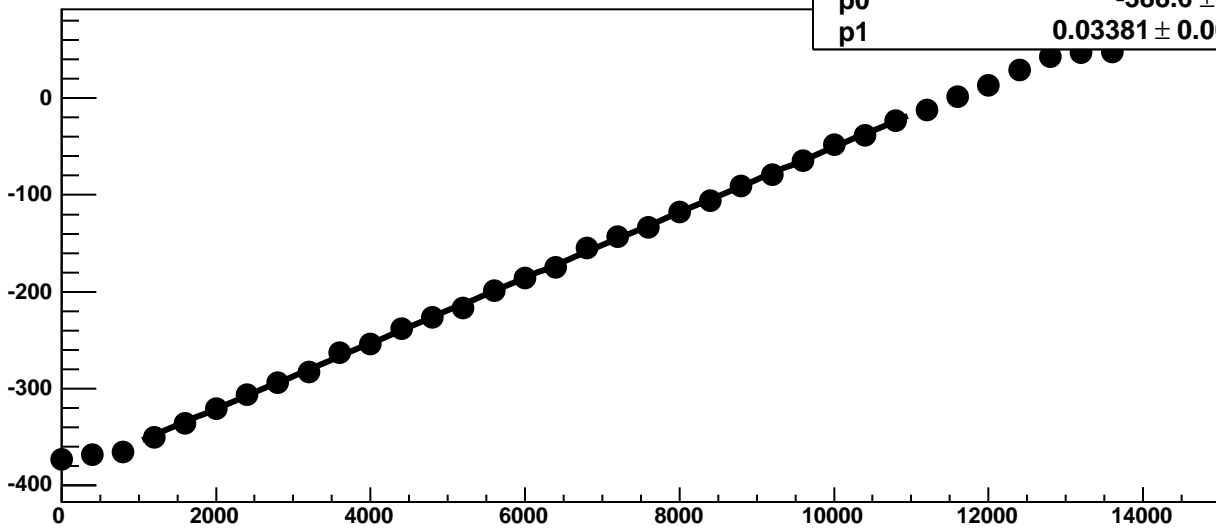
Chip 5, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

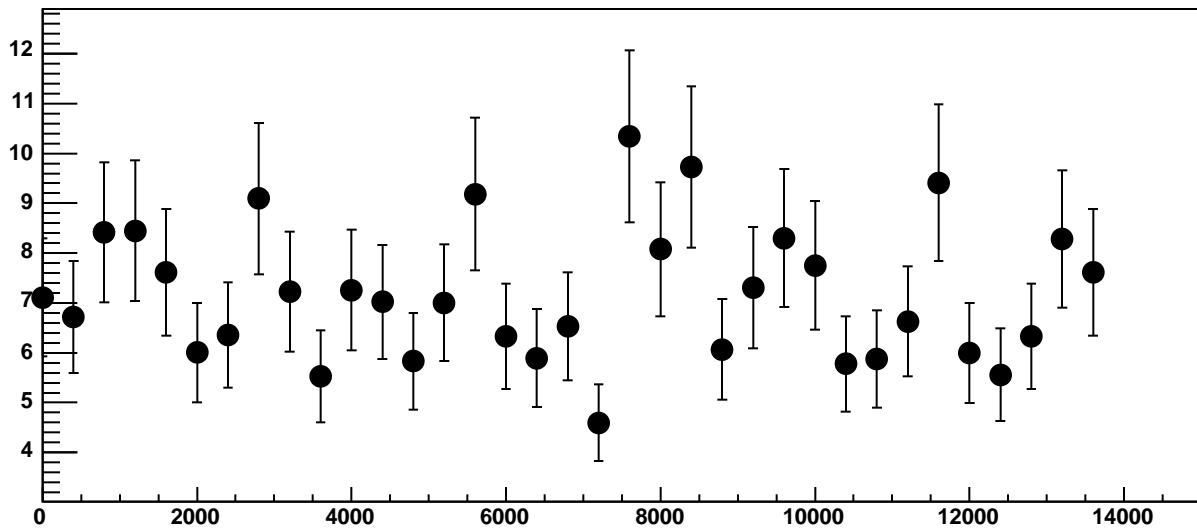


Chip 5, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

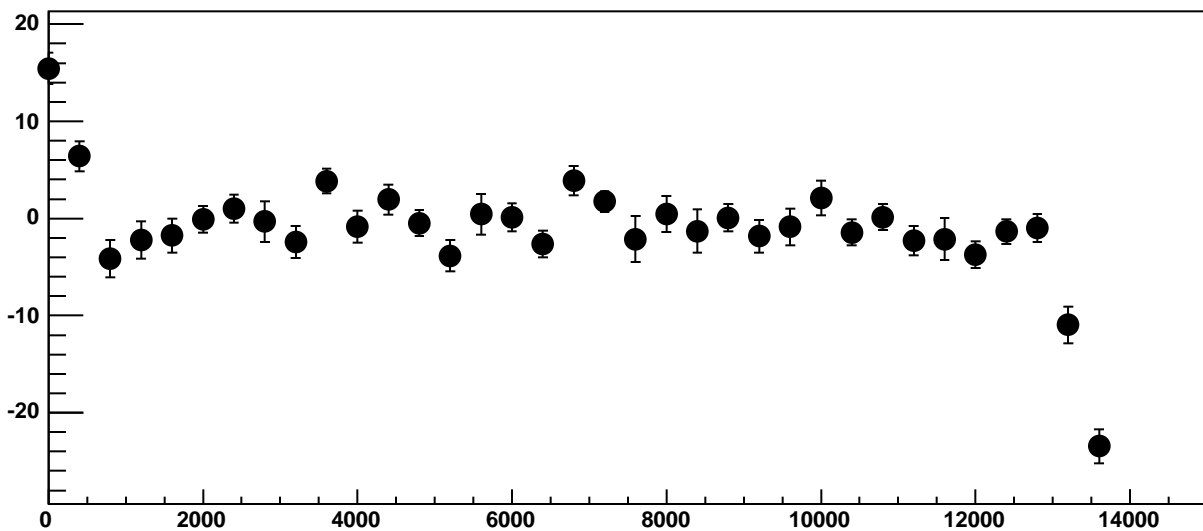


χ^2 / ndf 40.33 / 23
p0 -388.6 ± 0.7329
p1 0.03381 ± 0.0001093

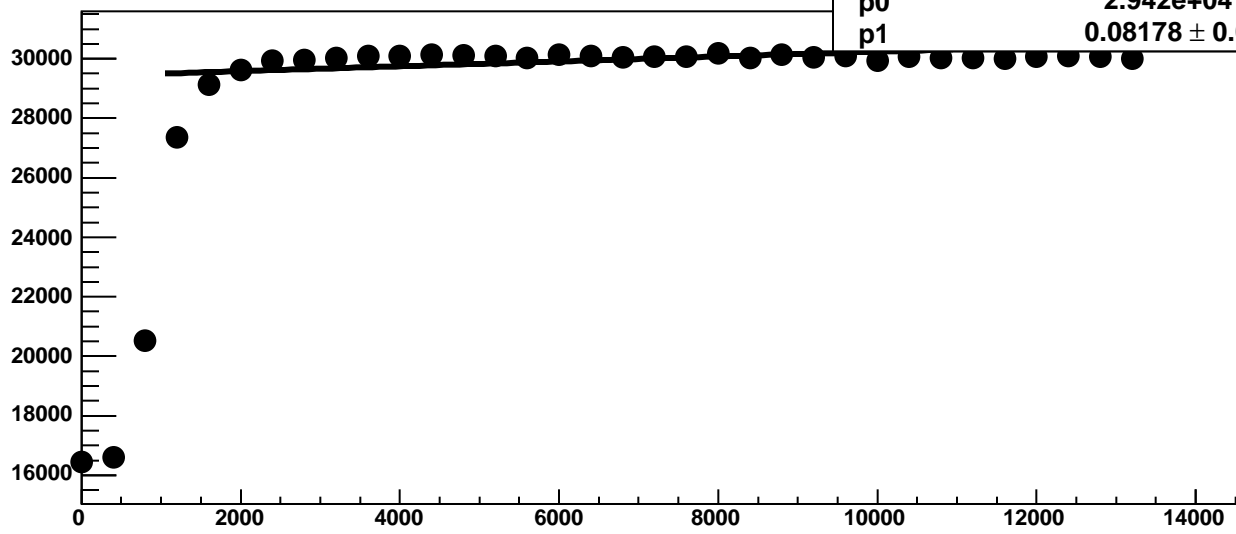
Chip 5, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



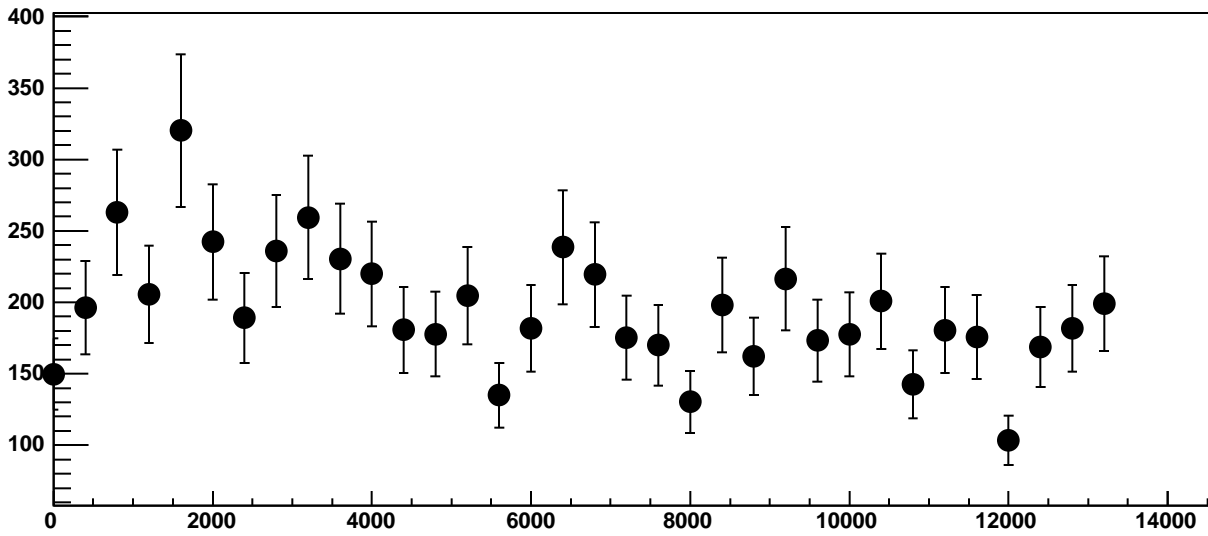
Chip 5, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



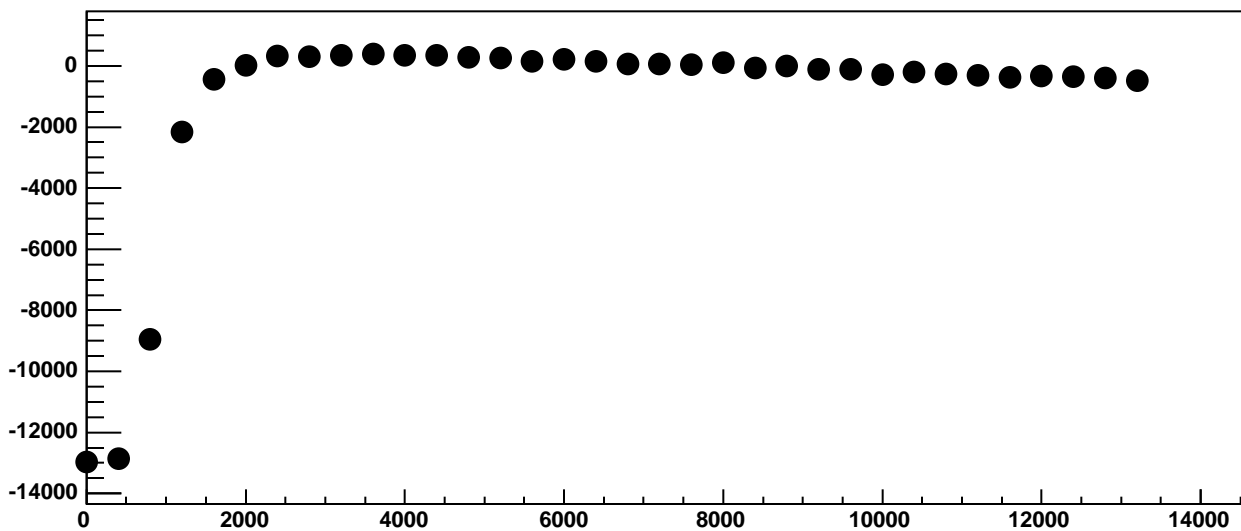
Chip 5, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC



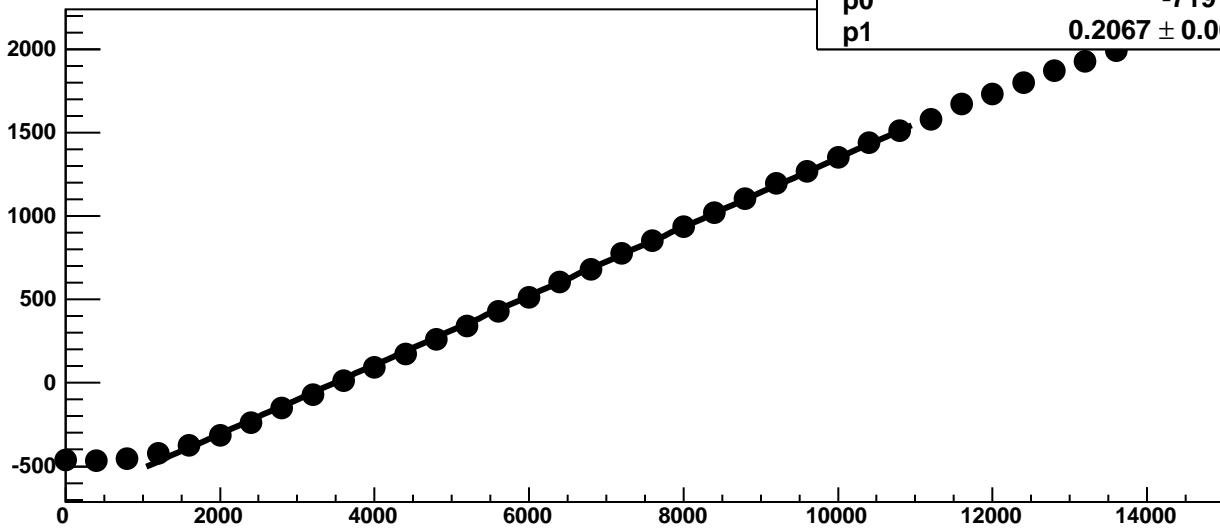
Chip 5, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC

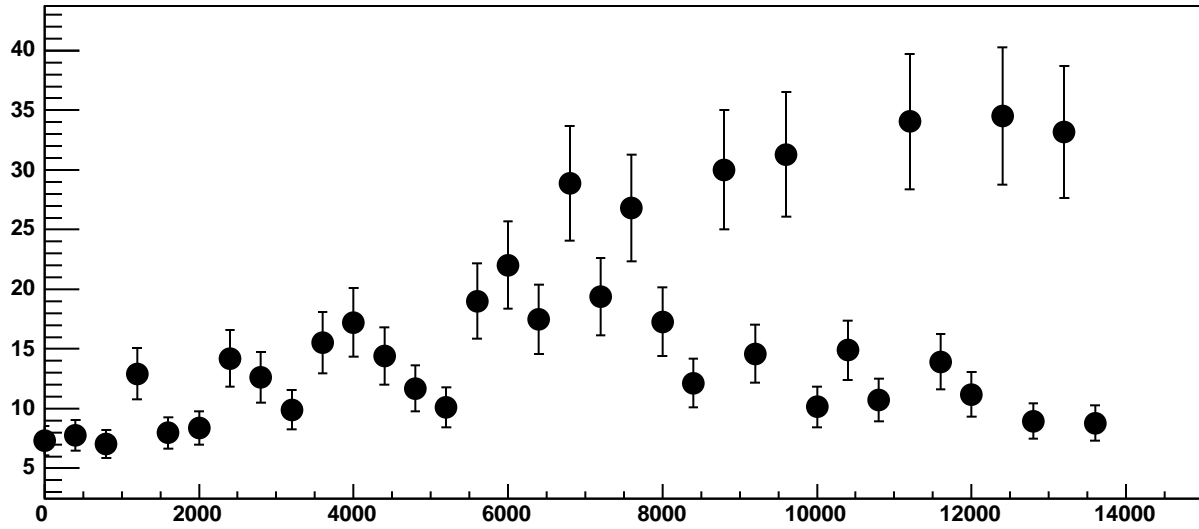


Chip 5, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

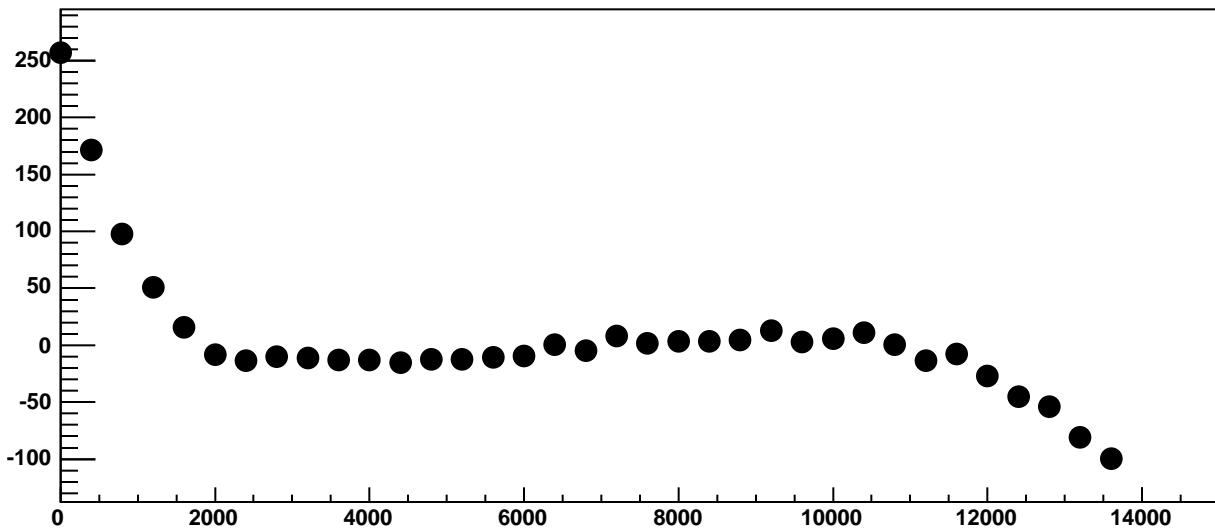


χ^2 / ndf 580.6 / 23
p0 -719 ± 1.171
p1 0.2067 ± 0.0001917

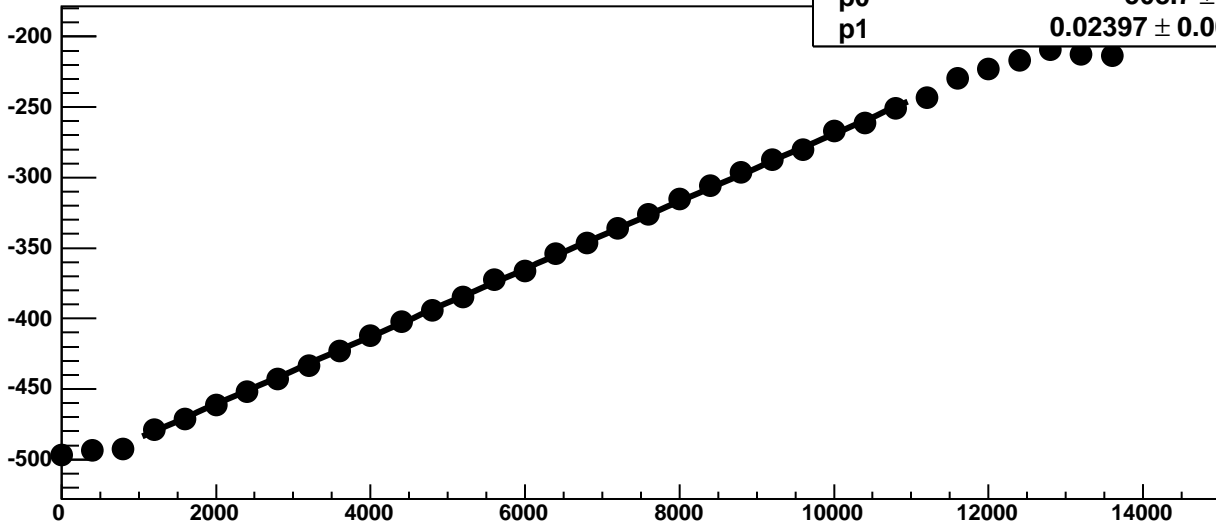
Chip 5, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

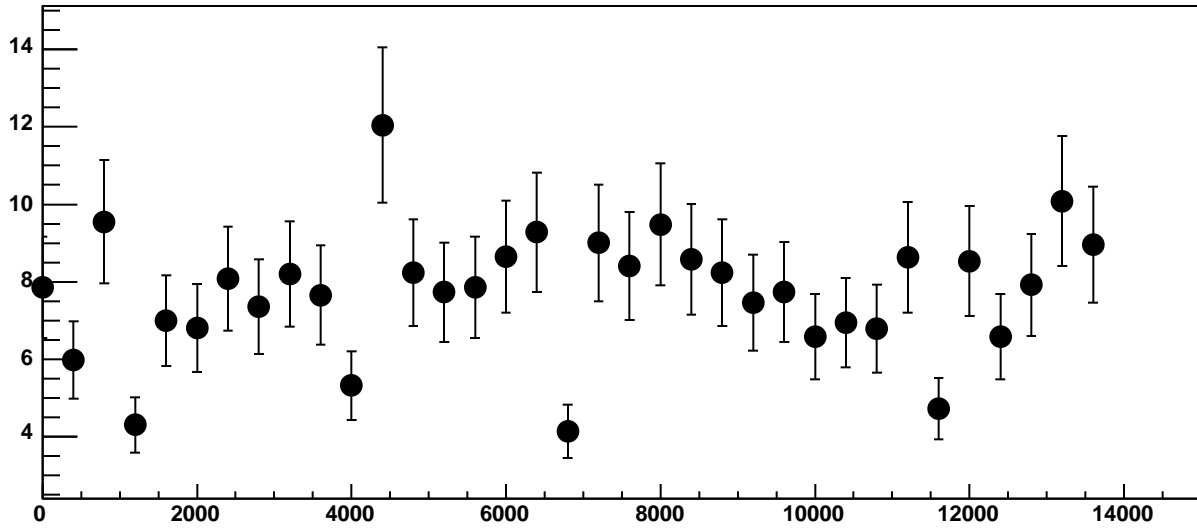


Chip 5, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

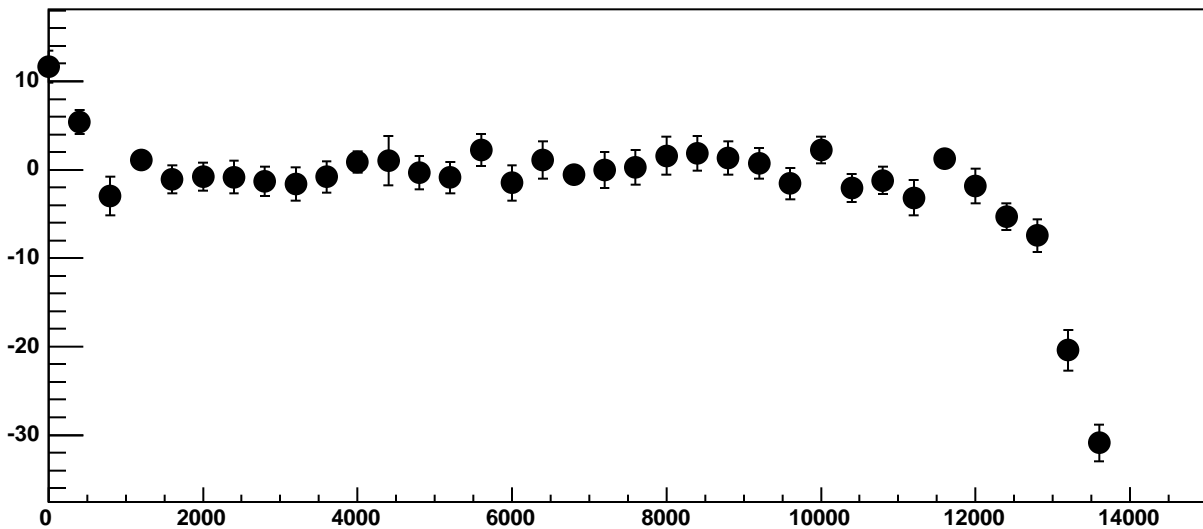


χ^2 / ndf 14.69 / 23
p0 -508.7 ± 0.6807
p1 0.02397 ± 0.0001054

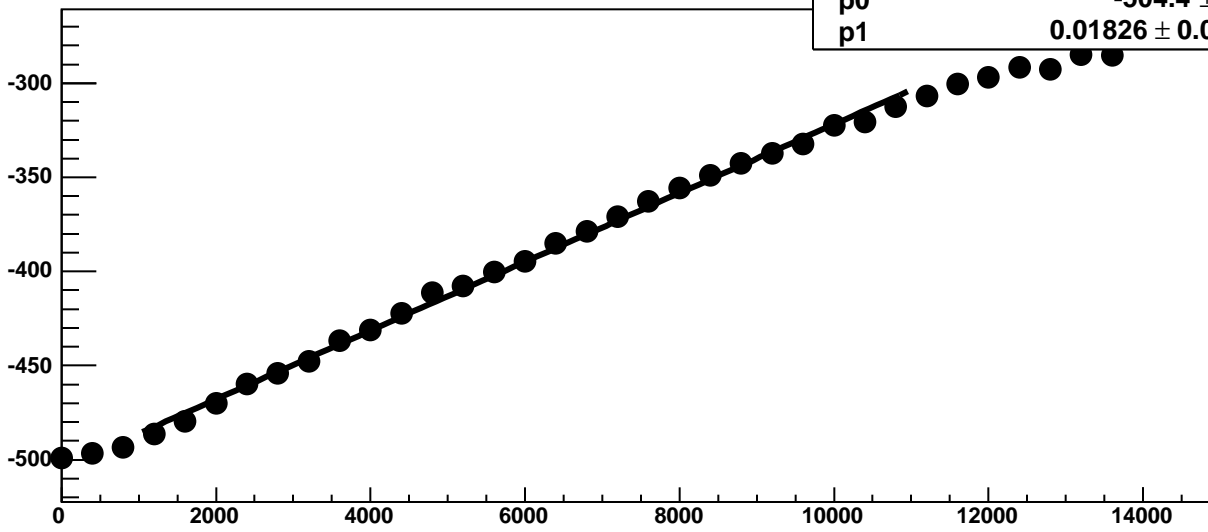
Chip 5, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



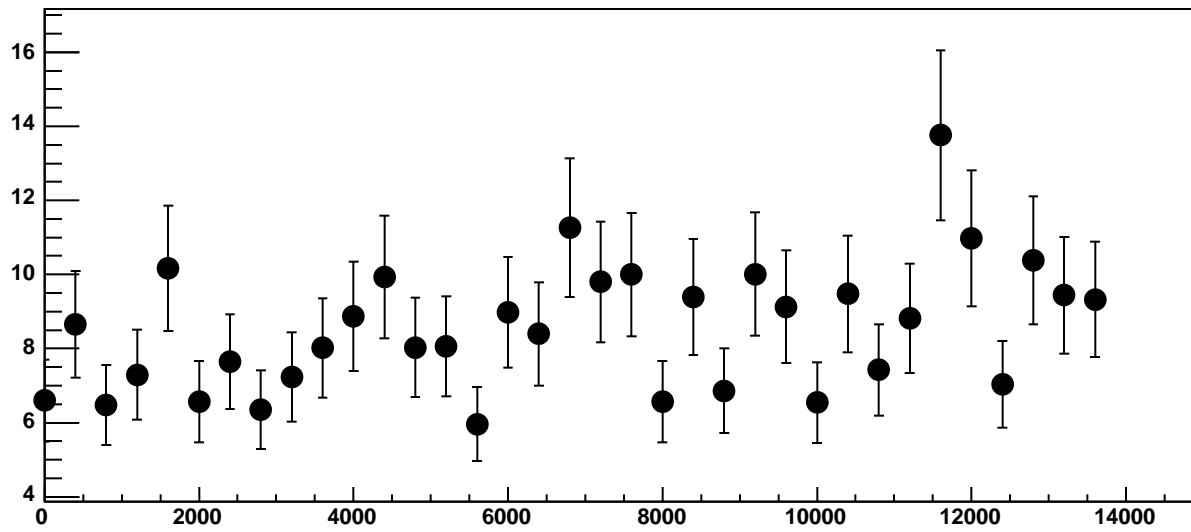
Chip 5, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



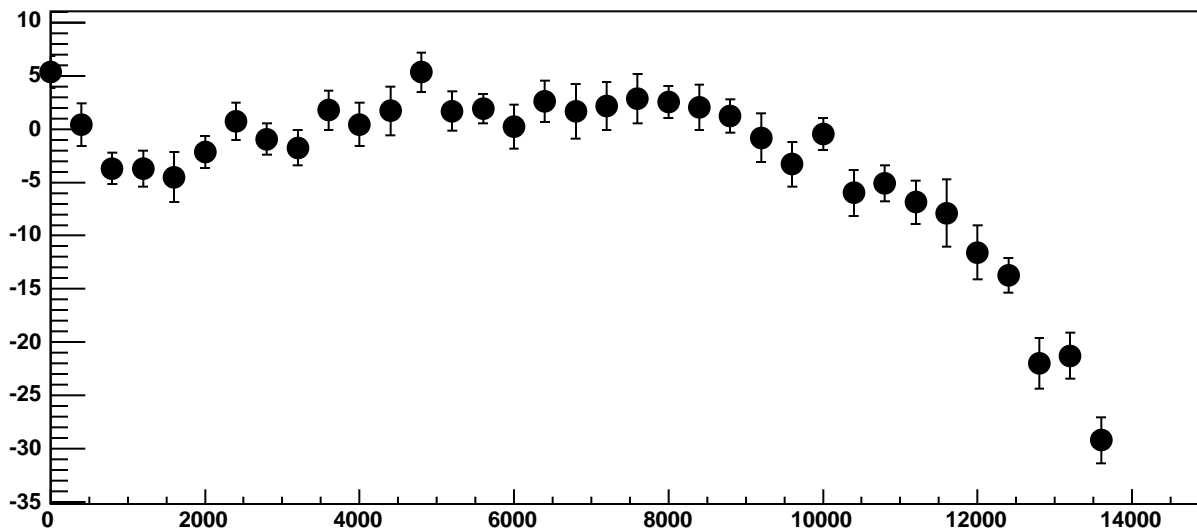
Chip 5, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



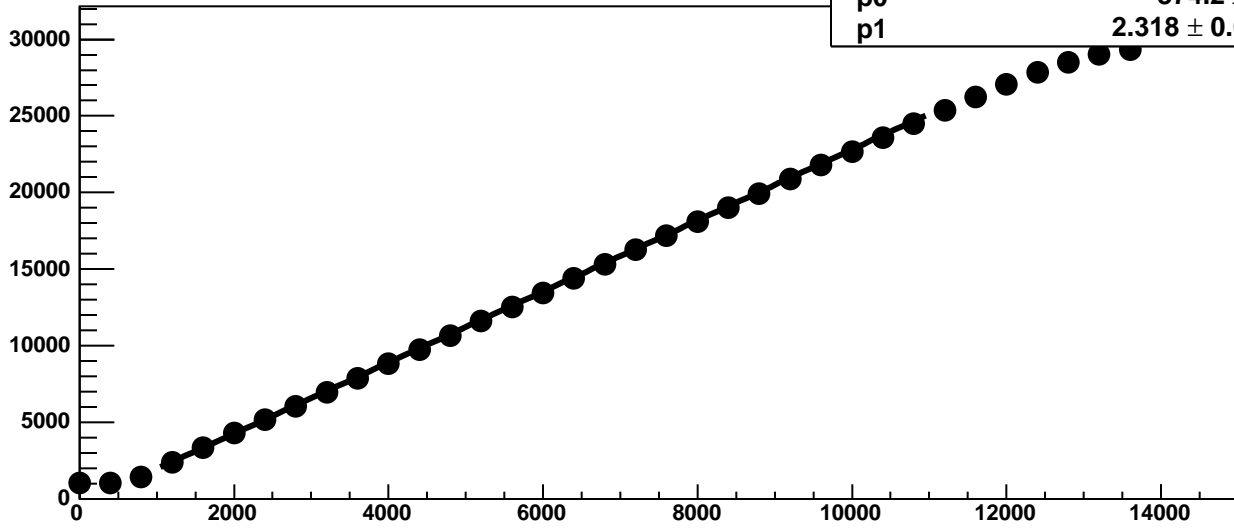
Chip 5, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



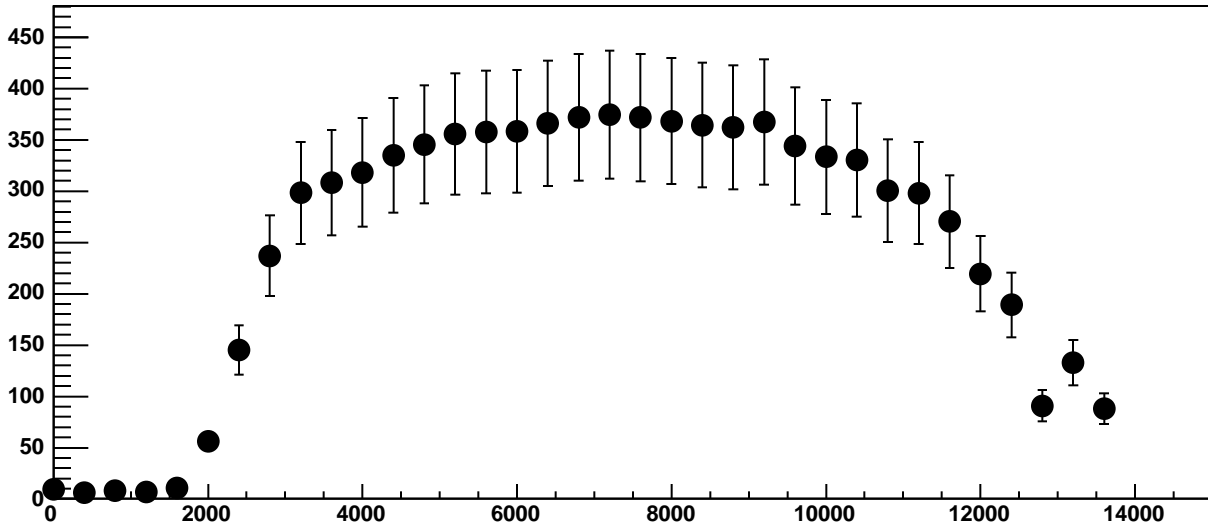
Chip 5, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



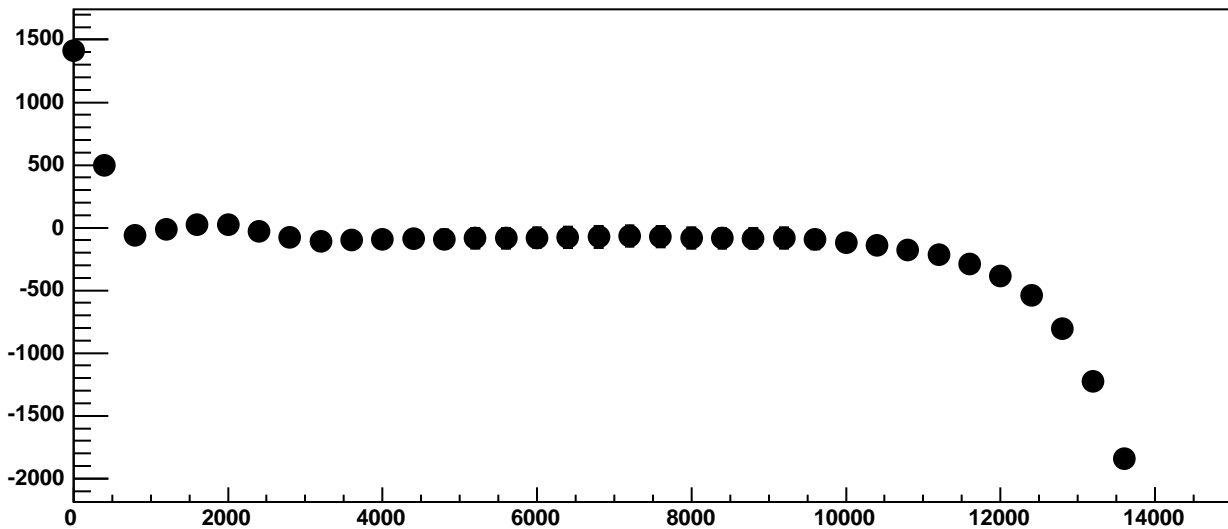
Chip 5, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC



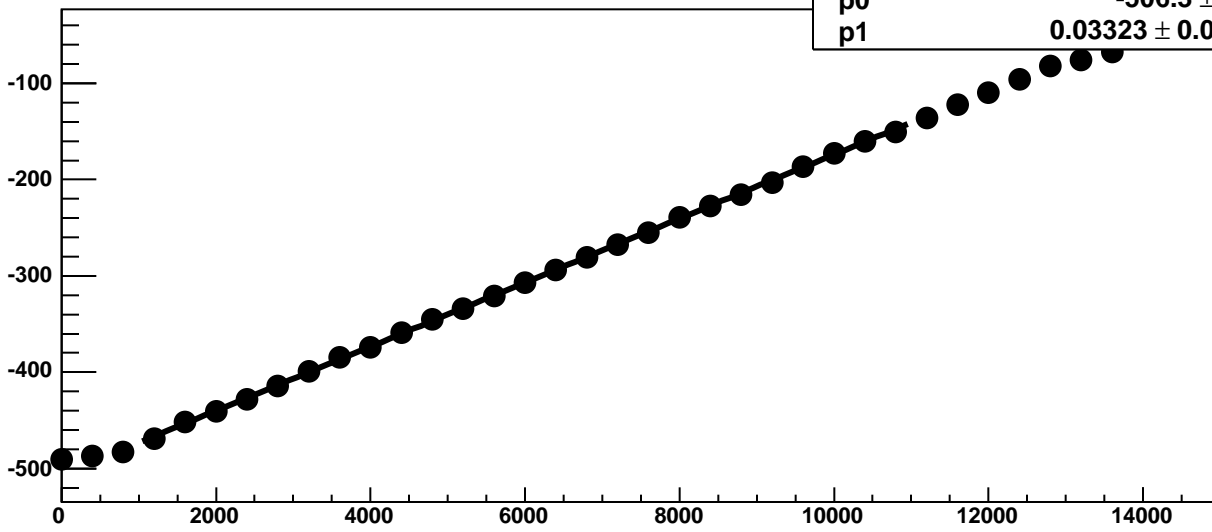
Chip 5, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC

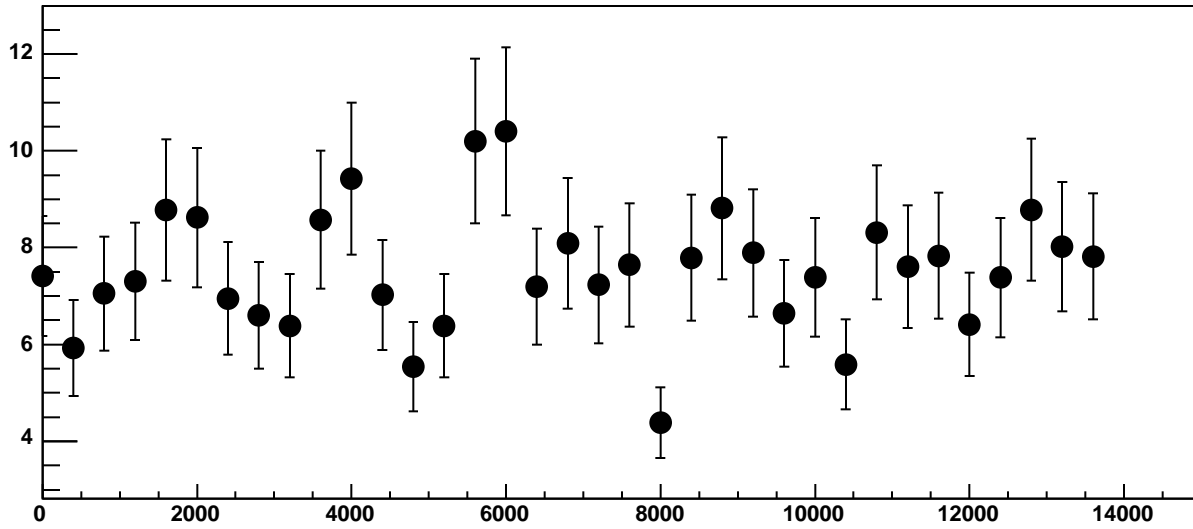


Chip 5, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

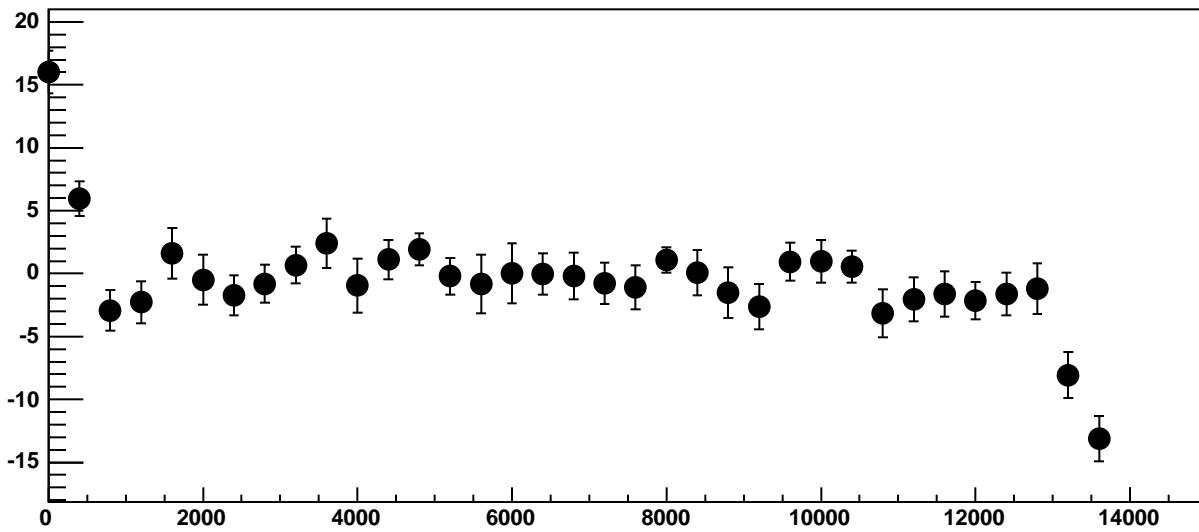


χ^2 / ndf 16.81 / 23
p0 -506.3 ± 0.7819
p1 0.03323 ± 0.0001146

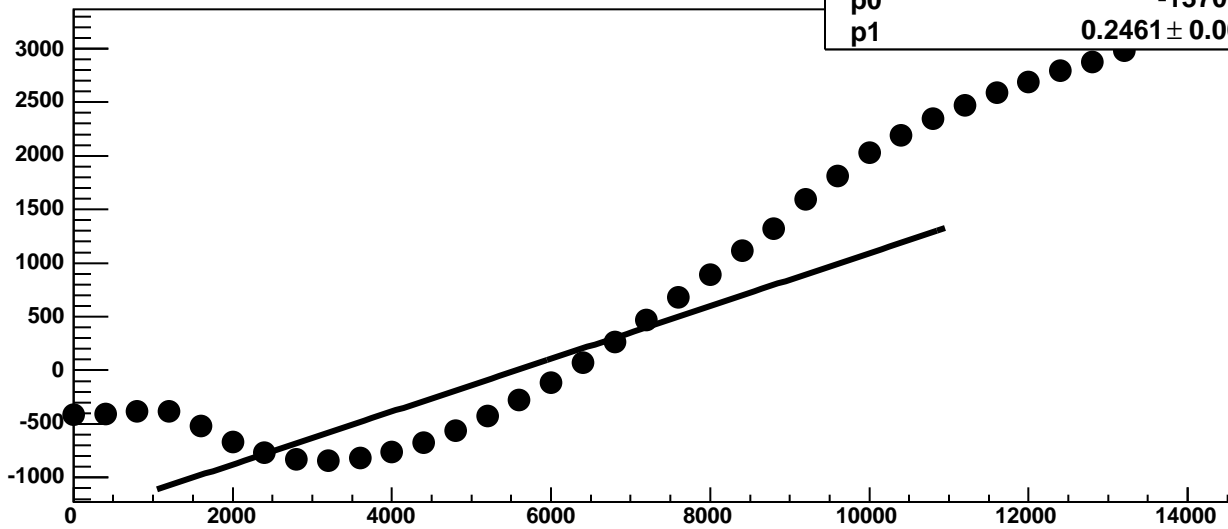
Chip 5, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



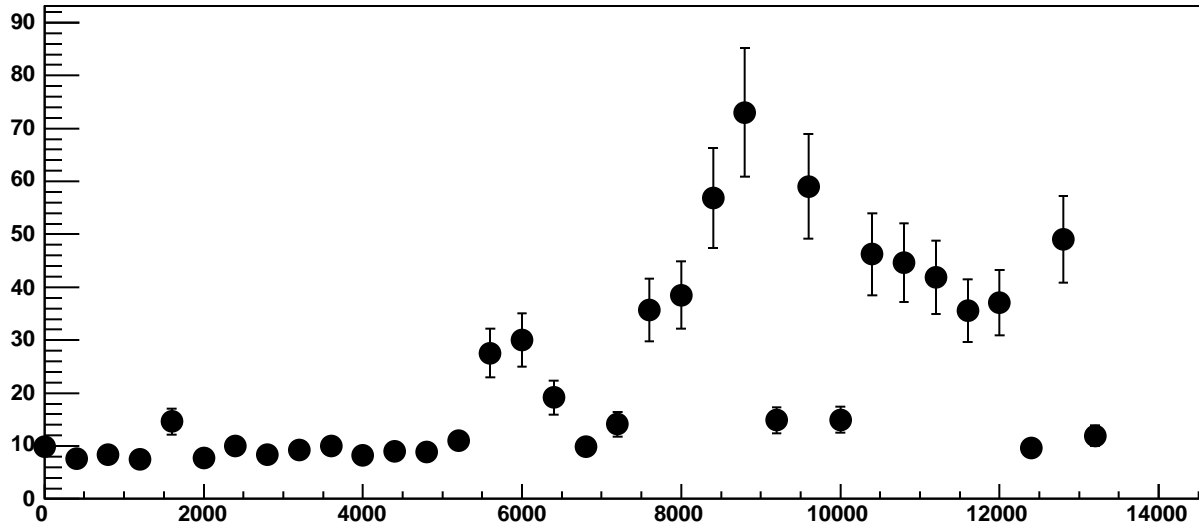
Chip 5, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



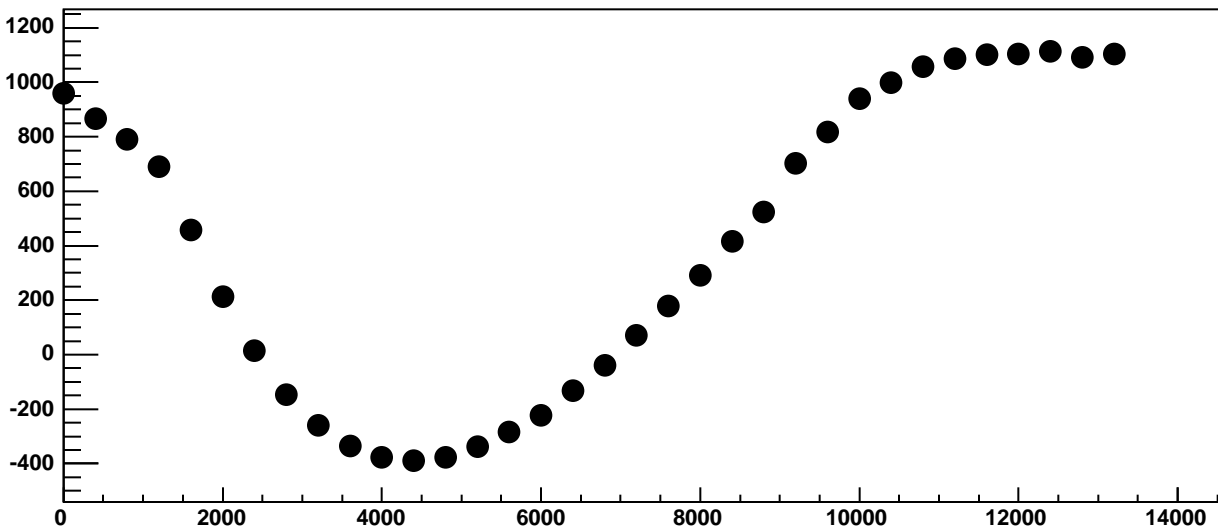
Chip 5, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



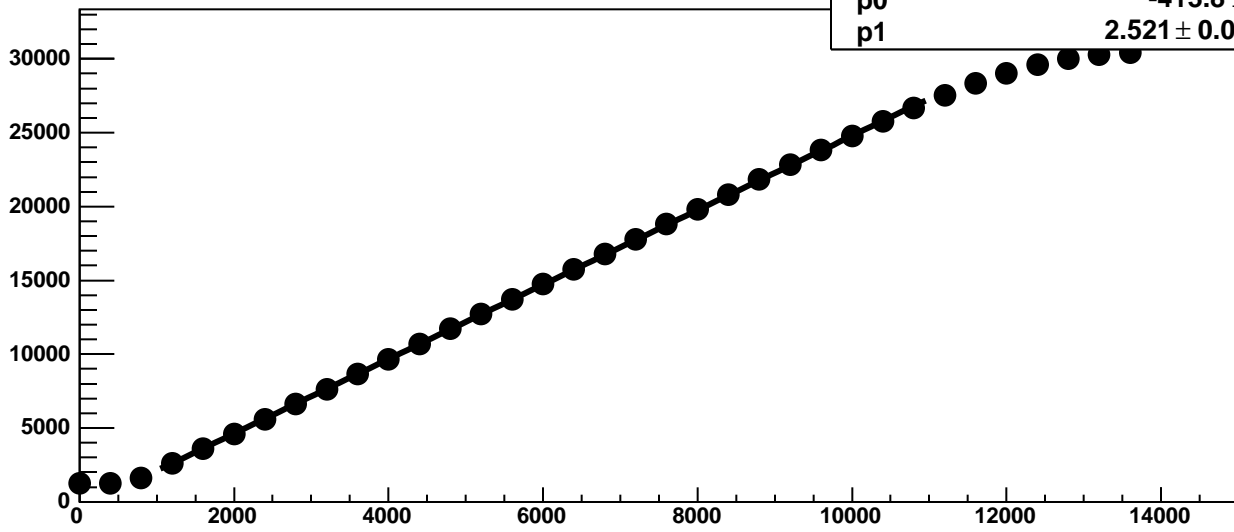
Chip 5, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



Chip 5, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

6914 / 23

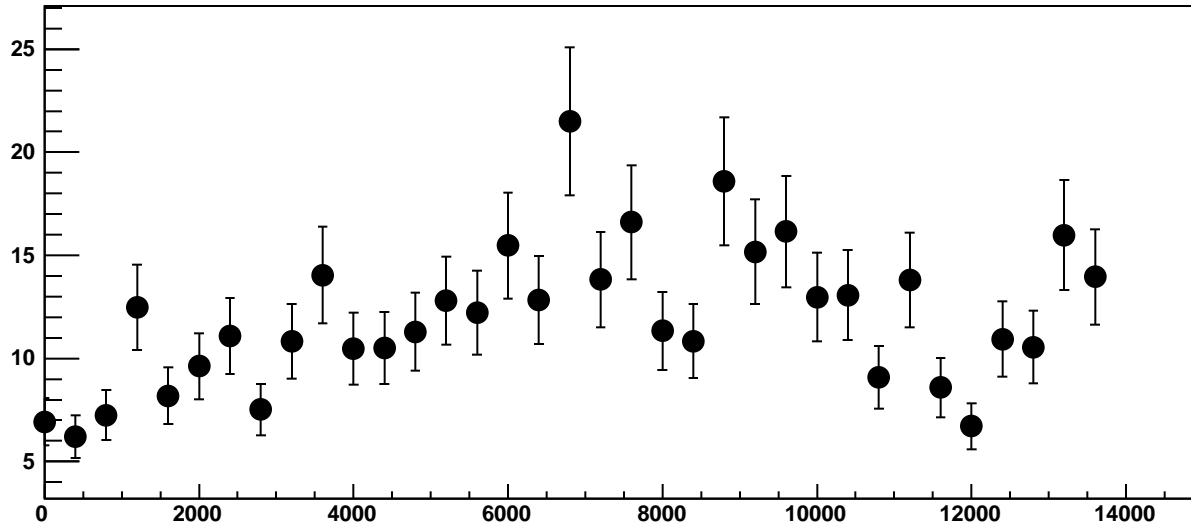
p0

-413.8 ± 1.087

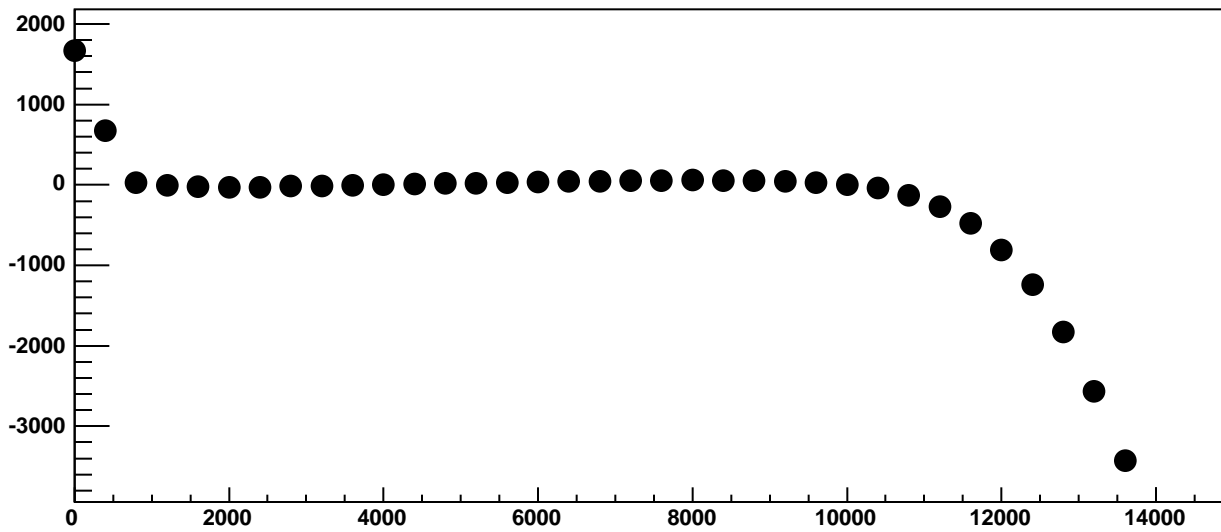
p1

2.521 ± 0.0001761

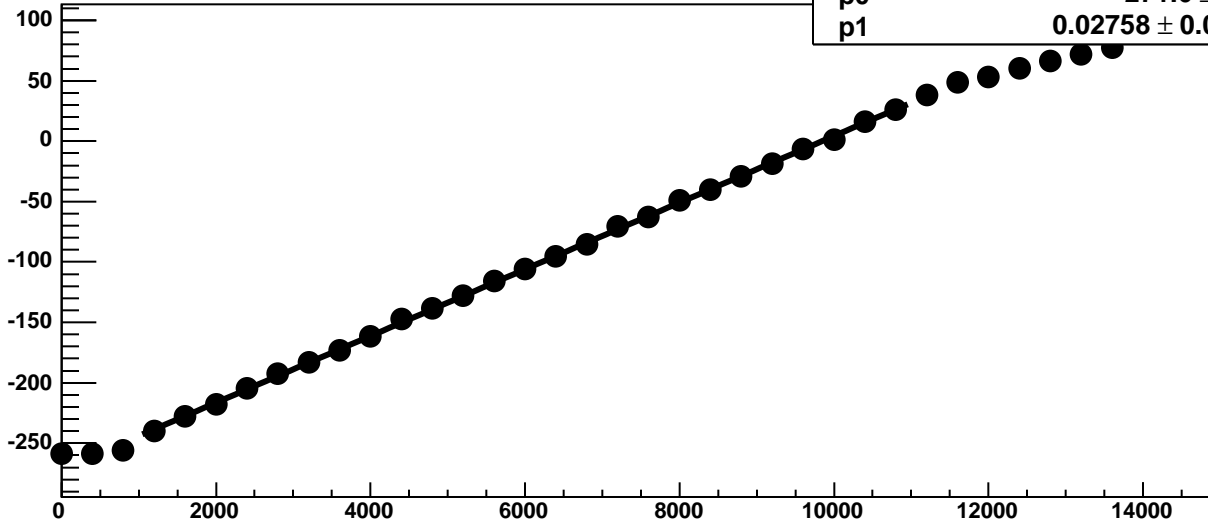
Chip 5, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



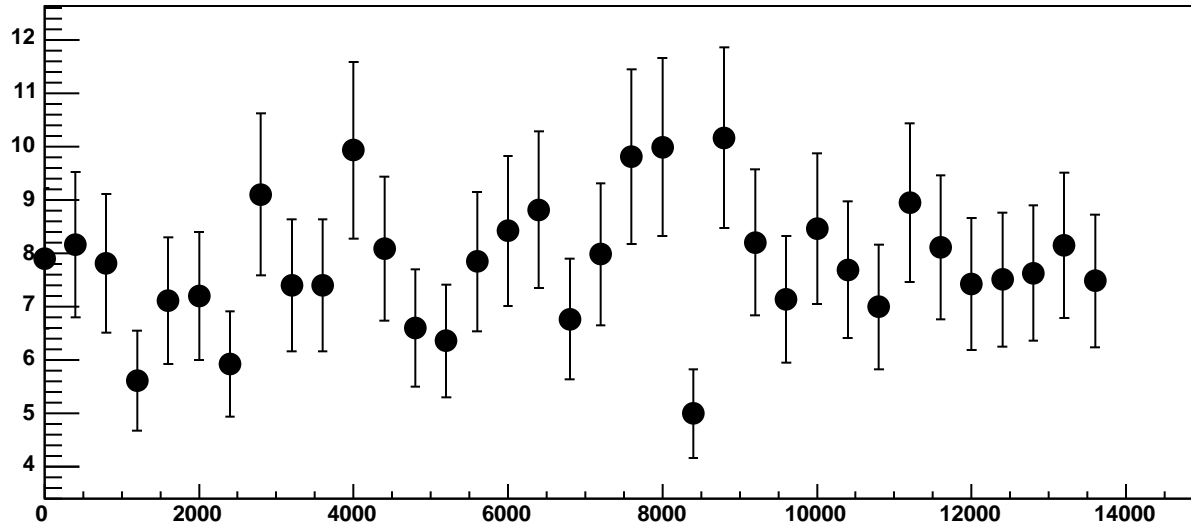
Chip 5, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC



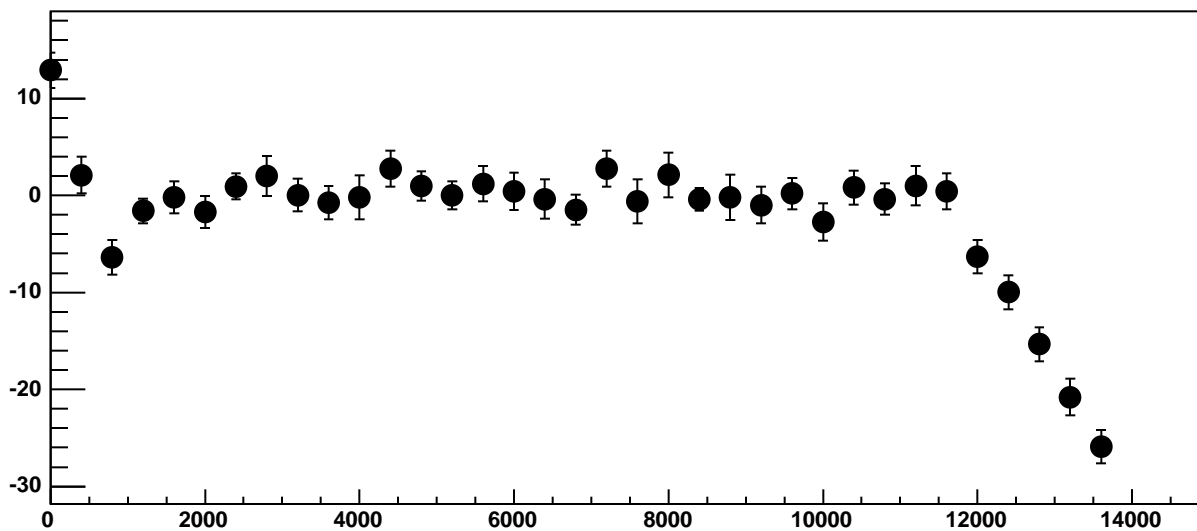
Chip 5, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



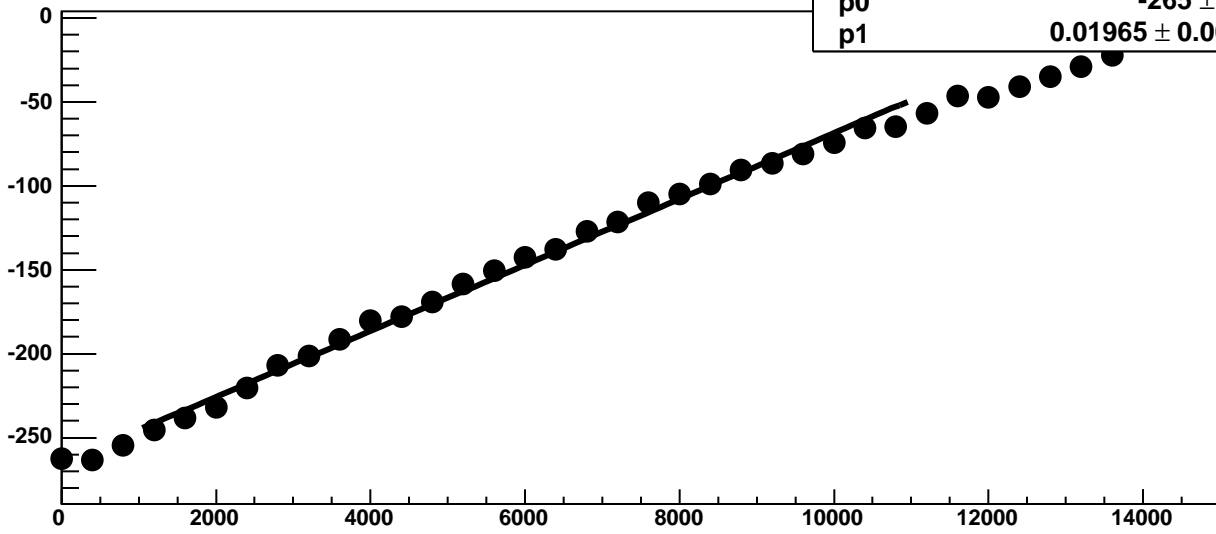
Chip 5, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



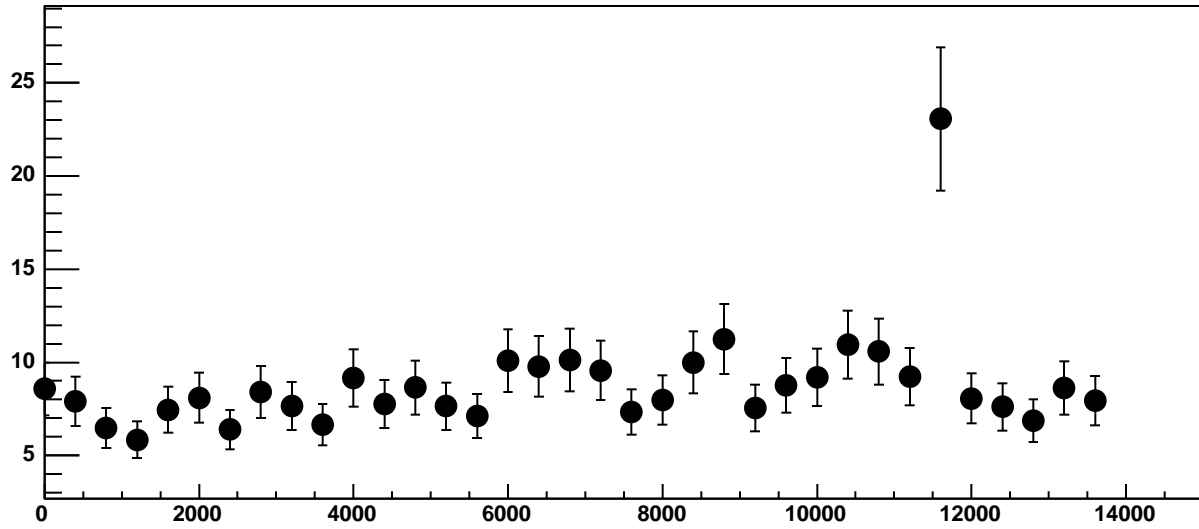
Chip 5, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



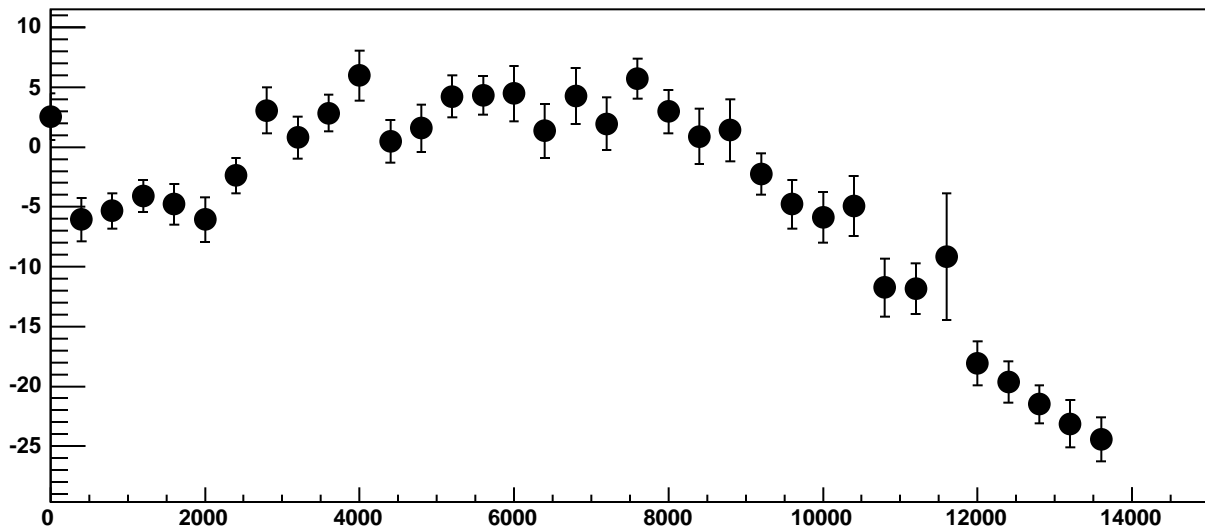
Chip 5, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



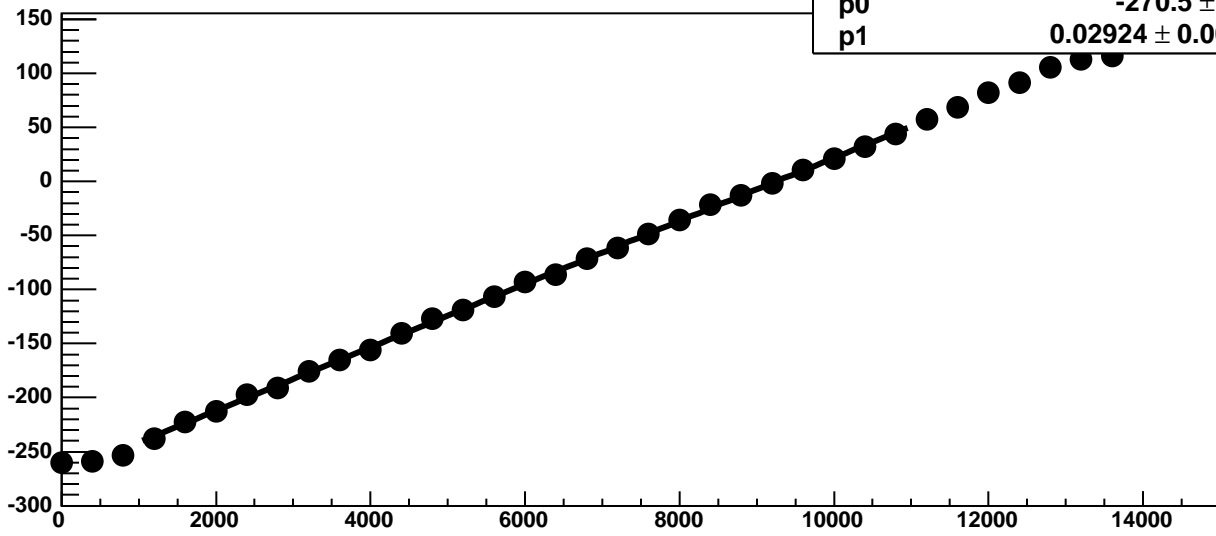
Chip 5, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

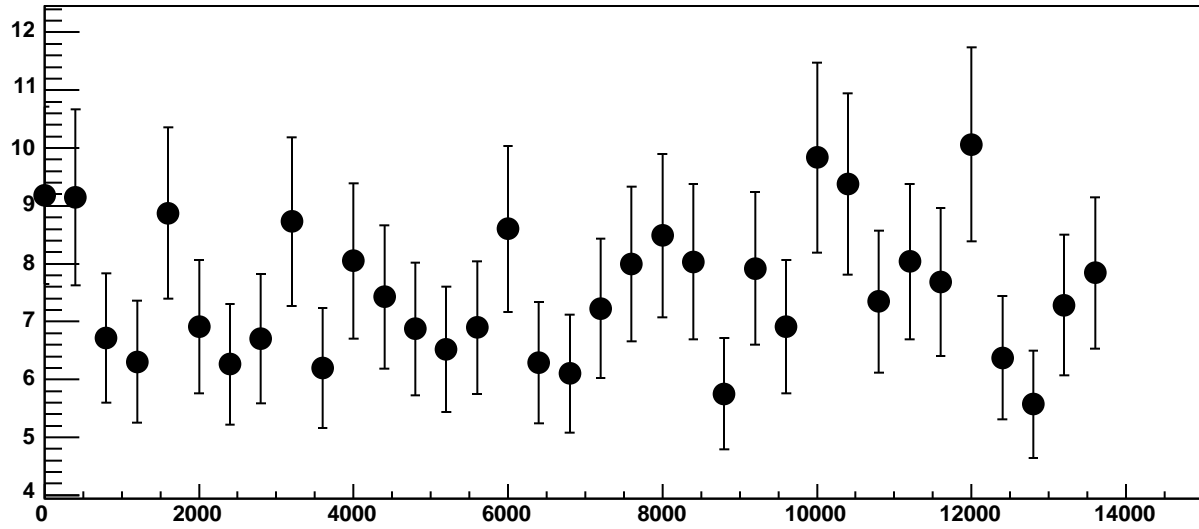


Chip 5, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC

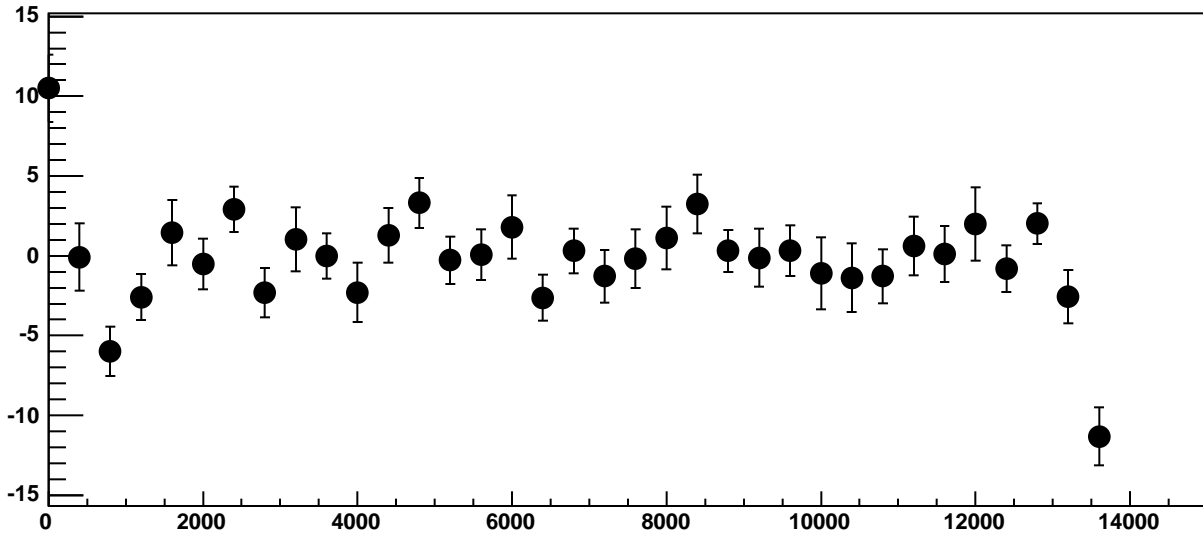


χ^2 / ndf 26.58 / 23
p0 -270.5 ± 0.7559
p1 0.02924 ± 0.0001173

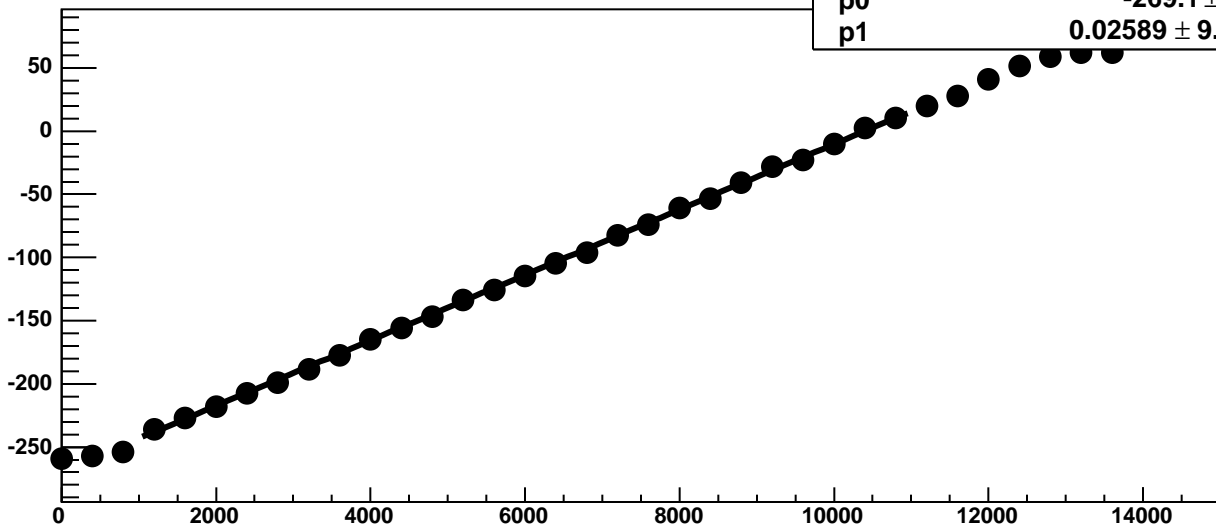
Chip 5, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

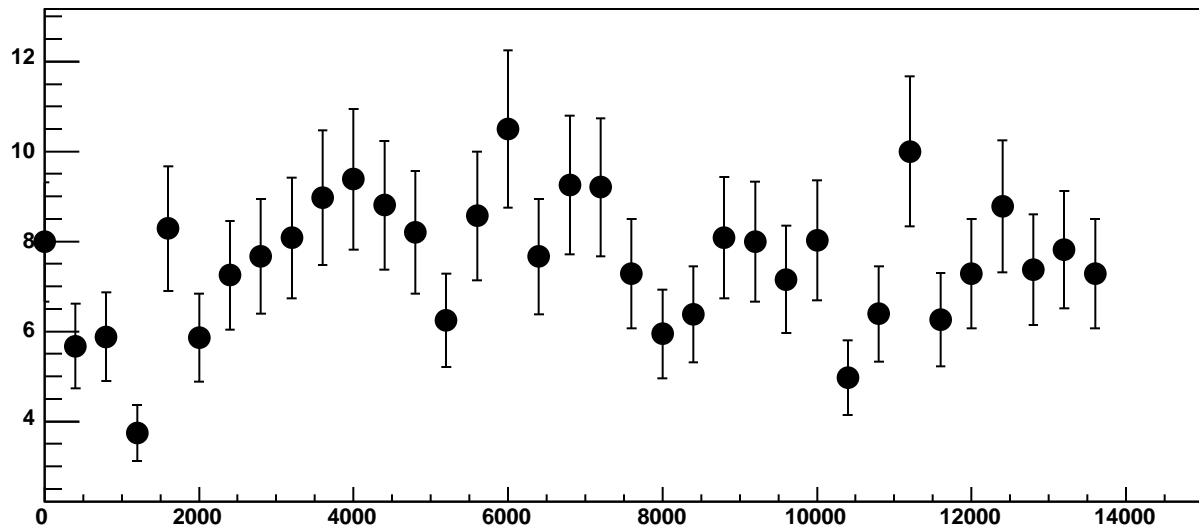


Chip 5, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

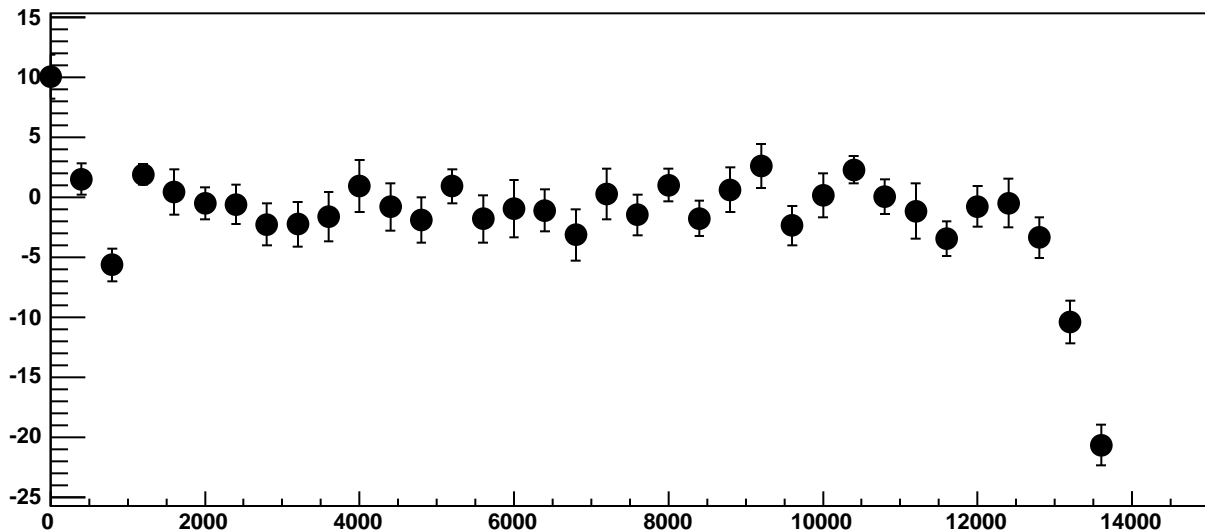


χ^2 / ndf 25.34 / 23
p0 -269.1 ± 0.6481
p1 $0.02589 \pm 9.73e-05$

Chip 5, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

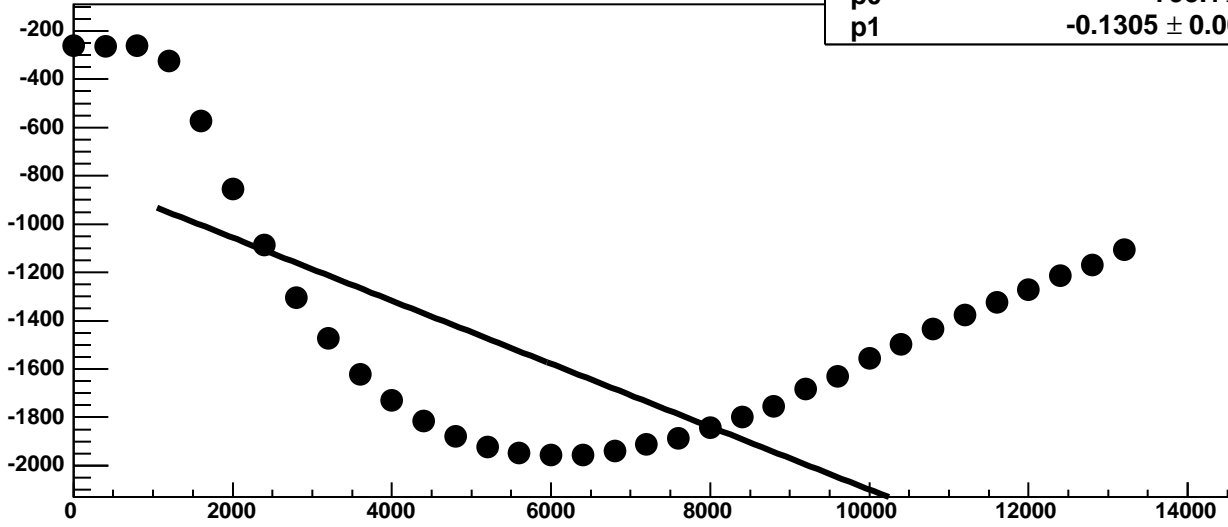


Chip 5, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

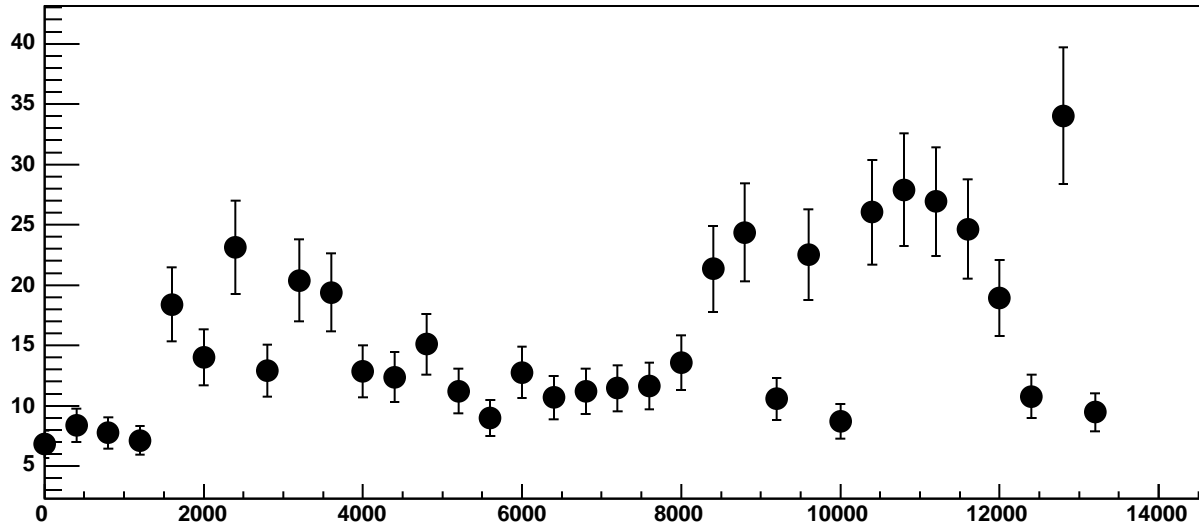


Chip 5, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

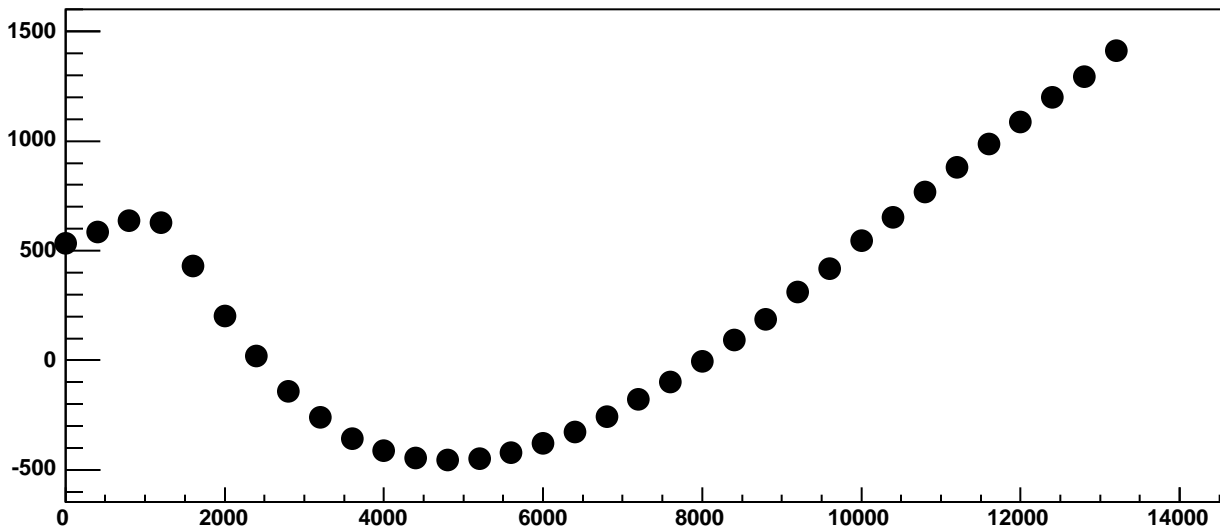
χ^2 / ndf 4.838e+05 / 23
p0 -795.1 ± 1.313
p1 -0.1305 ± 0.0002078



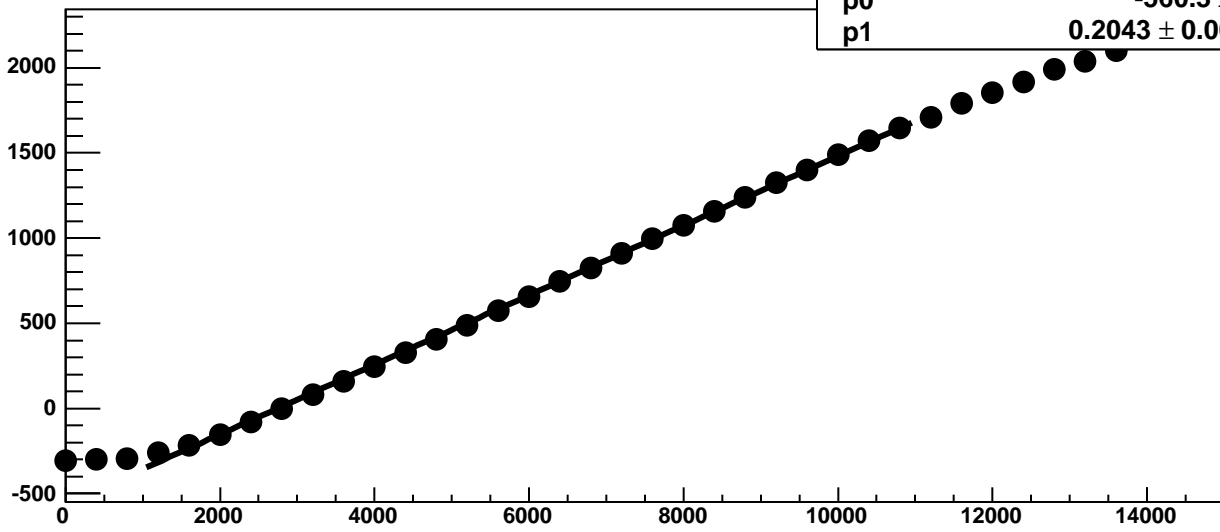
Chip 5, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



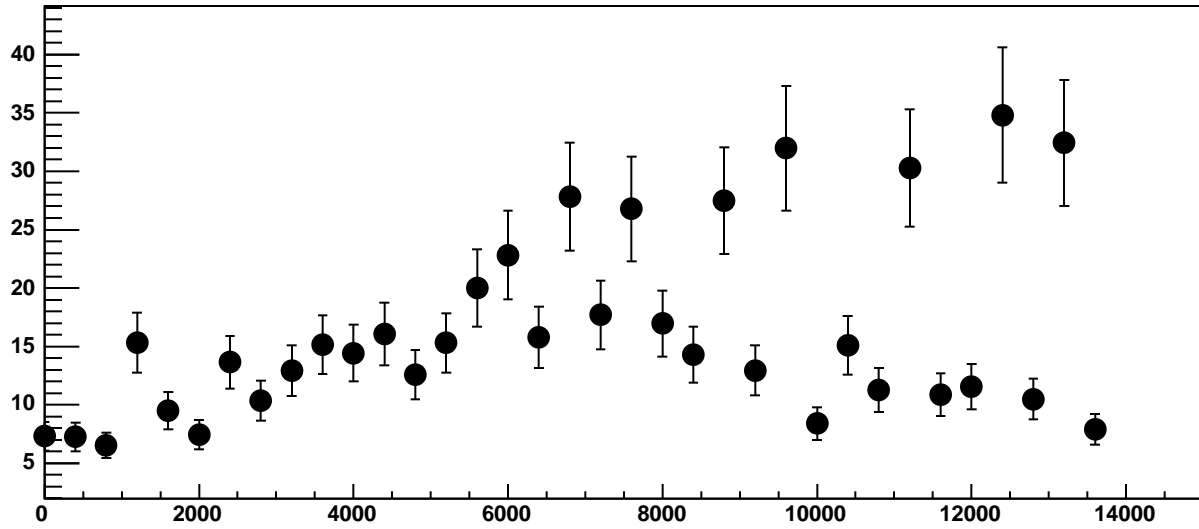
Chip 5, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



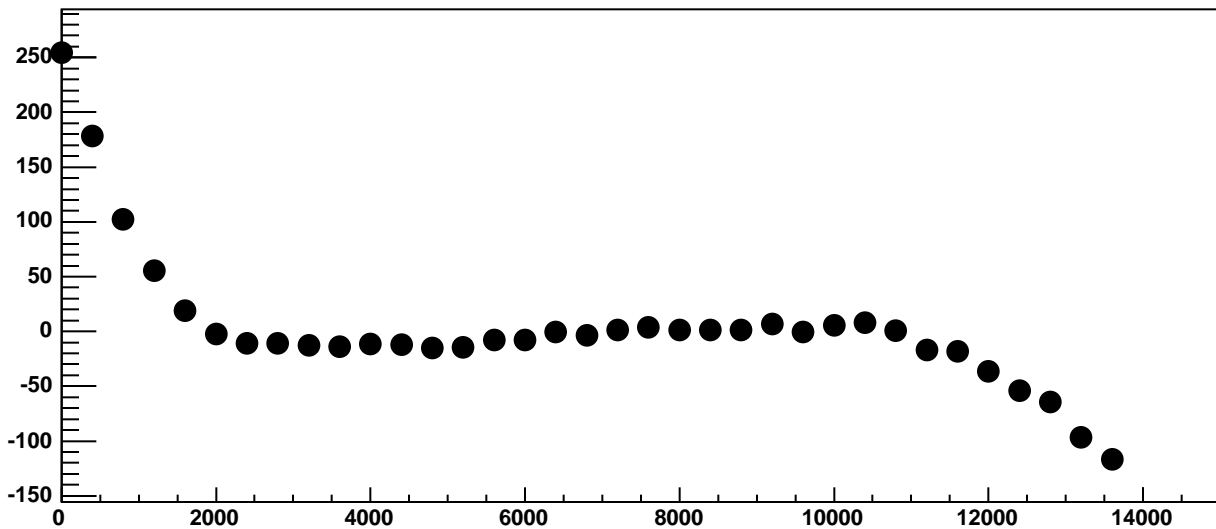
Chip 5, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC



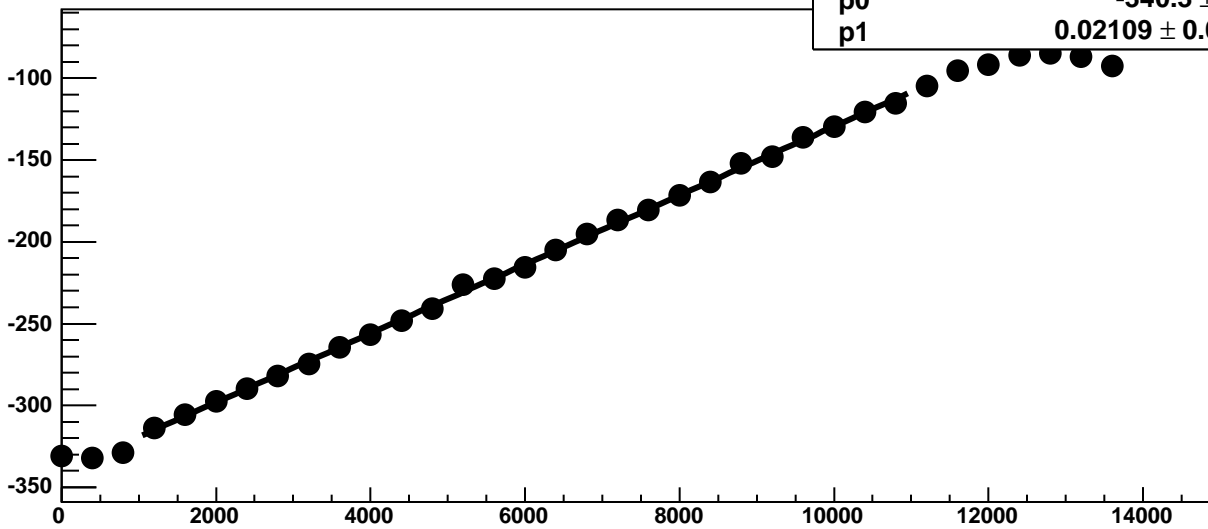
Chip 5, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



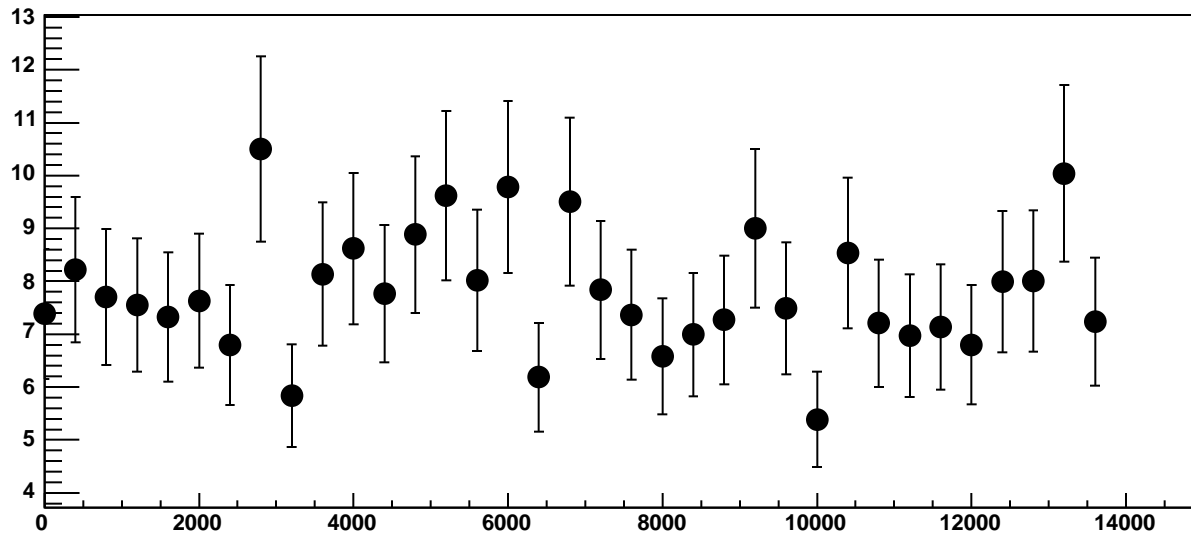
Chip 5, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC



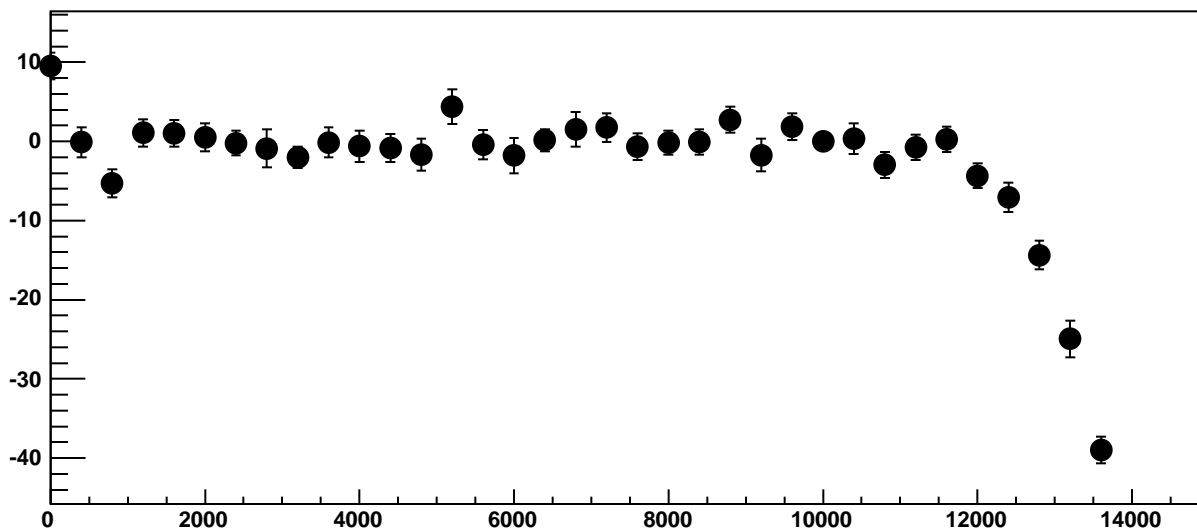
Chip 5, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



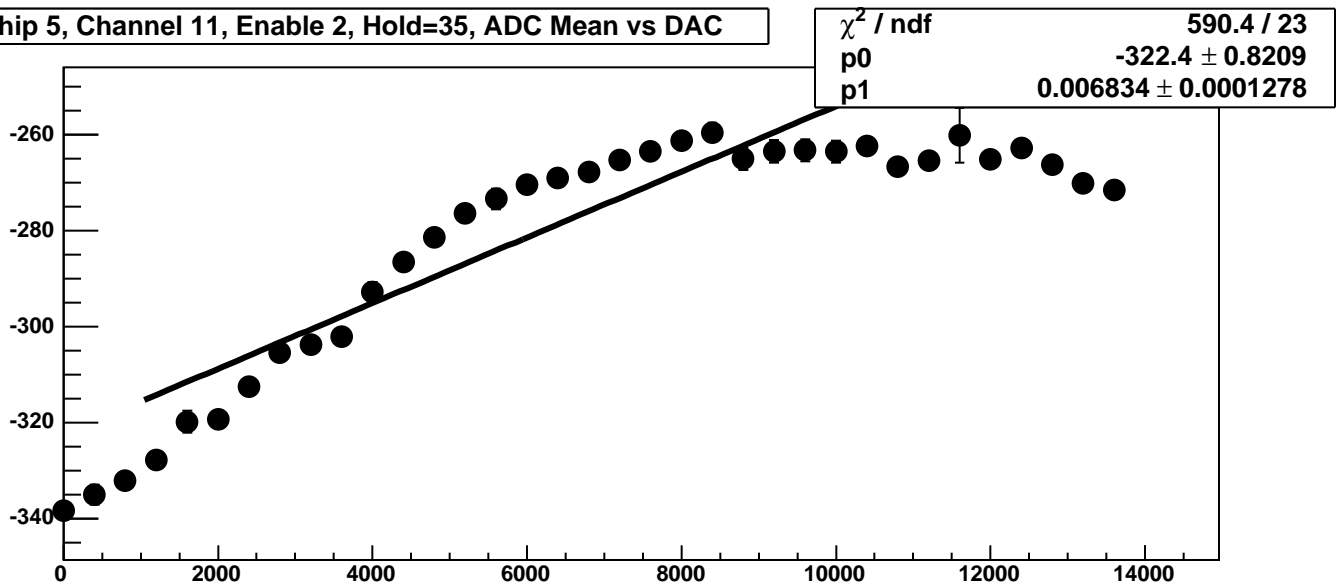
Chip 5, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



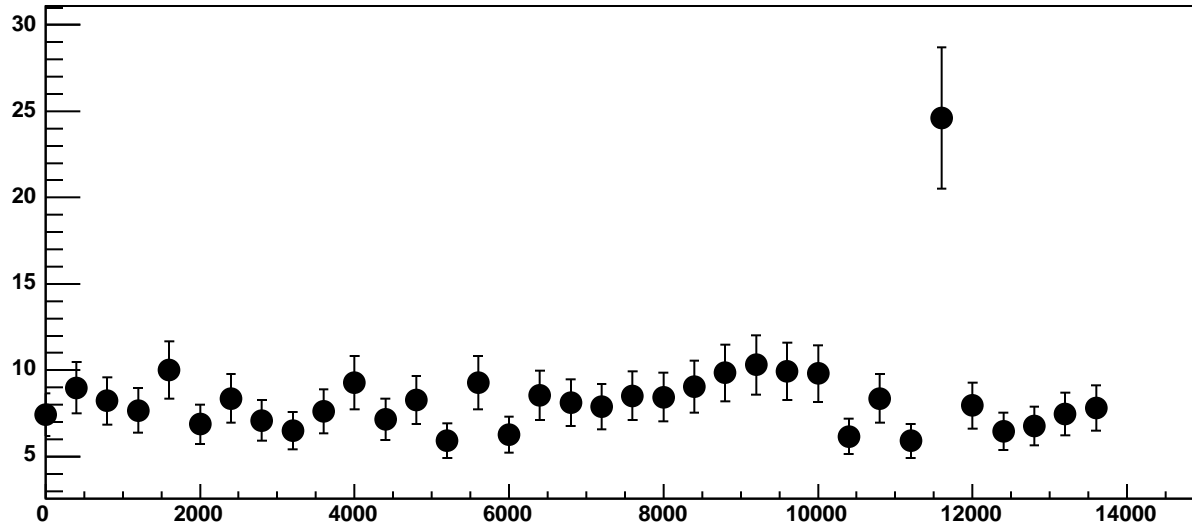
Chip 5, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



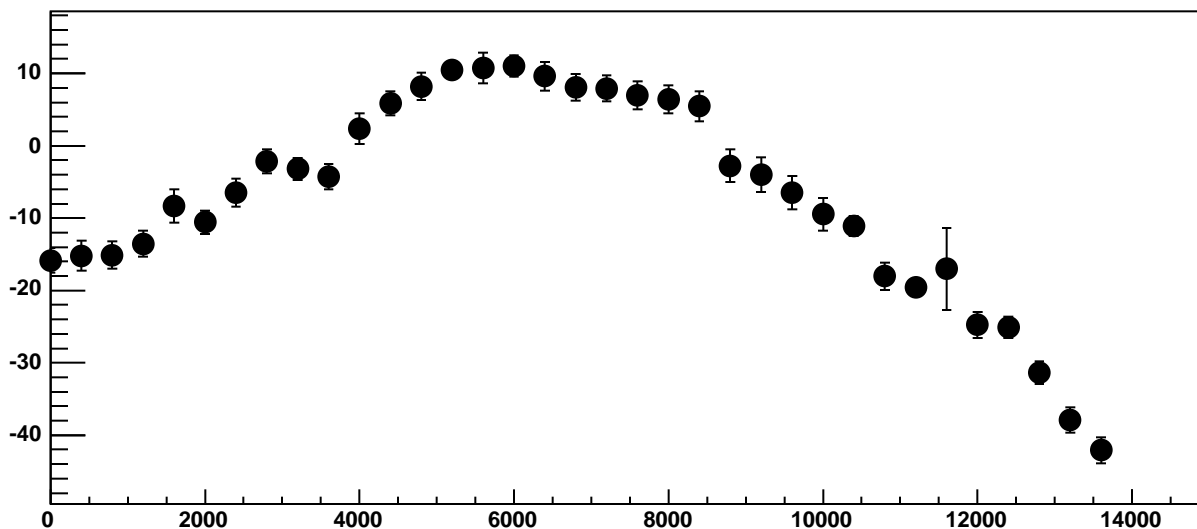
Chip 5, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



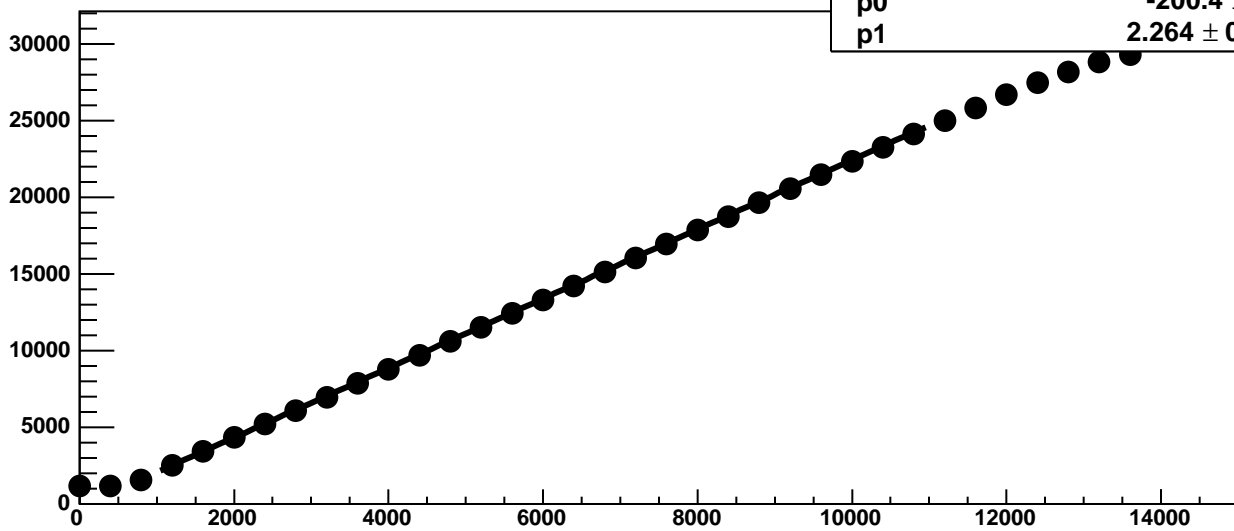
Chip 5, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



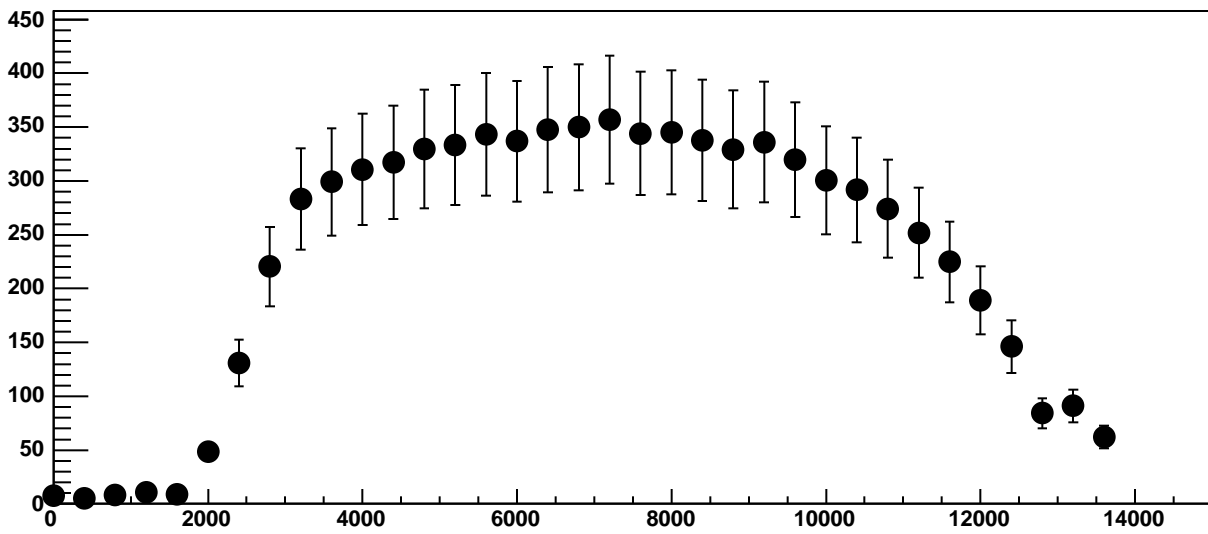
Chip 5, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



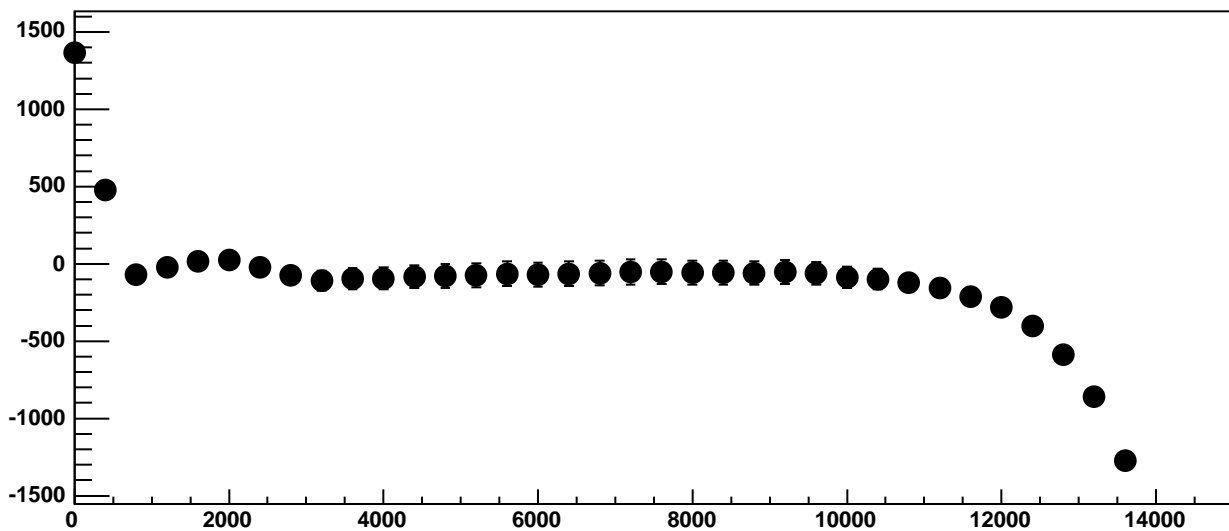
Chip 5, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC



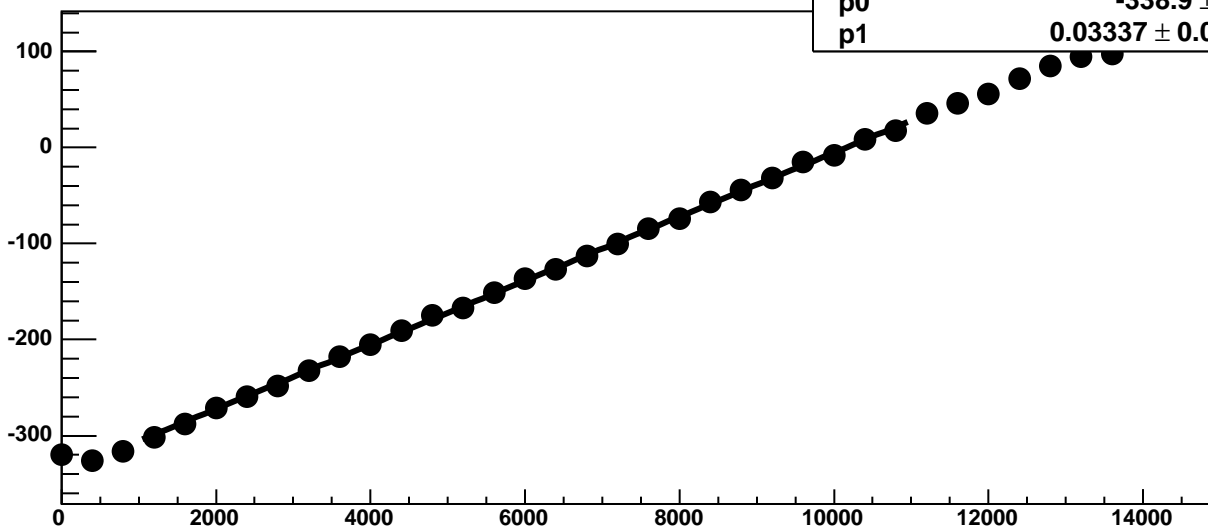
Chip 5, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC

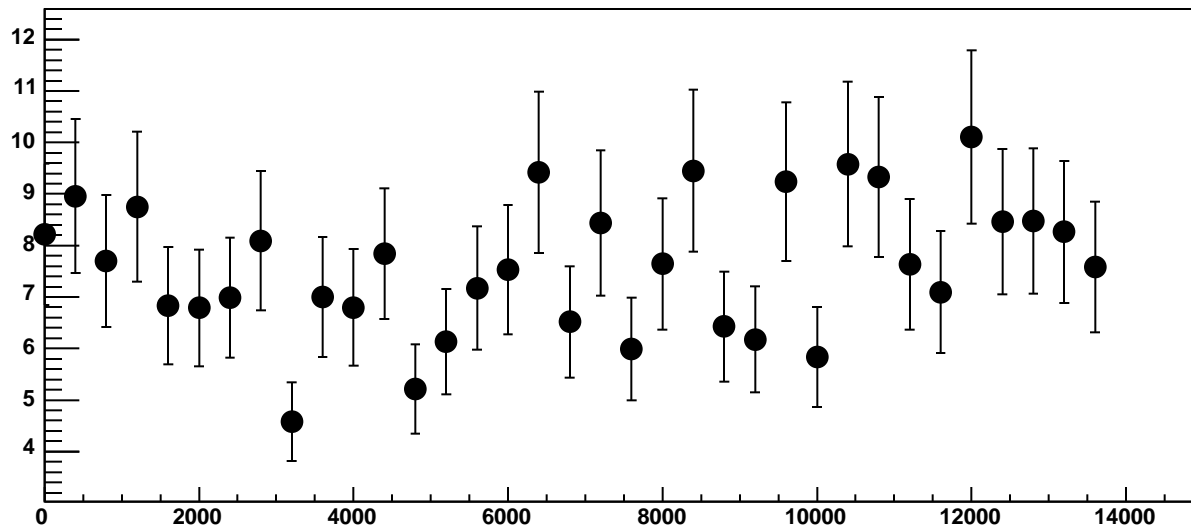


Chip 5, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

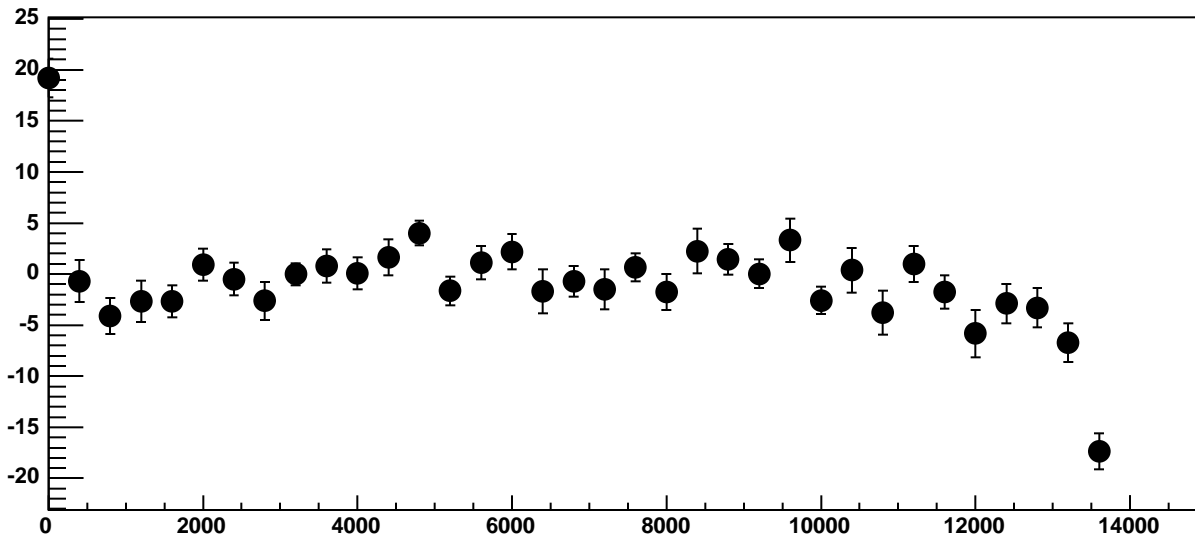


χ^2 / ndf 36.97 / 23
p0 -338.9 ± 0.7363
p1 0.03337 ± 0.0001155

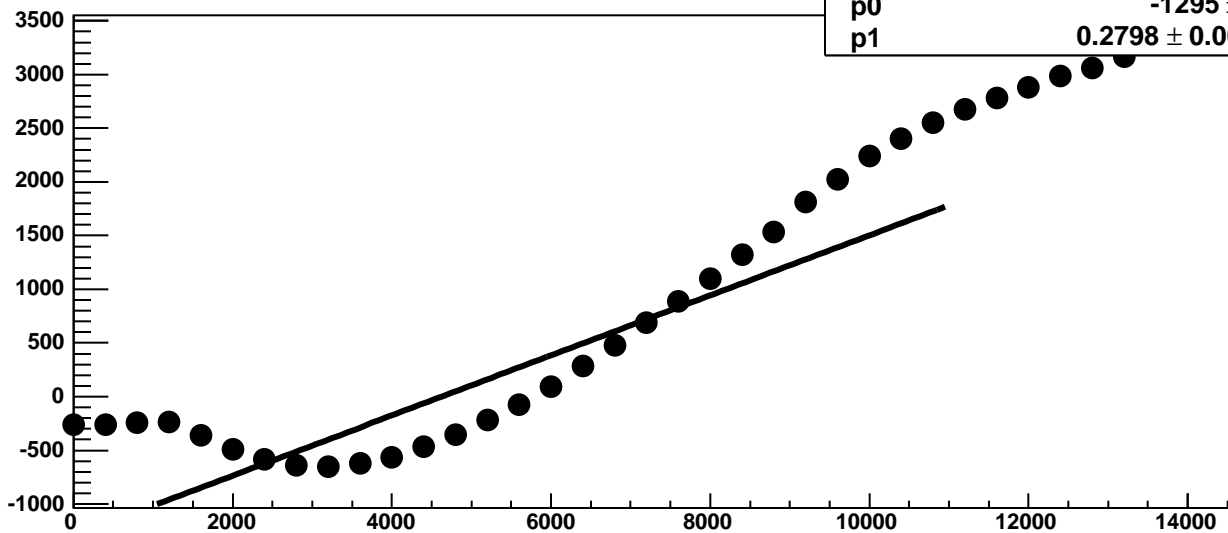
Chip 5, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

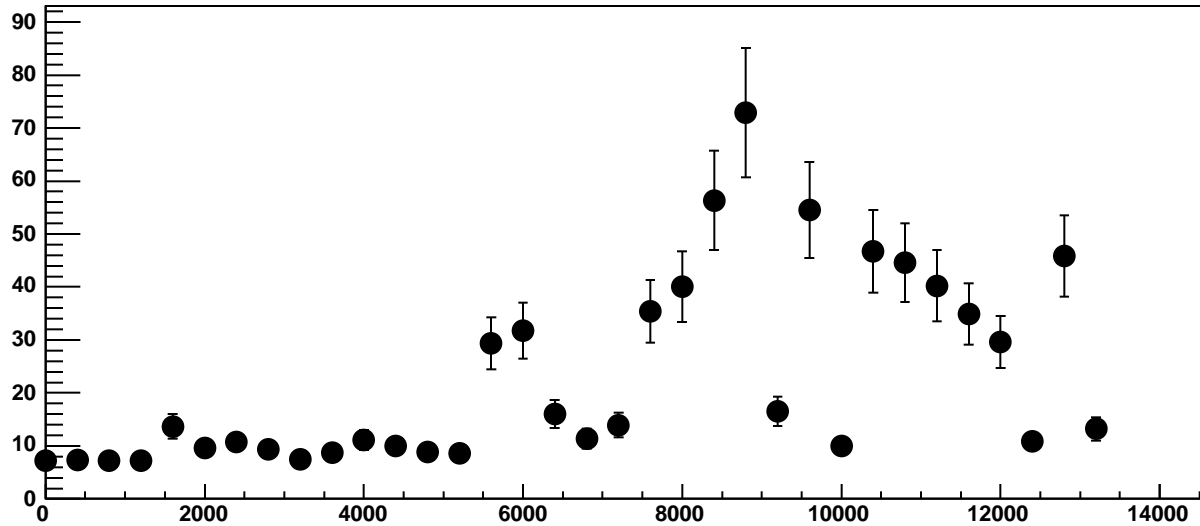


Chip 5, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

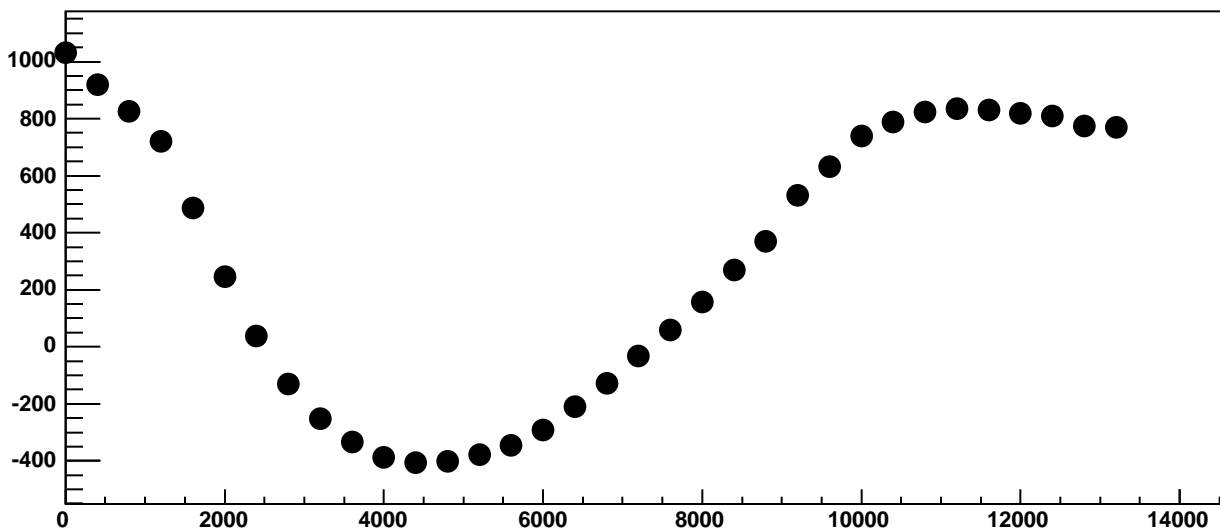


χ^2 / ndf 5.609e+05 / 23
p0 -1295 ± 1.133
p1 0.2798 ± 0.0002313

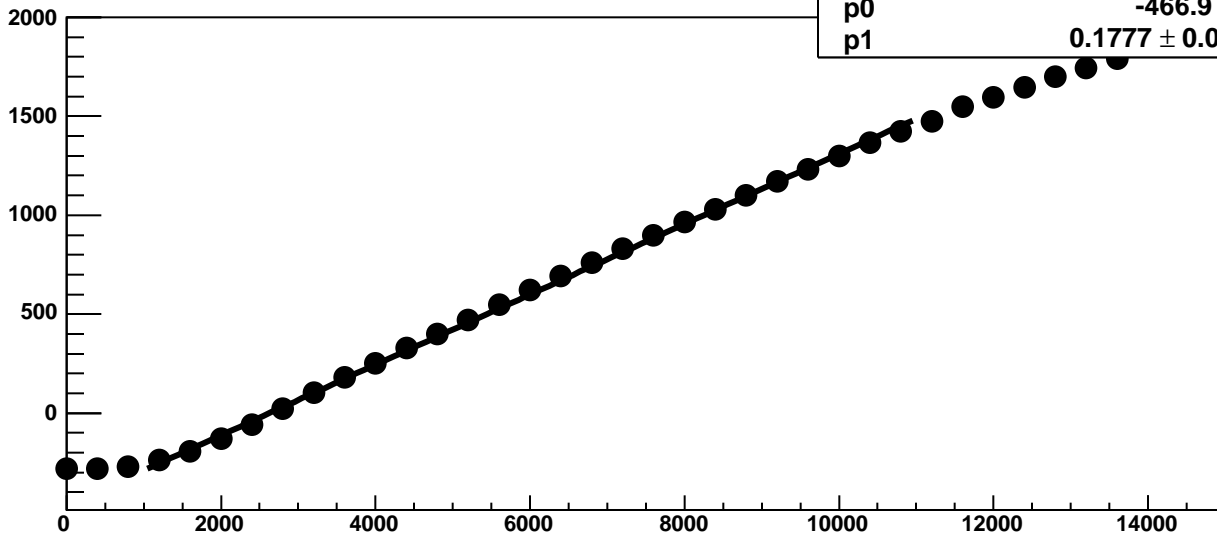
Chip 5, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



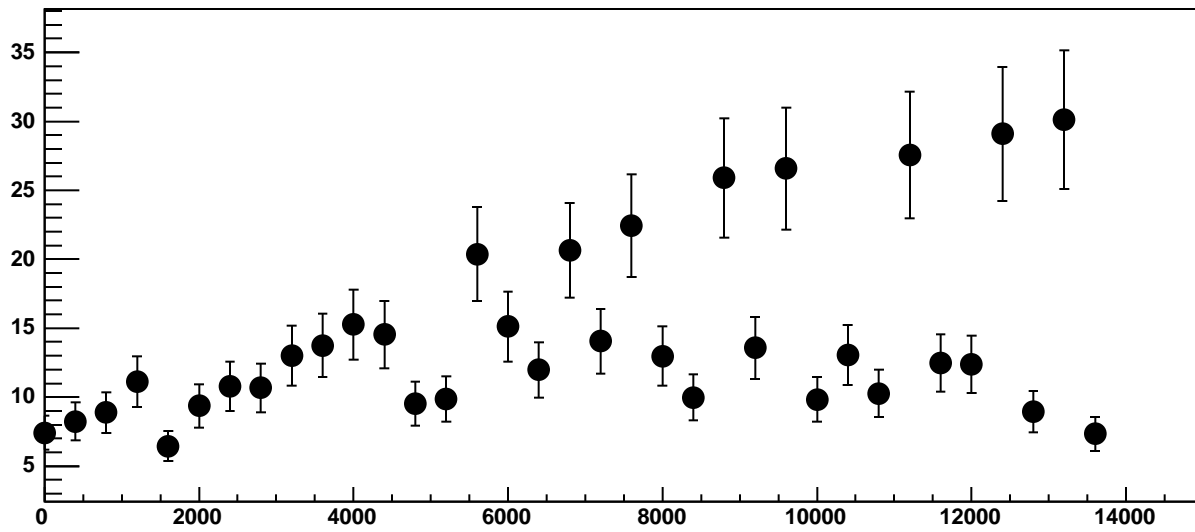
Chip 5, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



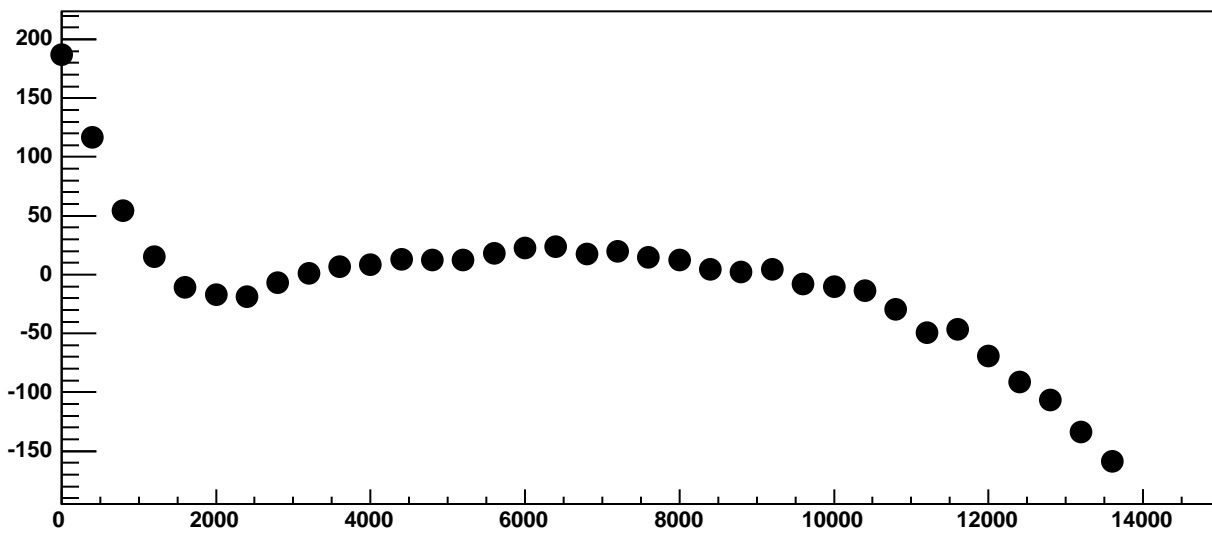
Chip 5, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



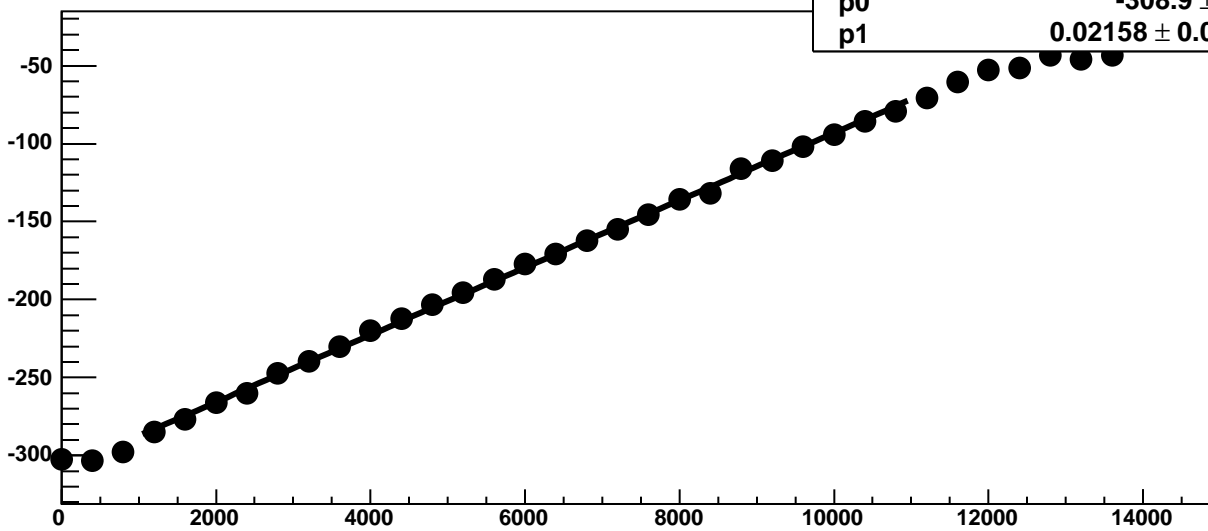
Chip 5, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



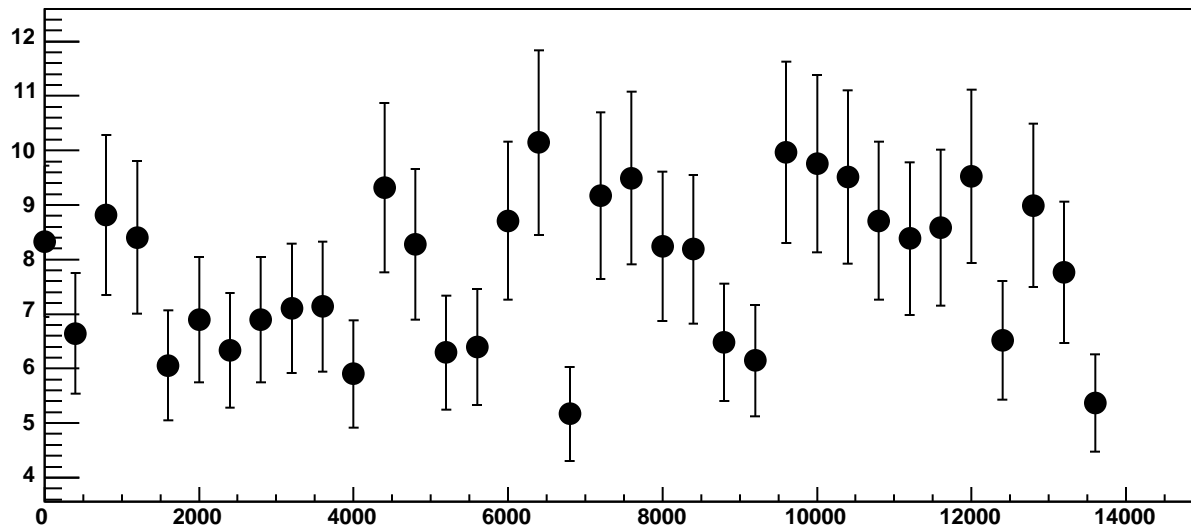
Chip 5, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



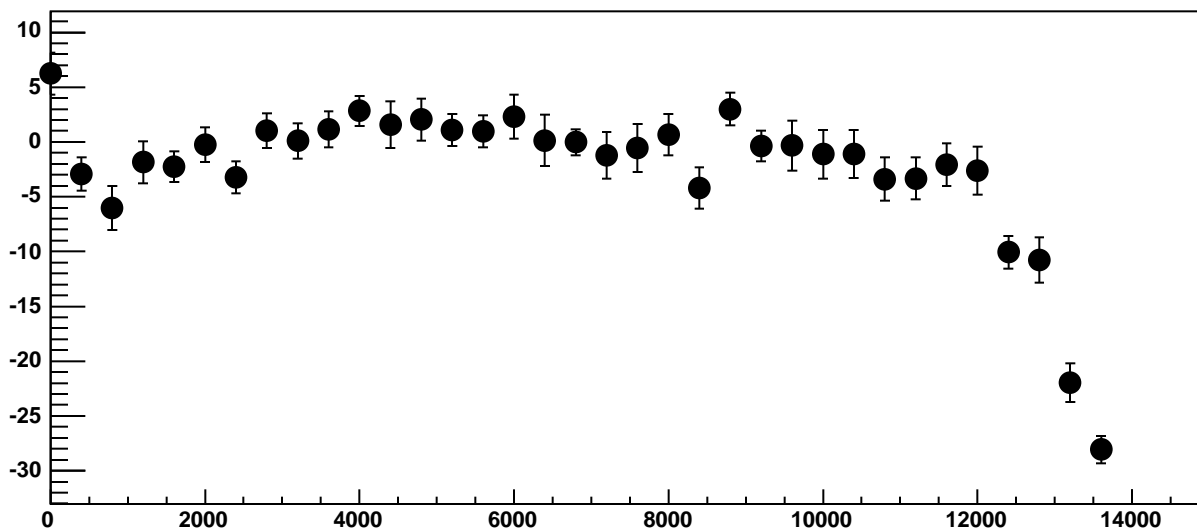
Chip 5, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC



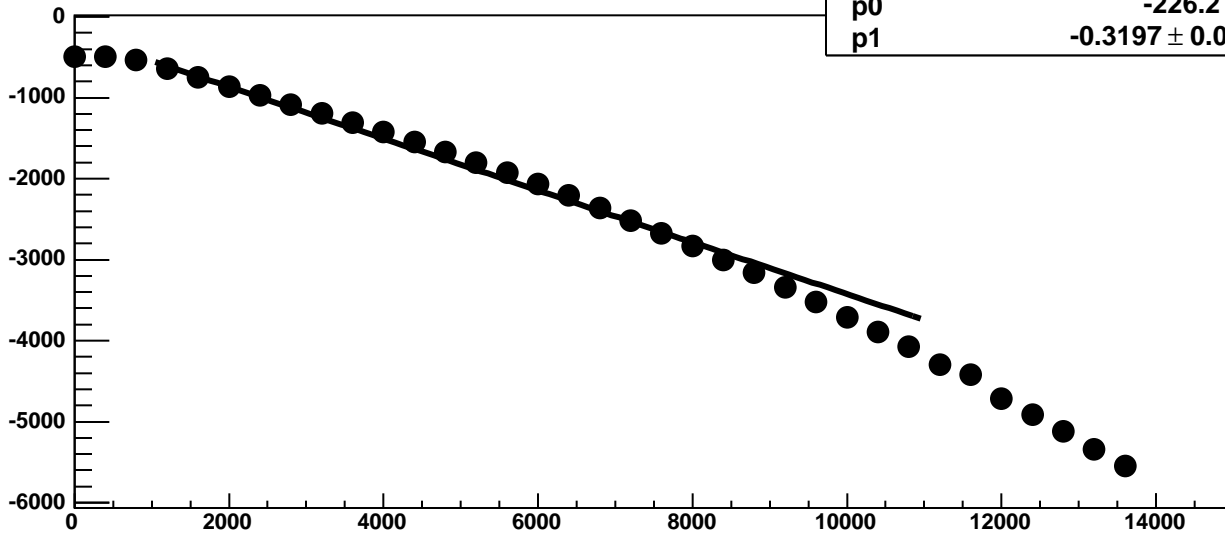
Chip 5, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

4153 / 23

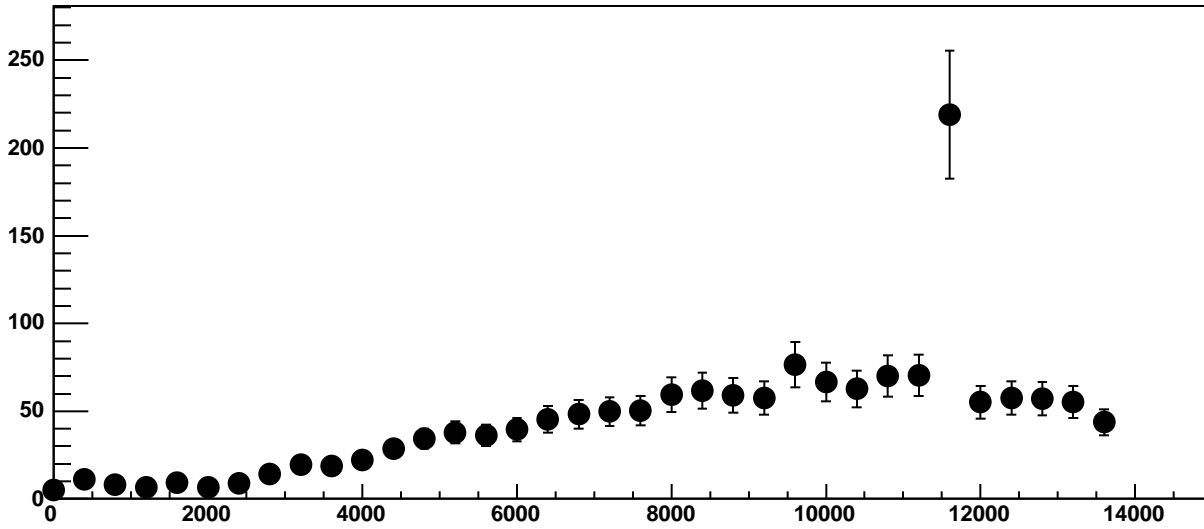
p0

-226.2 ± 1.385

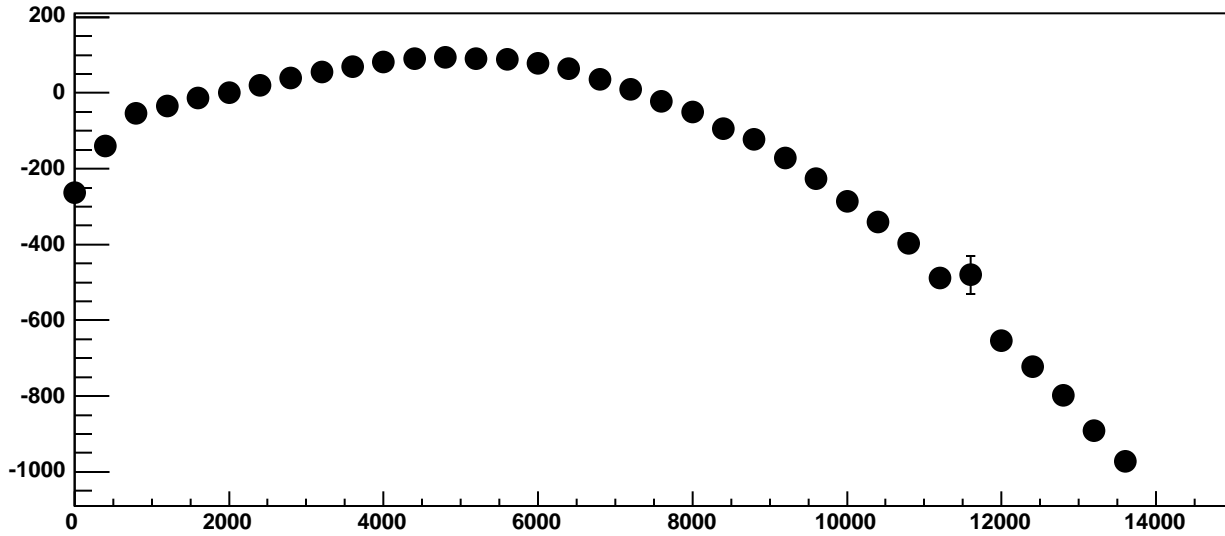
p1

-0.3197 ± 0.0004884

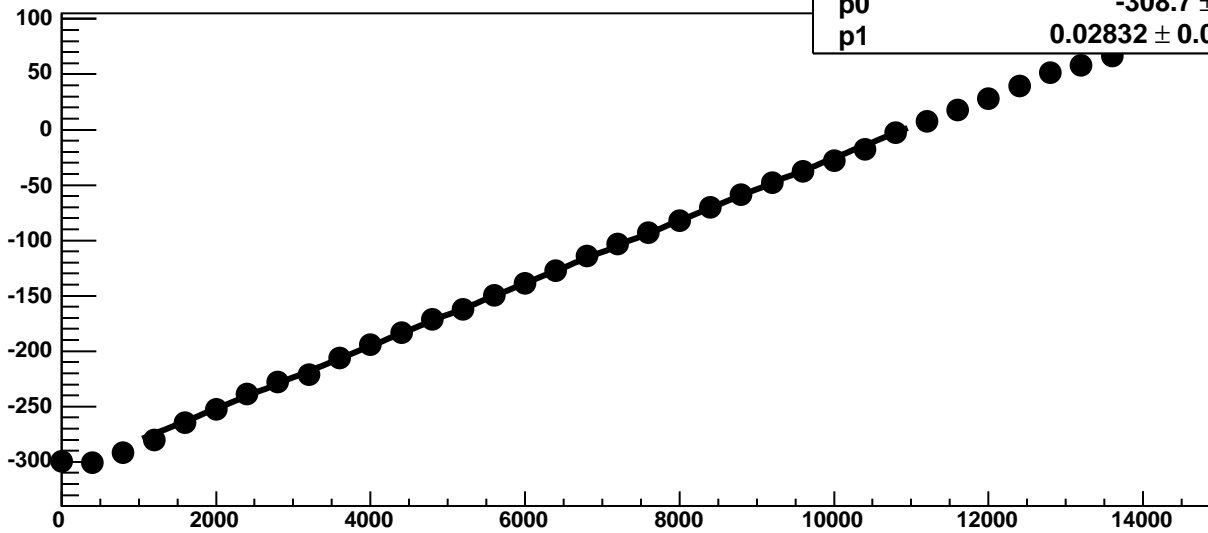
Chip 5, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

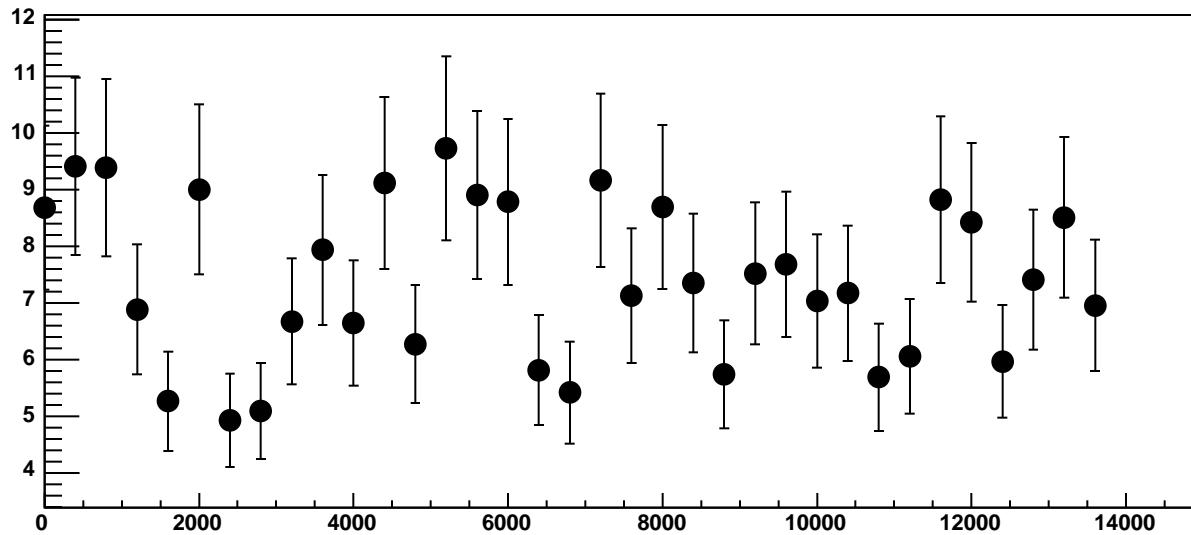


Chip 5, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

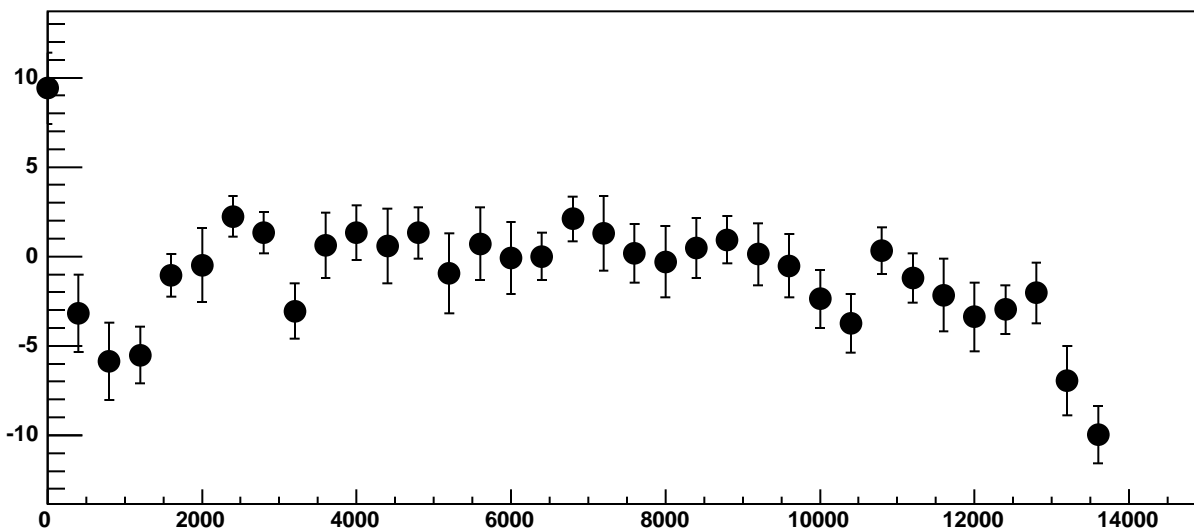


χ^2 / ndf 35.68 / 23
p0 -308.7 ± 0.6685
p1 0.02832 ± 0.0001024

Chip 5, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC

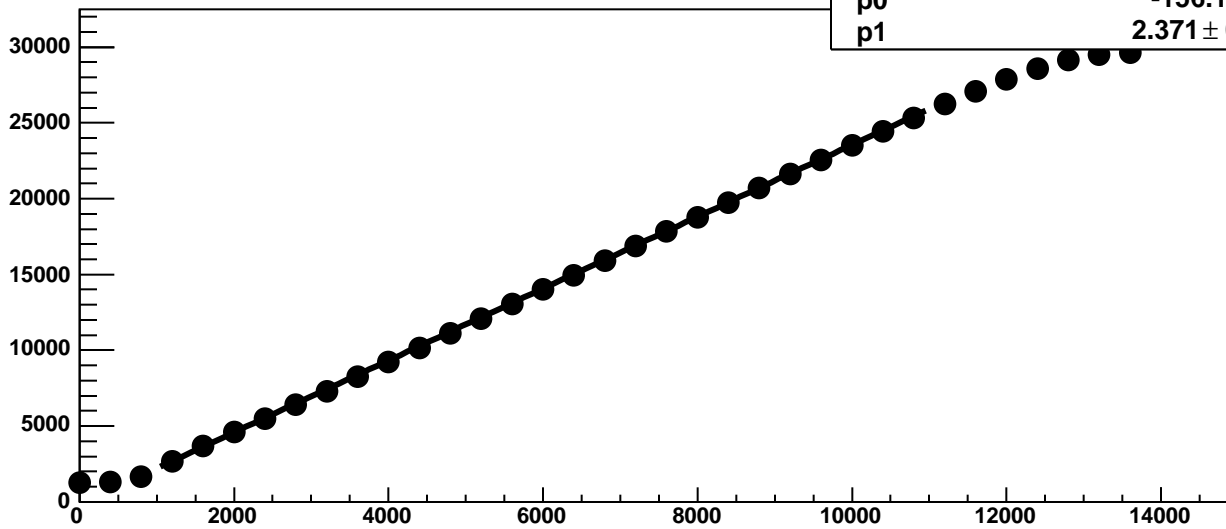


Chip 5, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

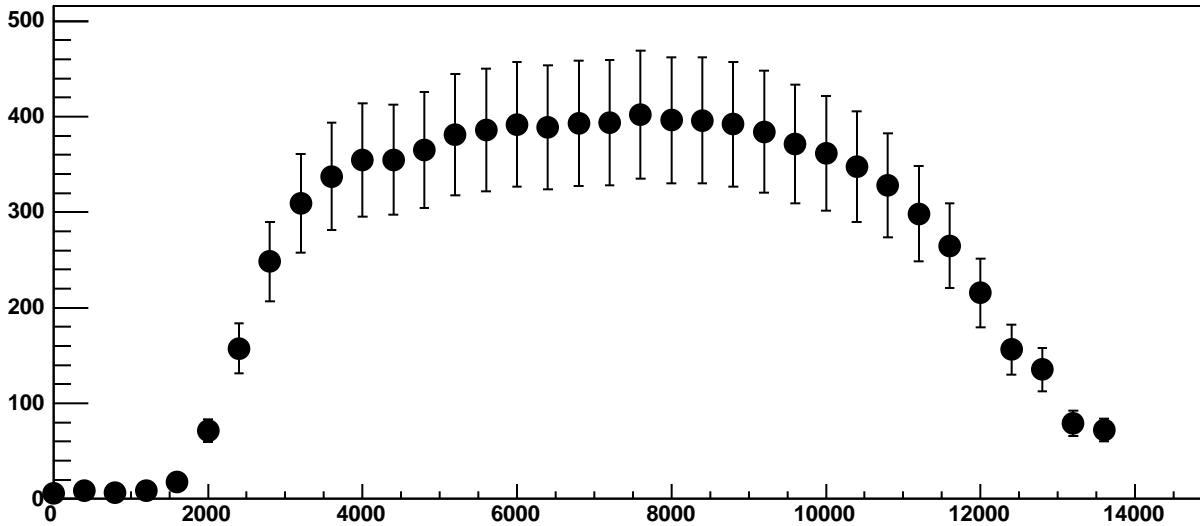


Chip 5, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC

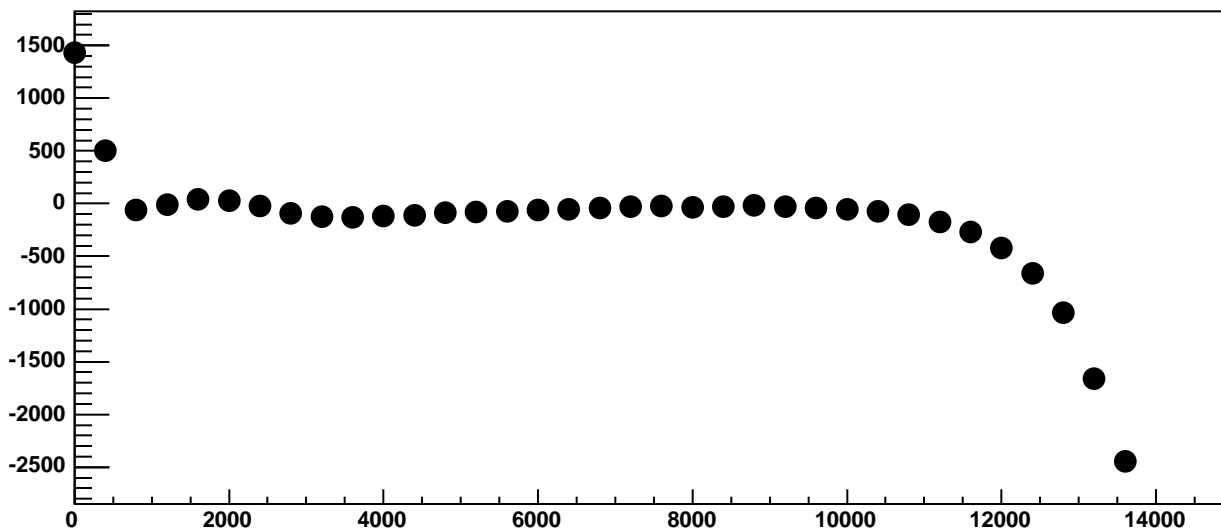
χ^2 / ndf 140.3 / 23
p0 -156.1 ± 4.336
p1 2.371 ± 0.00294



Chip 5, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC

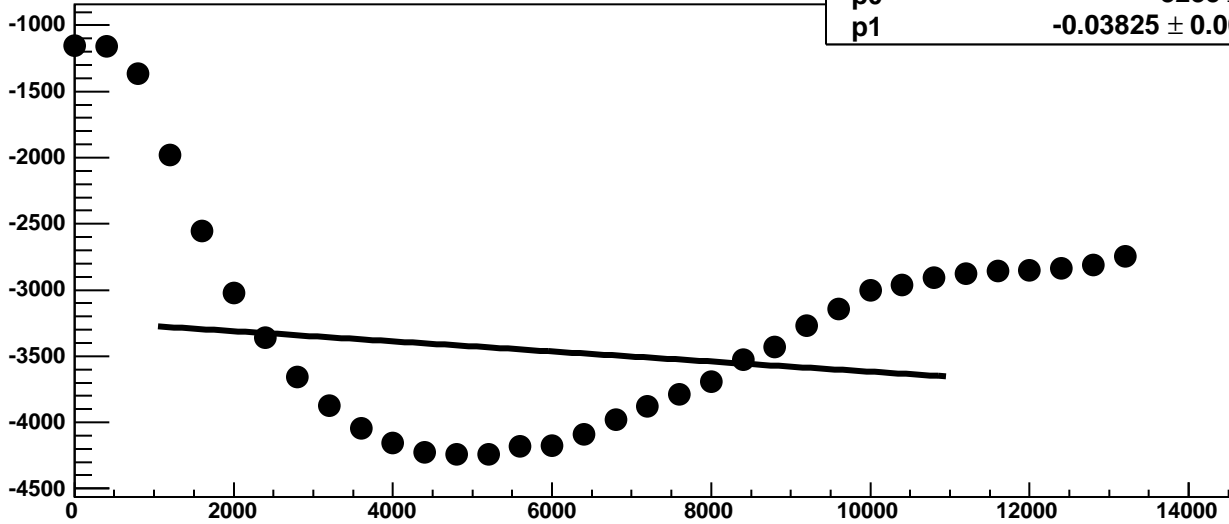


Chip 5, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC

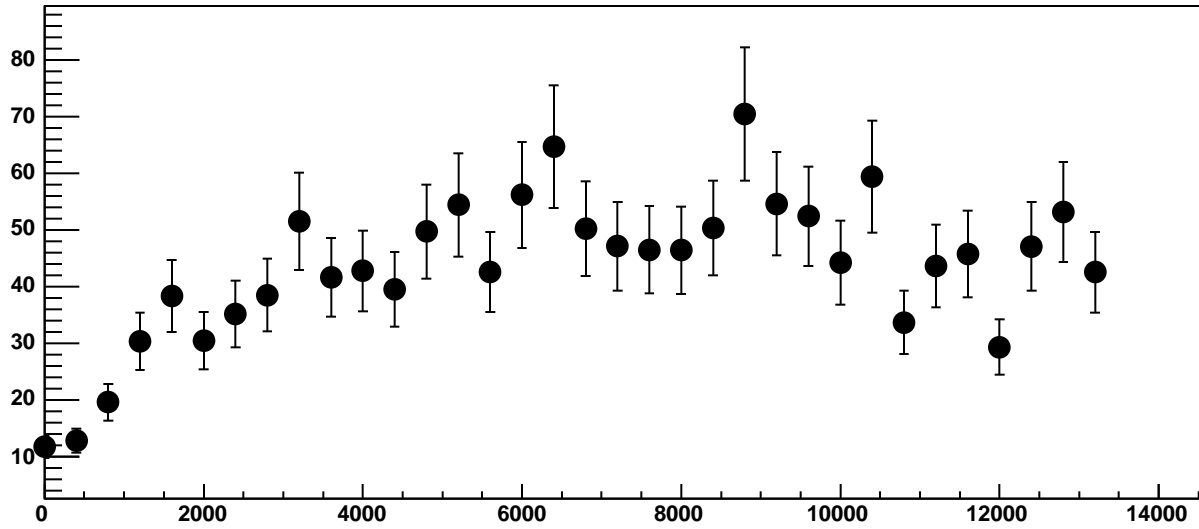


Chip 5, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

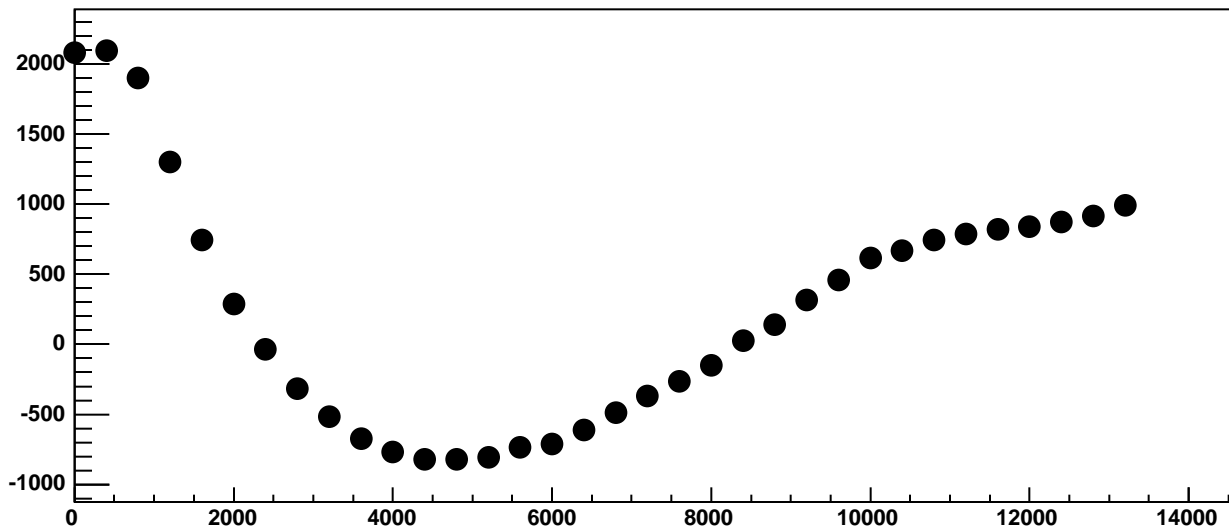
χ^2 / ndf 1.074e+05 / 23
p0 -3235 ± 3.955
p1 -0.03825 ± 0.0006425



Chip 5, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

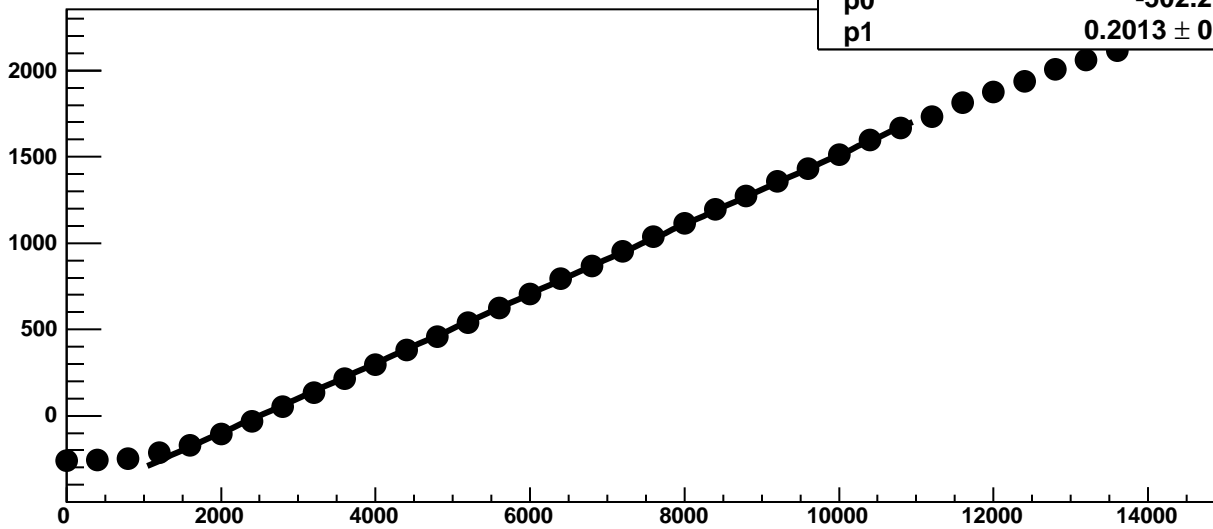


Chip 5, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

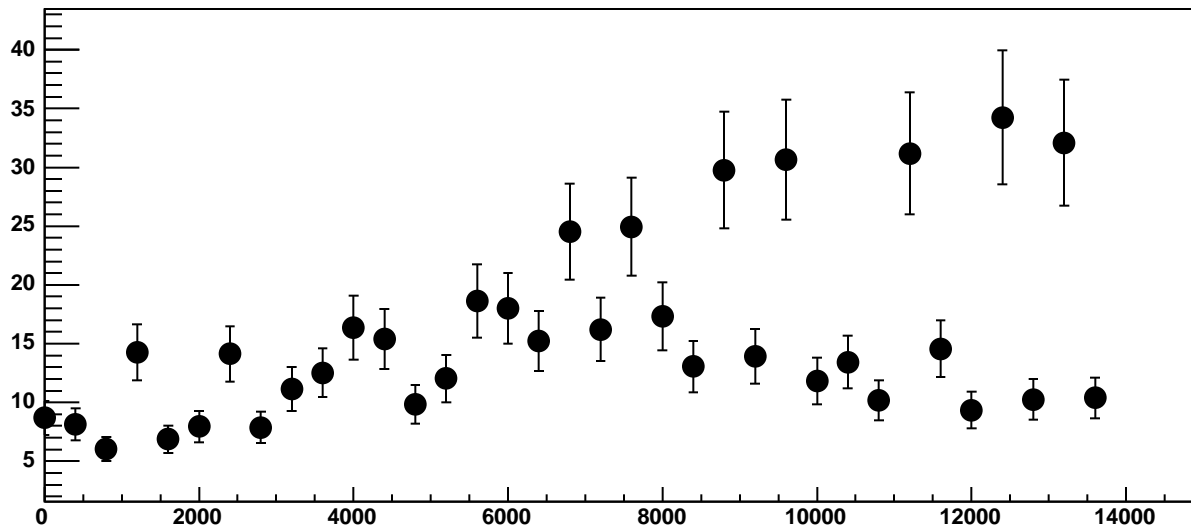


Chip 5, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

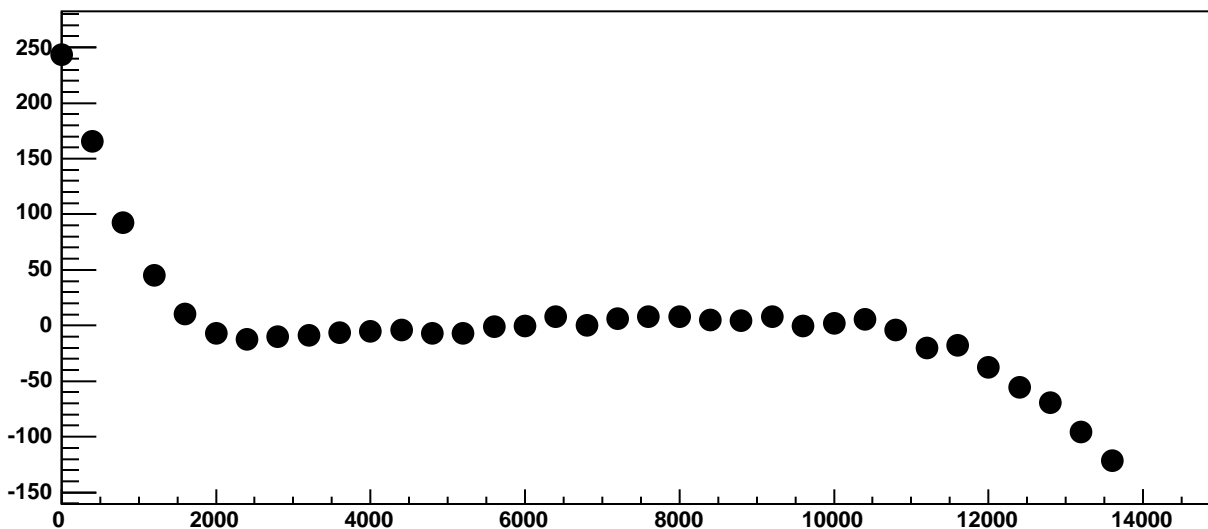
χ^2 / ndf 355.9 / 23
p0 -502.2 ± 1.078
p1 0.2013 ± 0.000184



Chip 5, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

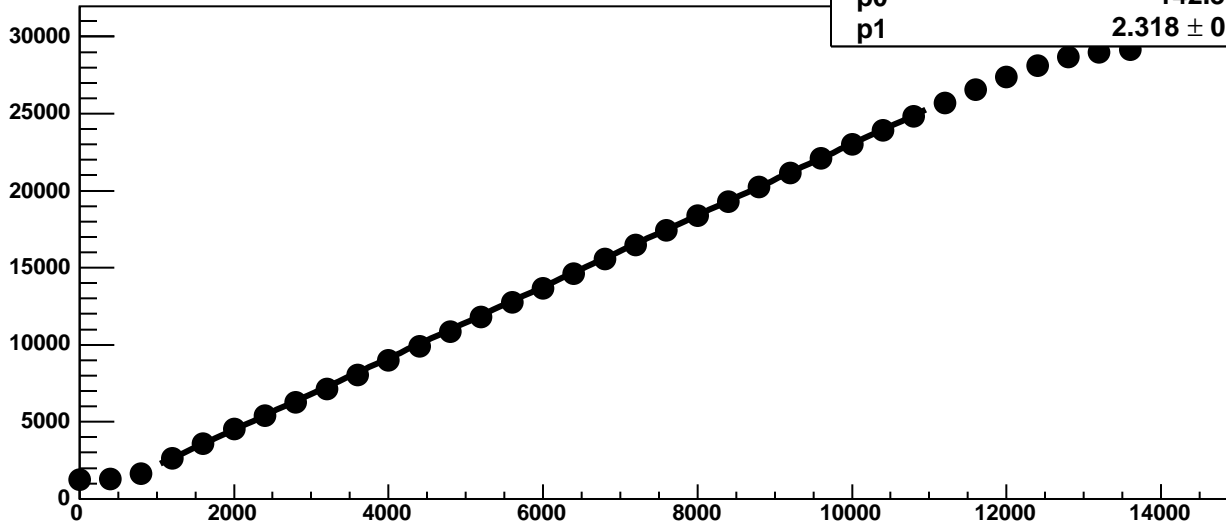


Chip 5, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

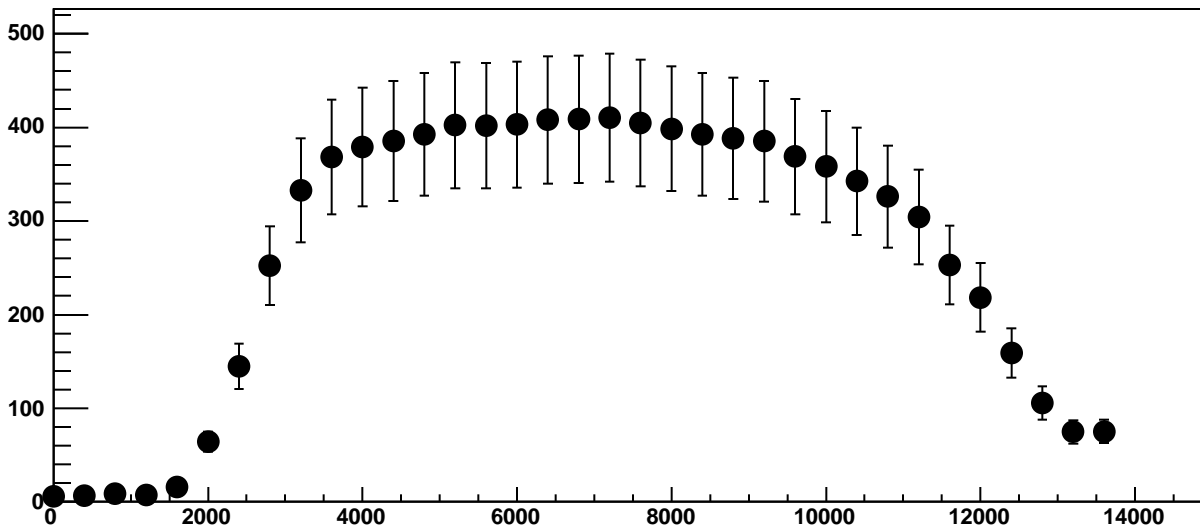


Chip 5, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC

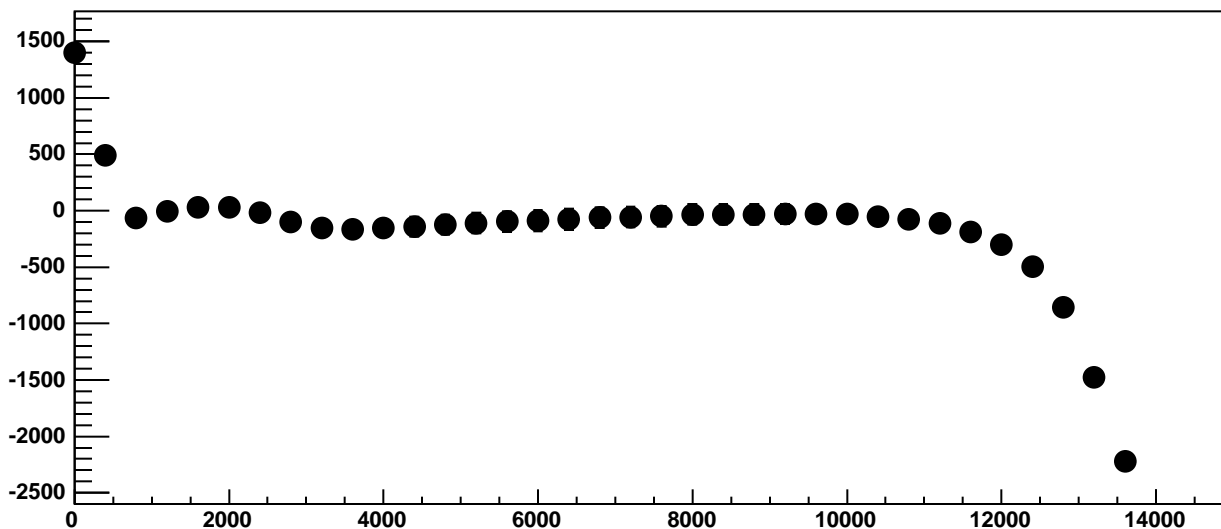
χ^2 / ndf 119.3 / 23
p0 -142.3 ± 4.148
p1 2.318 ± 0.002918



Chip 5, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC

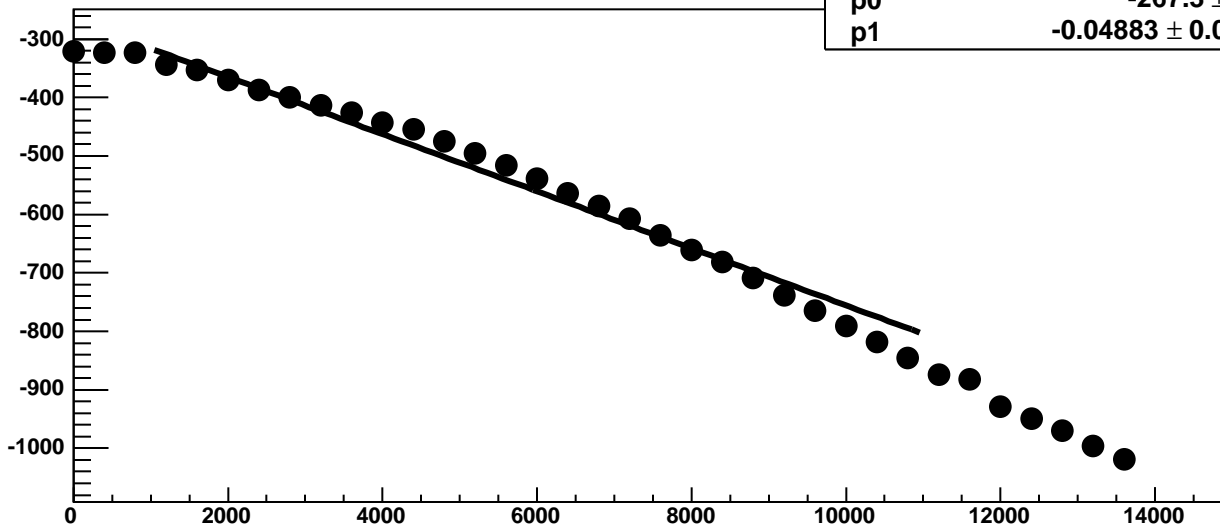


Chip 5, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC

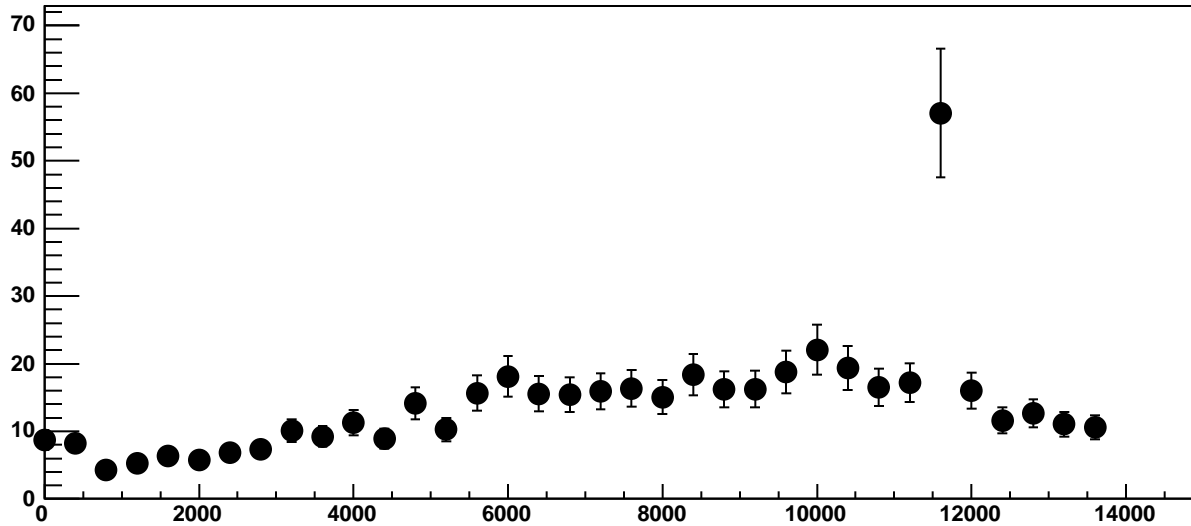


Chip 5, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

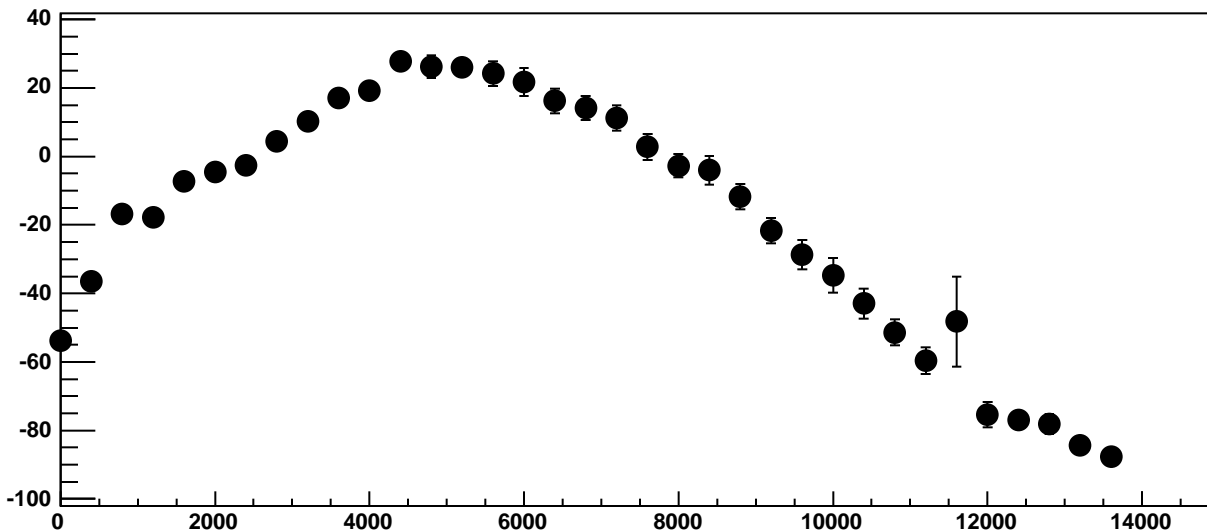
χ^2 / ndf 1302 / 23
p0 -267.3 ± 0.8294
p1 -0.04883 ± 0.0001838



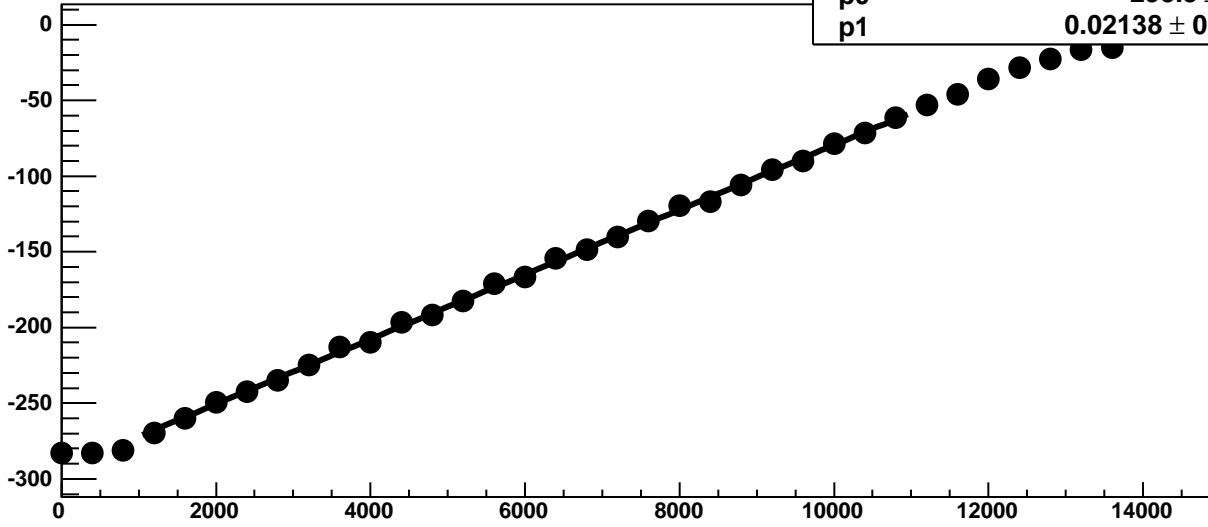
Chip 5, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

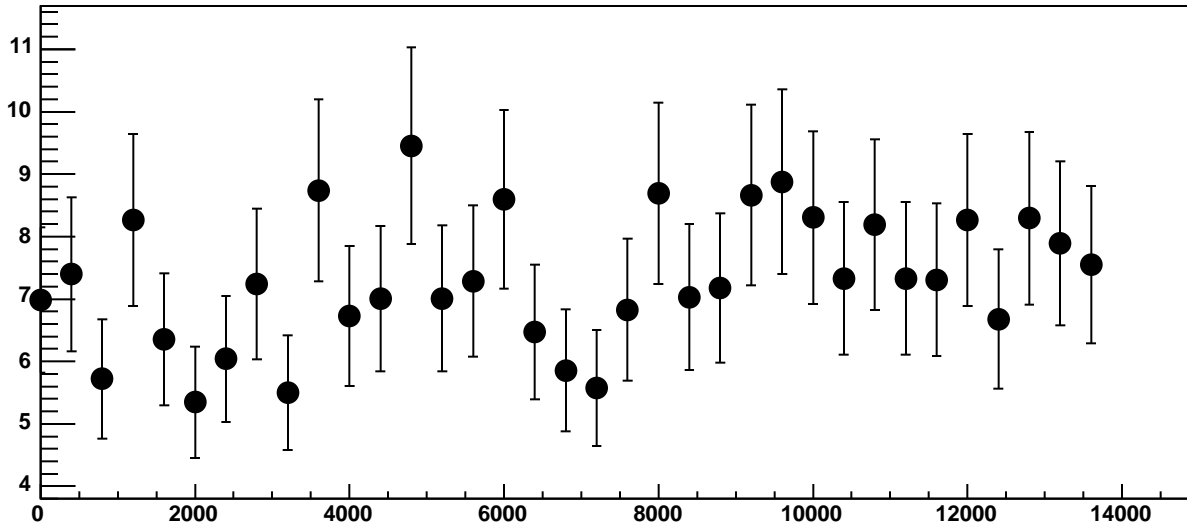


Chip 5, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

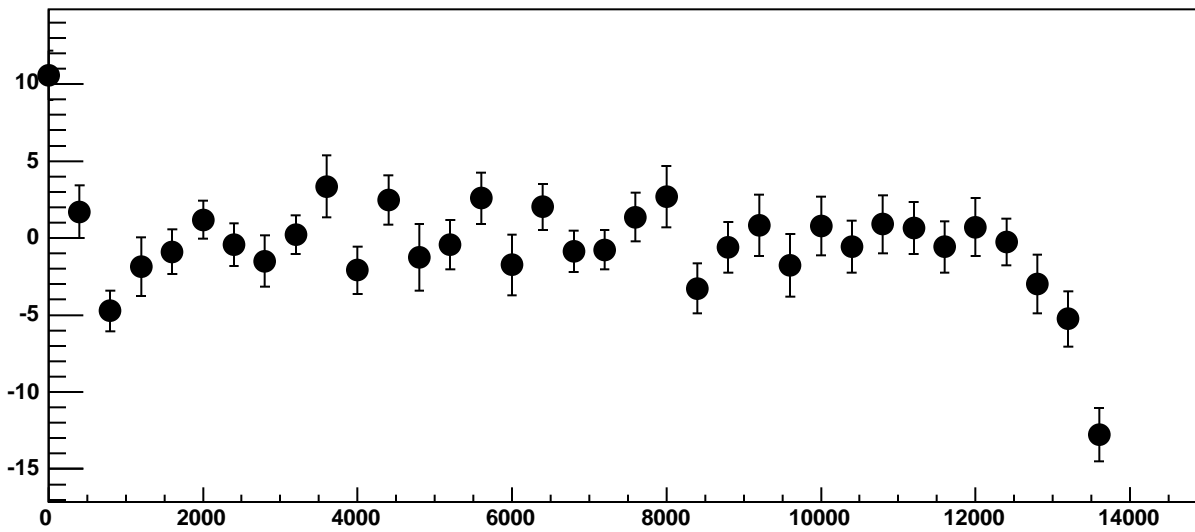


χ^2 / ndf 24.66 / 23
p0 -293.3 ± 0.7191
p1 0.02138 ± 0.000114

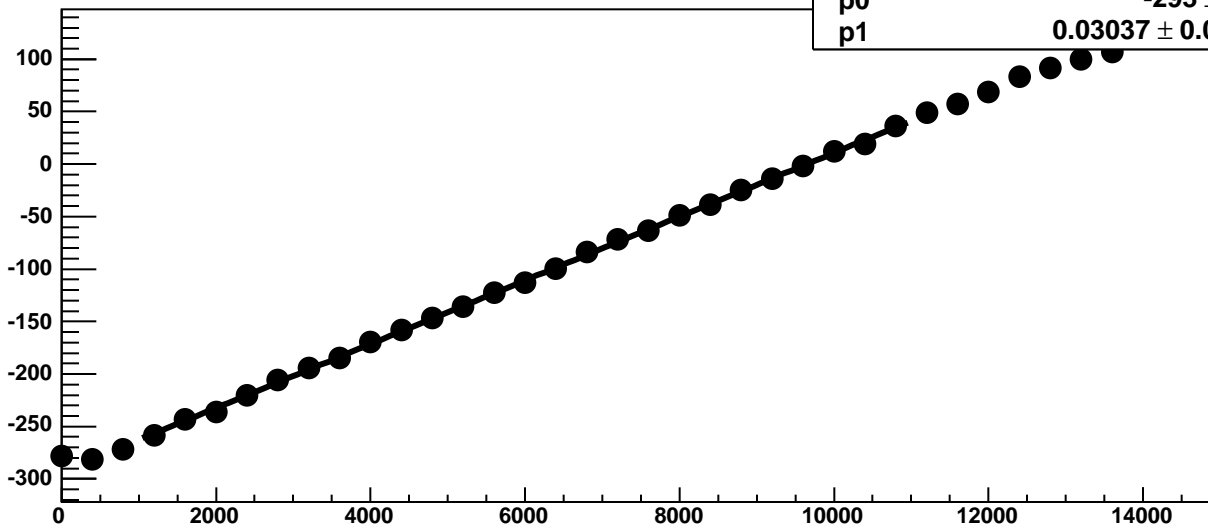
Chip 5, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

30.45 / 23

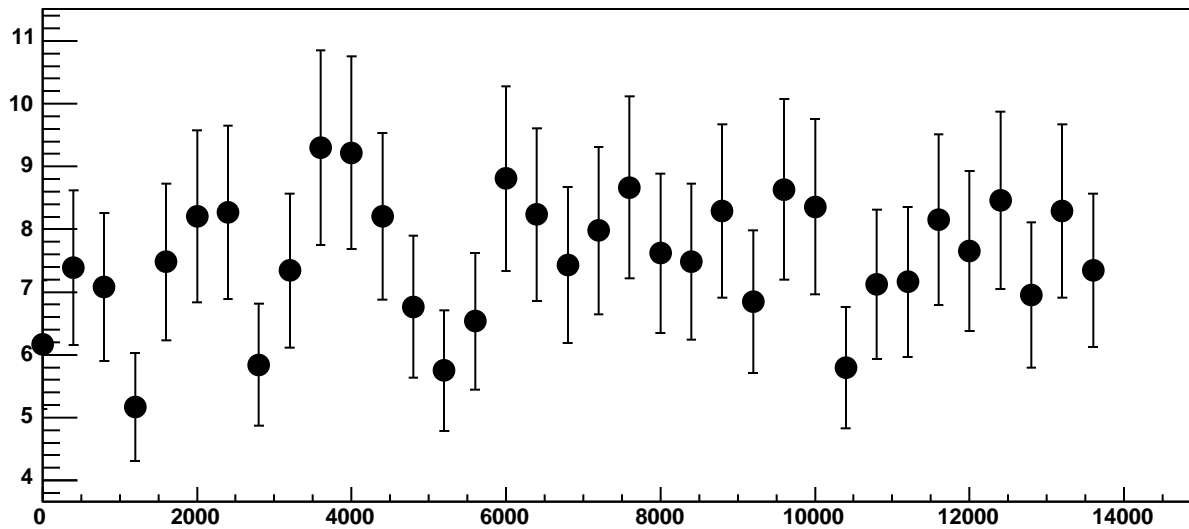
p0

-293 ± 0.7291

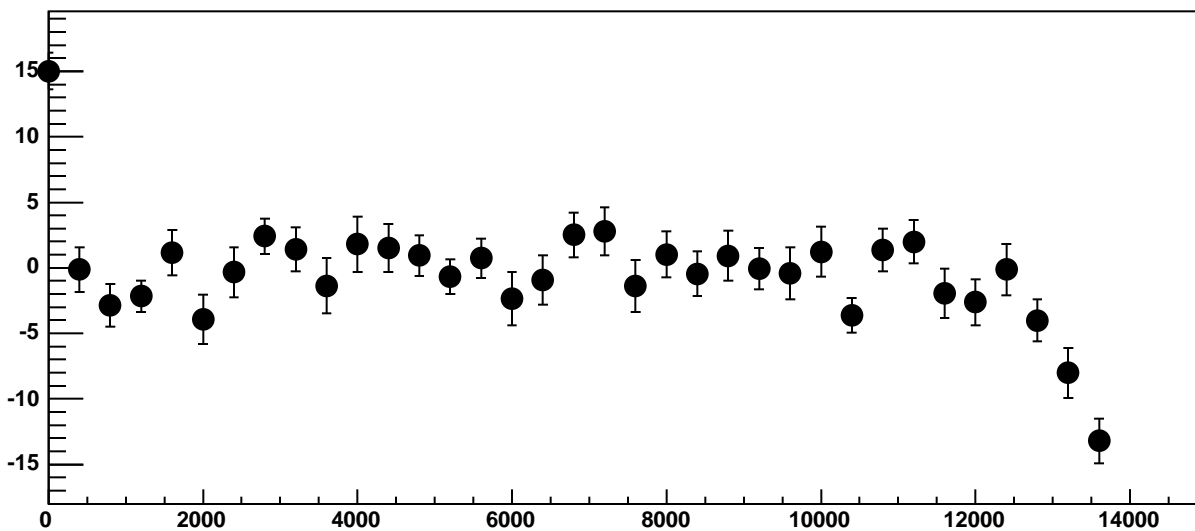
p1

0.03037 ± 0.0001109

Chip 5, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC

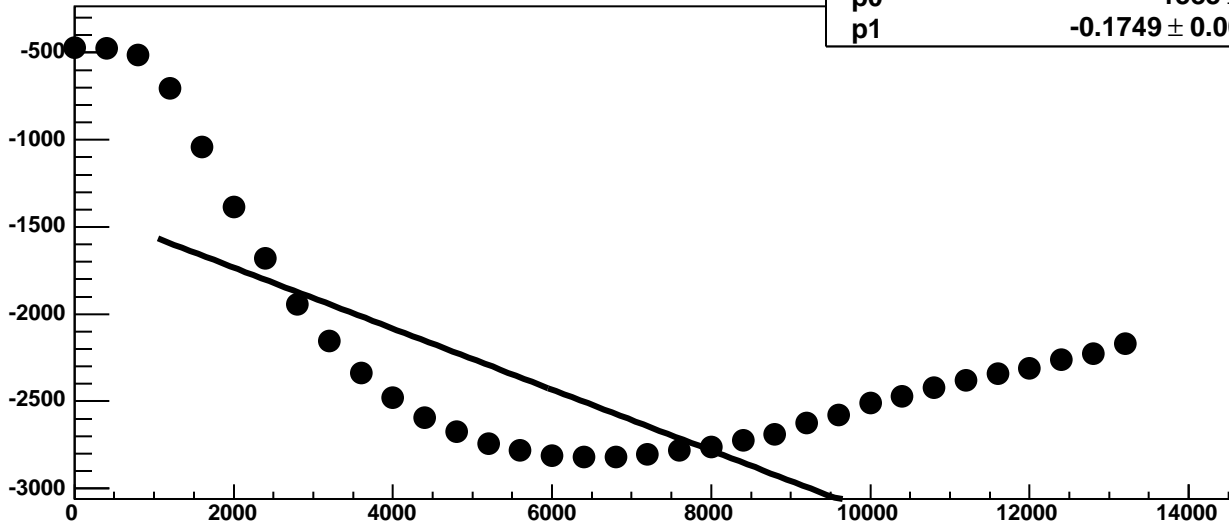


Chip 5, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC

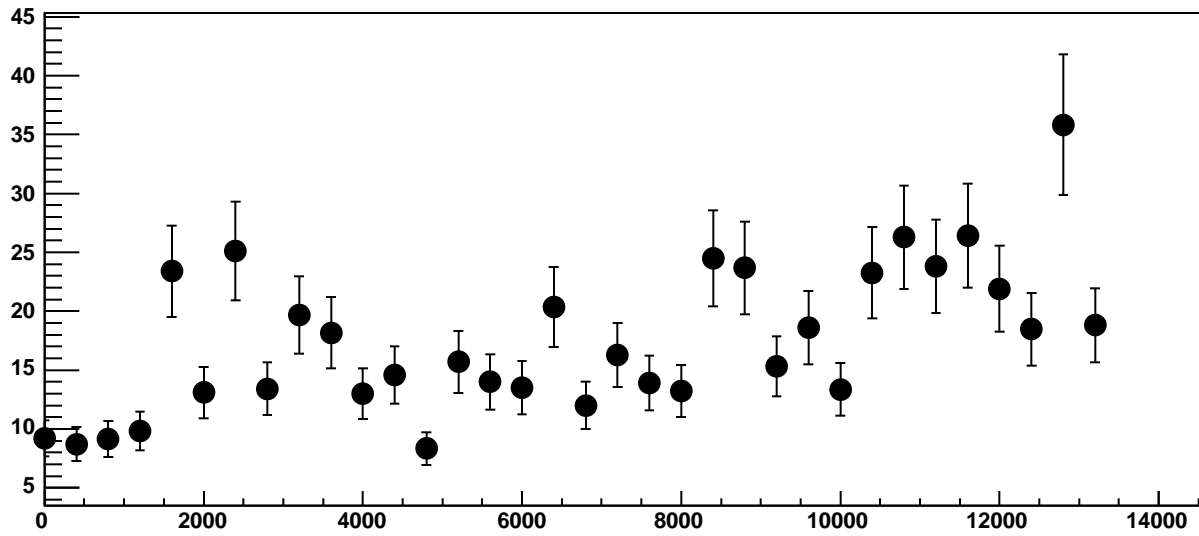


Chip 5, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

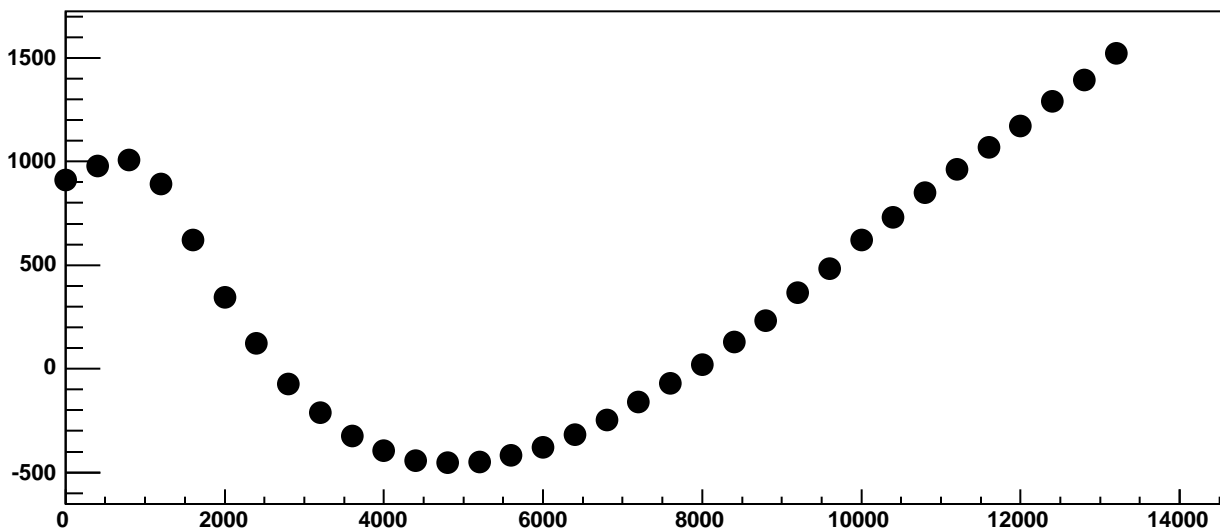
χ^2 / ndf 4.506e+05 / 23
p0 -1383 ± 1.544
p1 -0.1749 ± 0.0002533



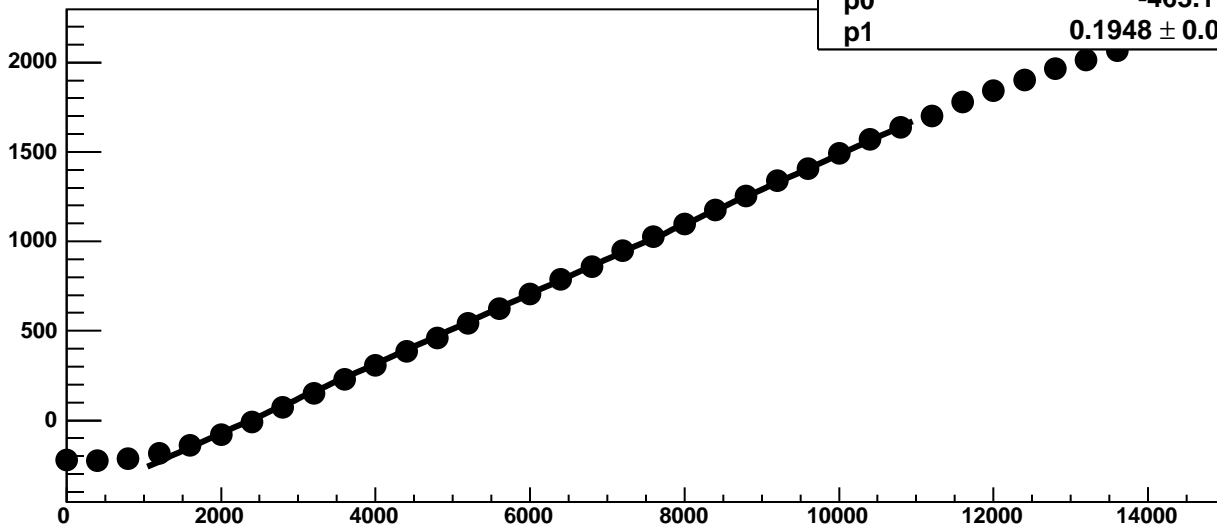
Chip 5, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



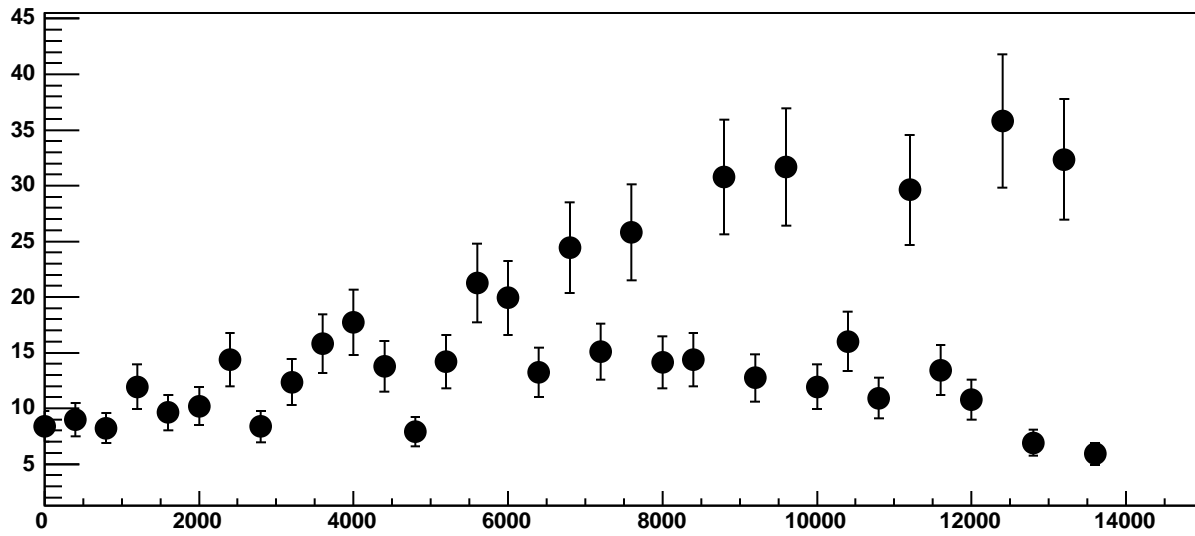
Chip 5, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



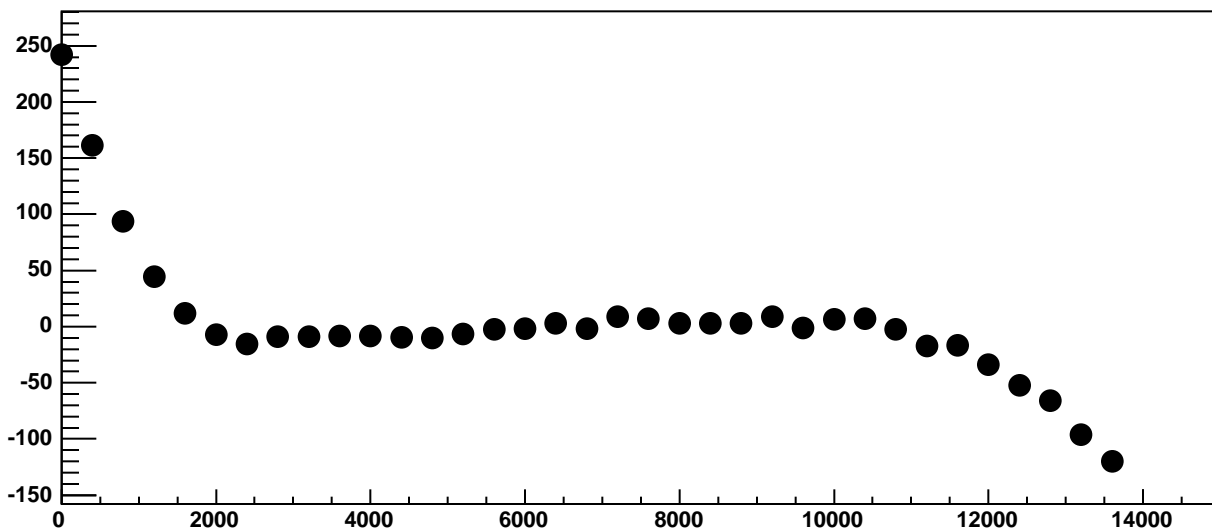
Chip 5, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



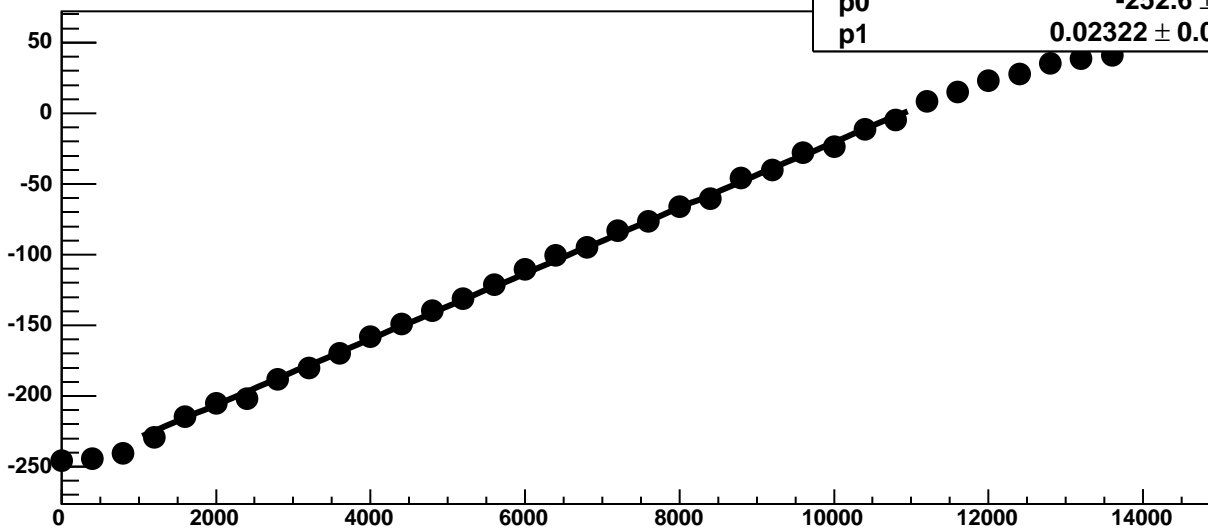
Chip 5, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



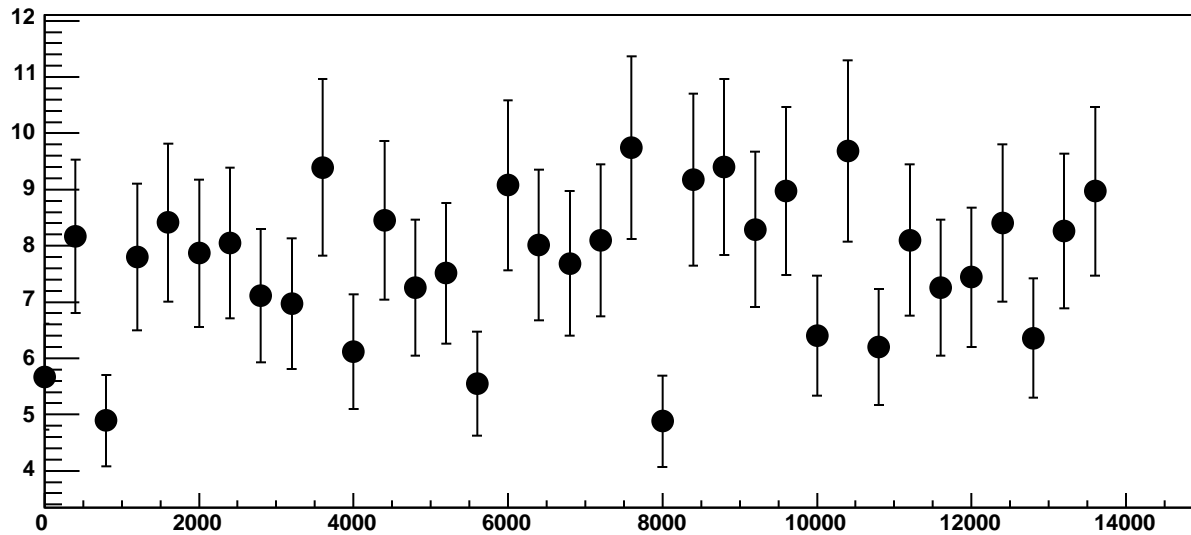
Chip 5, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



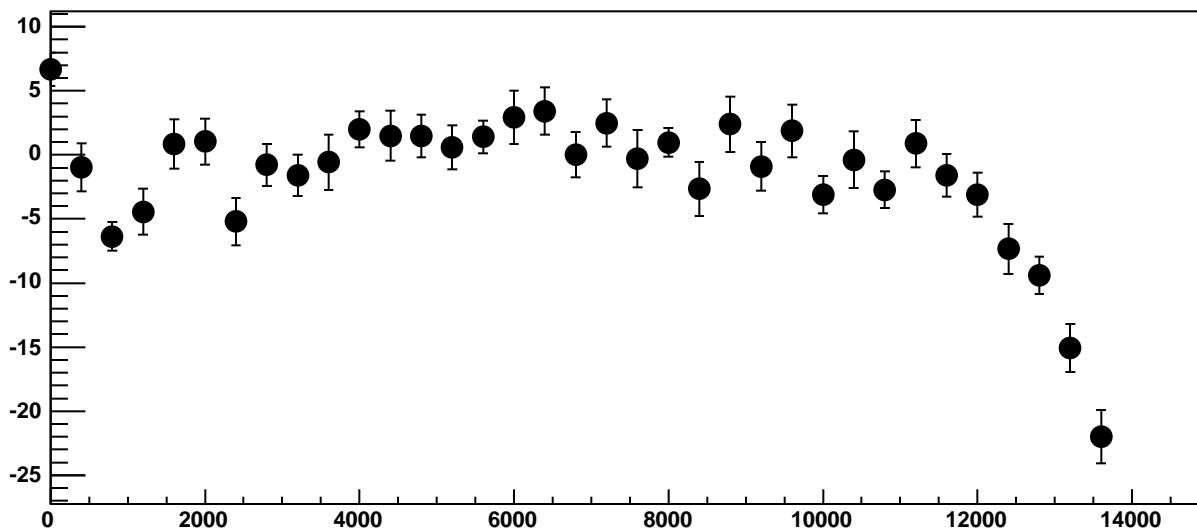
Chip 5, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC



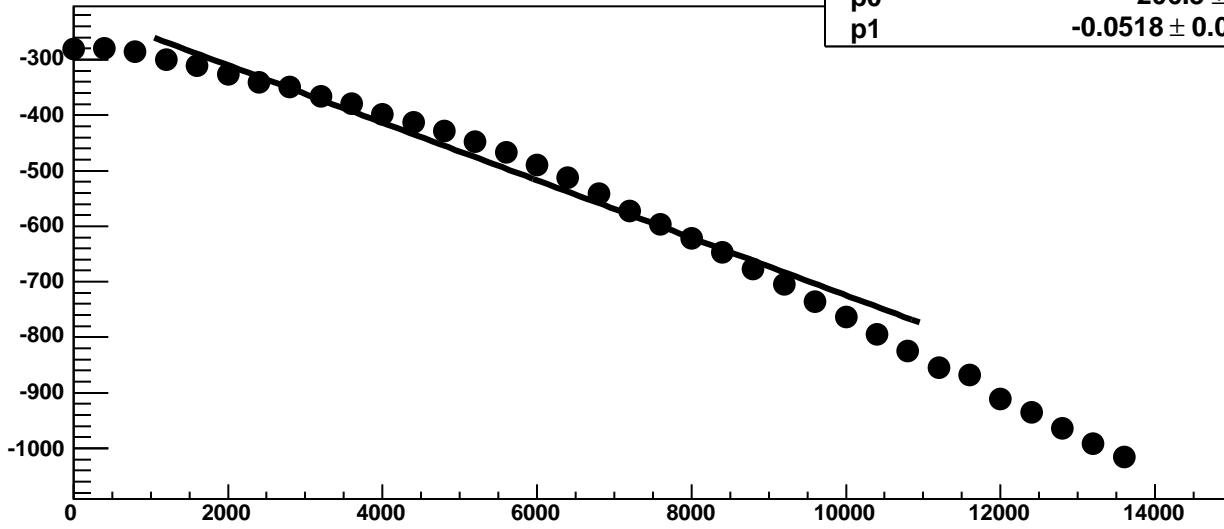
Chip 5, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC

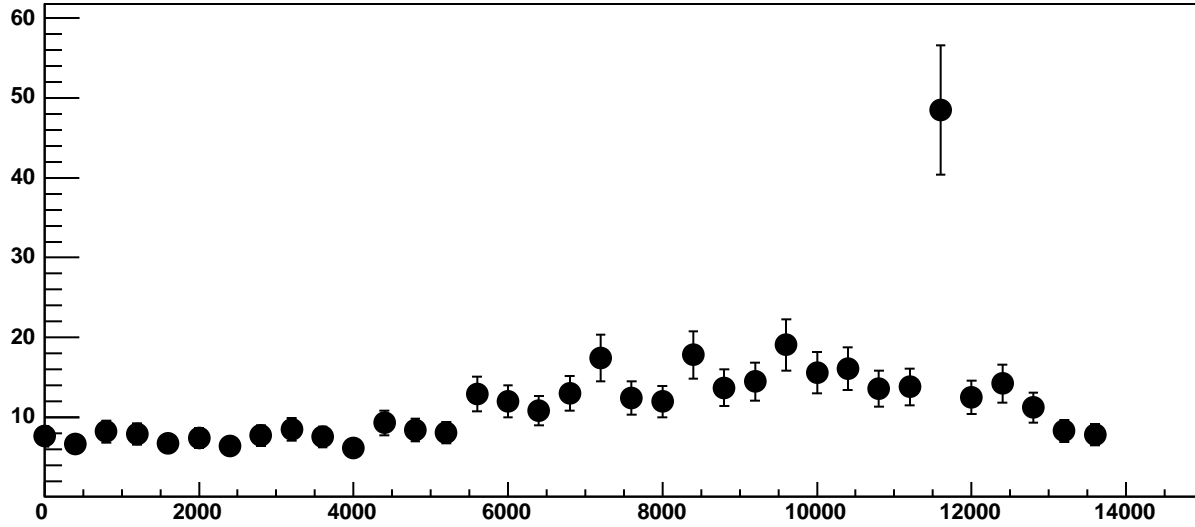


Chip 5, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

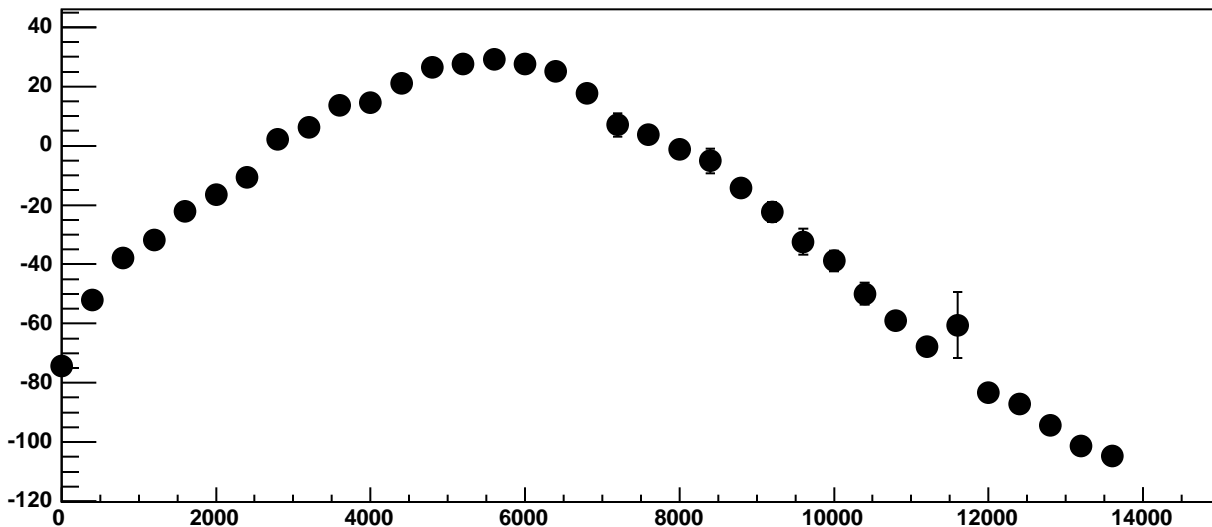


χ^2 / ndf 2468 / 23
p0 -206.3 ± 0.8867
p1 -0.0518 ± 0.0001741

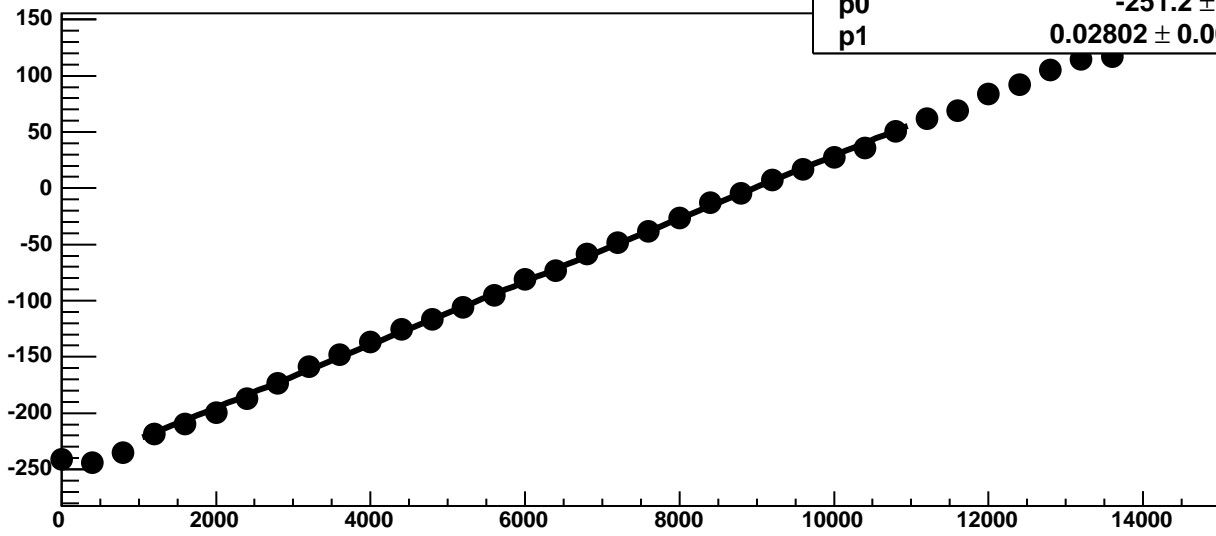
Chip 5, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

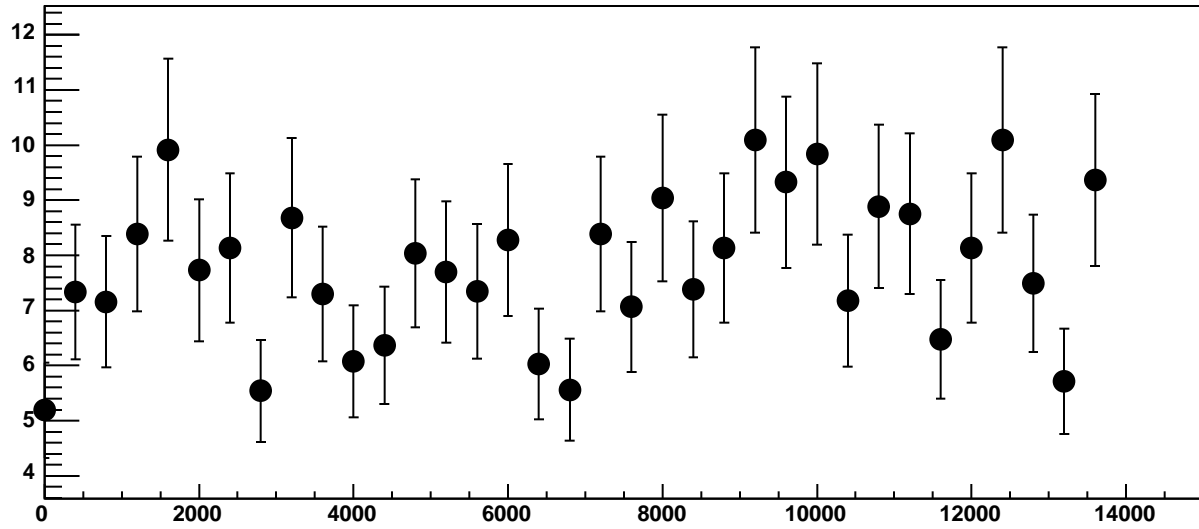


Chip 5, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

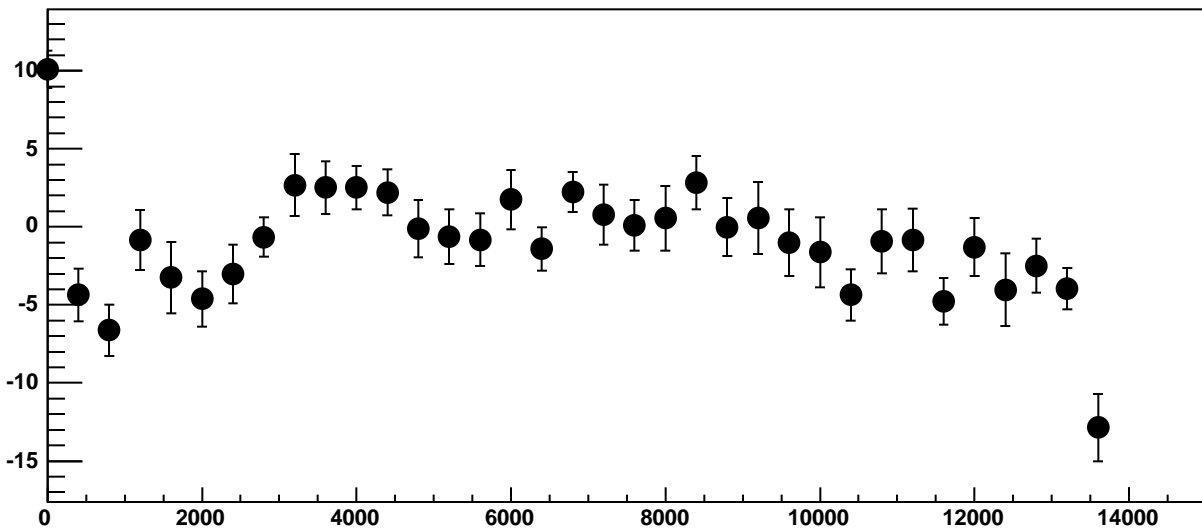


χ^2 / ndf 37.81 / 23
p0 -251.2 ± 0.8207
p1 0.02802 ± 0.0001288

Chip 5, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC

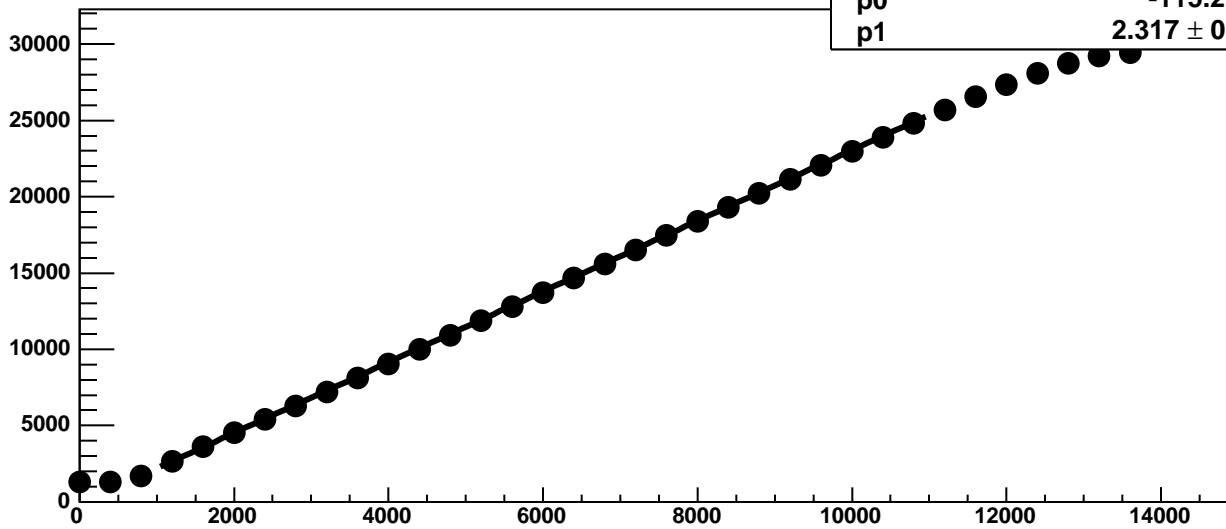


Chip 5, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

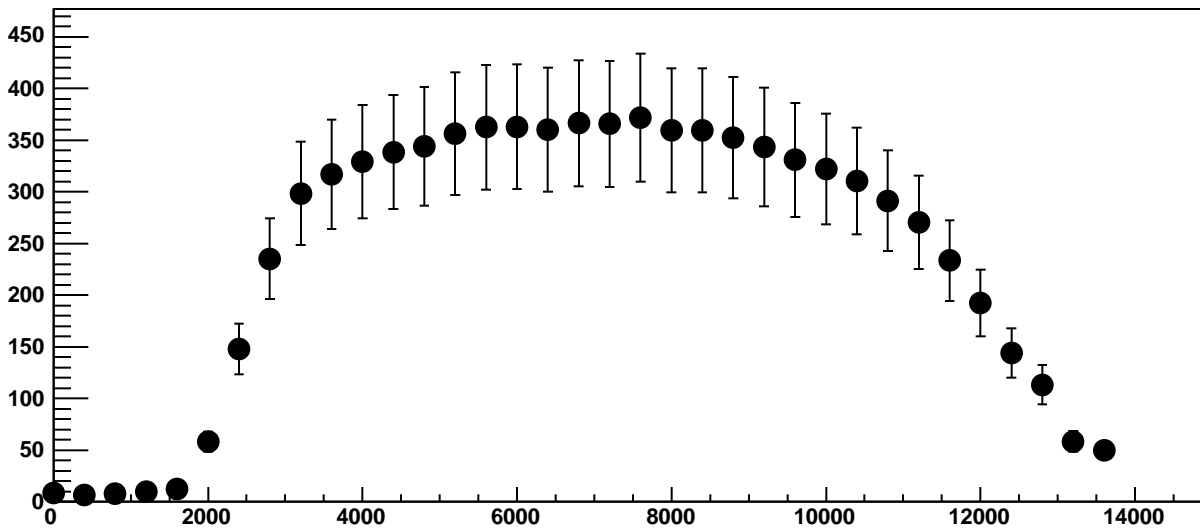


Chip 5, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC

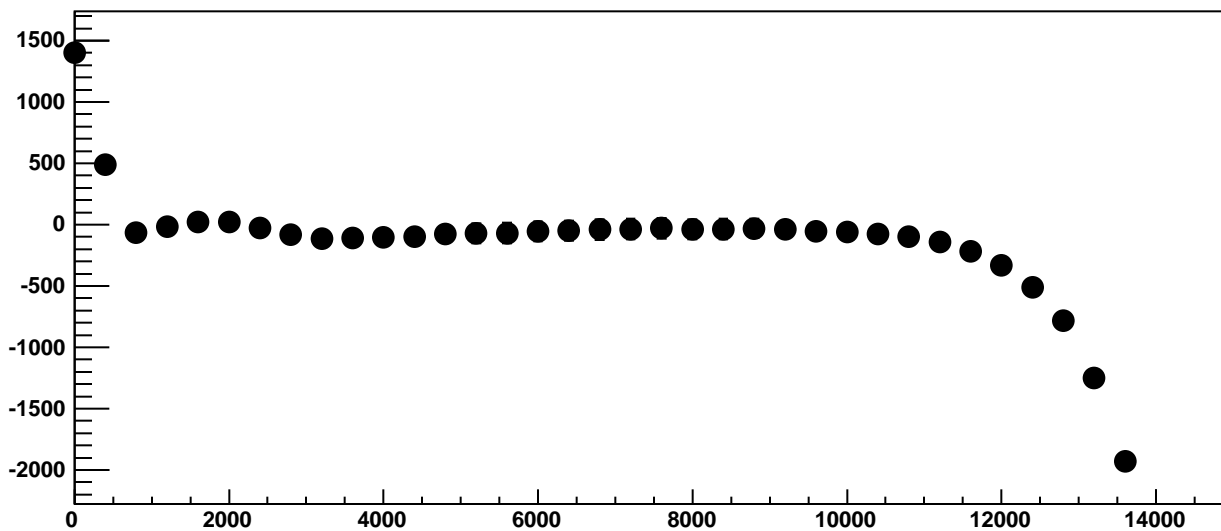
χ^2 / ndf 127.8 / 23
p0 -115.2 ± 4.164
p1 2.317 ± 0.002665



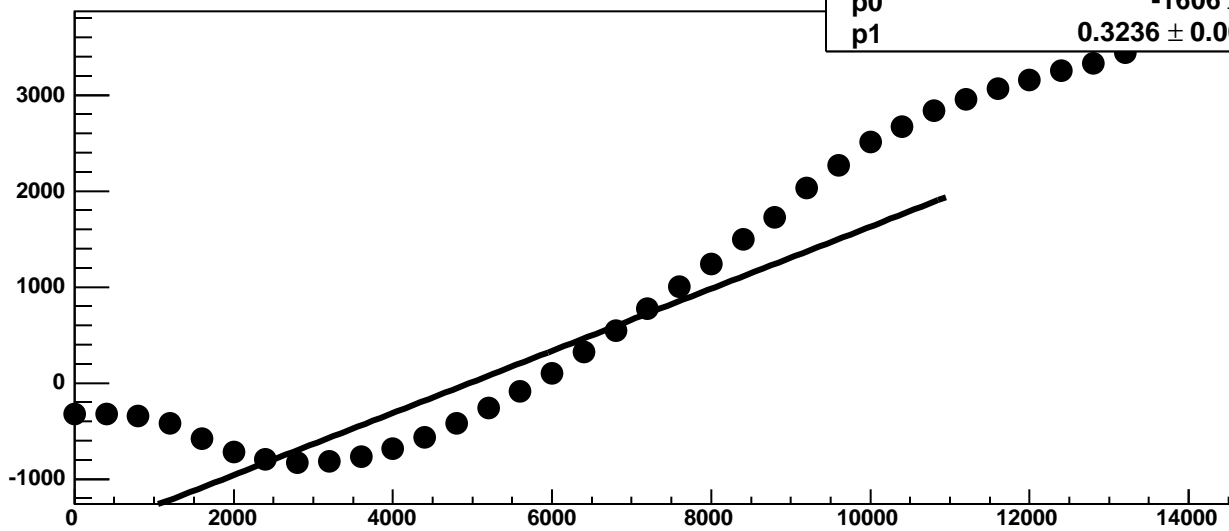
Chip 5, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC

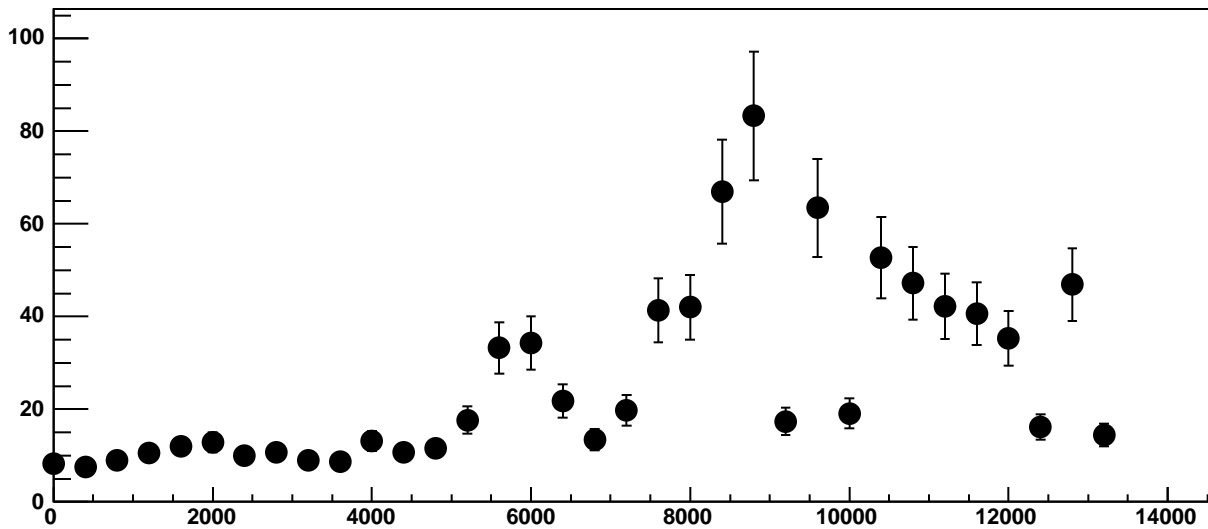


Chip 5, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

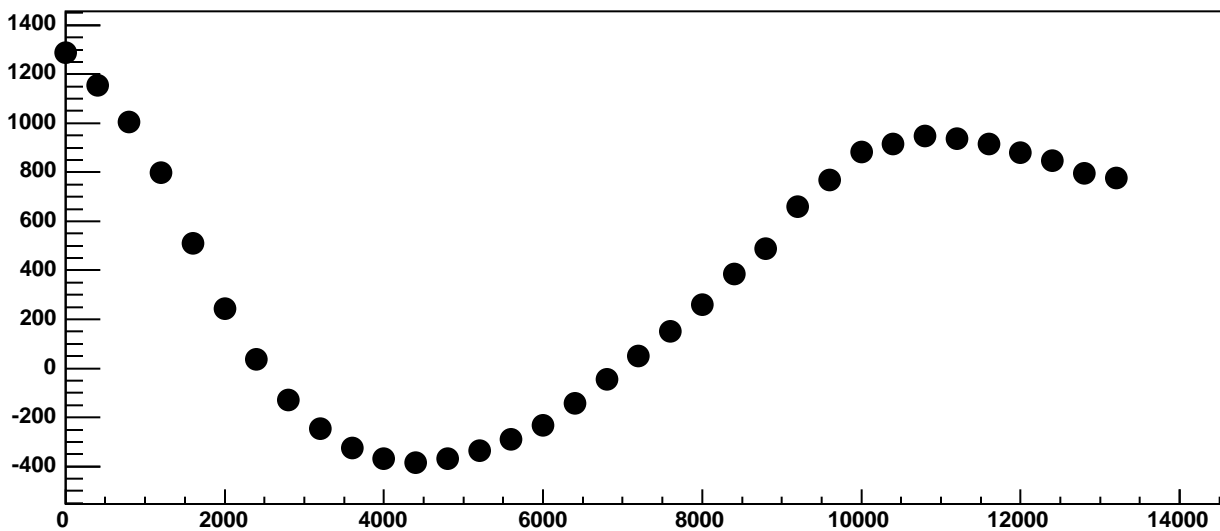


χ^2 / ndf 3.492e+05 / 23
p0 -1606 ± 1.418
p1 0.3236 ± 0.0003132

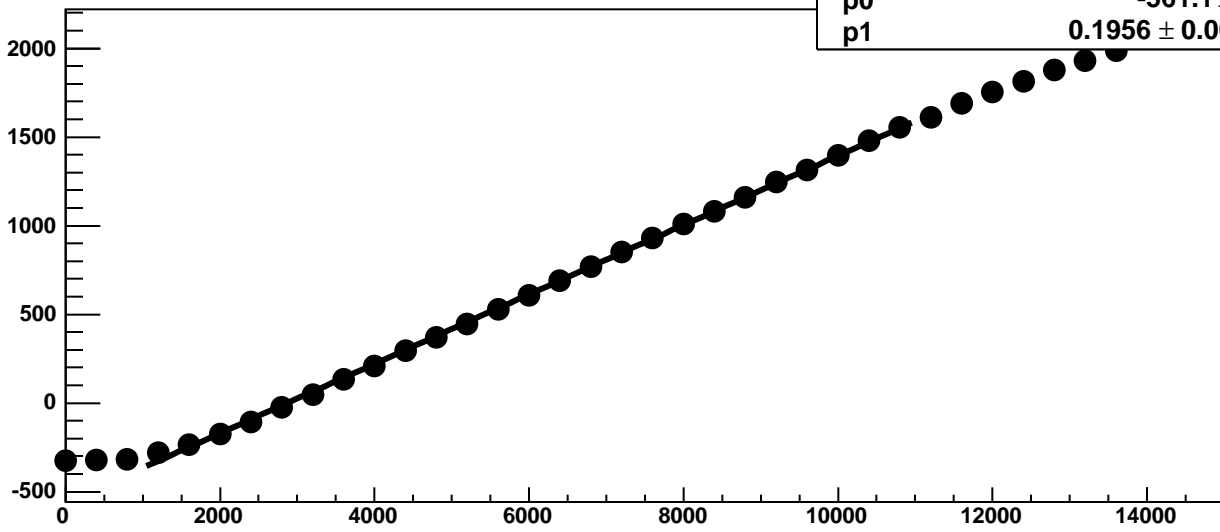
Chip 5, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



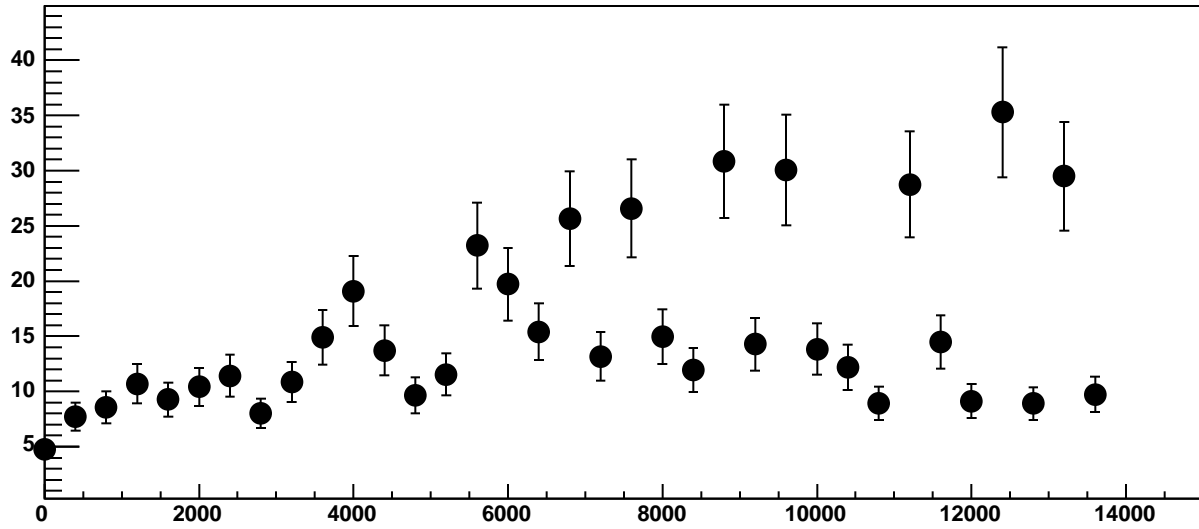
Chip 5, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



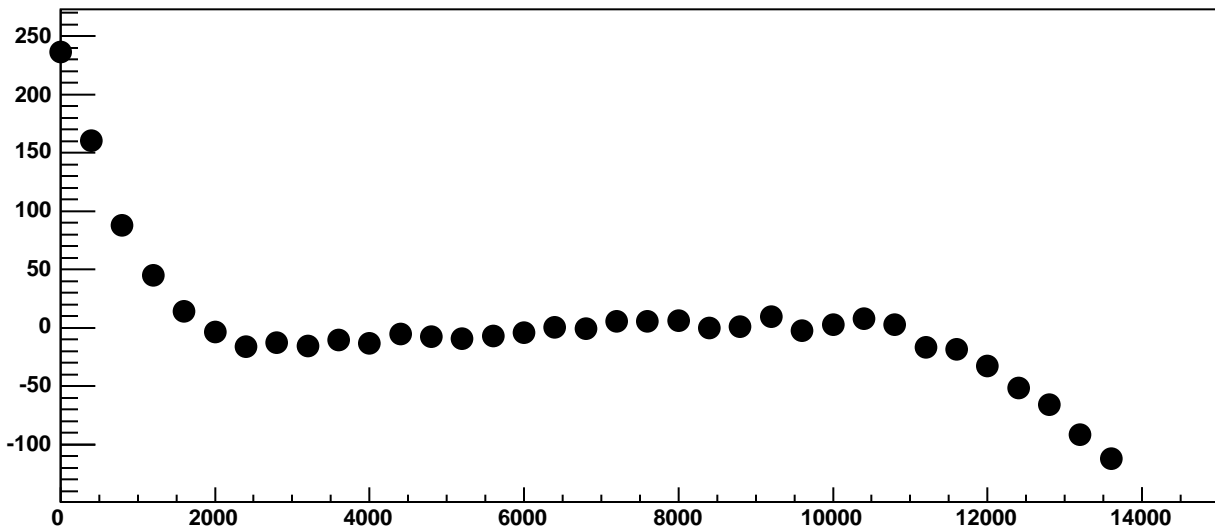
Chip 5, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



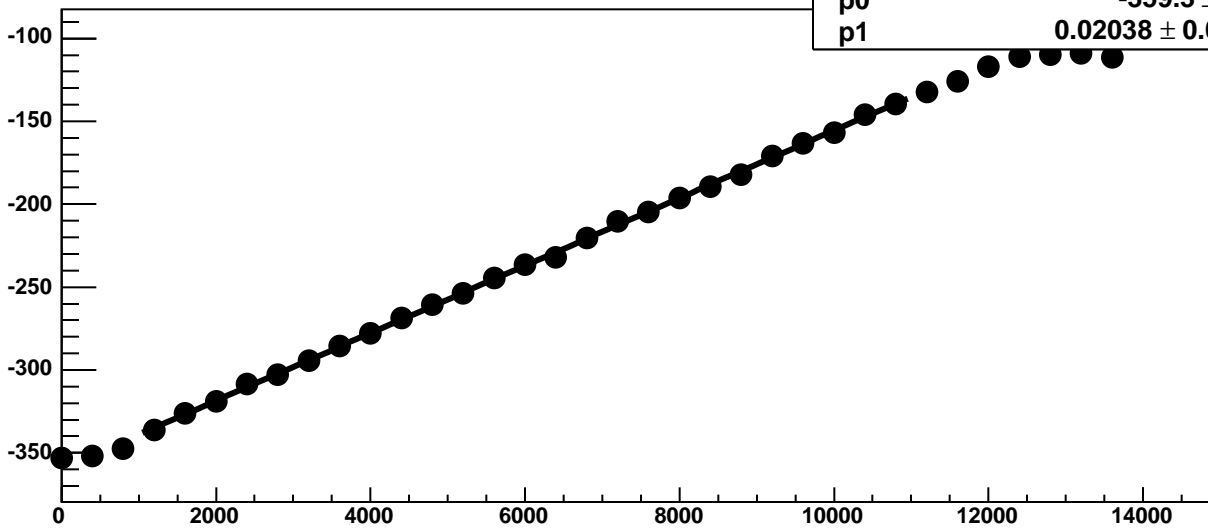
Chip 5, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



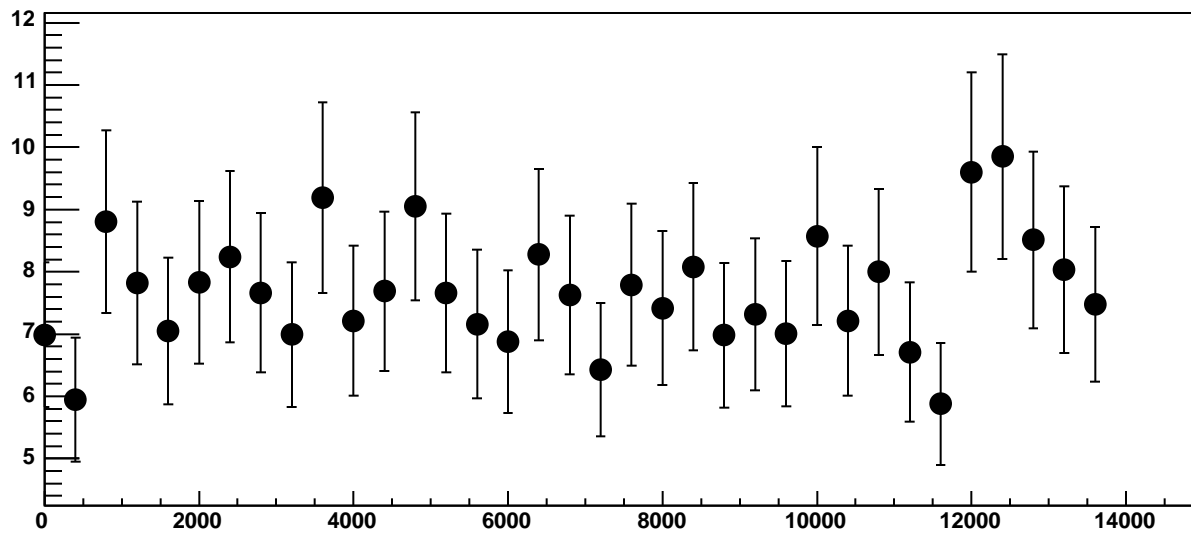
Chip 5, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



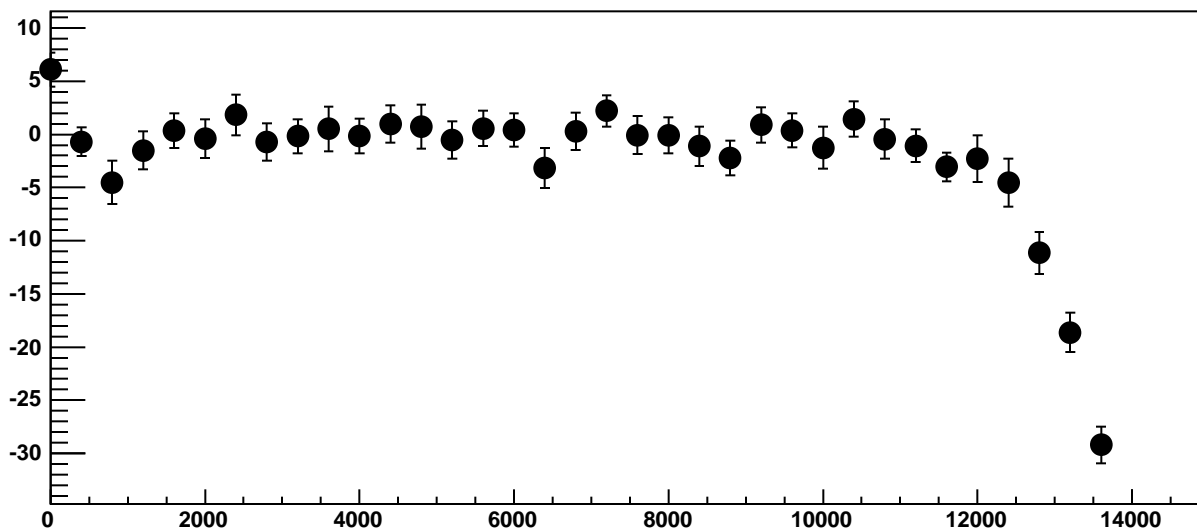
Chip 5, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



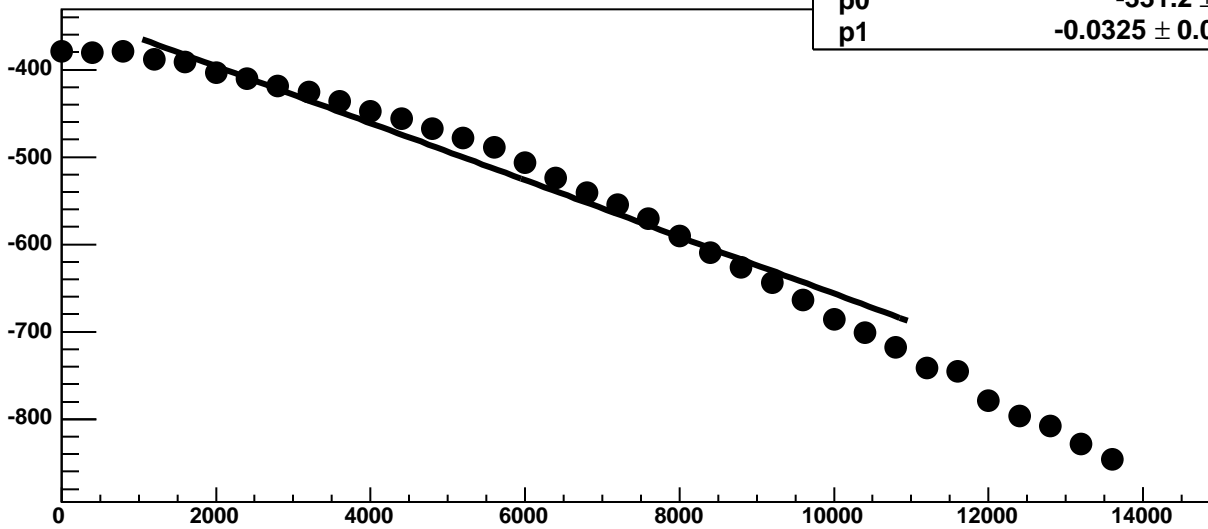
Chip 5, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

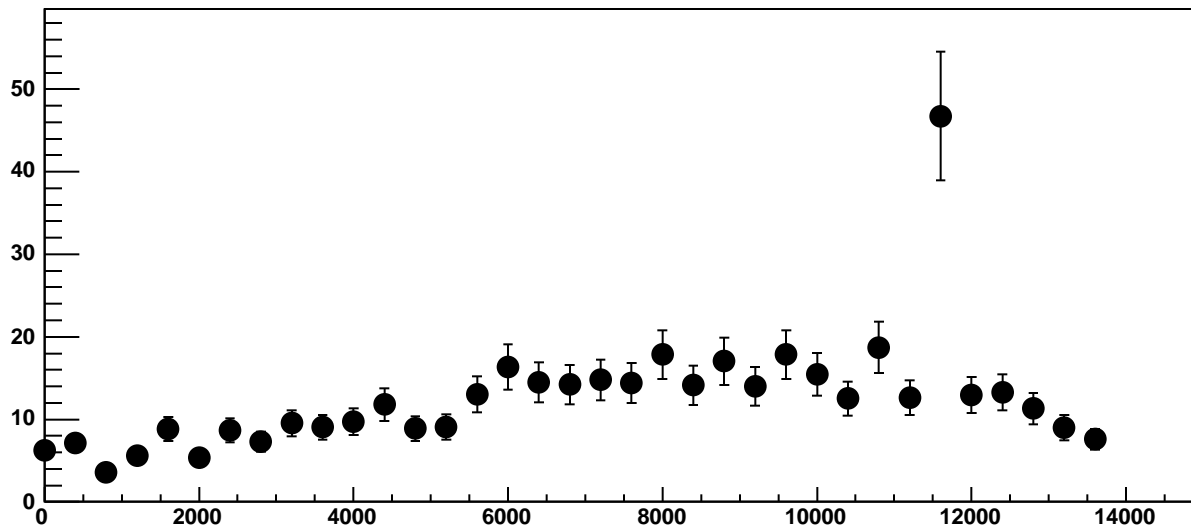


Chip 5, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

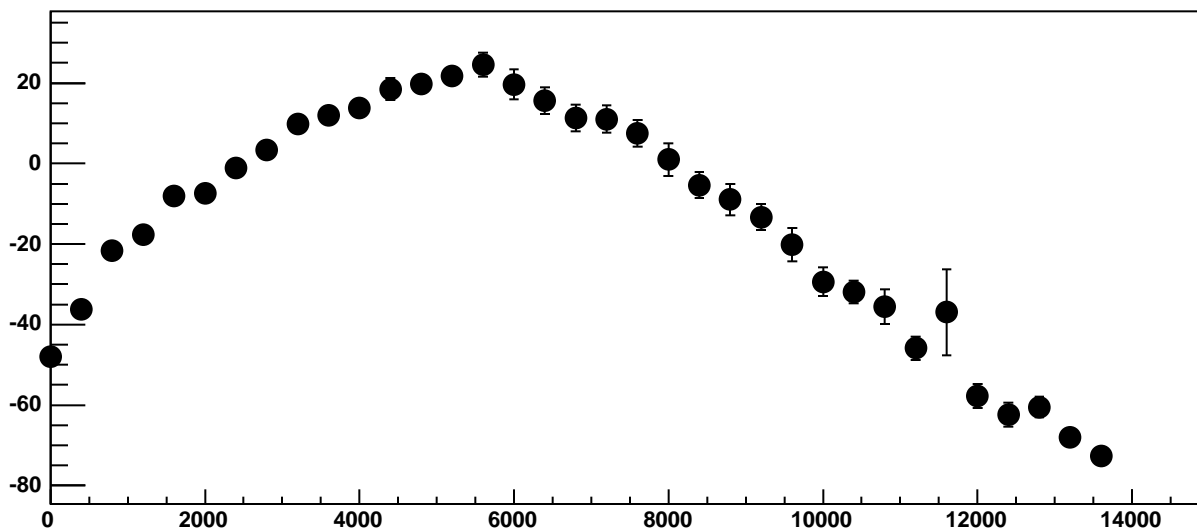


χ^2 / ndf 1036 / 23
p0 -331.2 ± 0.8573
p1 -0.0325 ± 0.0001728

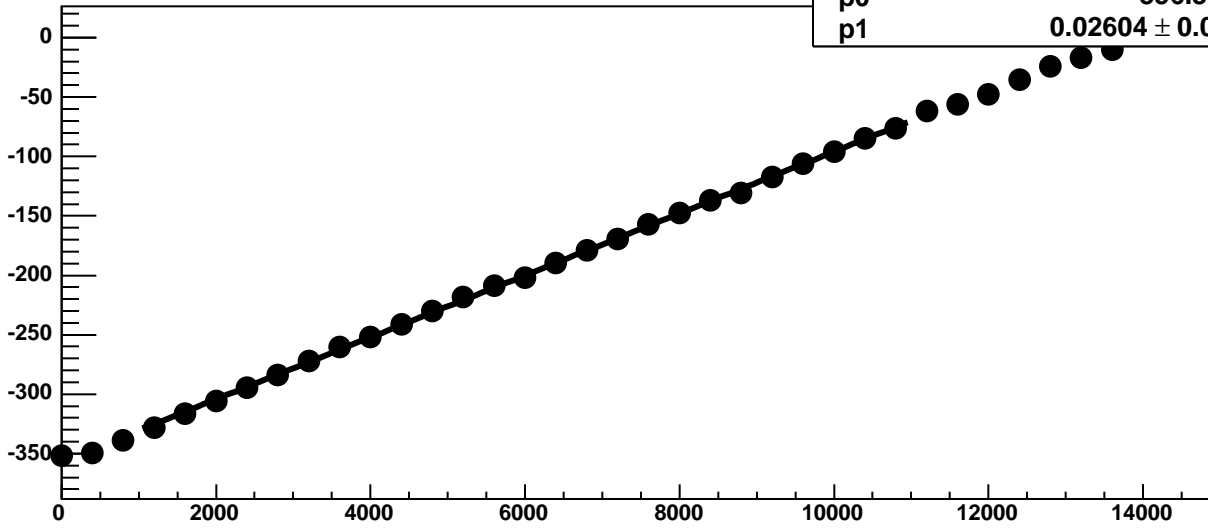
Chip 5, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



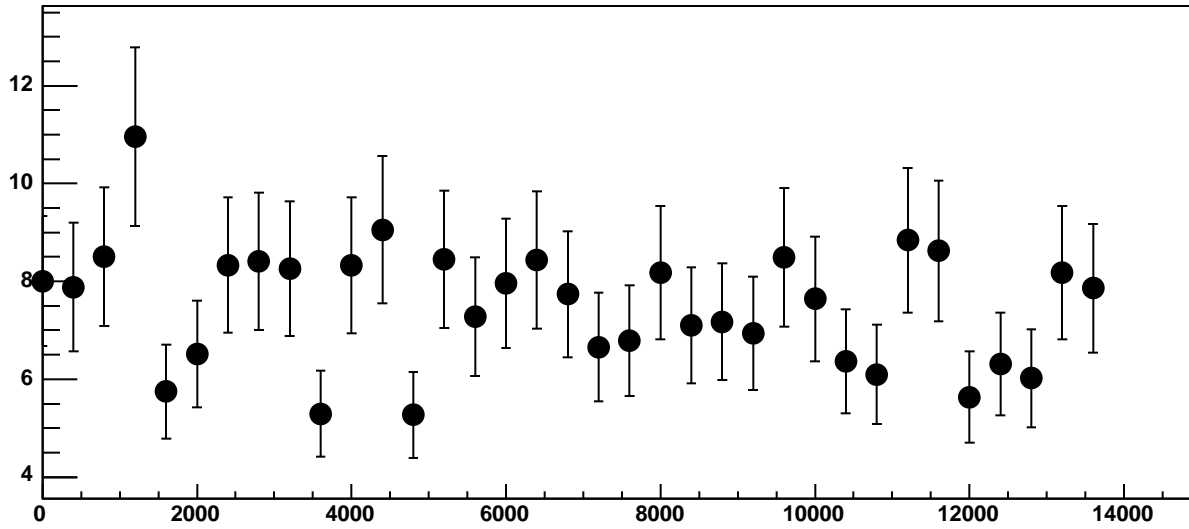
Chip 5, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC



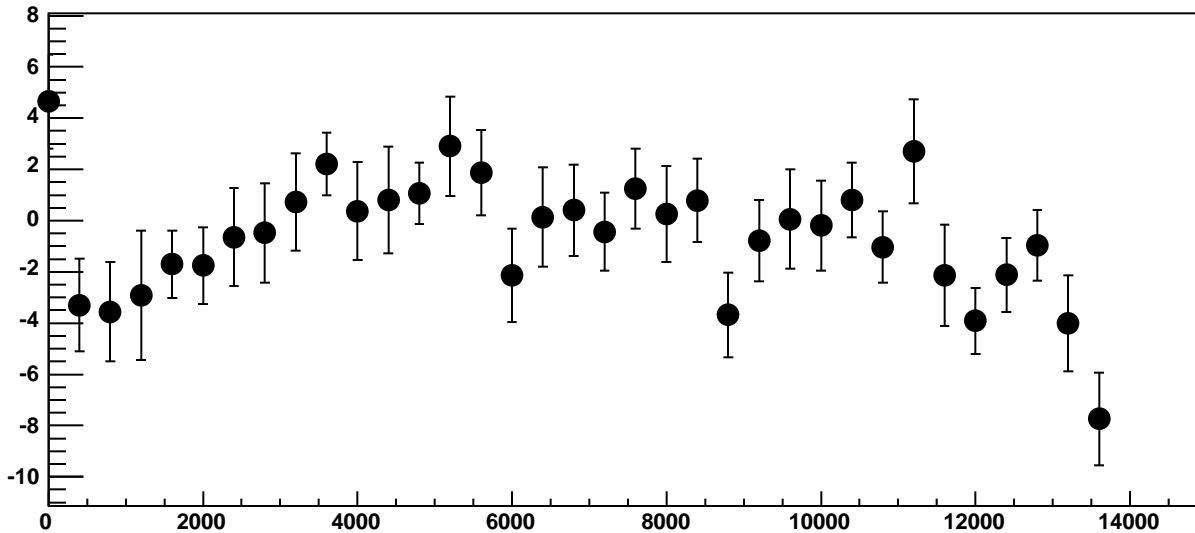
Chip 5, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



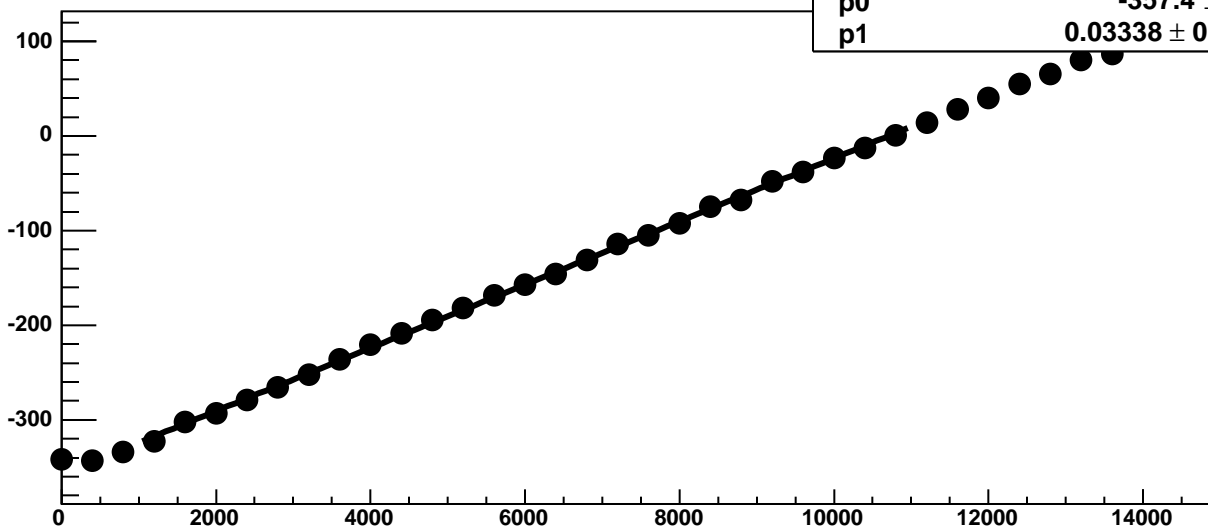
Chip 5, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

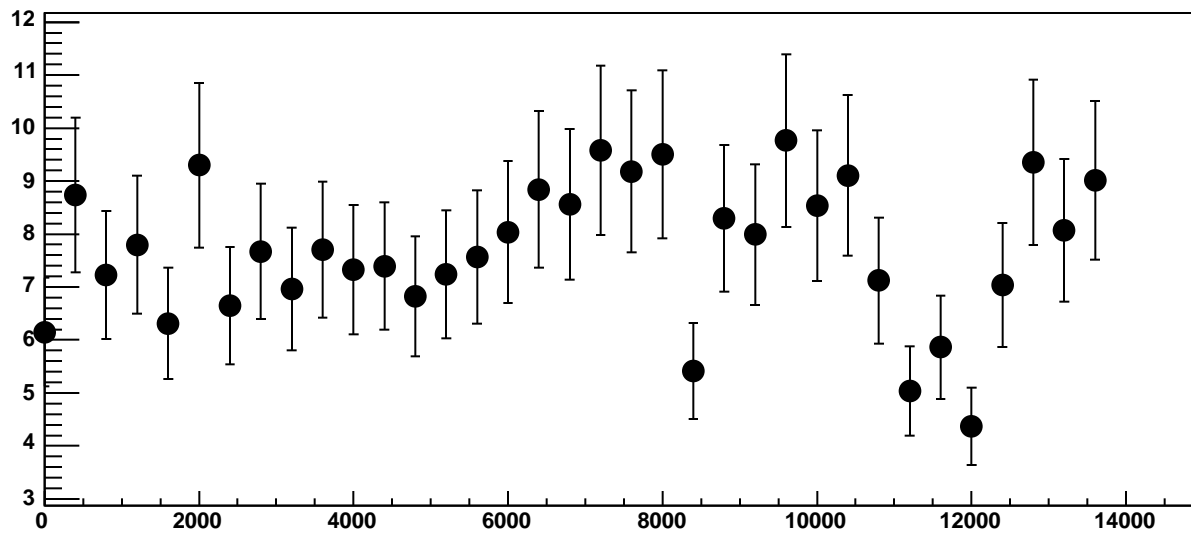


Chip 5, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

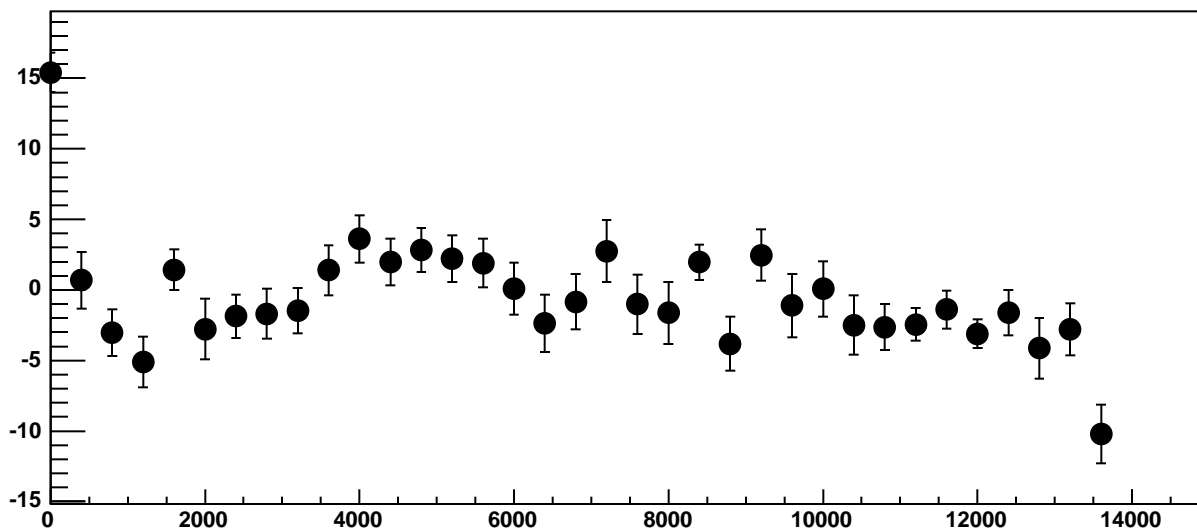


χ^2 / ndf 43.47 / 23
p0 -357.4 ± 0.7861
p1 0.03338 ± 0.000122

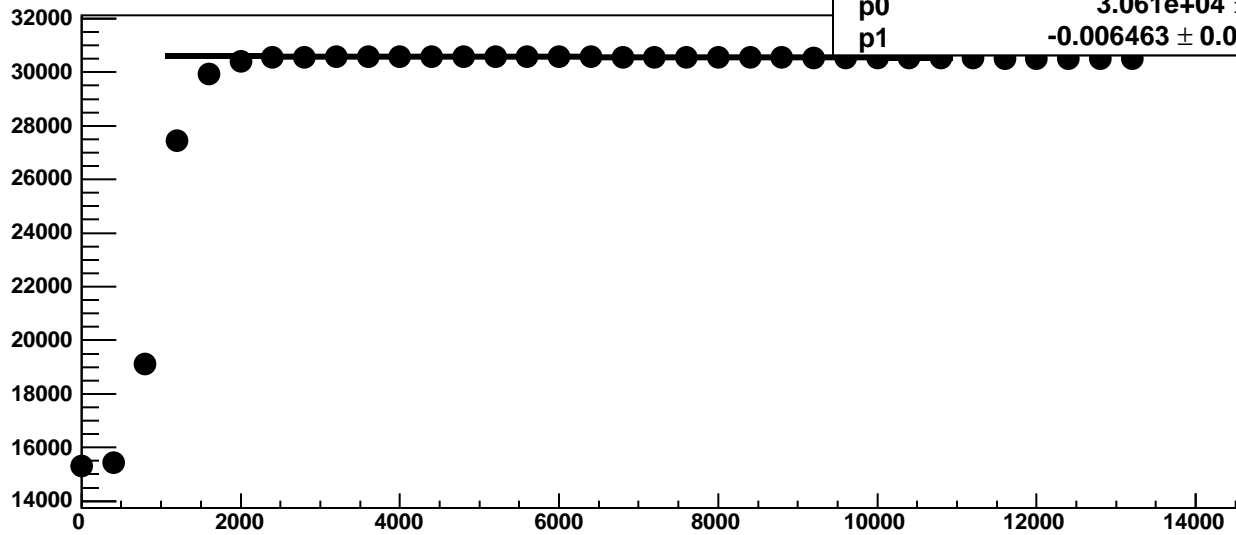
Chip 5, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



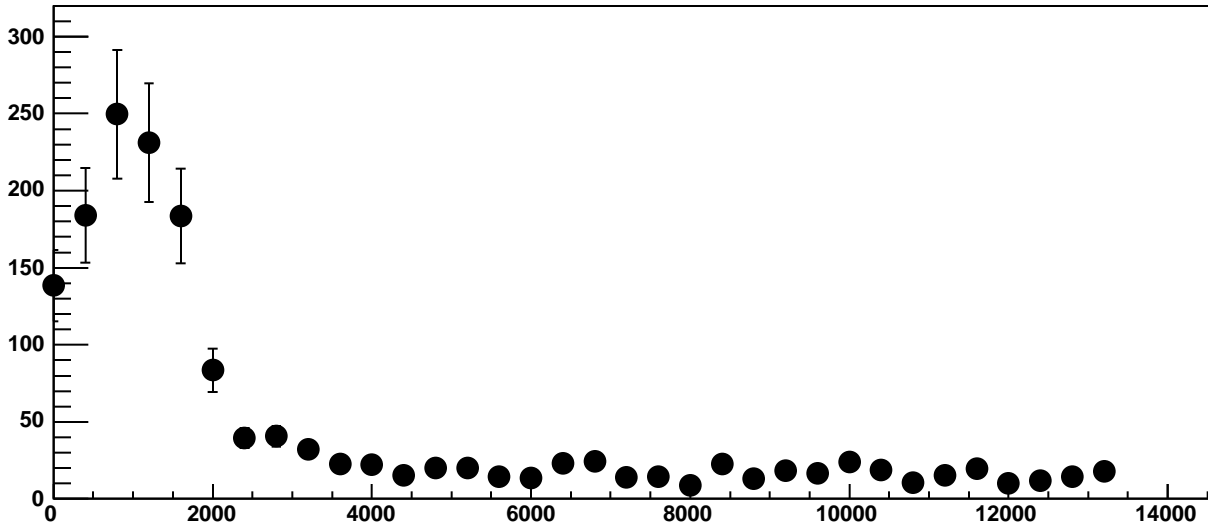
Chip 5, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



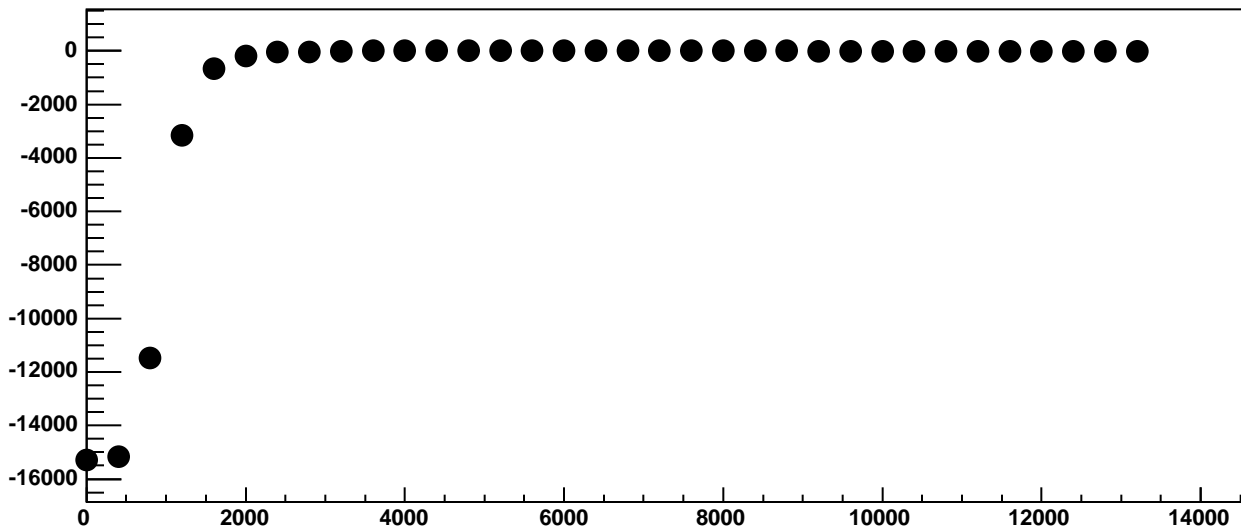
Chip 5, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC



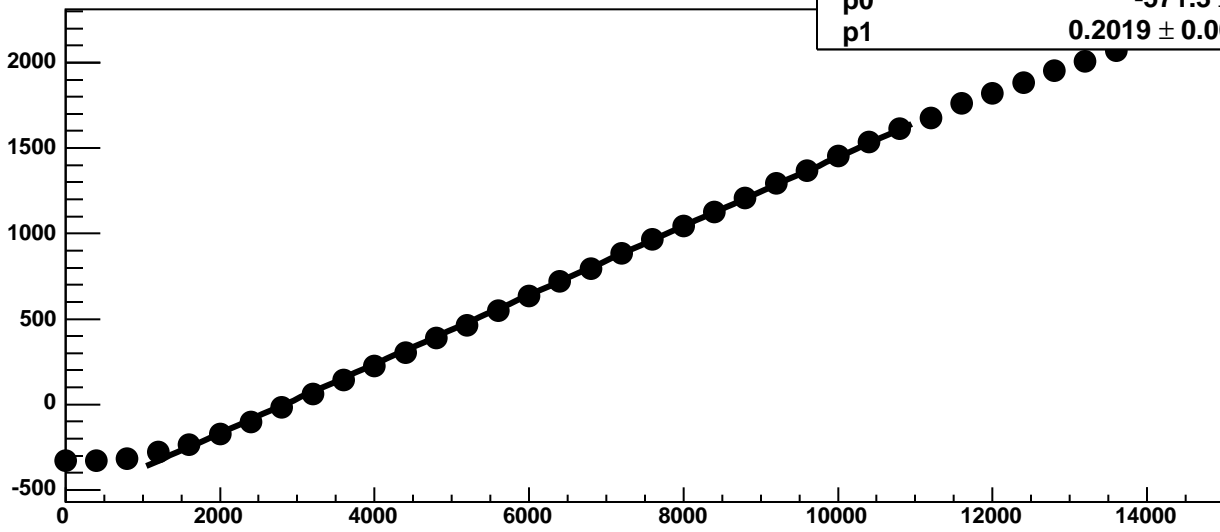
Chip 5, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC



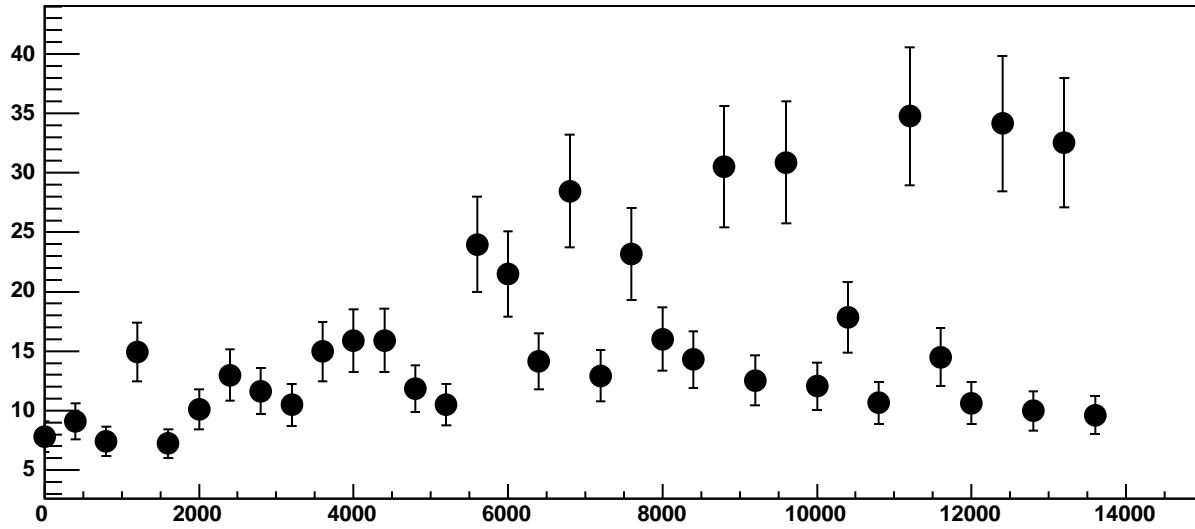
Chip 5, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC



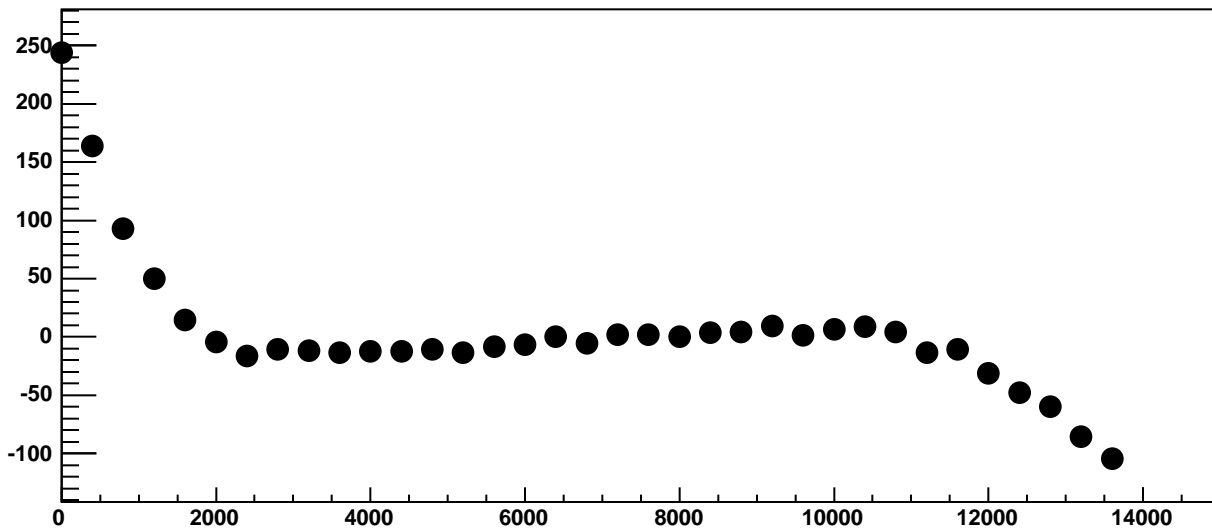
Chip 5, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



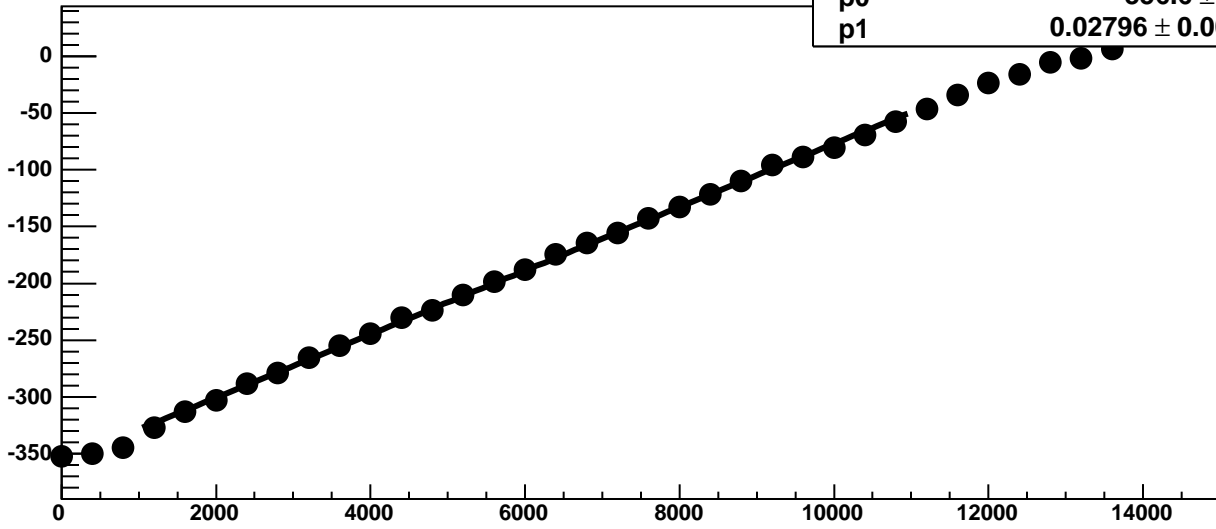
Chip 5, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

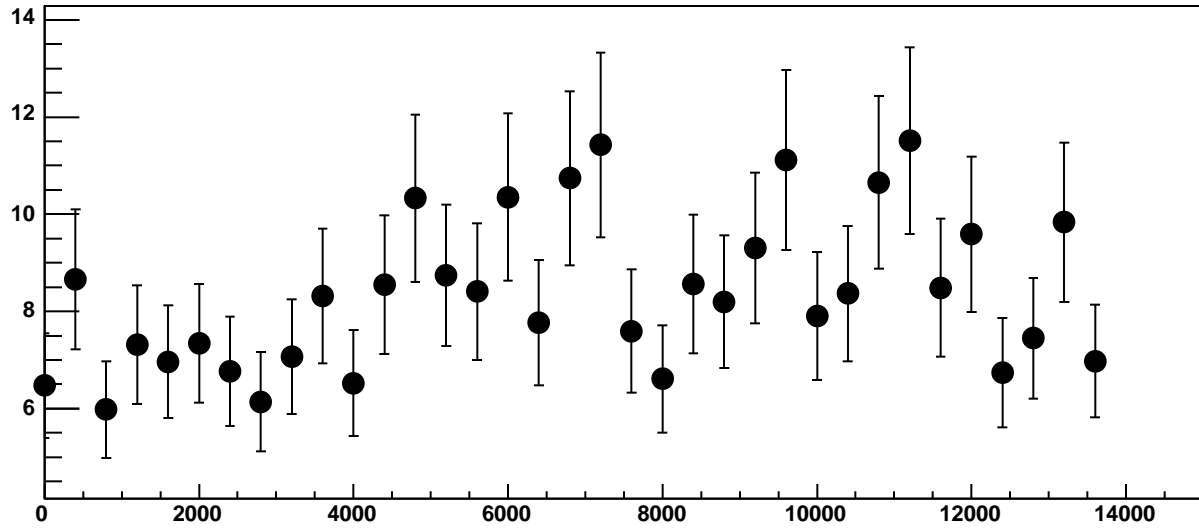


Chip 5, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

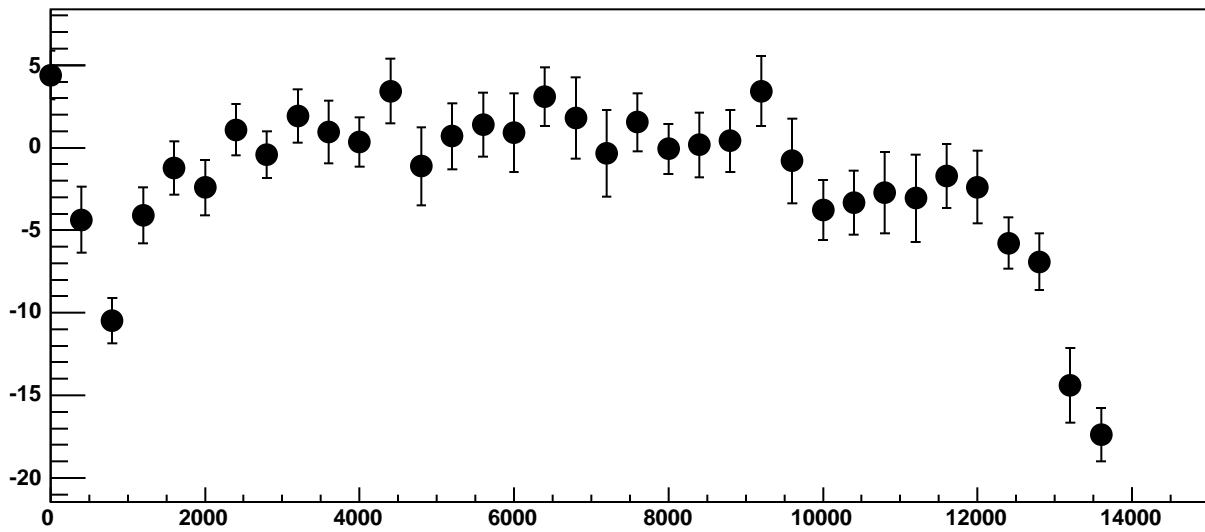


χ^2 / ndf 30.64 / 23
p0 -356.6 ± 0.7868
p1 0.02796 ± 0.0001267

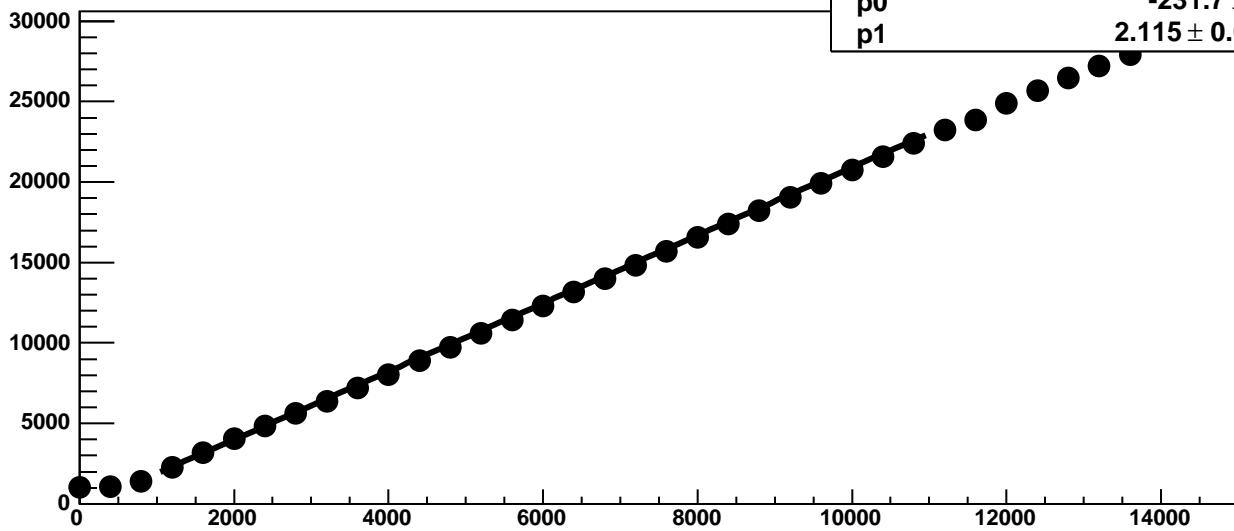
Chip 5, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



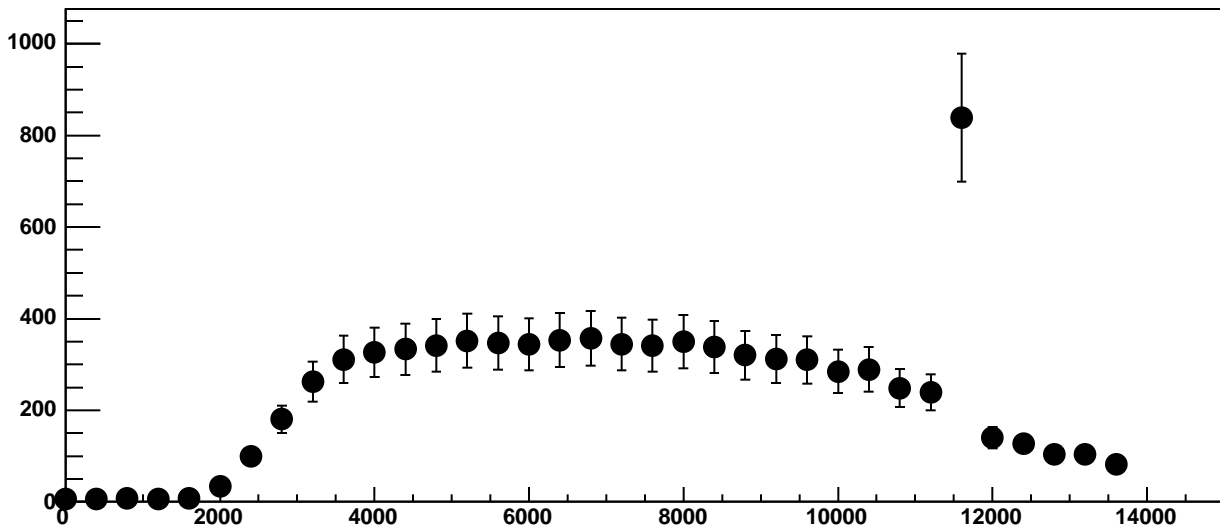
Chip 5, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



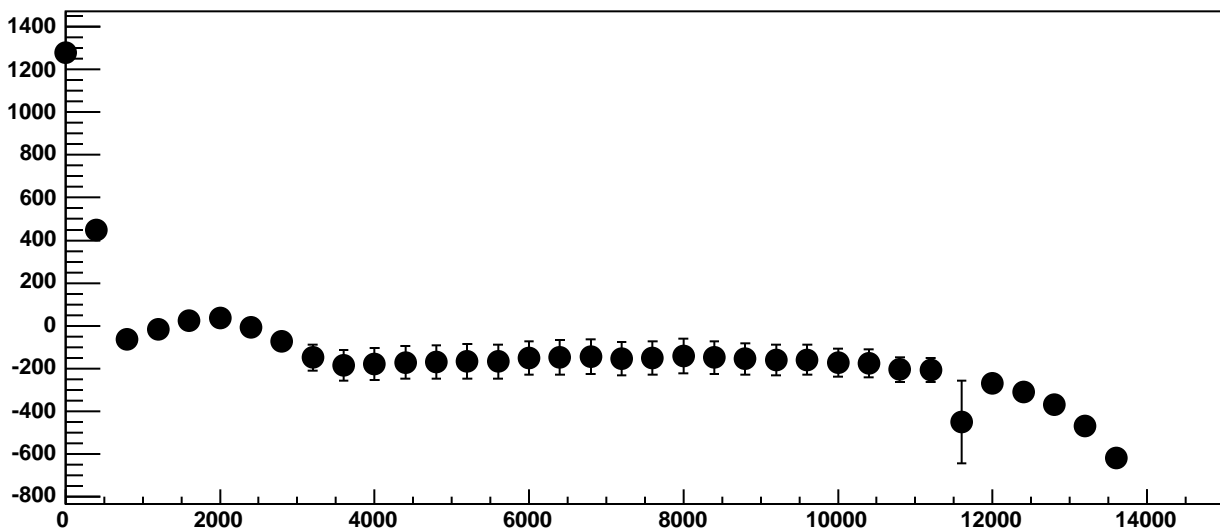
Chip 5, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC



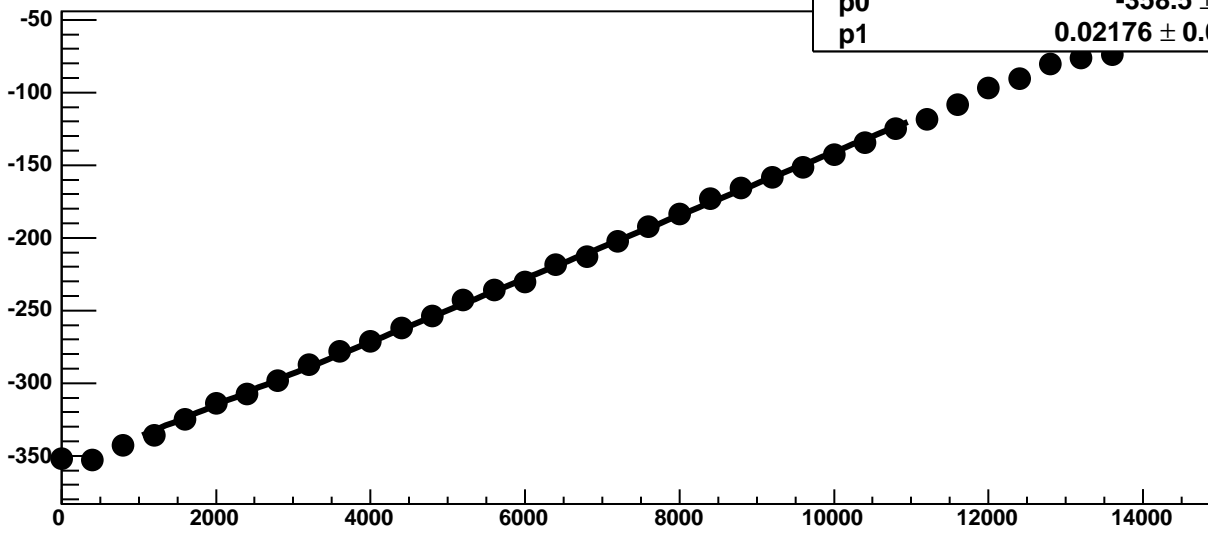
Chip 5, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC

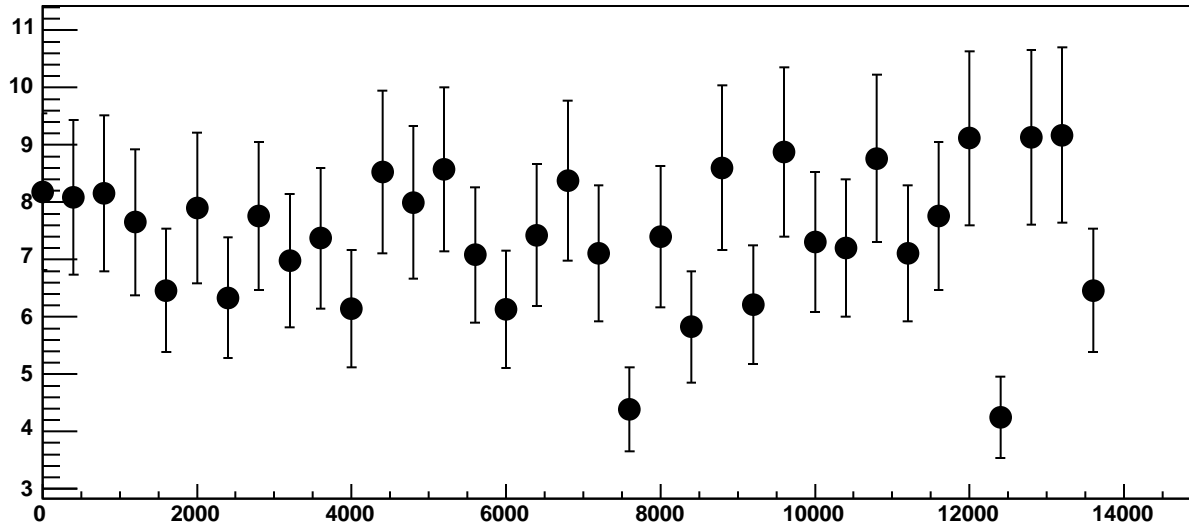


Chip 5, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

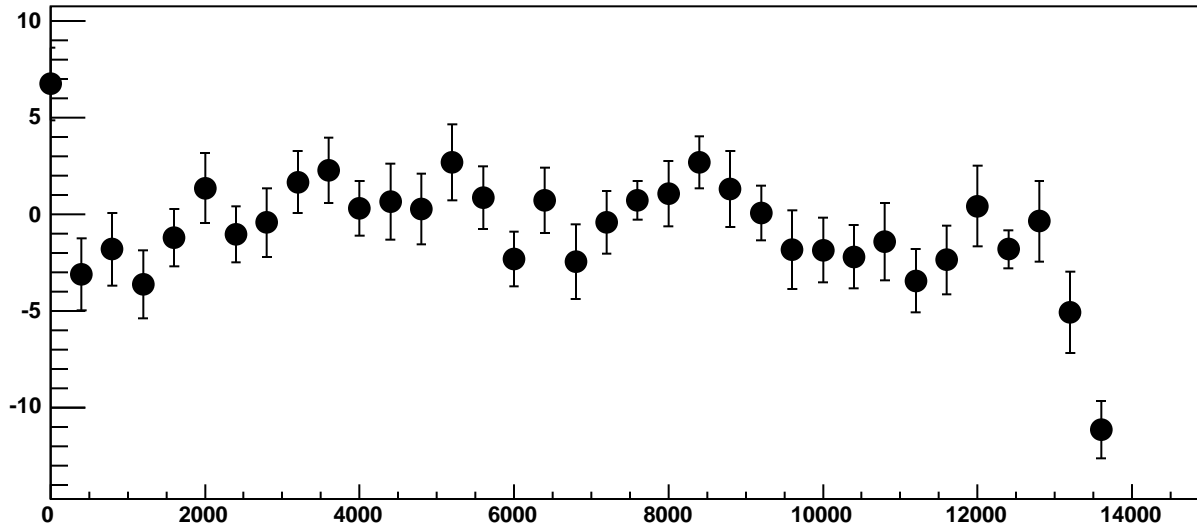


χ^2 / ndf 25.51 / 23
p0 -358.5 ± 0.7644
p1 0.02176 ± 0.0001151

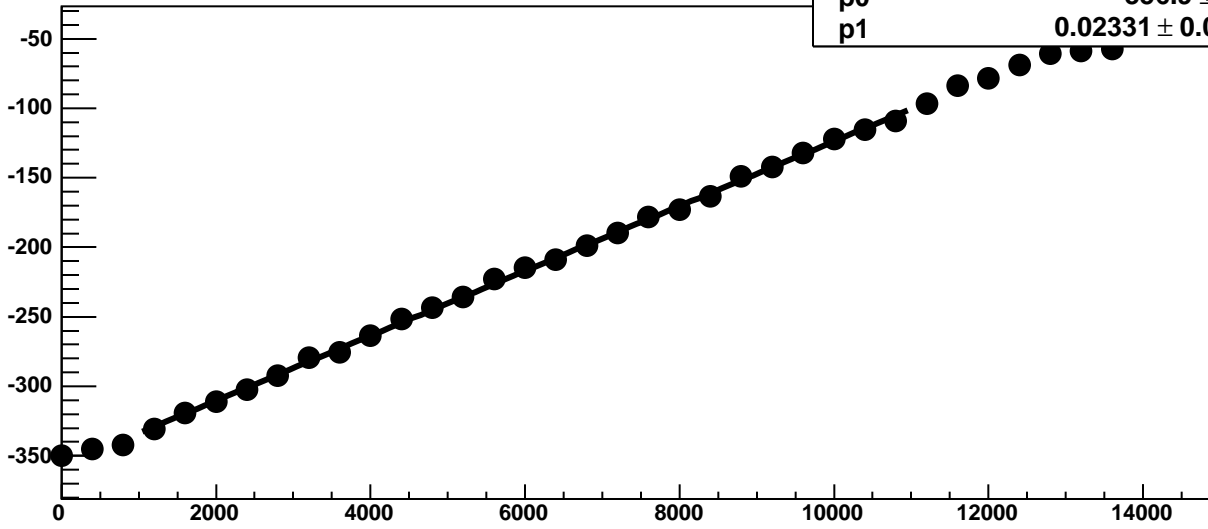
Chip 5, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

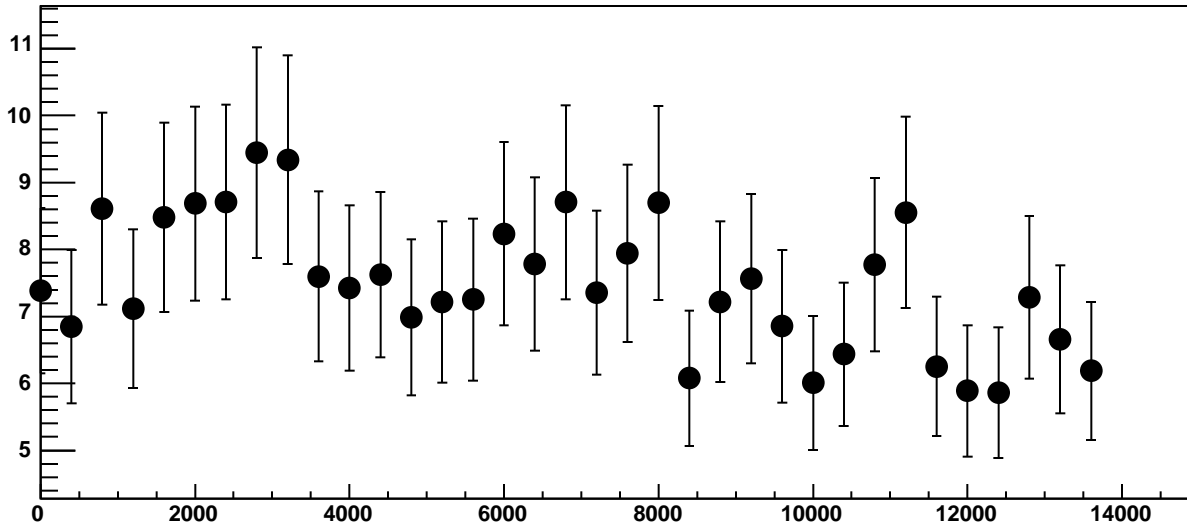


Chip 5, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

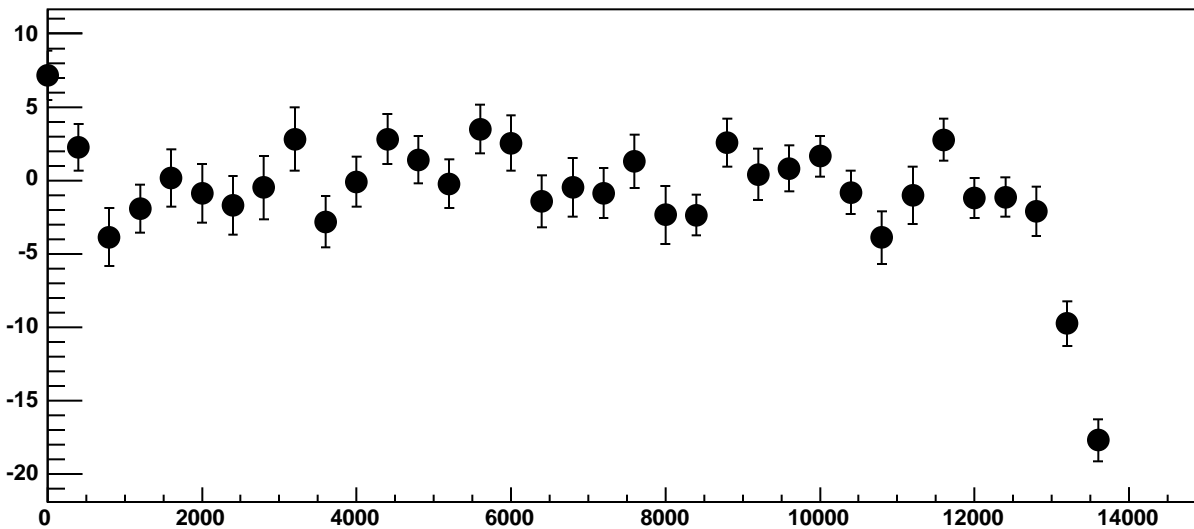


χ^2 / ndf 31.43 / 23
p0 -356.9 ± 0.8338
p1 0.02331 ± 0.0001192

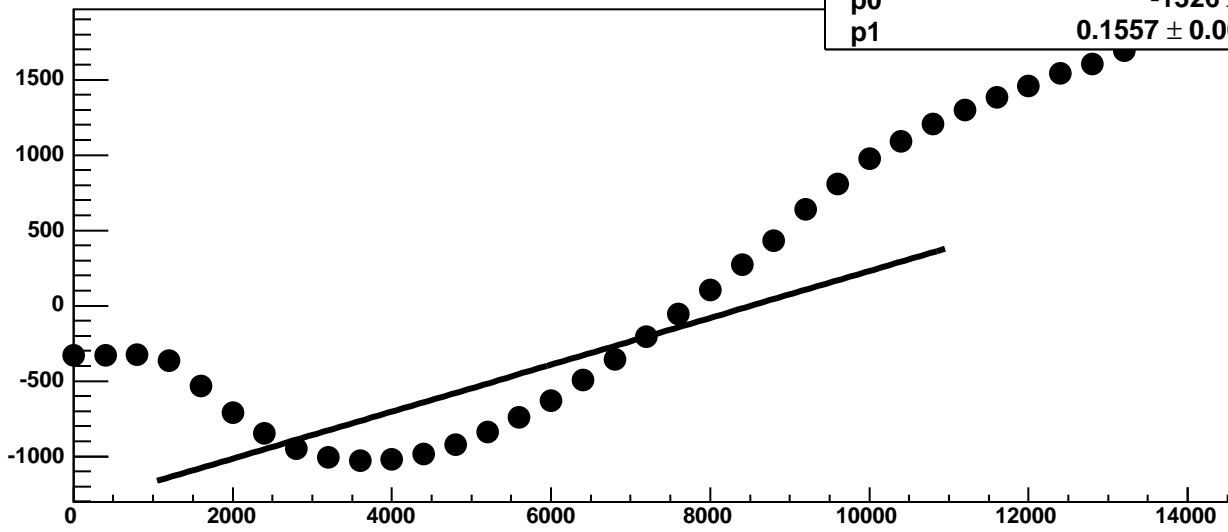
Chip 5, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



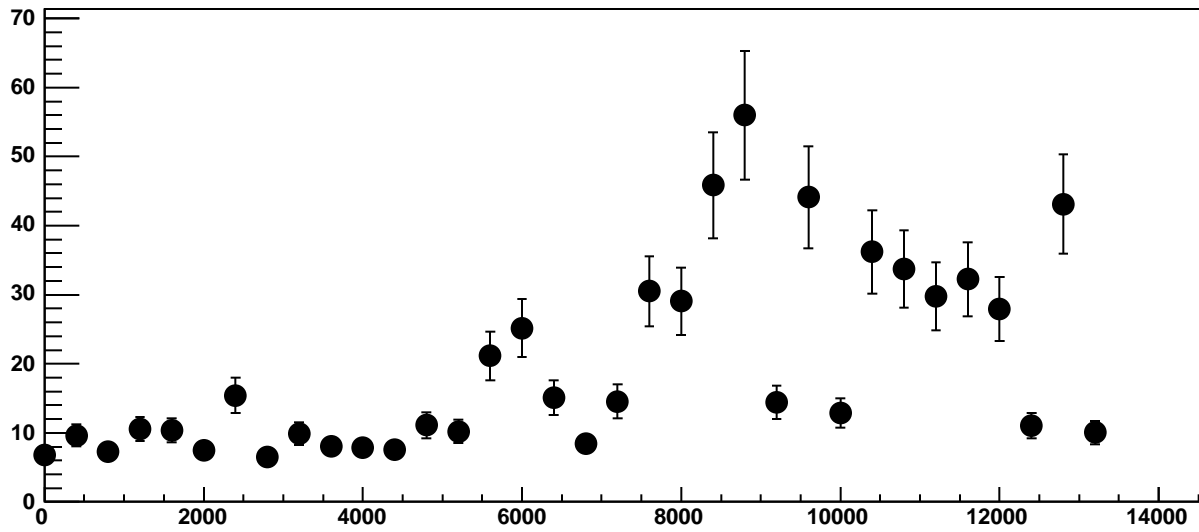
Chip 5, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



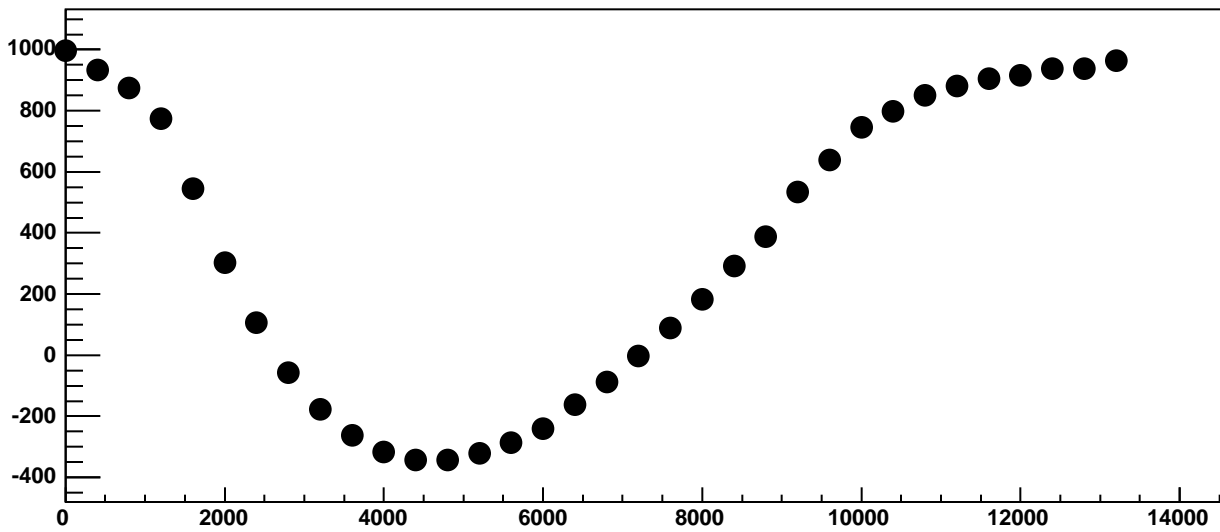
Chip 5, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC



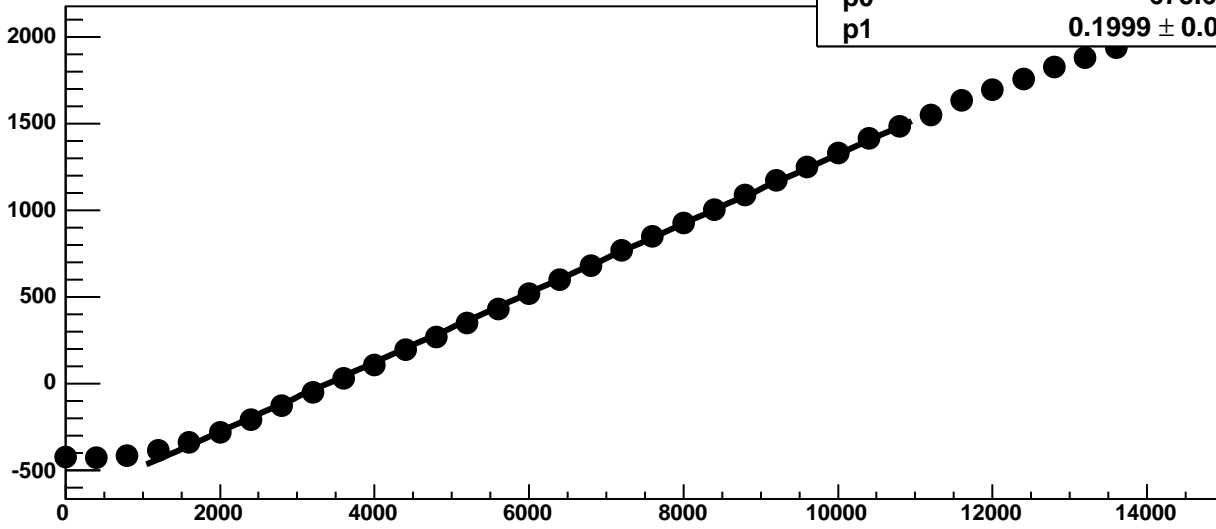
Chip 5, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



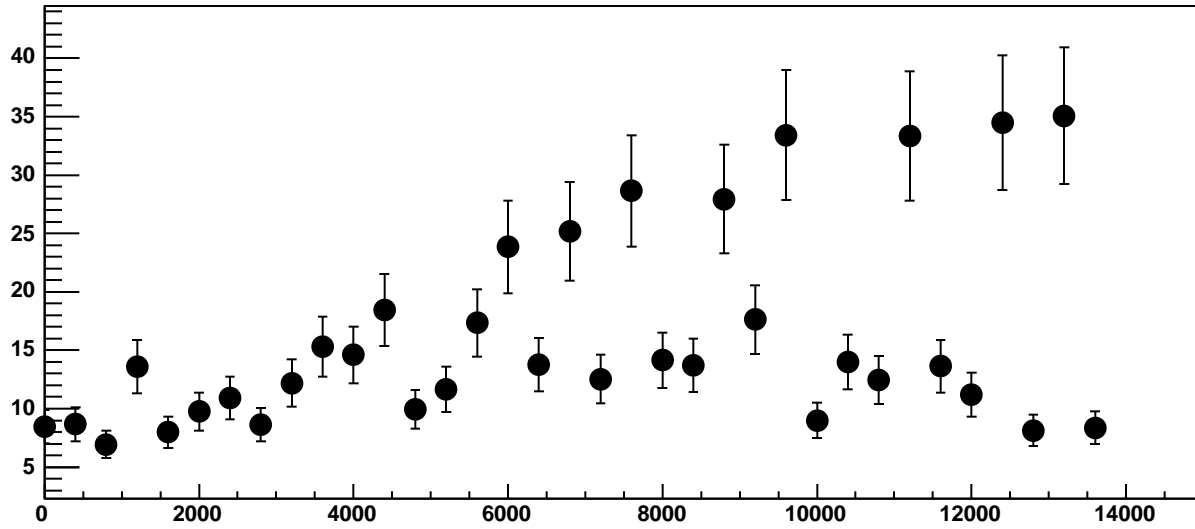
Chip 5, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC



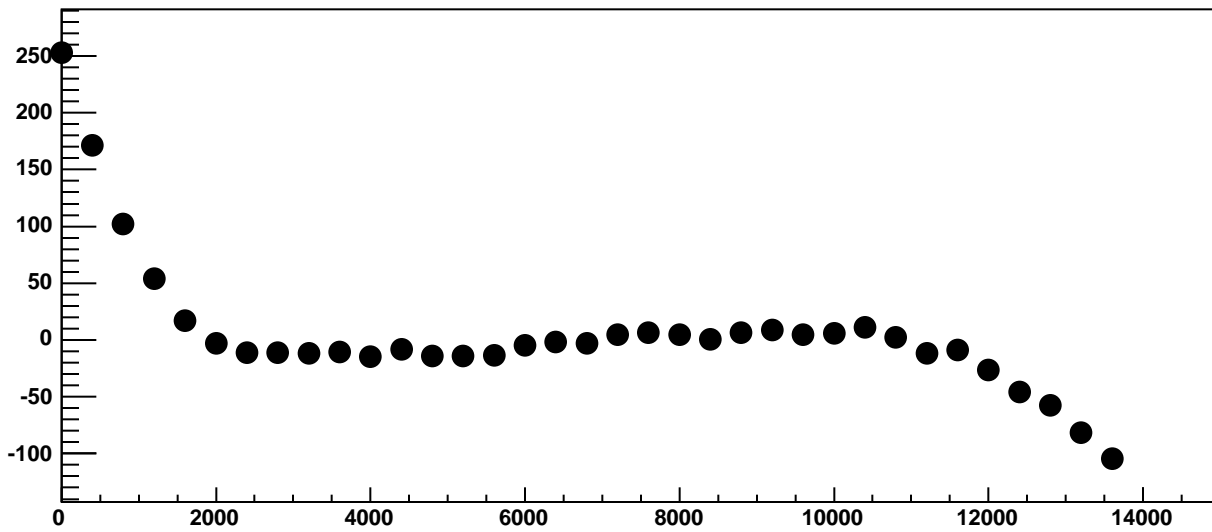
Chip 5, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



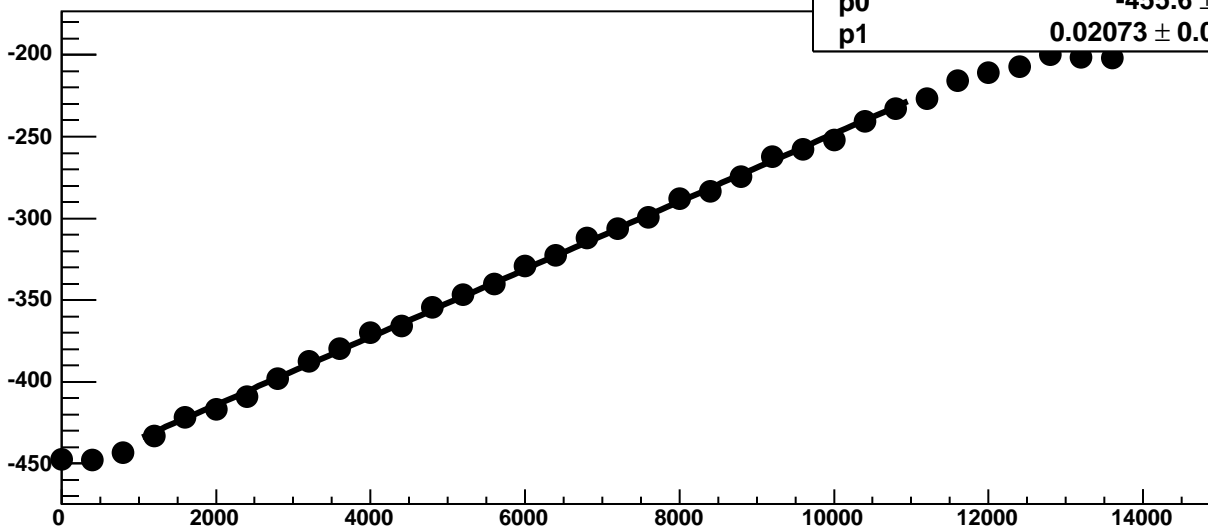
Chip 5, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



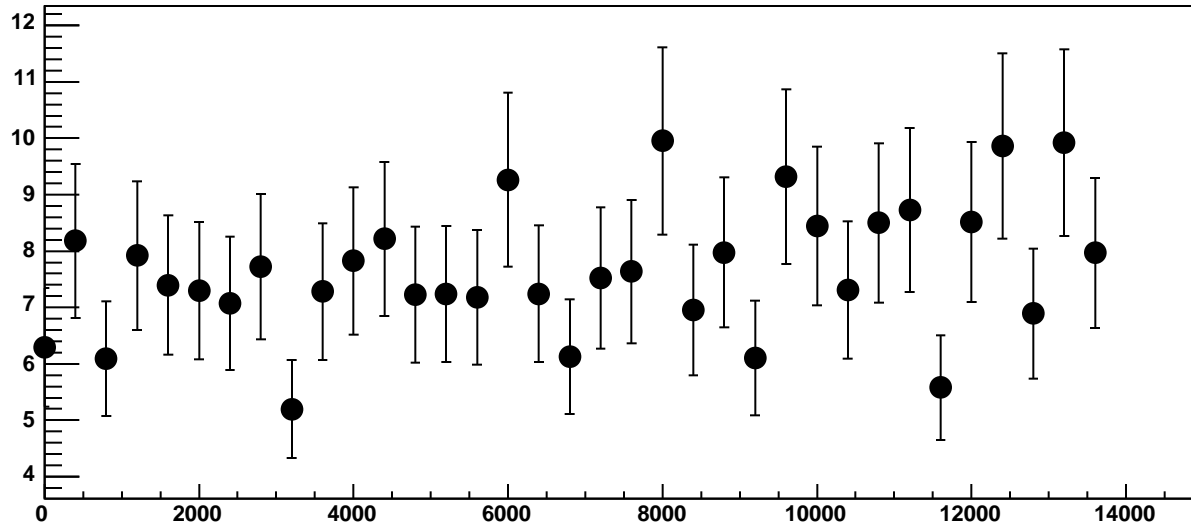
Chip 5, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



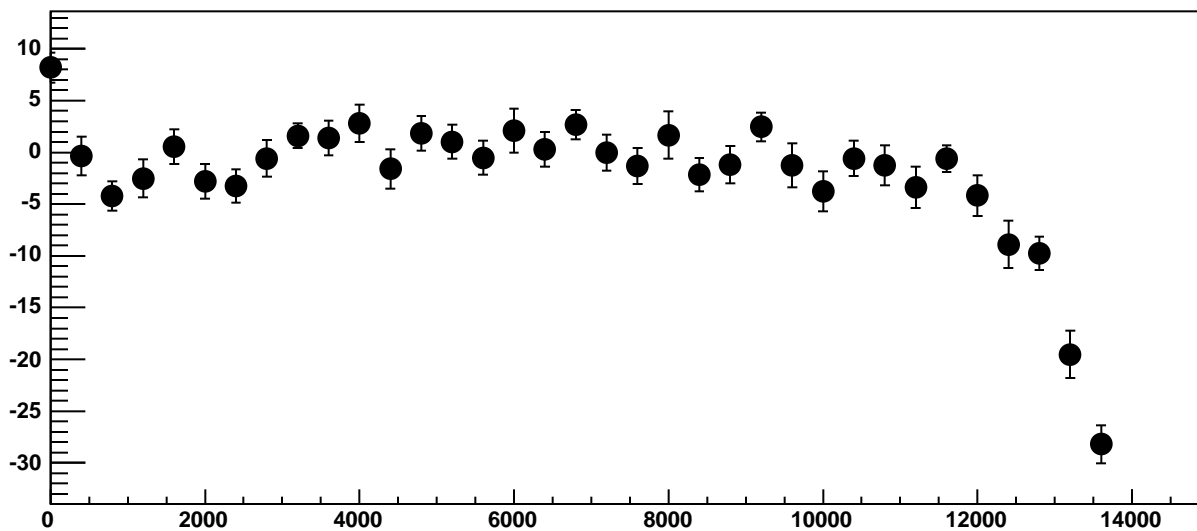
Chip 5, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



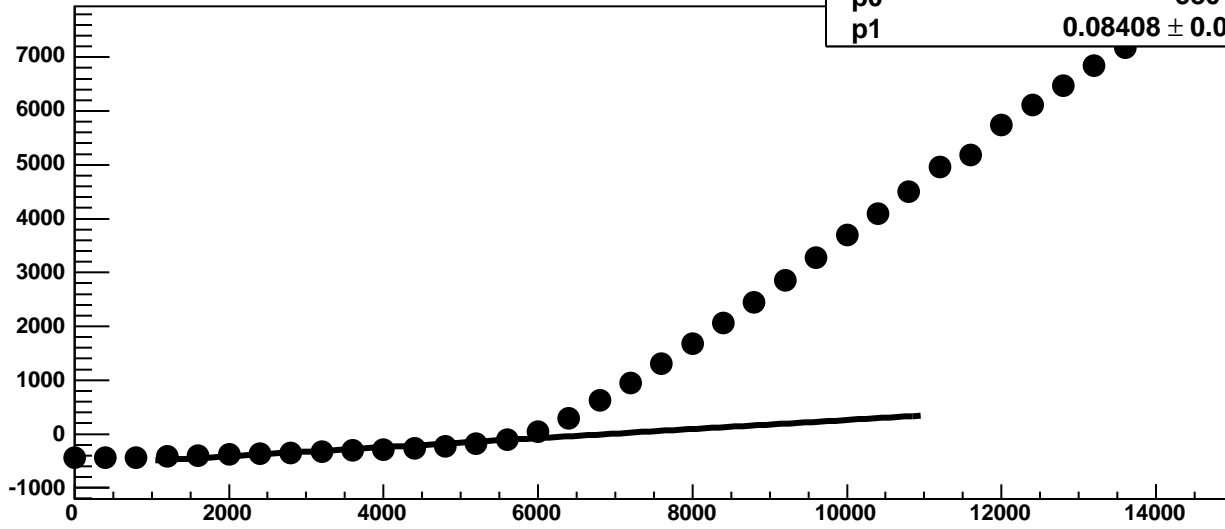
Chip 5, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

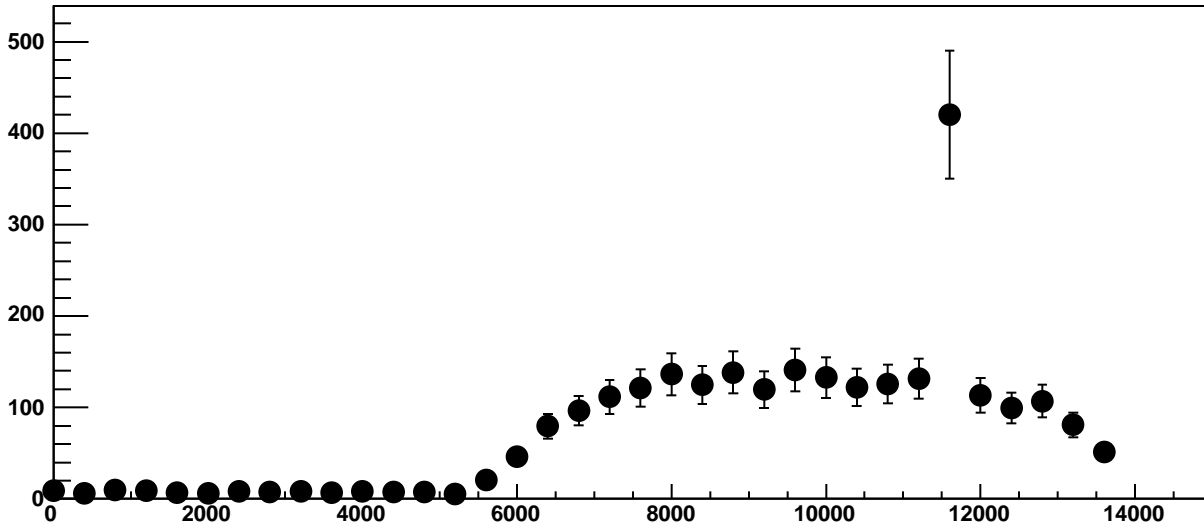


Chip 5, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC

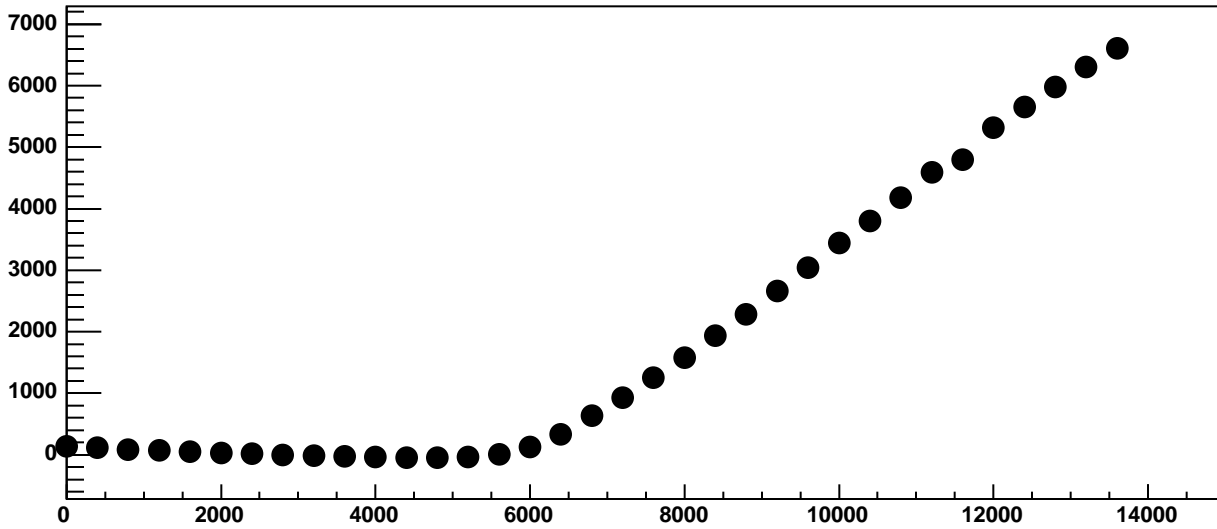


χ^2 / ndf 9.364e+04 / 23
p0 -580 ± 1.323
p1 0.08408 ± 0.0003642

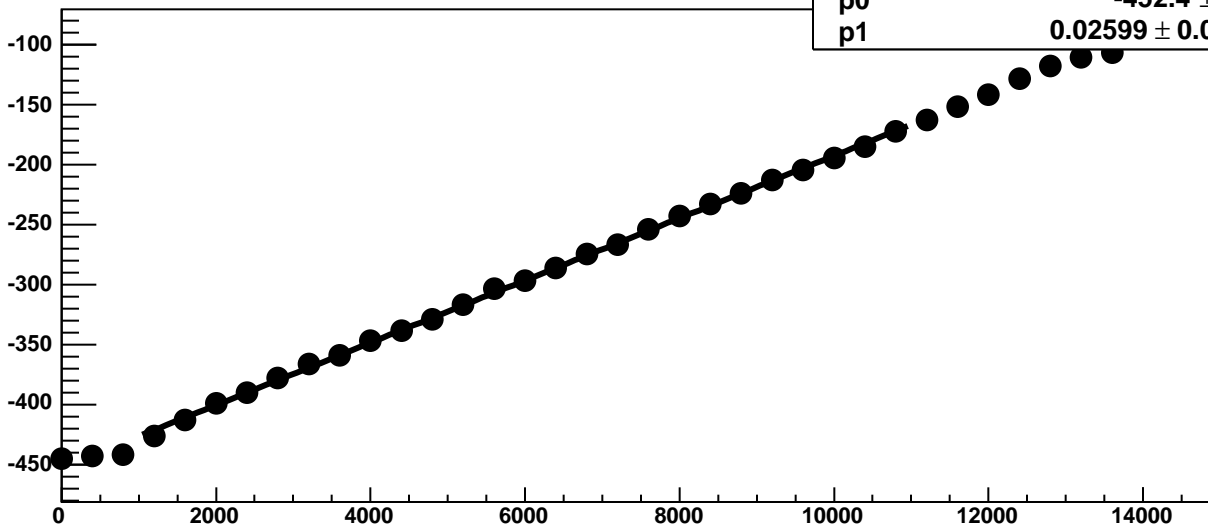
Chip 5, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



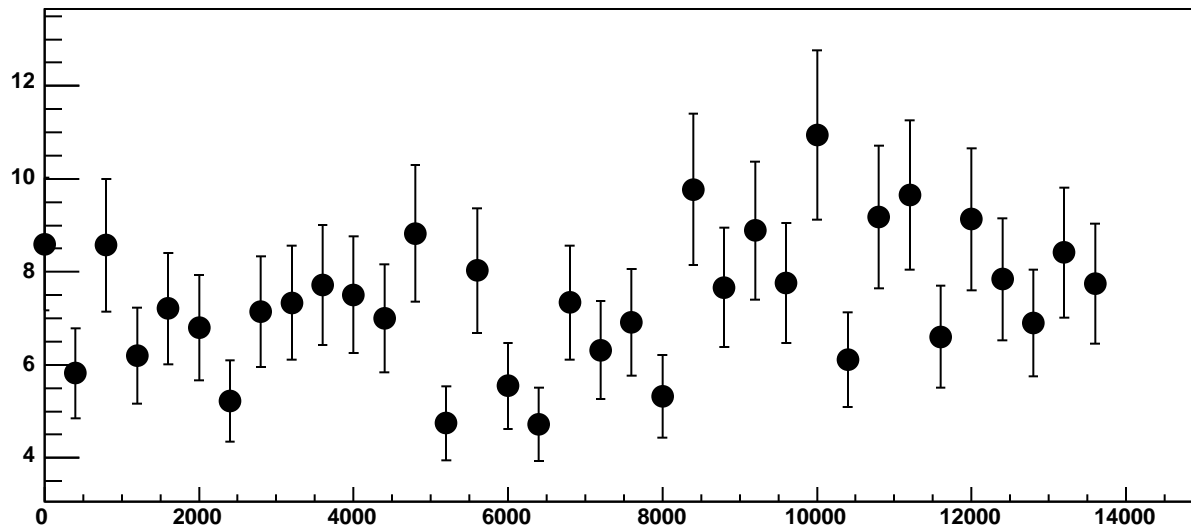
Chip 5, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC



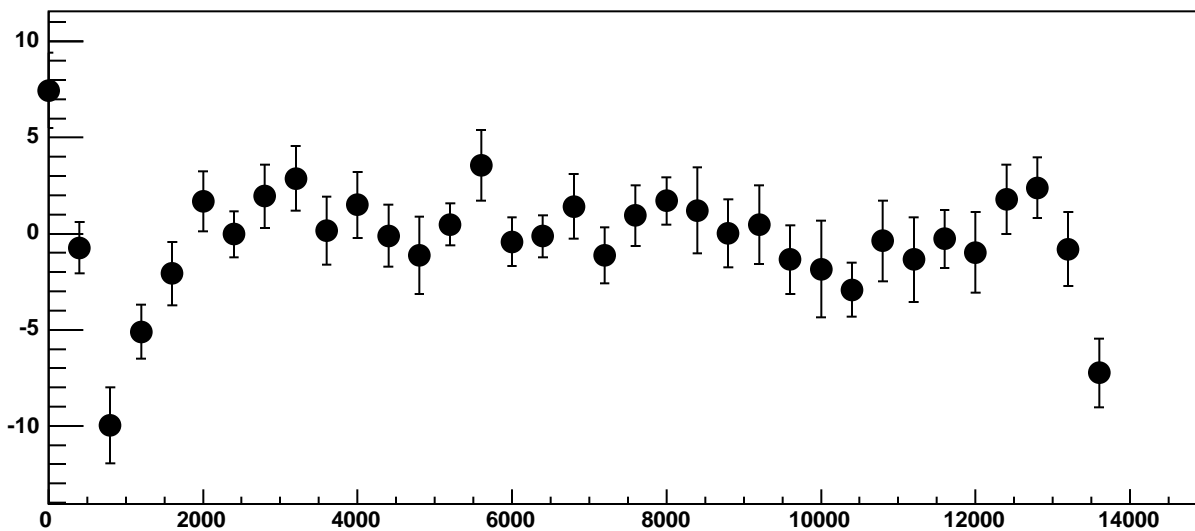
Chip 5, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



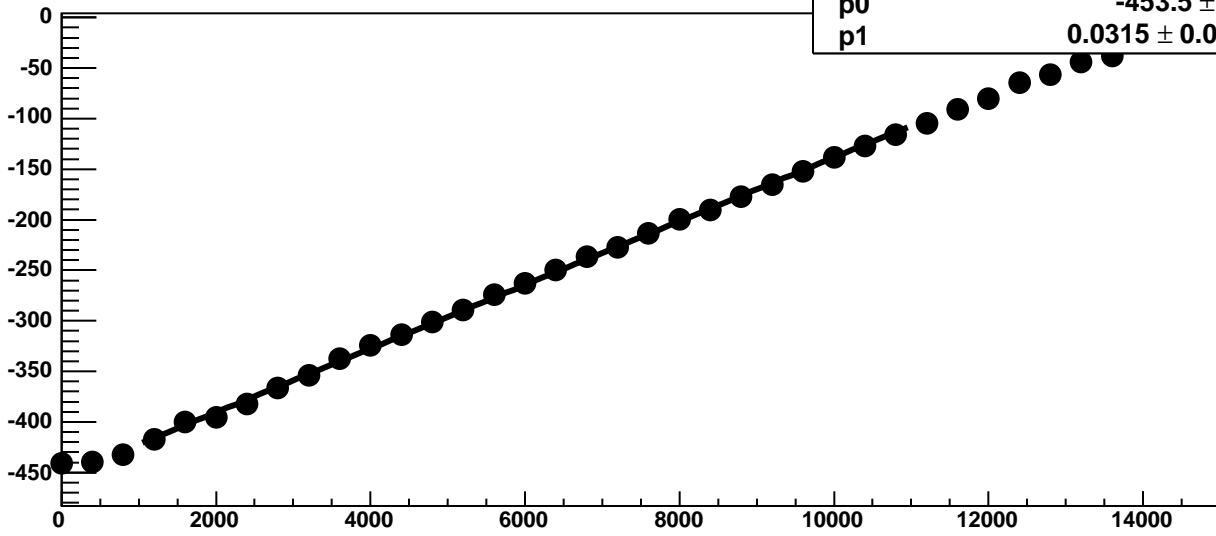
Chip 5, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

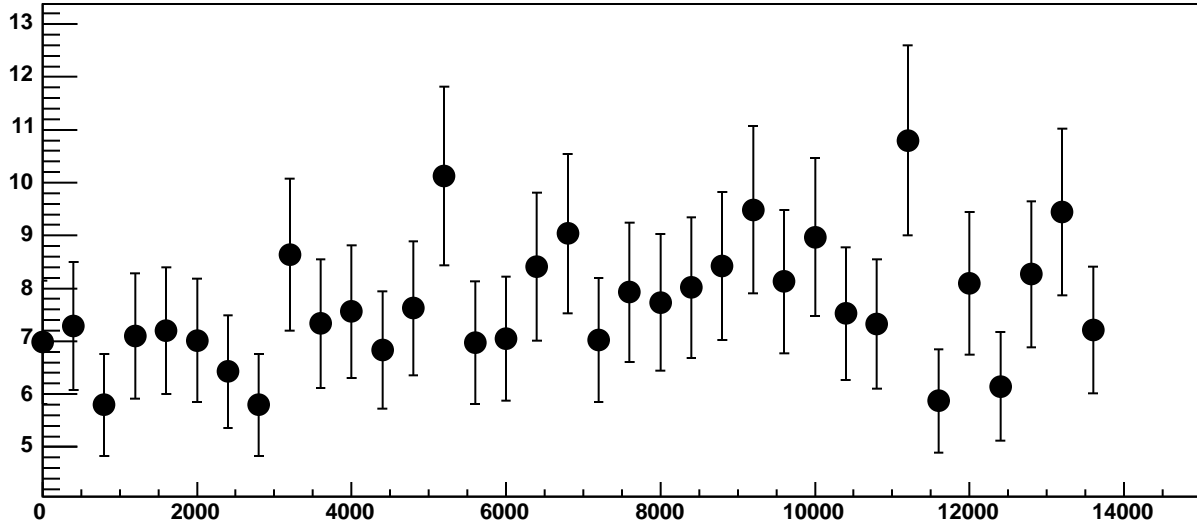


Chip 5, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

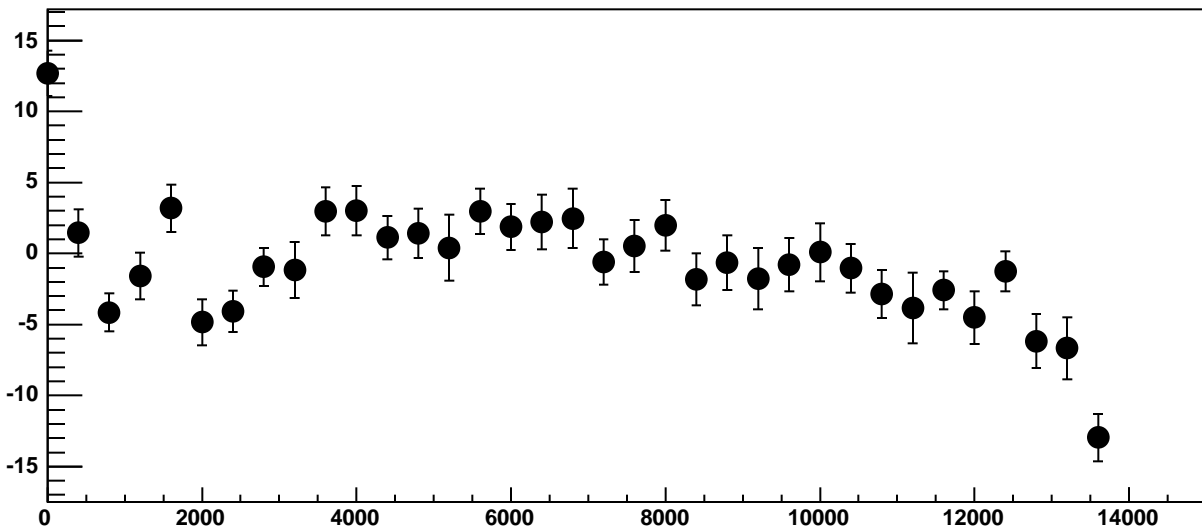


χ^2 / ndf 43.71 / 23
p0 -453.5 ± 0.7566
p1 0.0315 ± 0.0001189

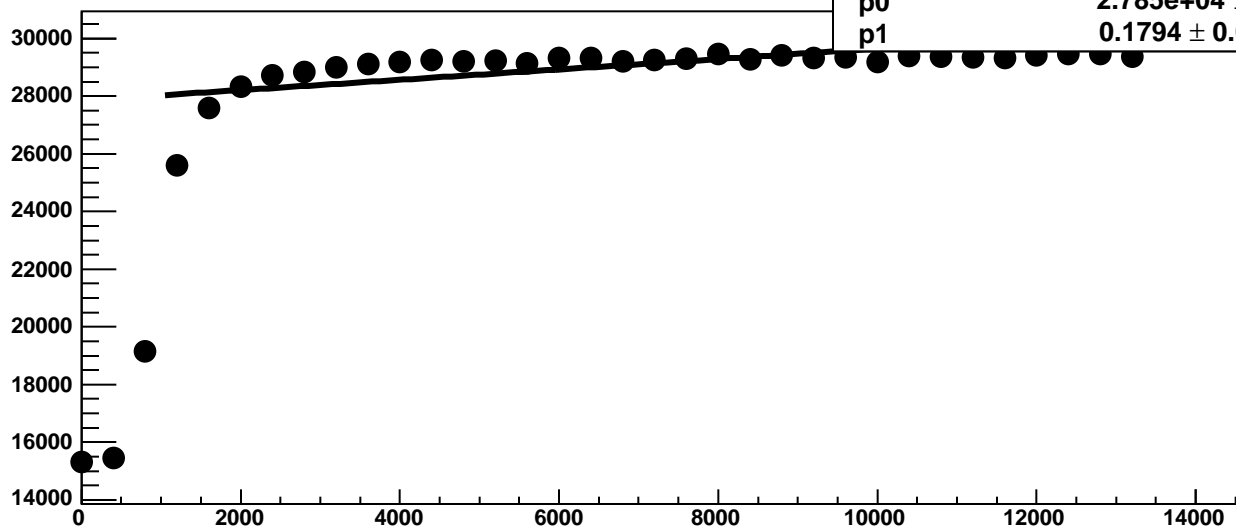
Chip 5, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



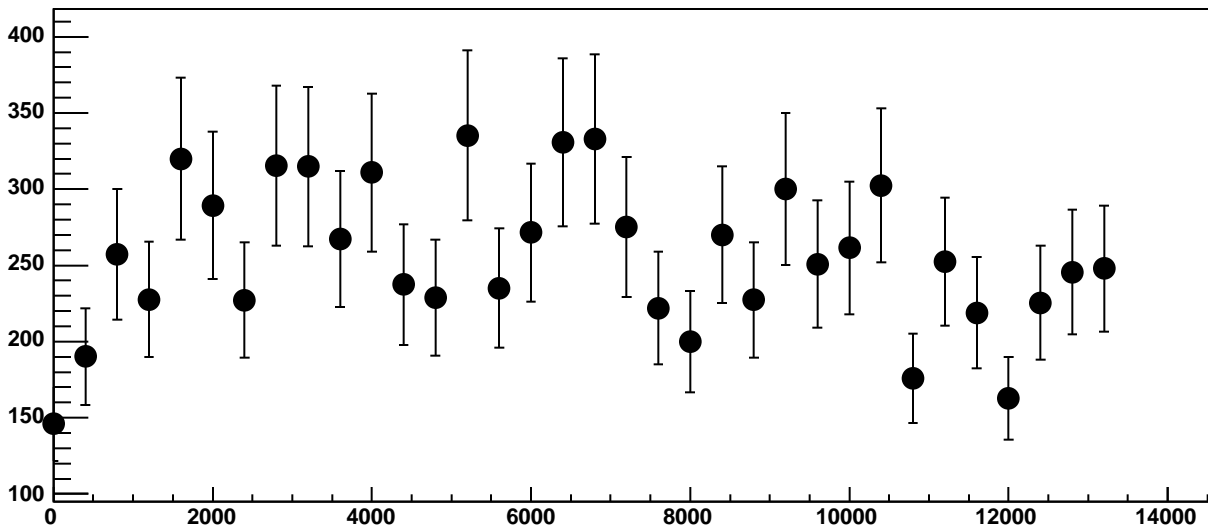
Chip 5, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



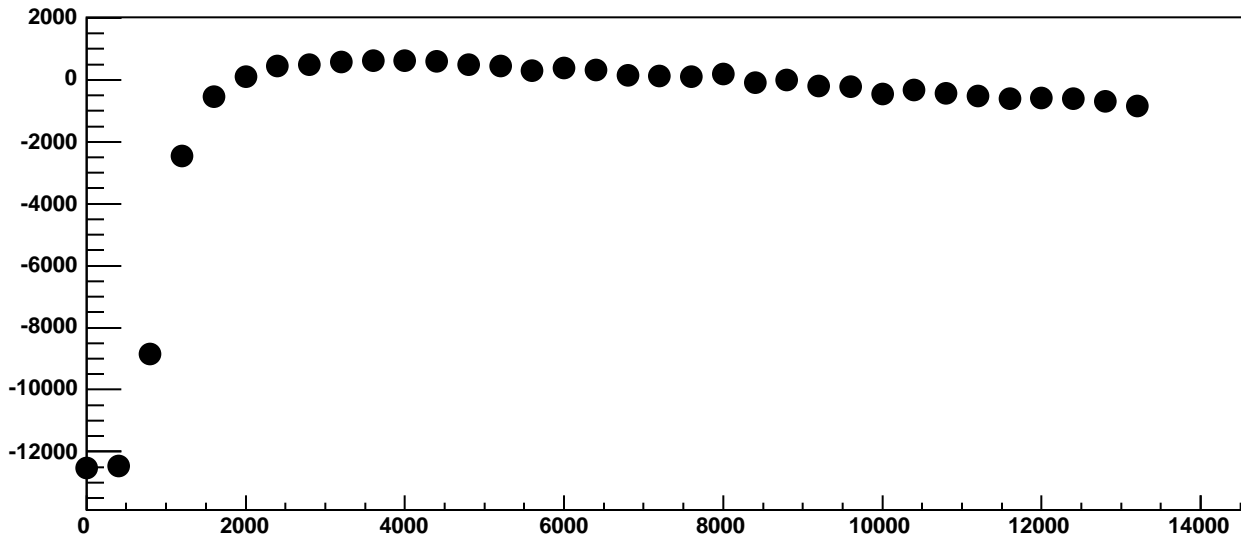
Chip 5, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC



Chip 5, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC

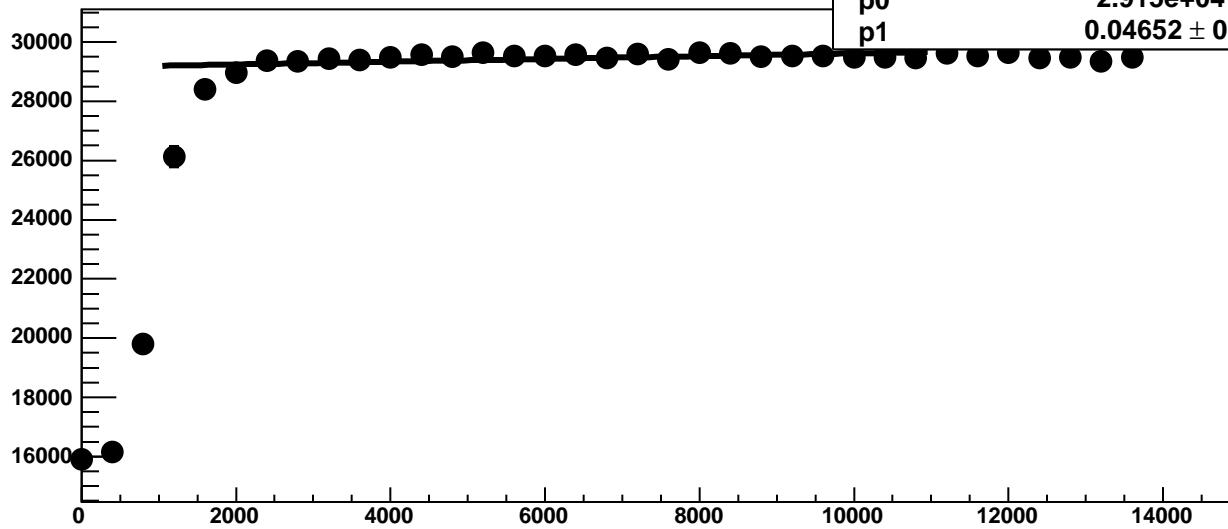


Chip 5, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC

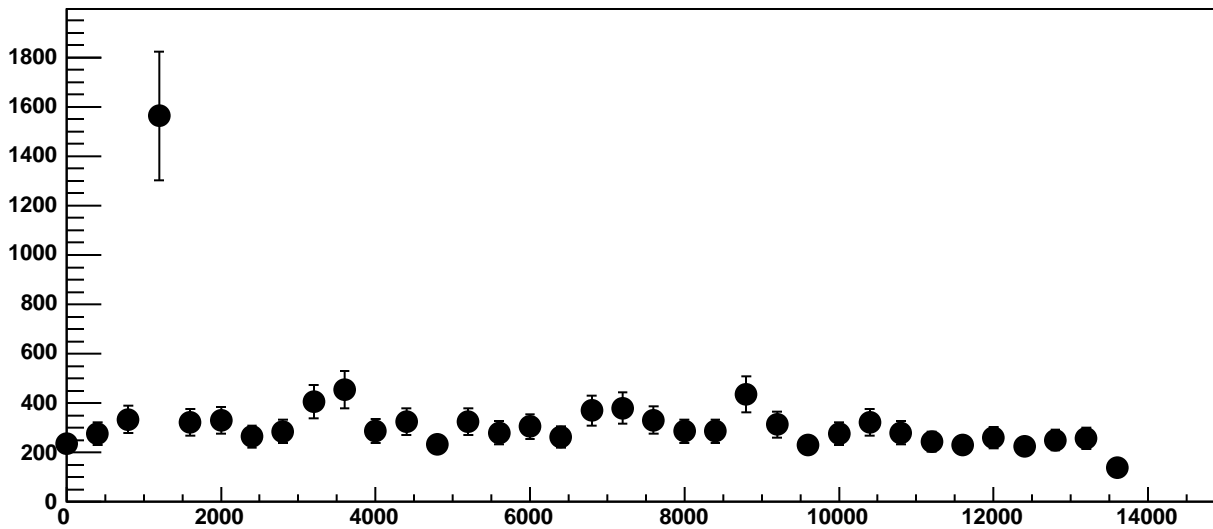


Chip 6, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC

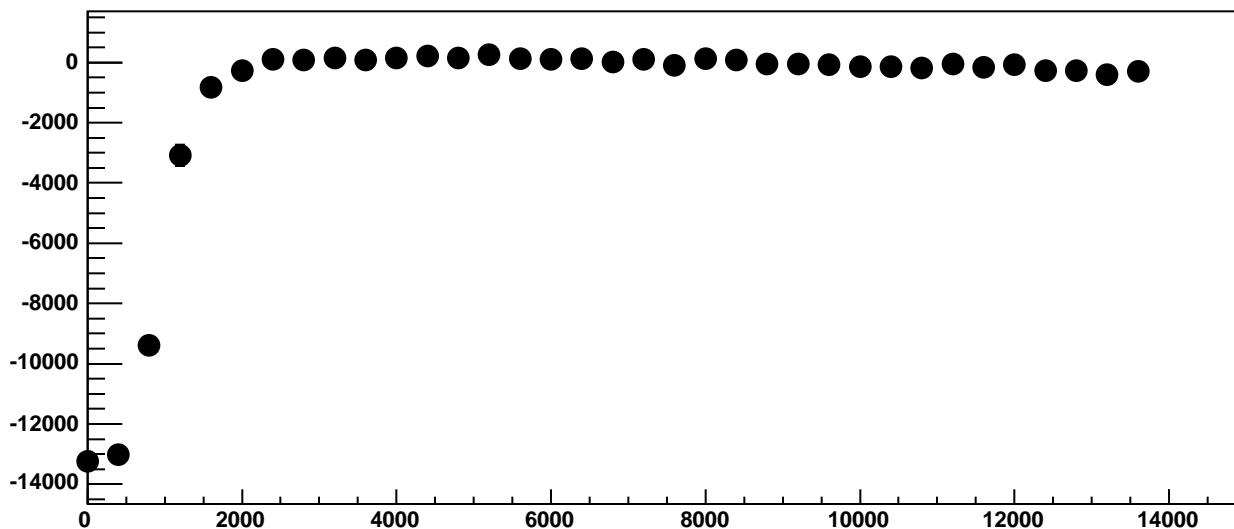
χ^2 / ndf 291.1 / 23
p0 $2.915\text{e}+04 \pm 35.07$
p1 0.04652 ± 0.005072



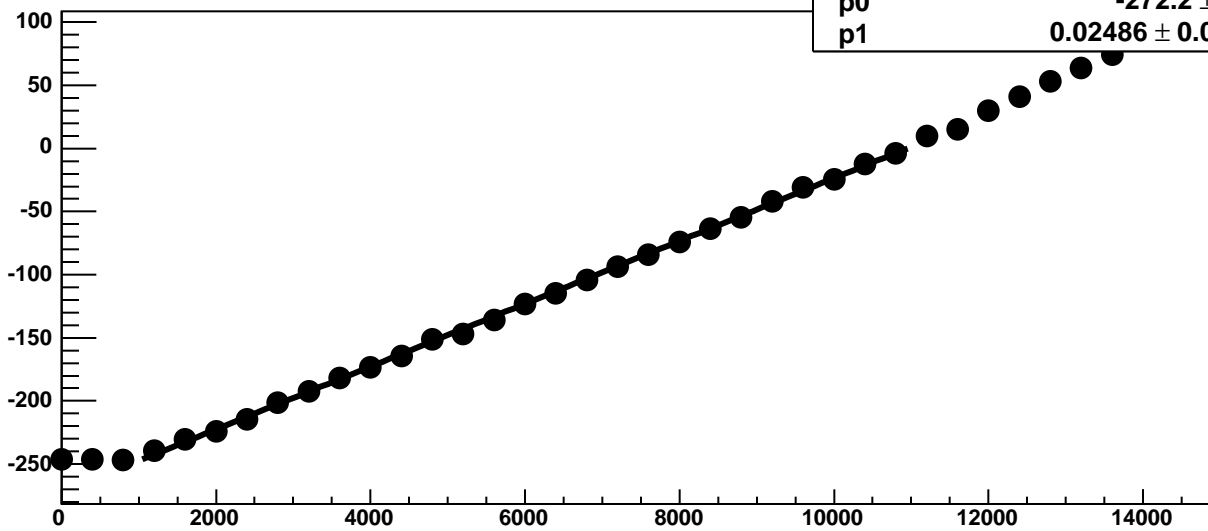
Chip 6, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



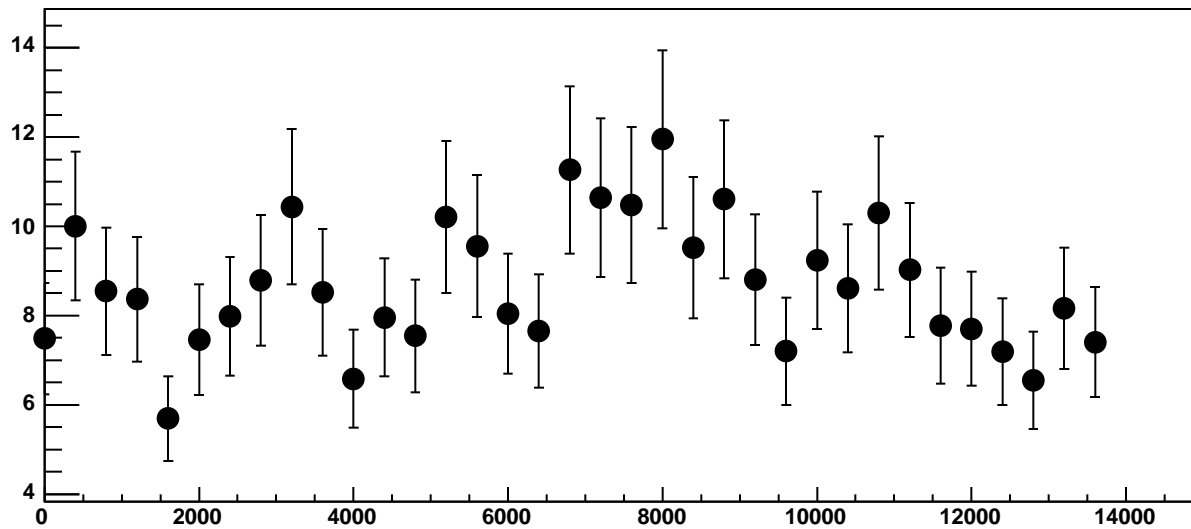
Chip 6, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC



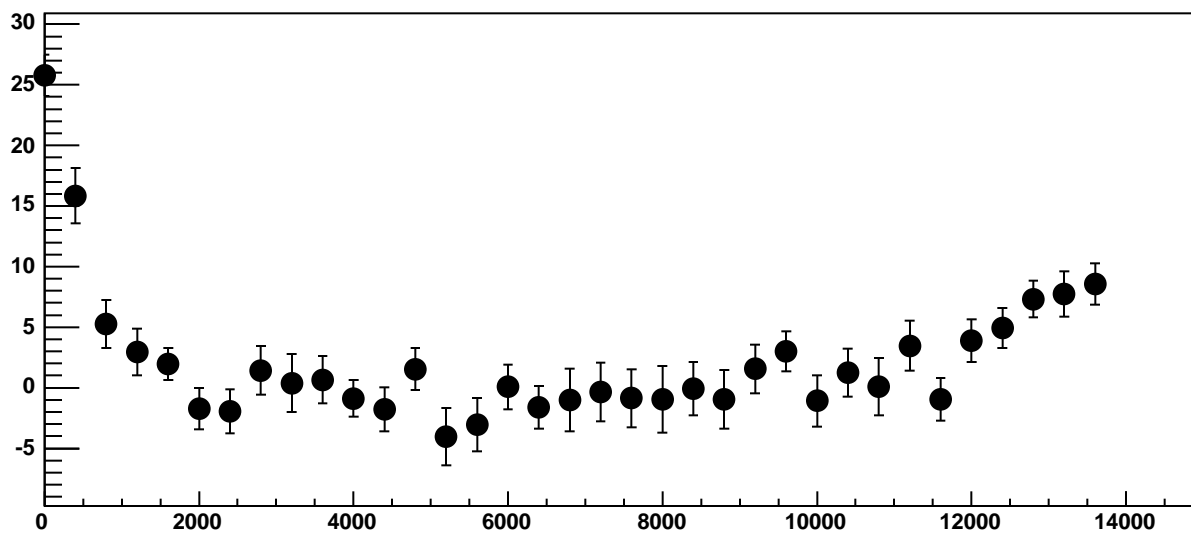
Chip 6, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC



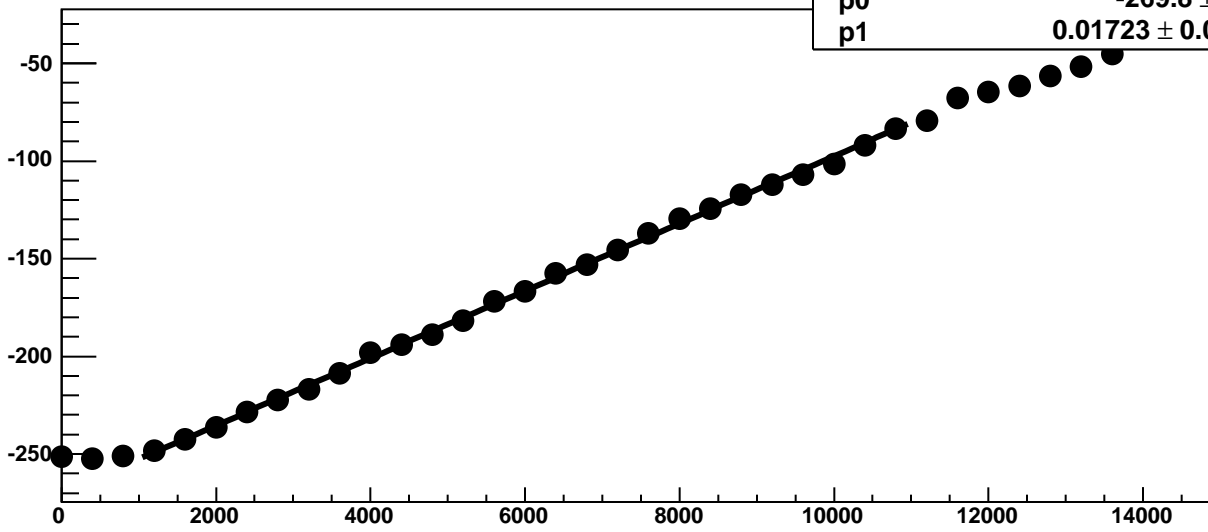
Chip 6, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 6, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

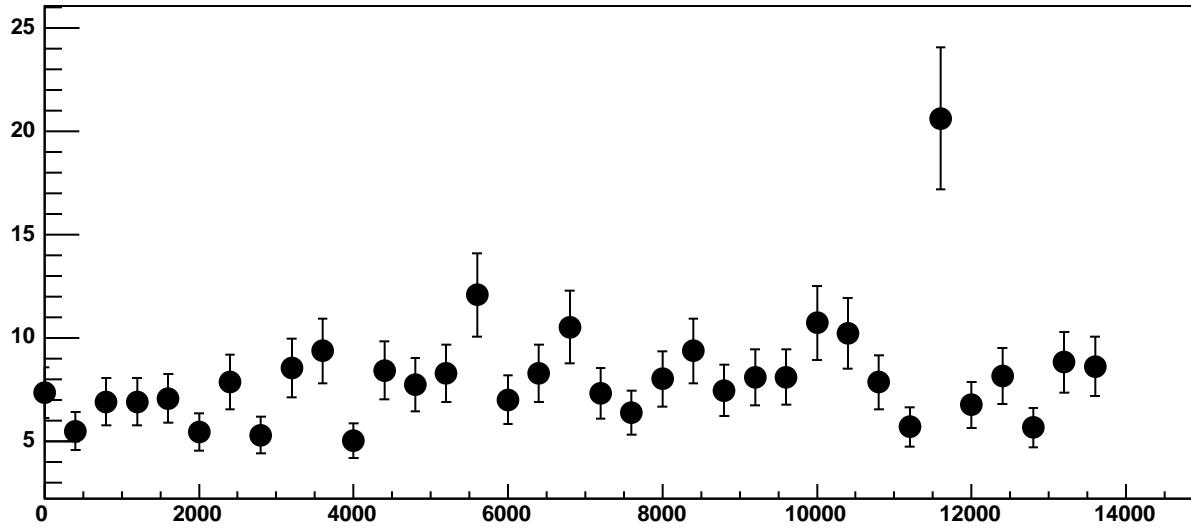


Chip 6, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

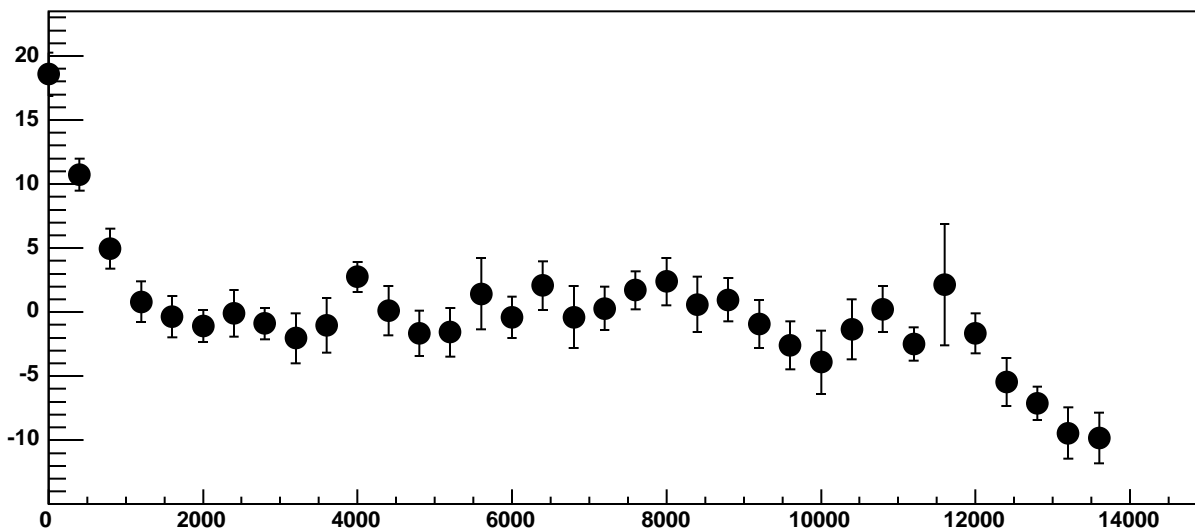


χ^2 / ndf 20.24 / 23
p0 -269.8 ± 0.7369
p1 0.01723 ± 0.0001201

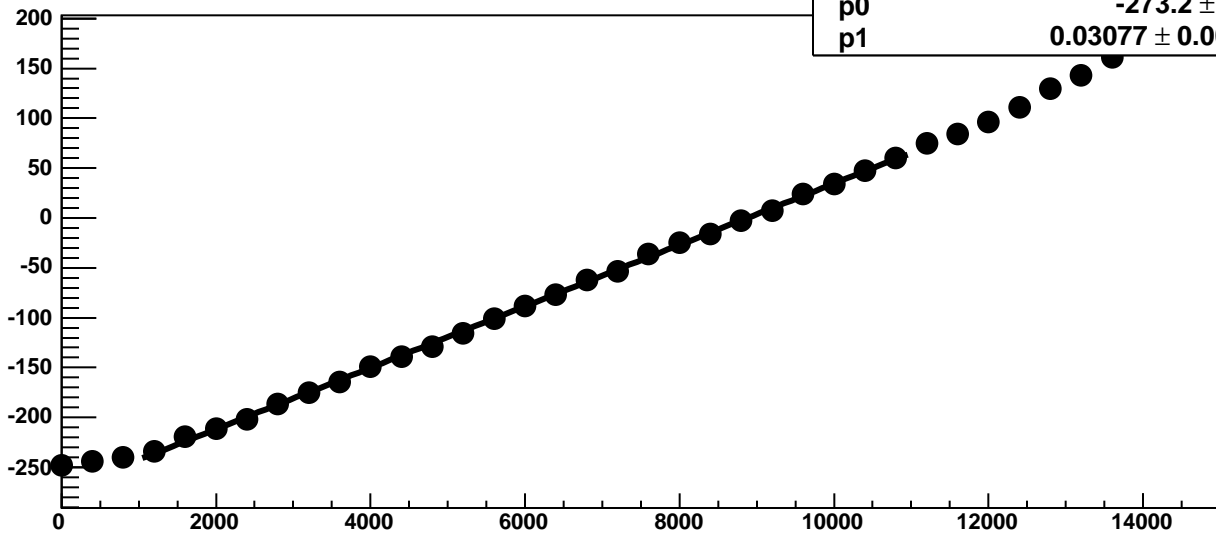
Chip 6, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

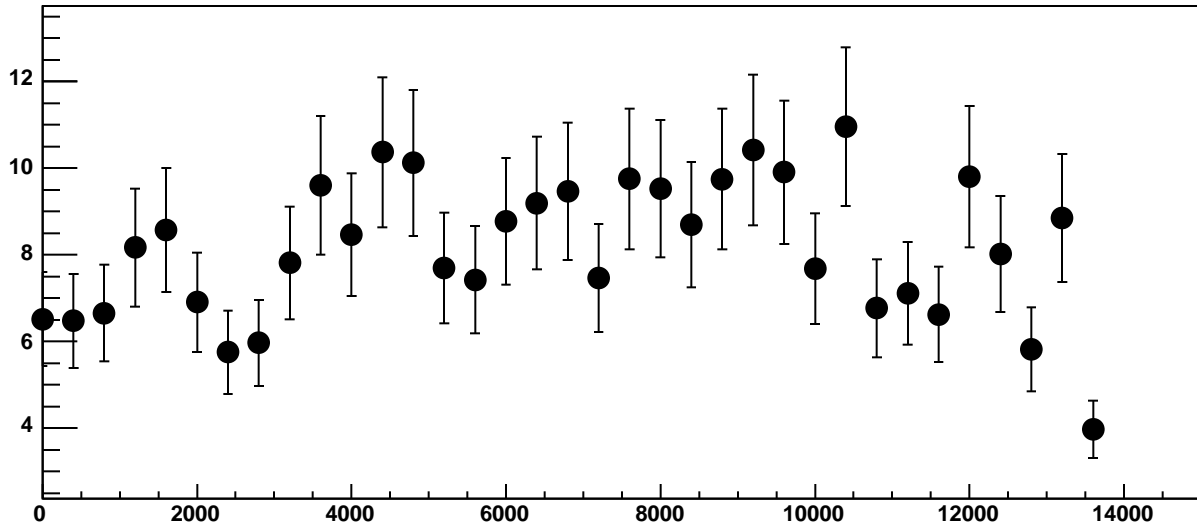


Chip 6, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

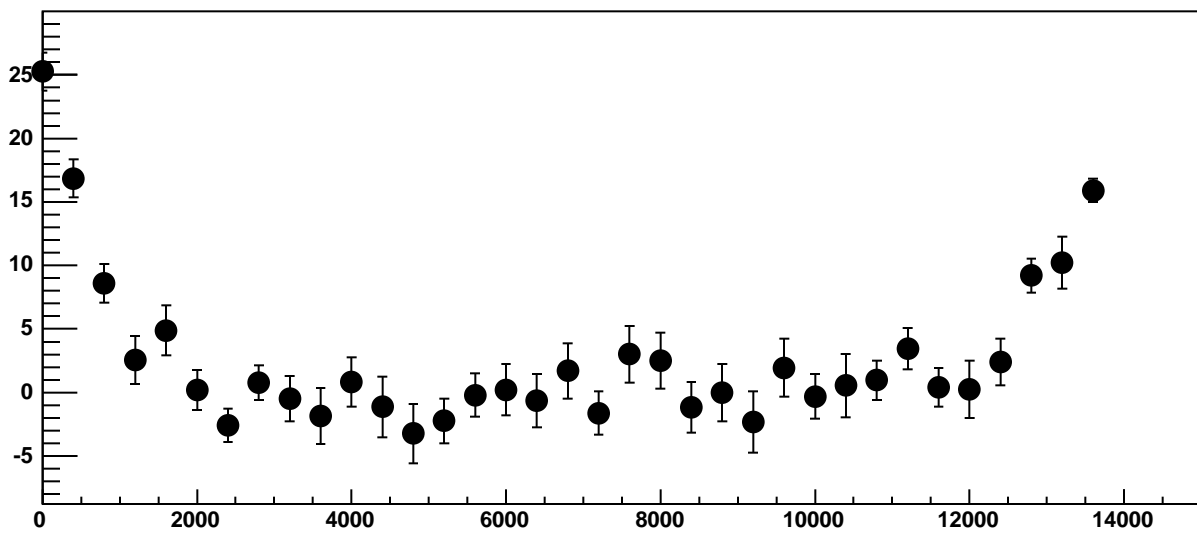


χ^2 / ndf 24.09 / 23
p0 -273.2 ± 0.8025
p1 0.03077 ± 0.0001266

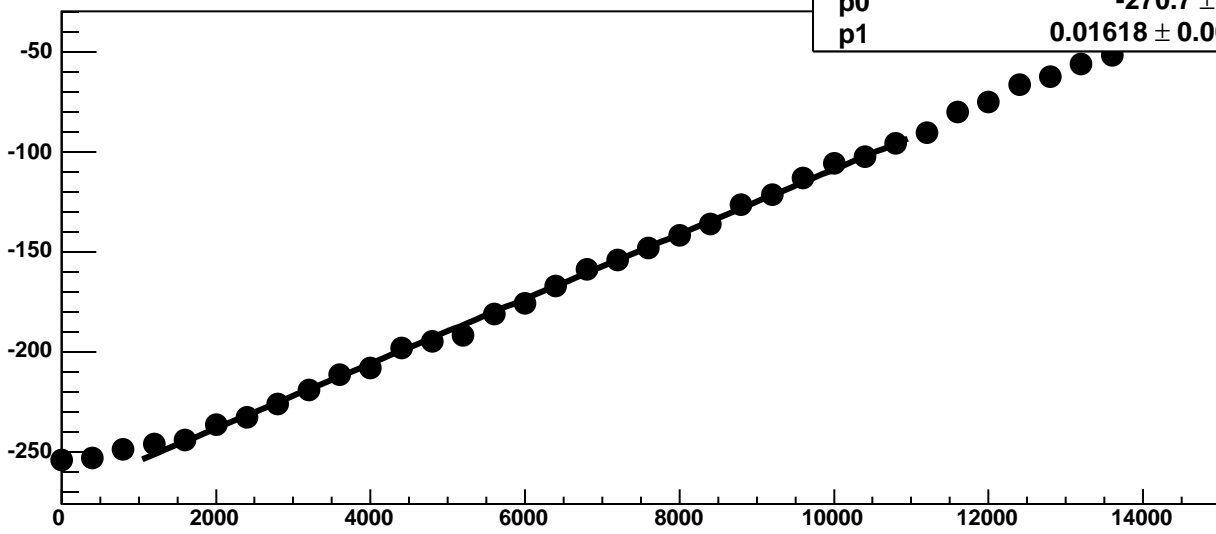
Chip 6, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC

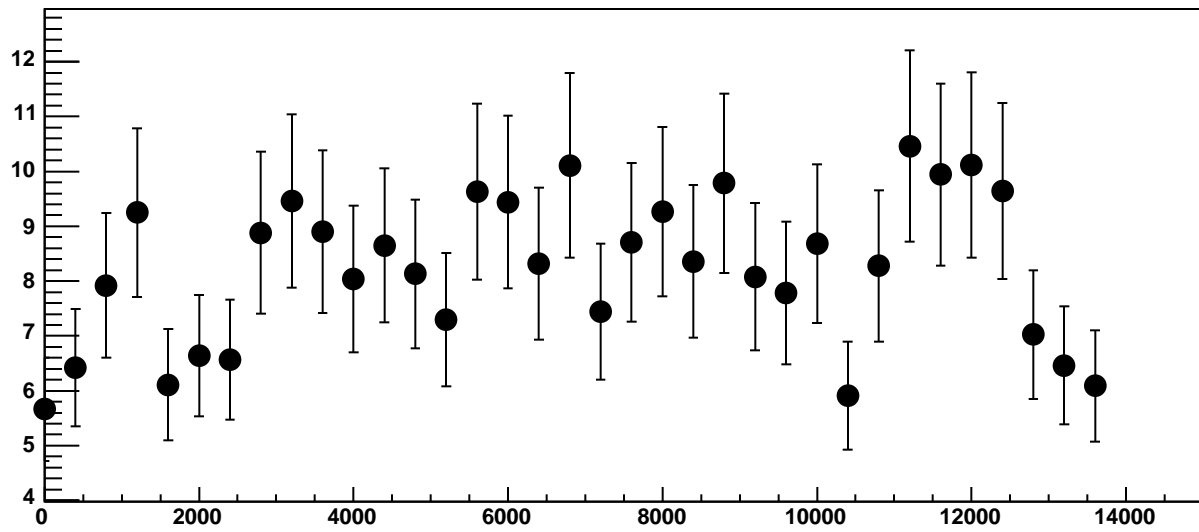


Chip 6, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

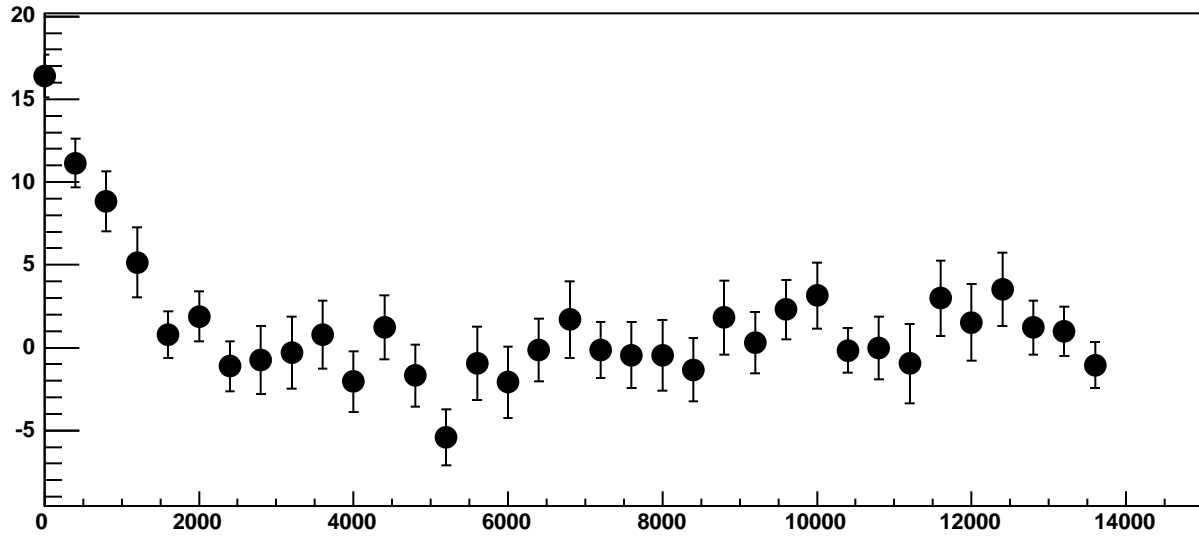


χ^2 / ndf 28.55 / 23
p0 -270.7 ± 0.8006
p1 0.01618 ± 0.0001205

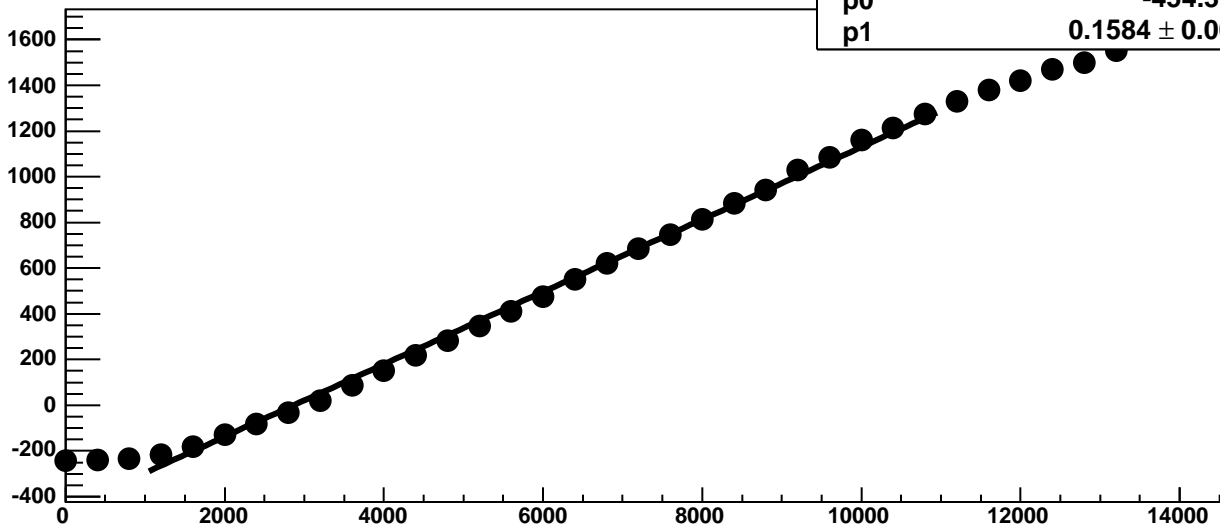
Chip 6, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



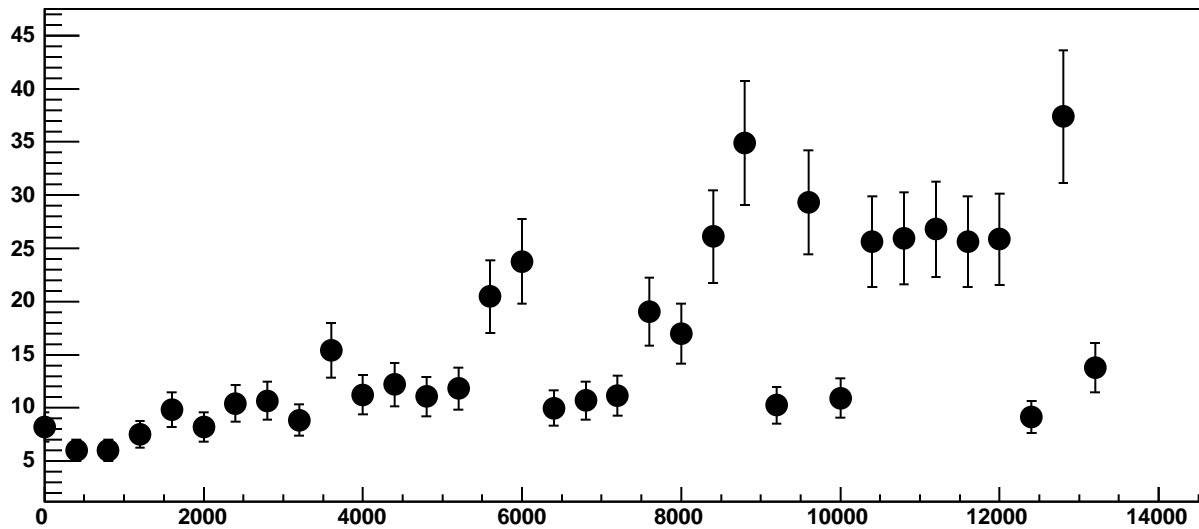
Chip 6, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



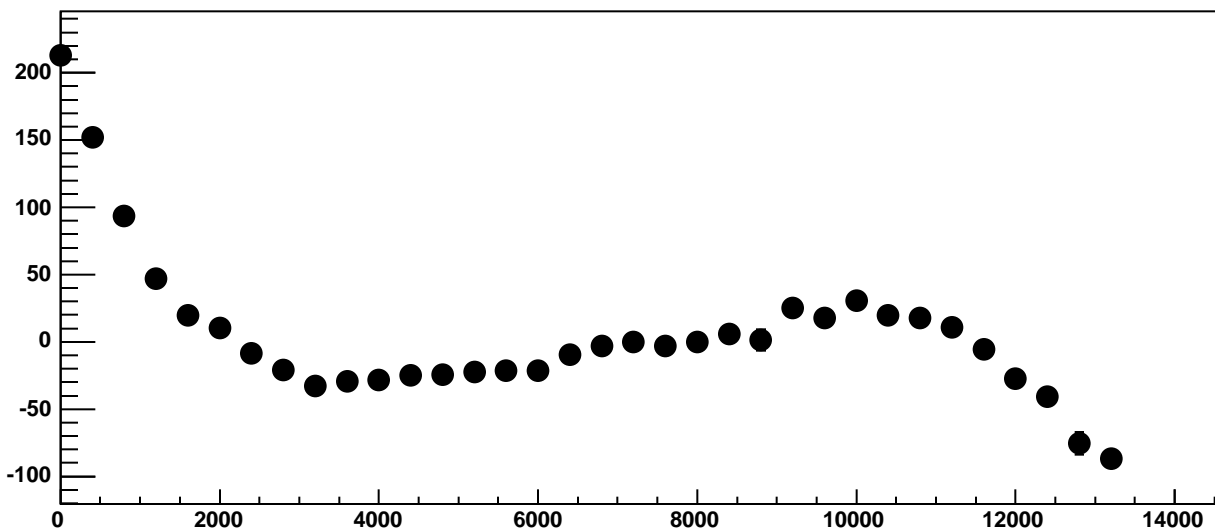
Chip 6, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



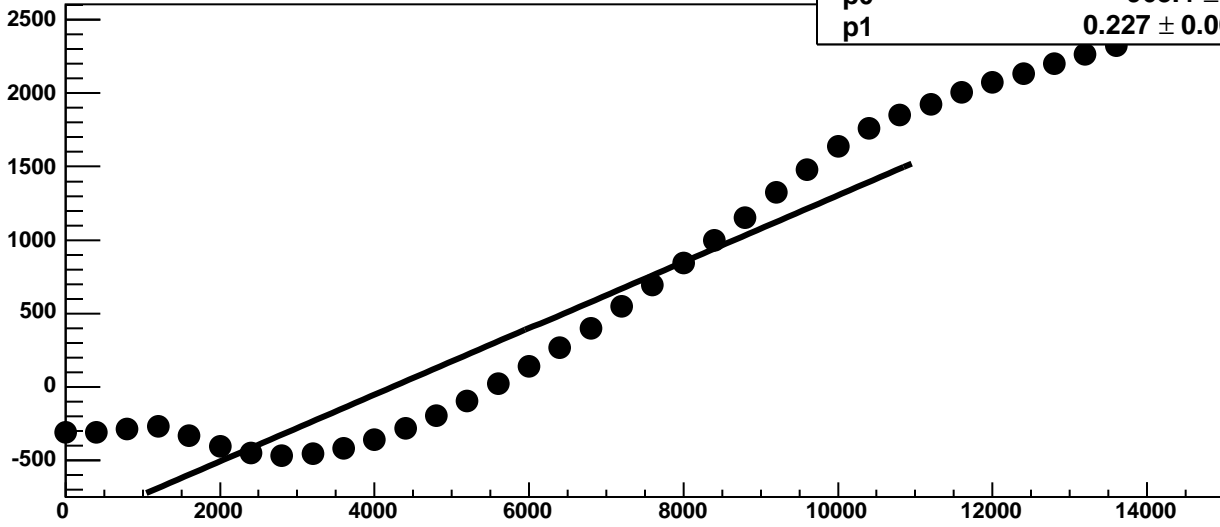
Chip 6, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

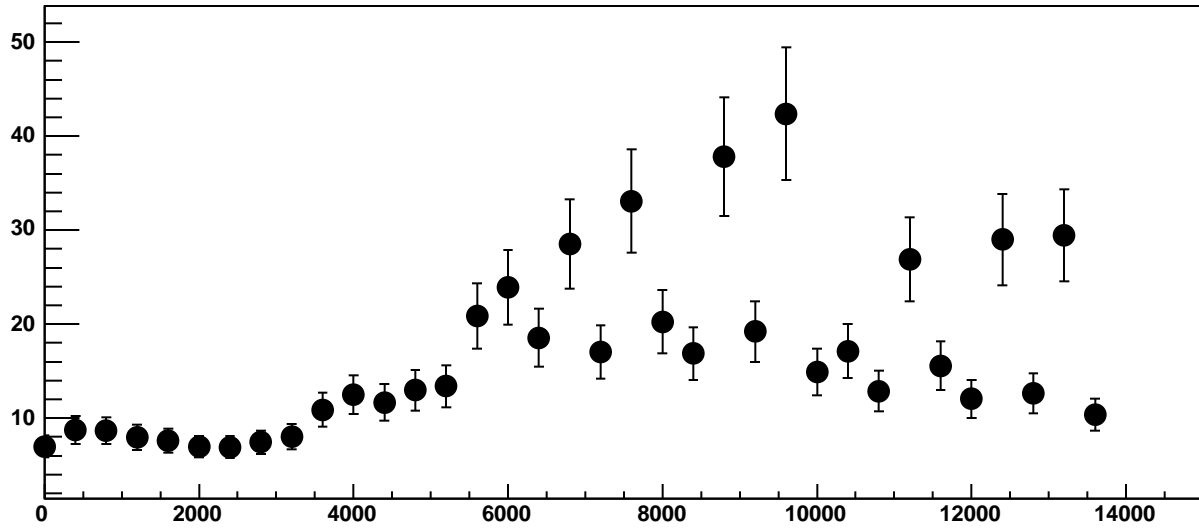


Chip 6, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC

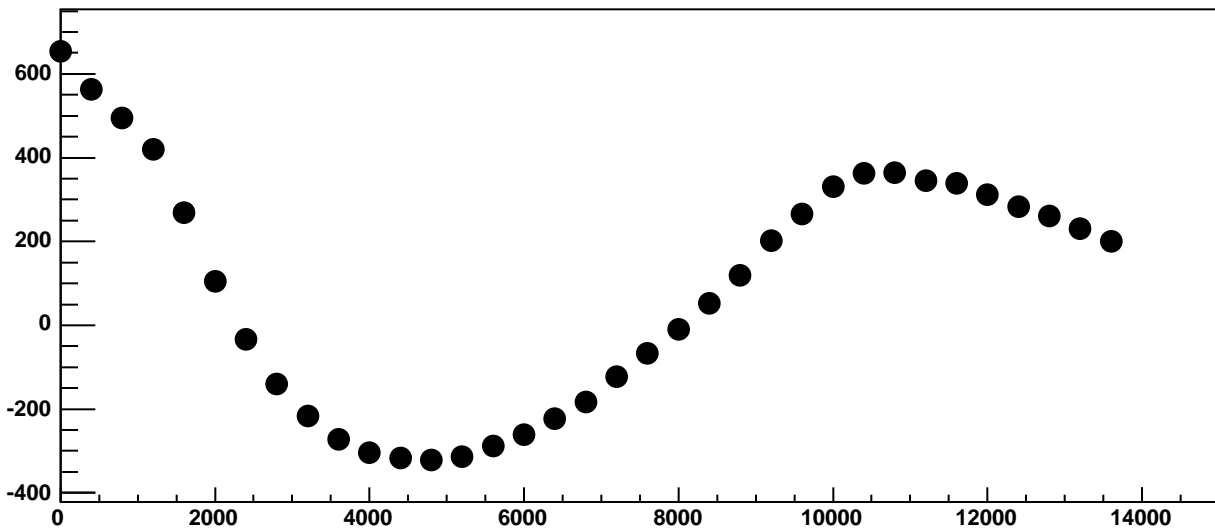


χ^2 / ndf 2.073e+05 / 23
p0 -963.4 ± 0.9598
p1 0.227 ± 0.0002022

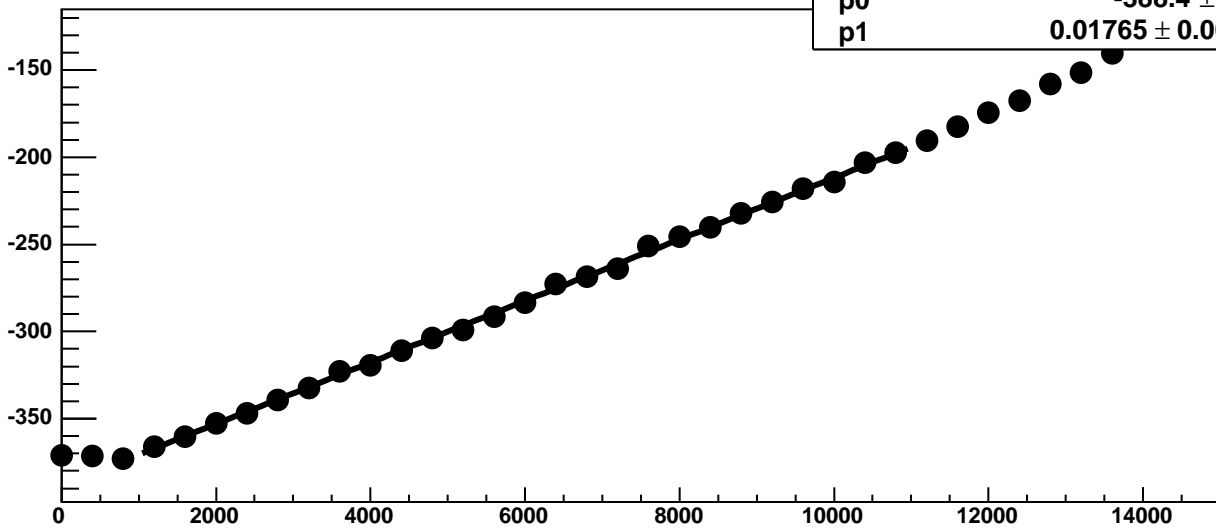
Chip 6, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



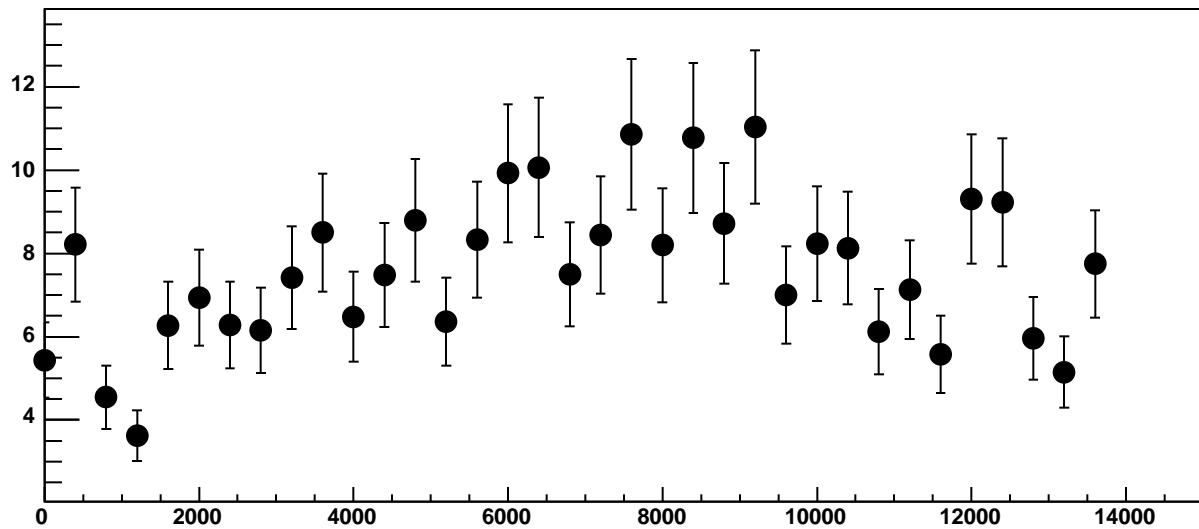
Chip 6, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC



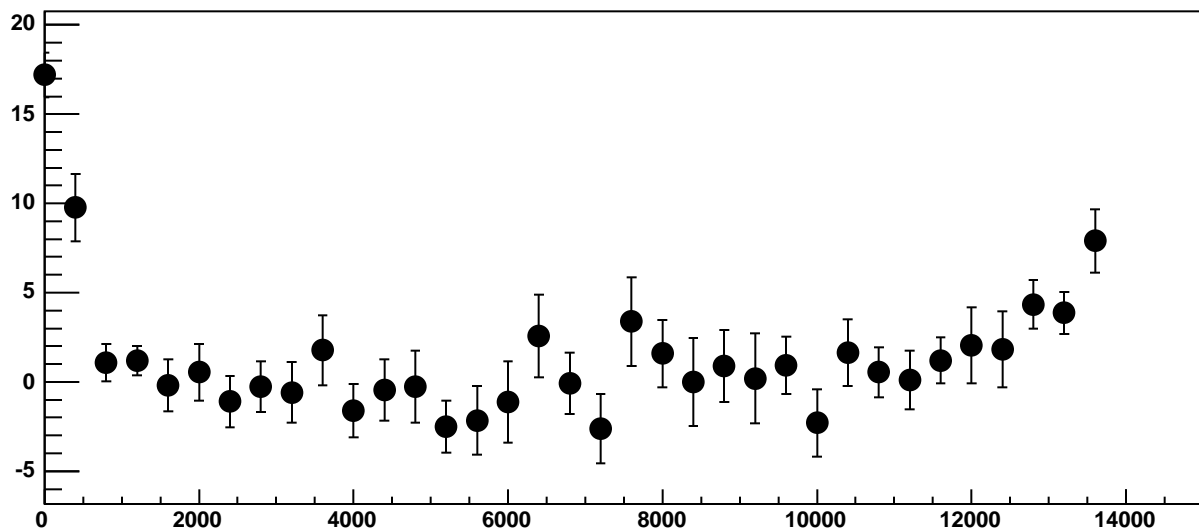
Chip 6, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



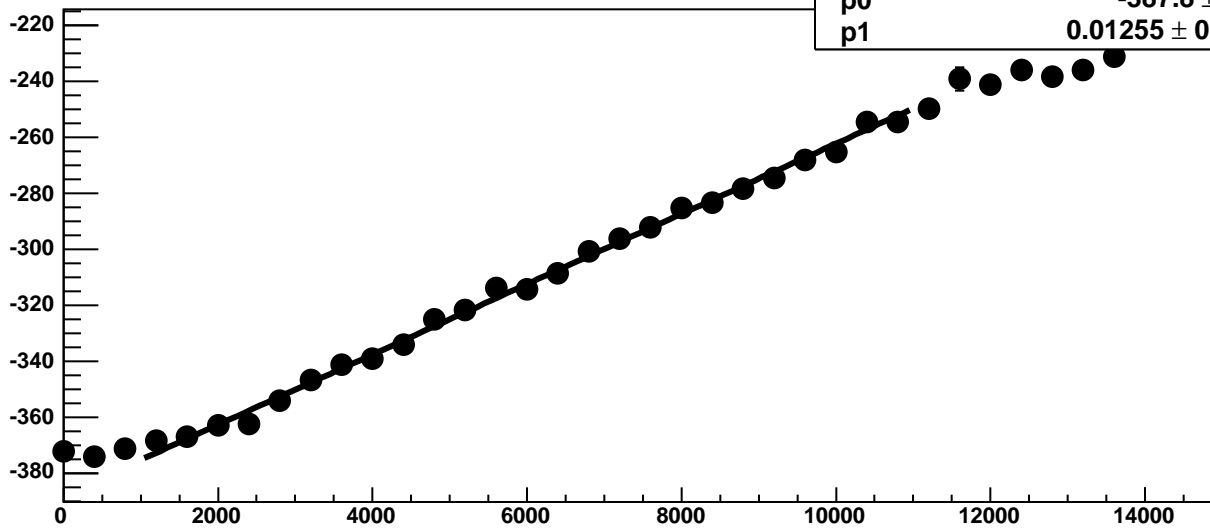
Chip 6, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



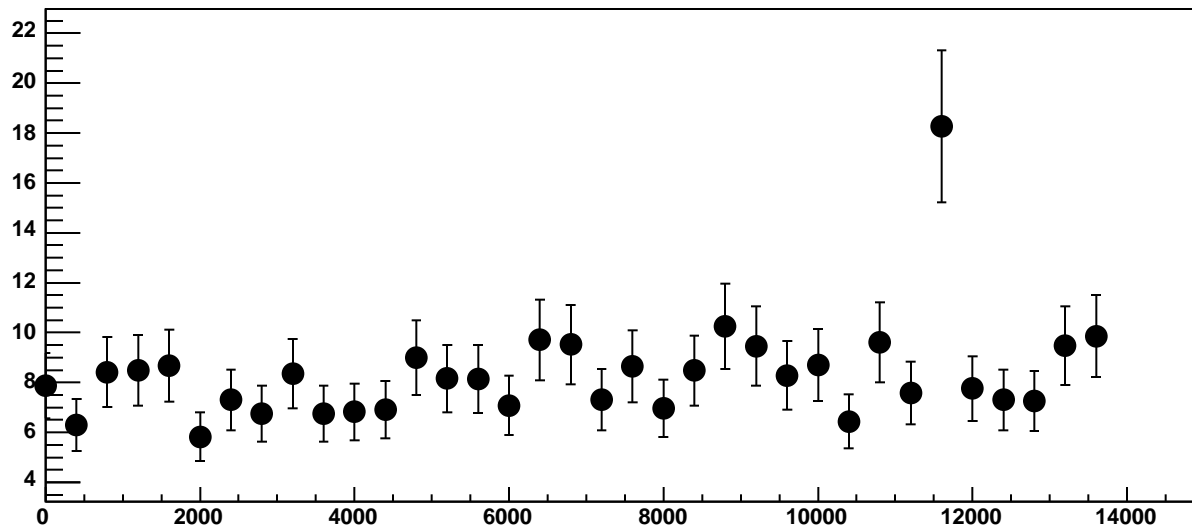
Chip 6, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



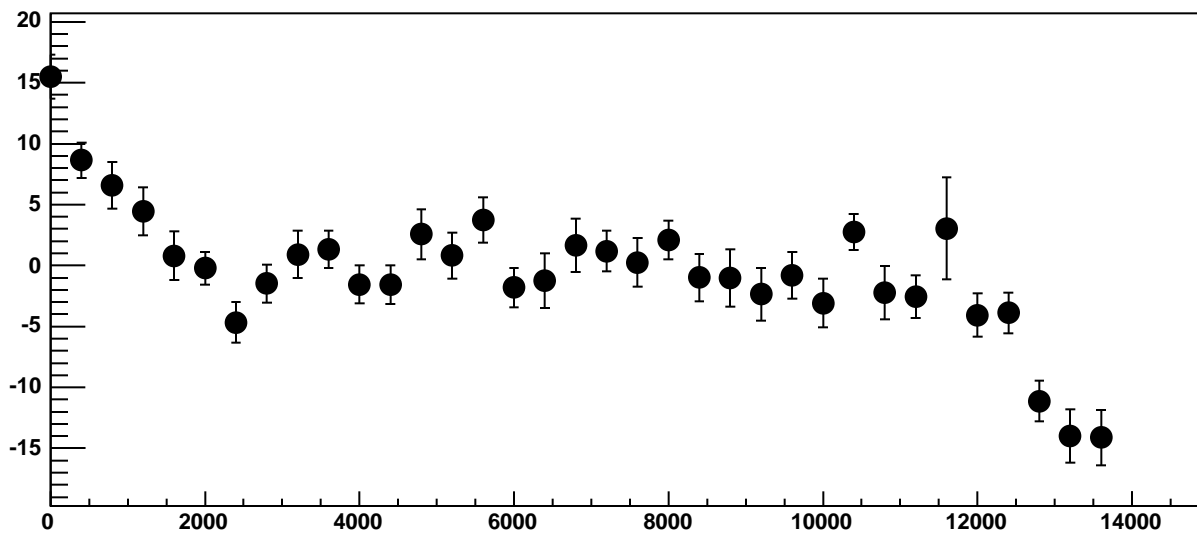
Chip 6, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



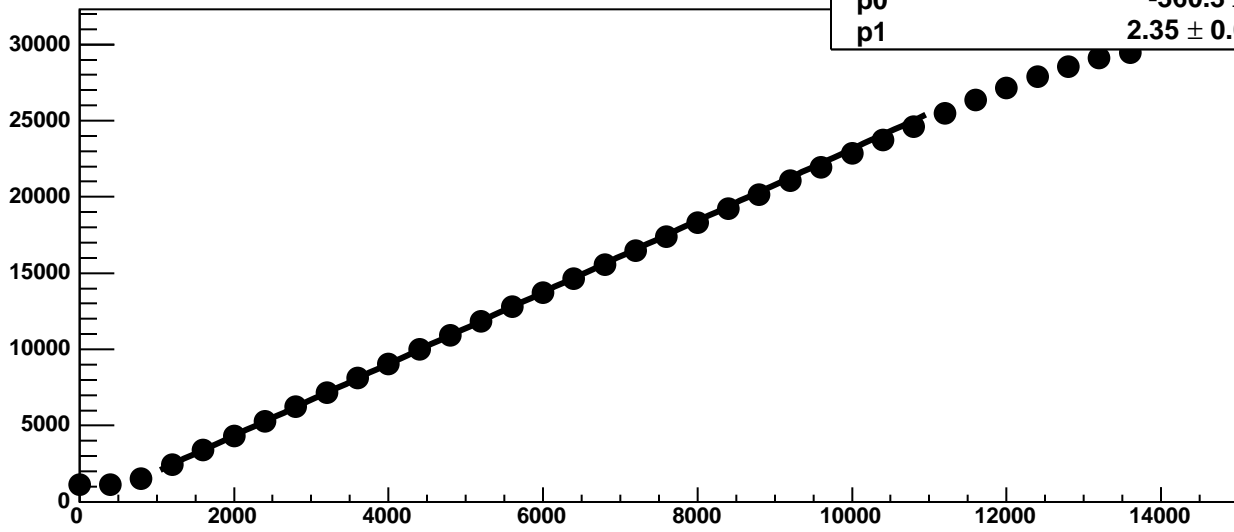
Chip 6, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



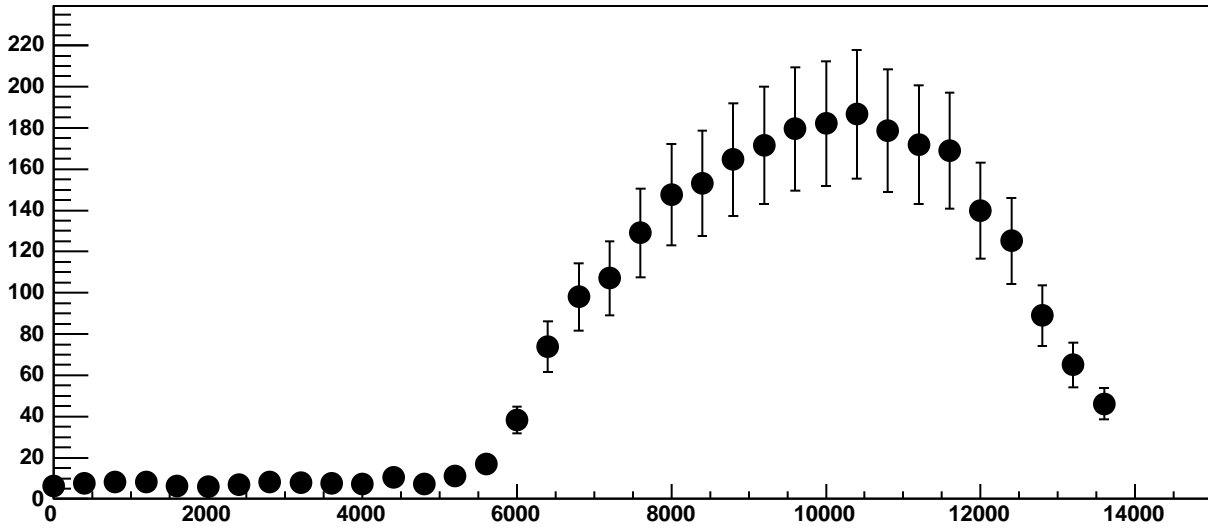
Chip 6, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



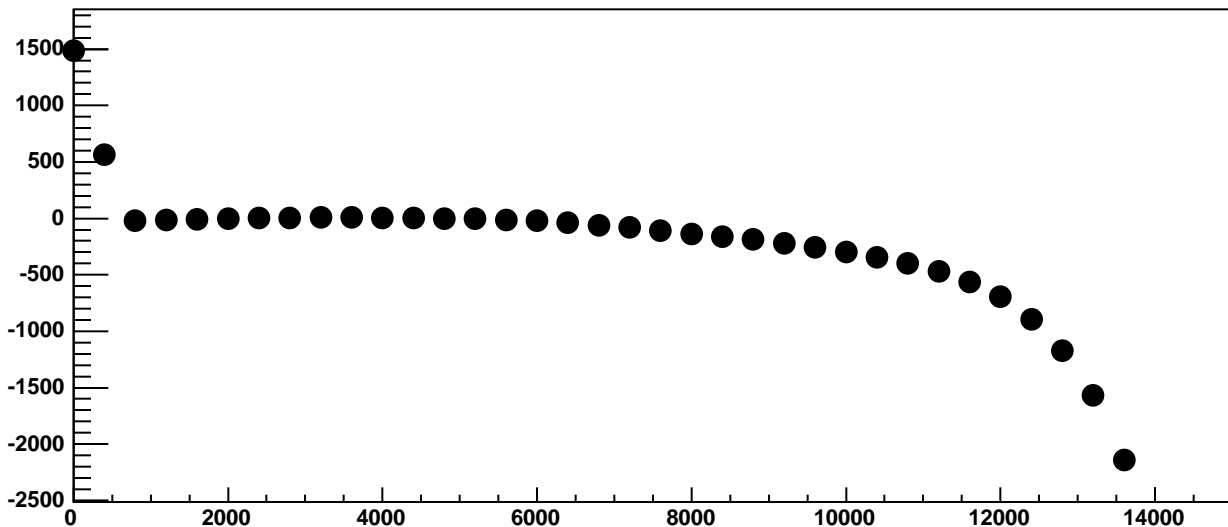
Chip 6, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC



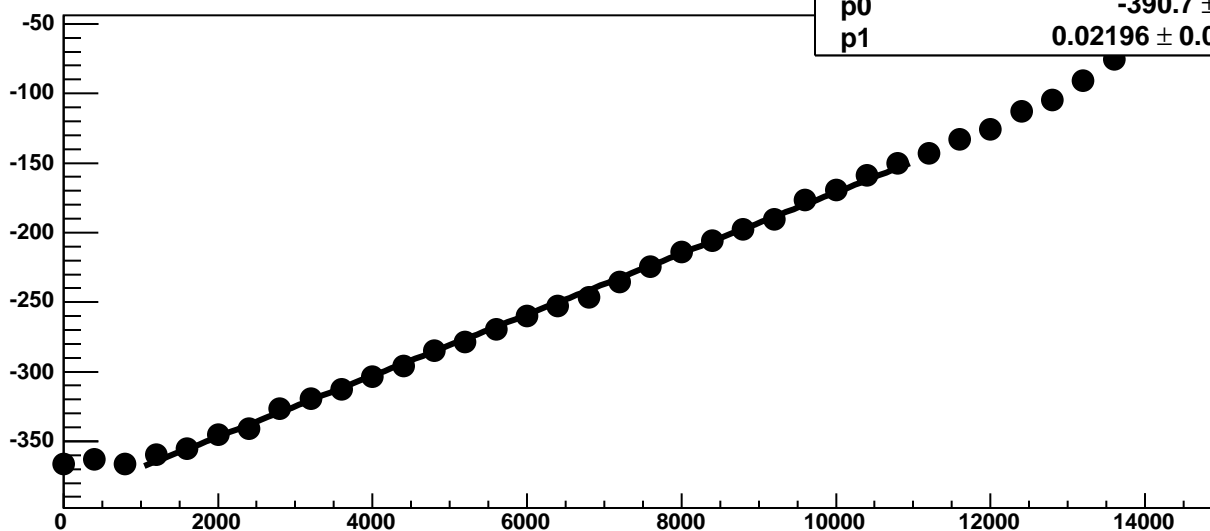
Chip 6, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC

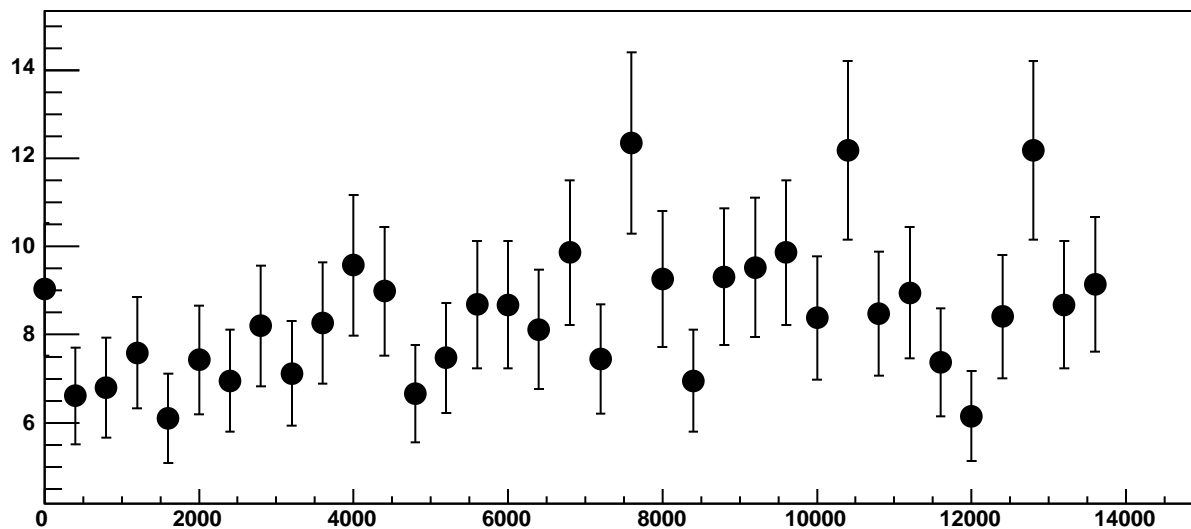


Chip 6, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

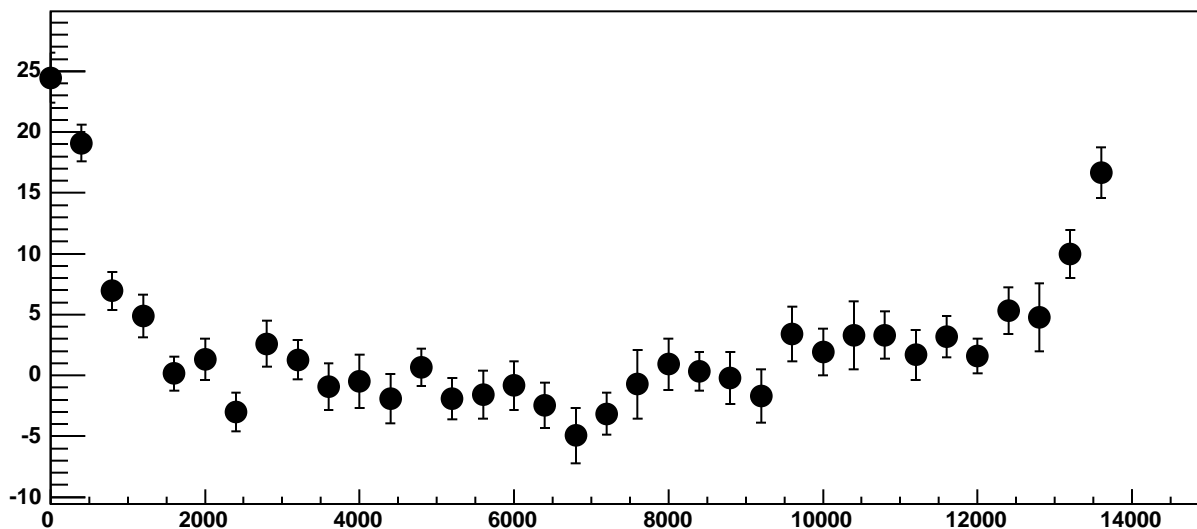


χ^2 / ndf 36.4 / 23
p0 -390.7 ± 0.8034
p1 0.02196 ± 0.0001299

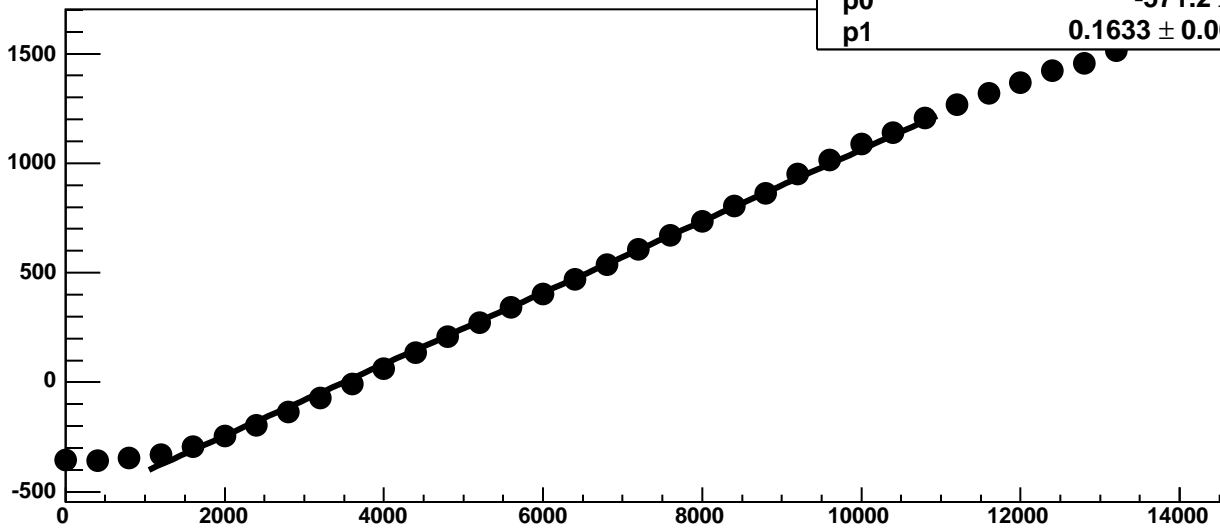
Chip 6, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



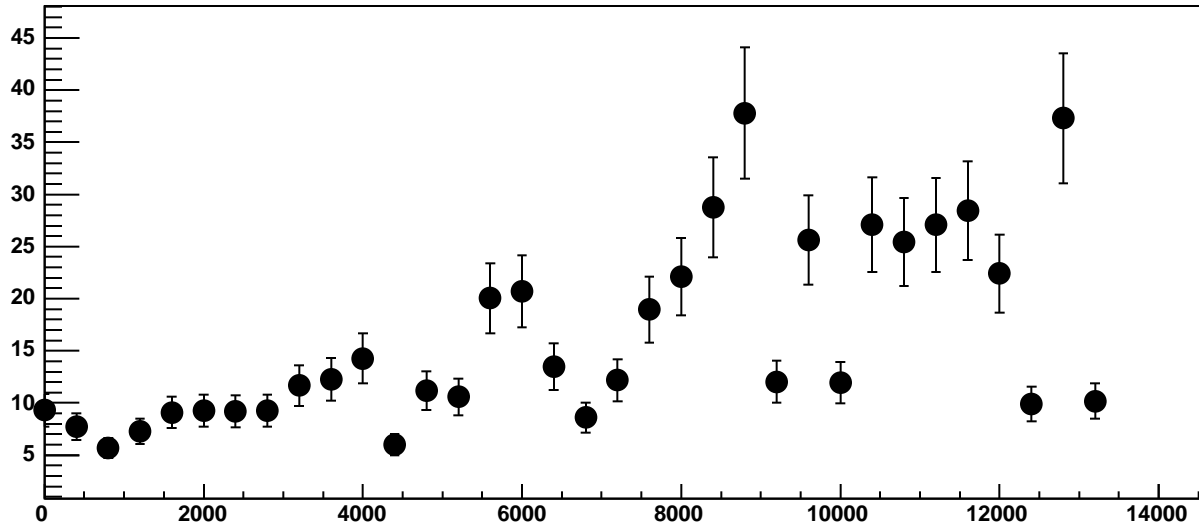
Chip 6, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



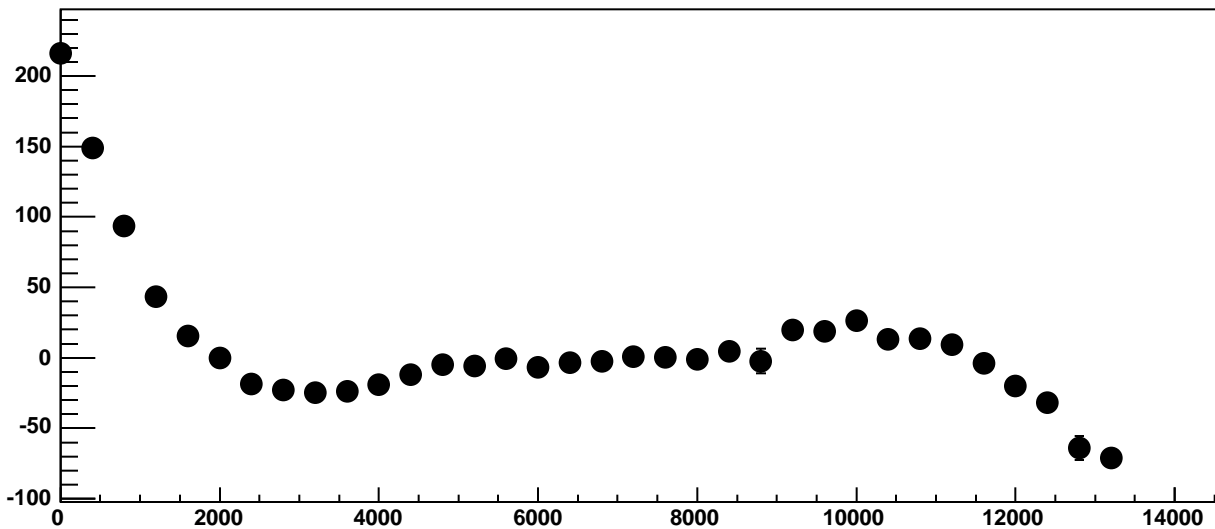
Chip 6, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



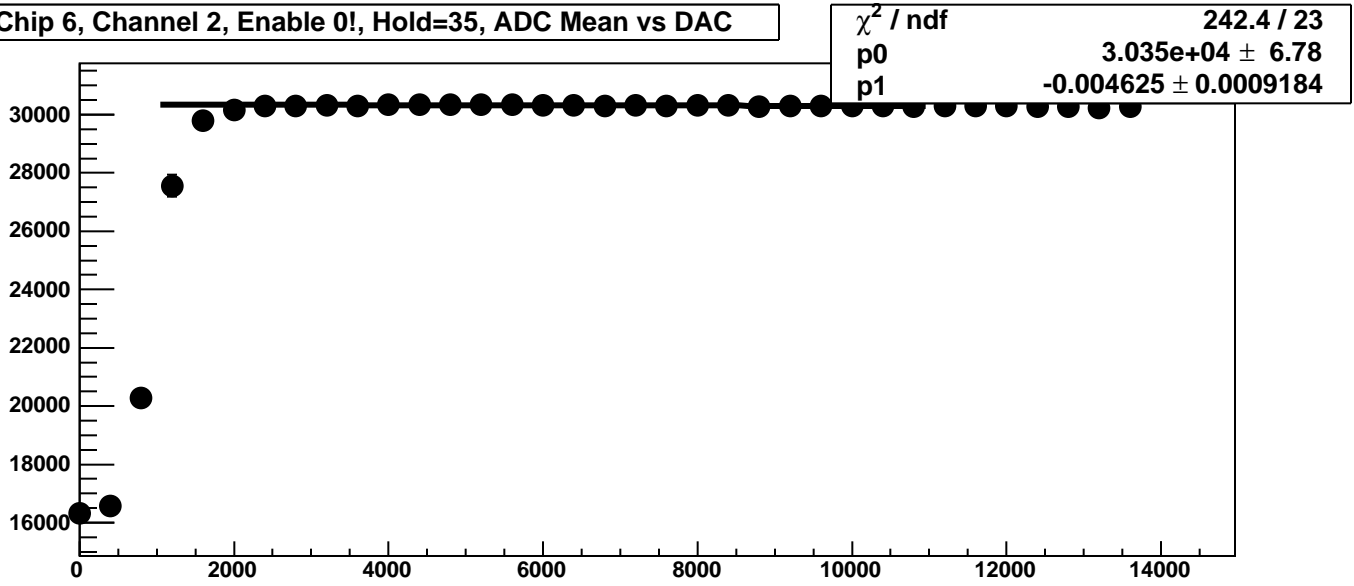
Chip 6, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



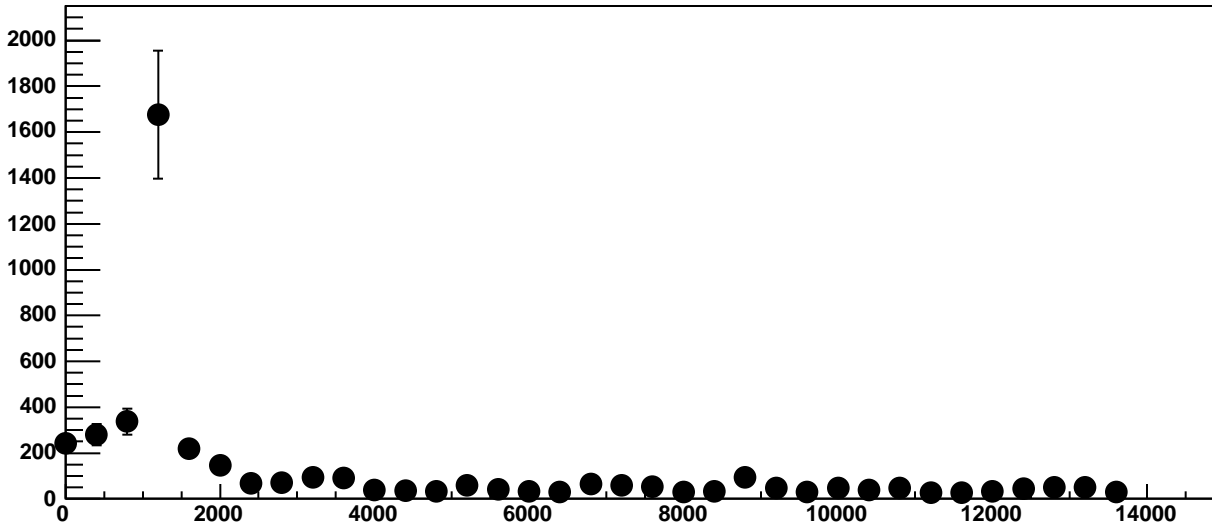
Chip 6, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



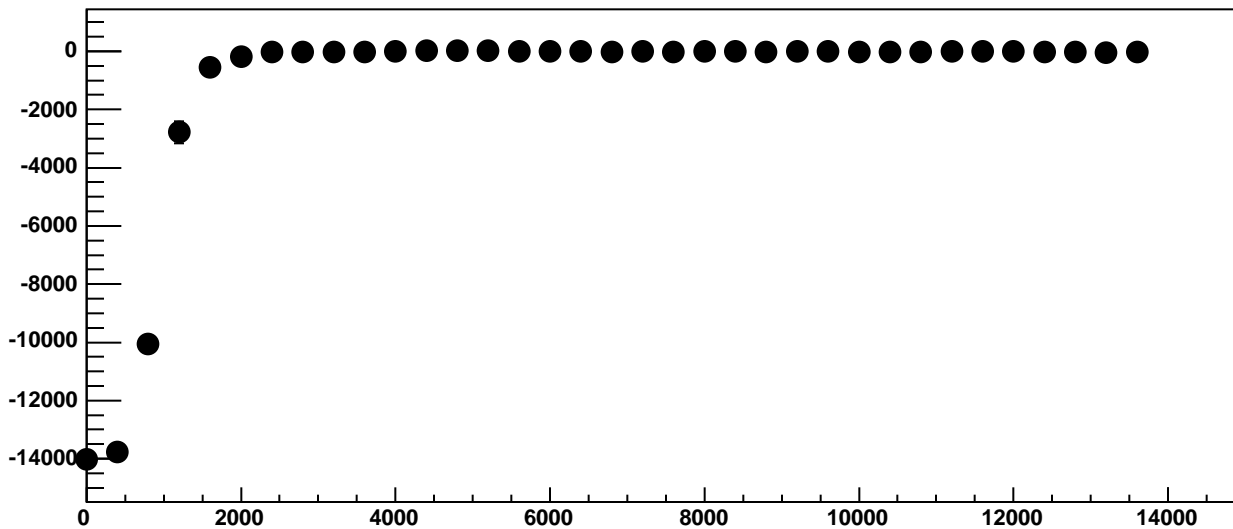
Chip 6, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC



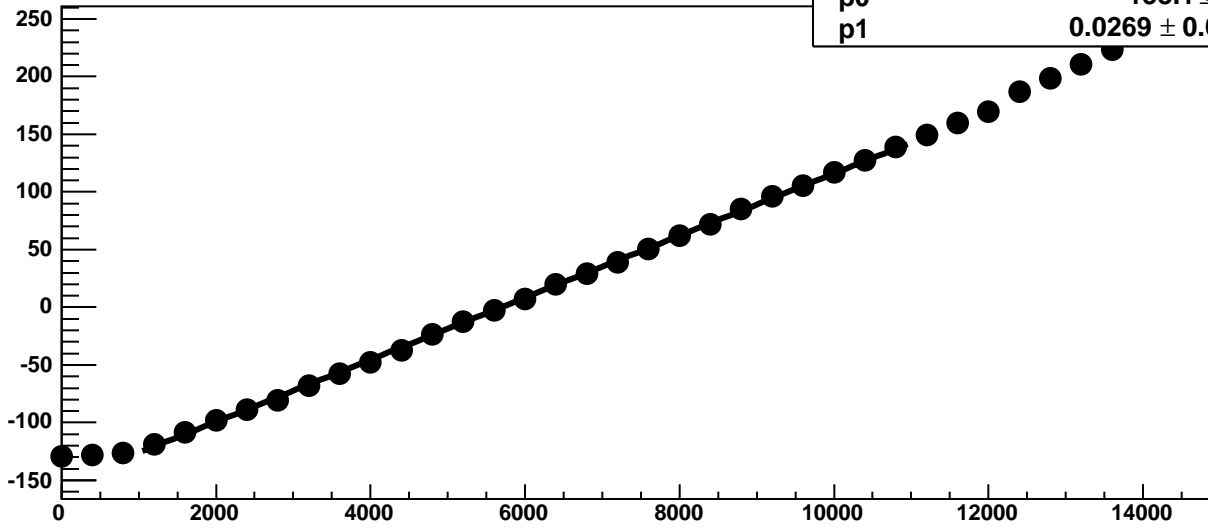
Chip 6, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



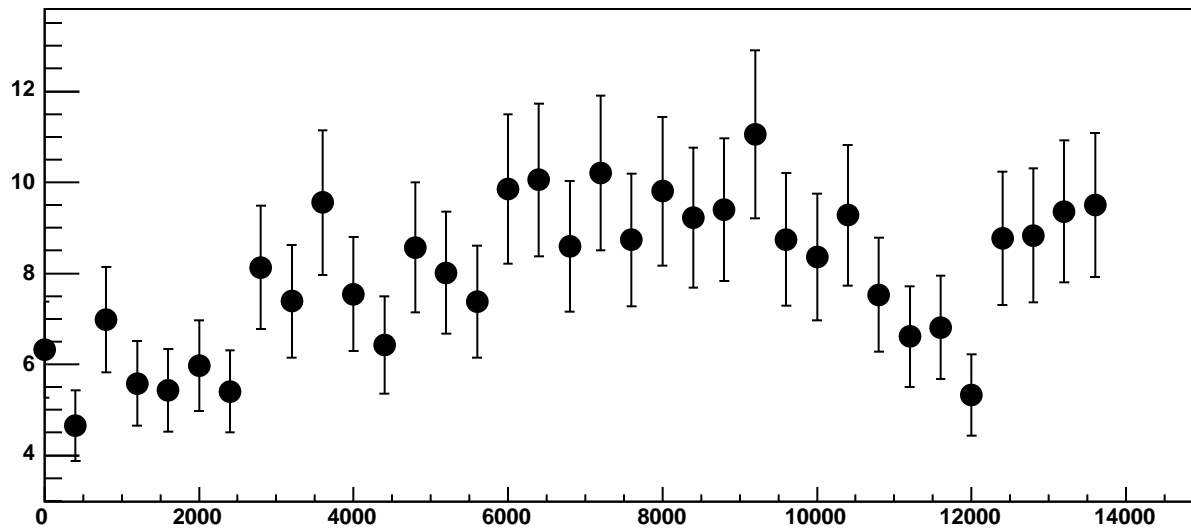
Chip 6, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC



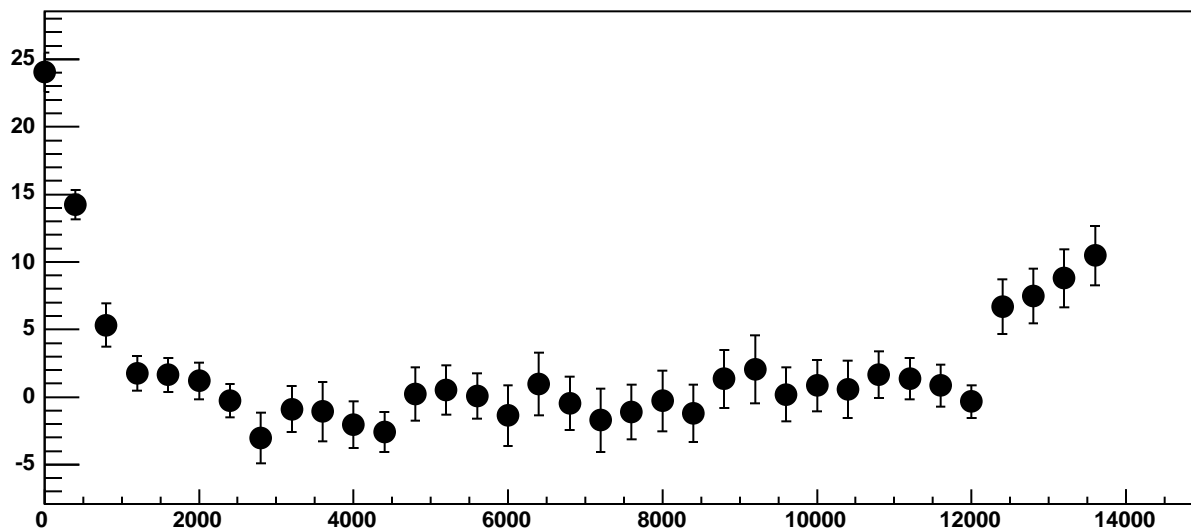
Chip 6, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



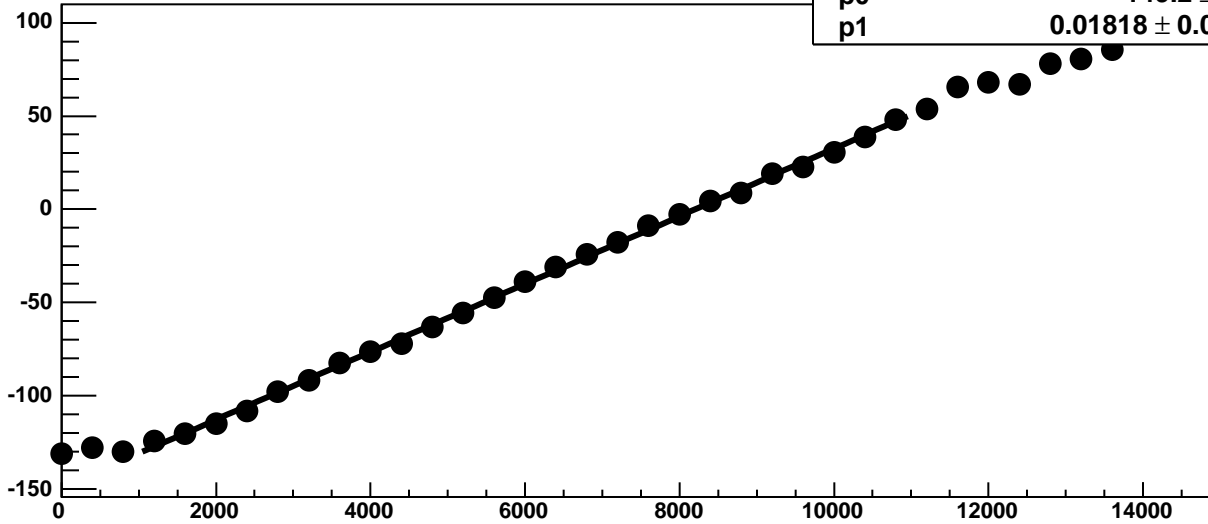
Chip 6, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



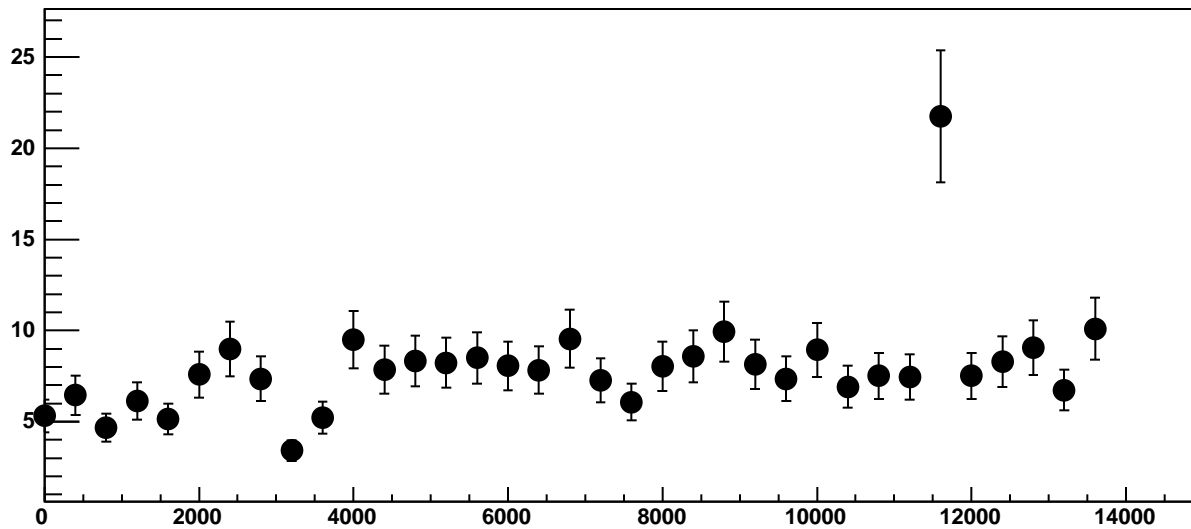
Chip 6, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



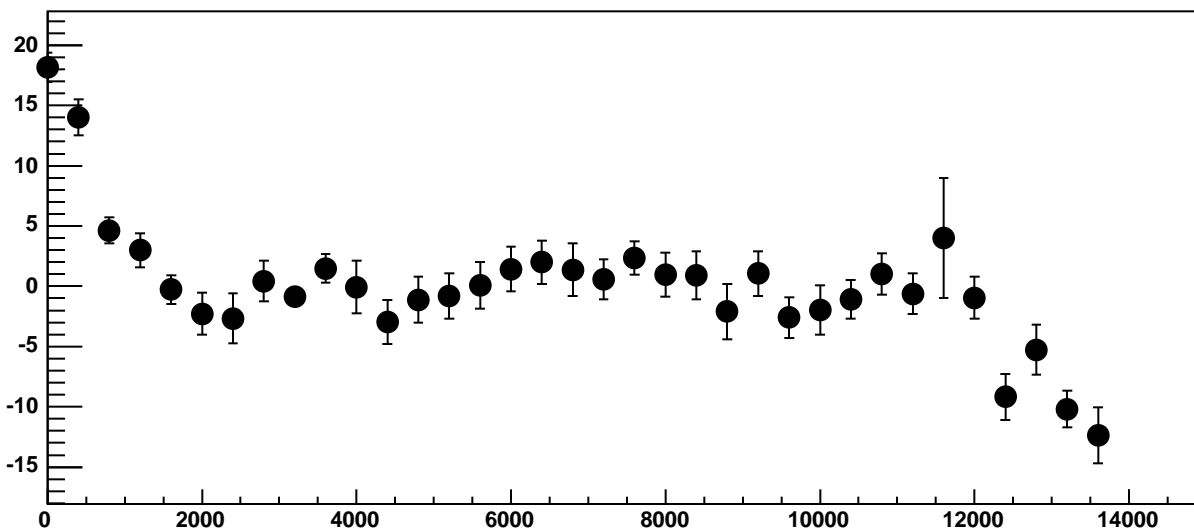
Chip 6, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



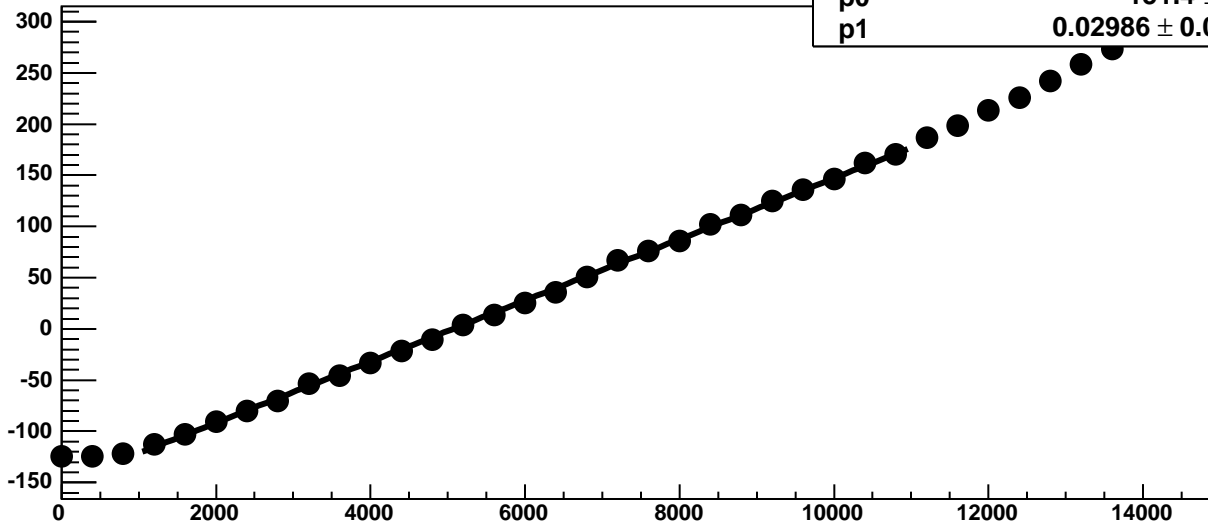
Chip 6, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

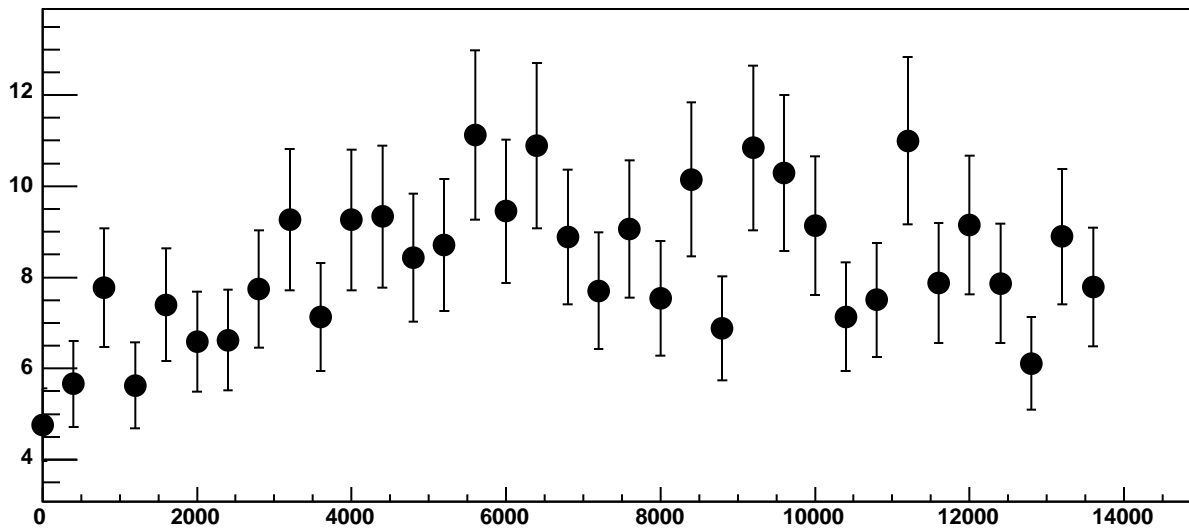


Chip 6, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

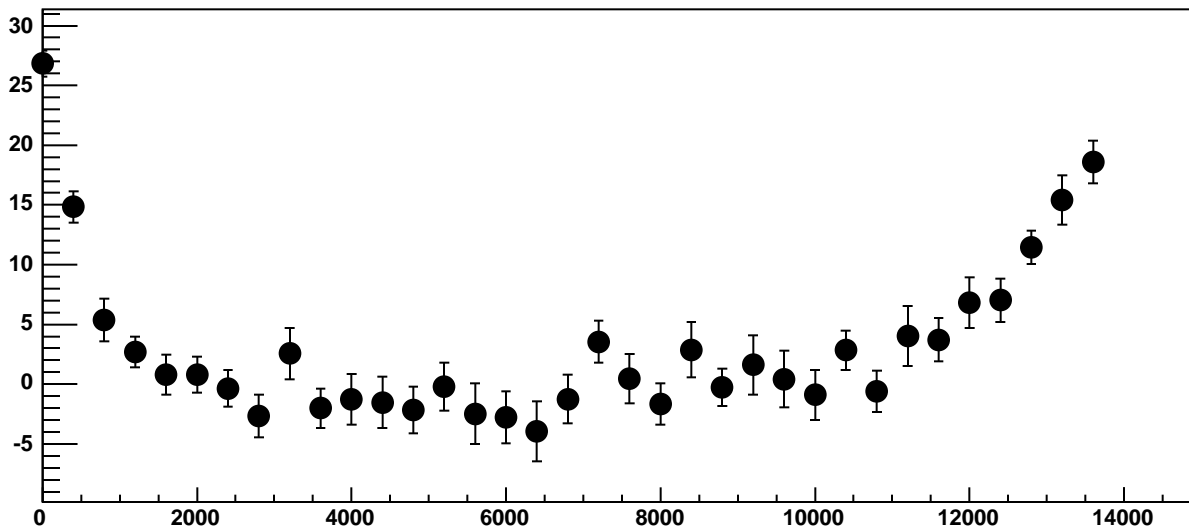


χ^2 / ndf 28.03 / 23
p0 -151.4 ± 0.7621
p1 0.02986 ± 0.0001192

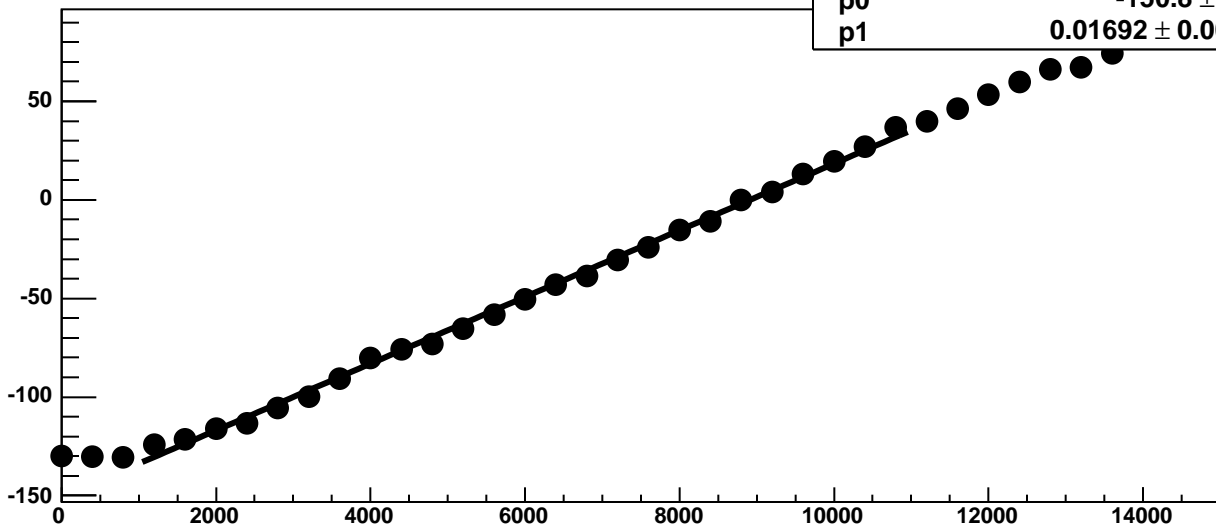
Chip 6, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

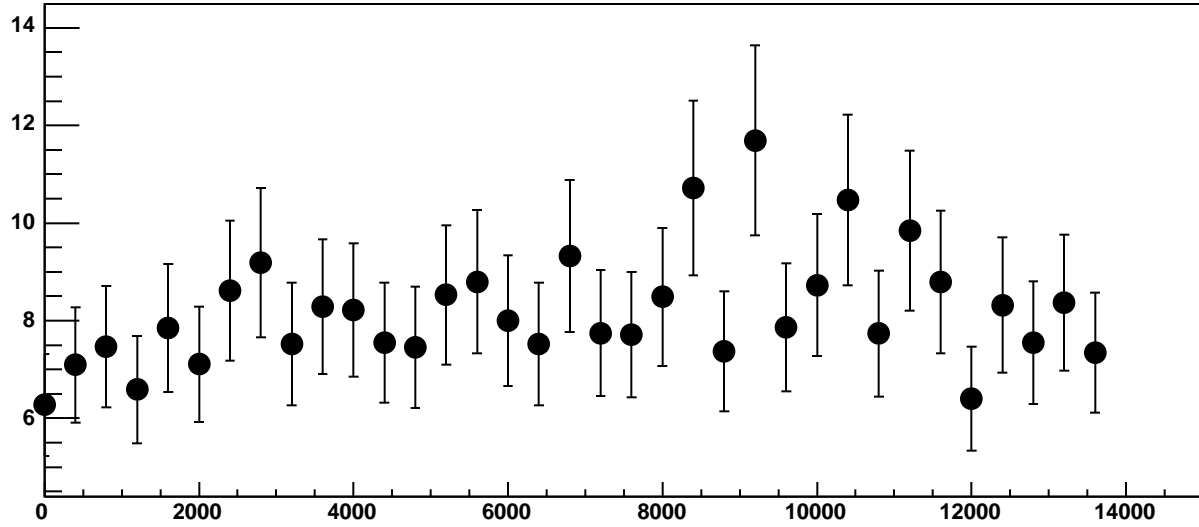


Chip 6, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

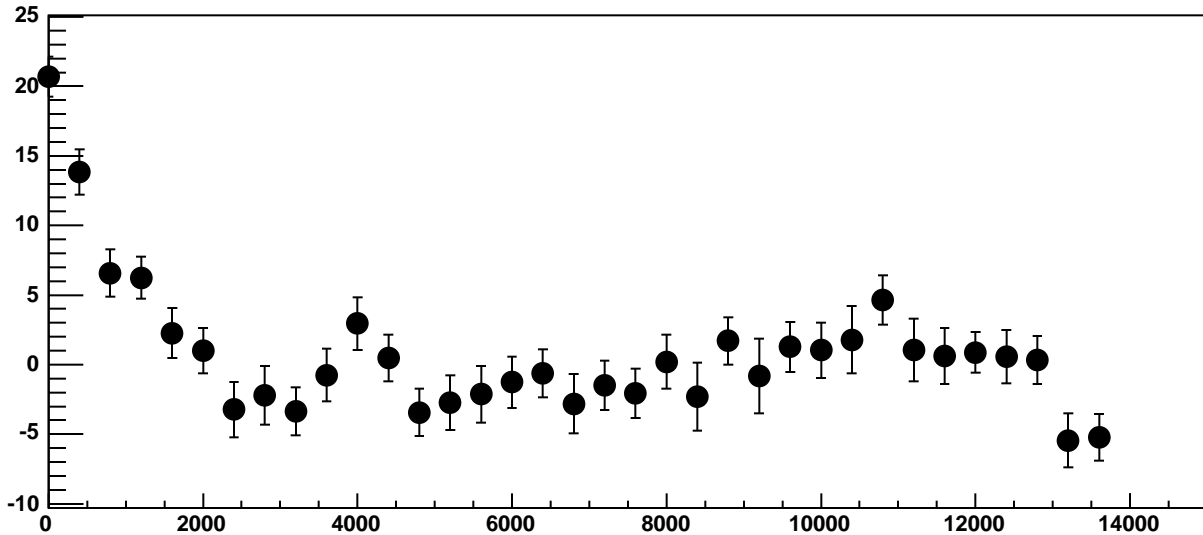


χ^2 / ndf 50.88 / 23
p0 -150.8 ± 0.8243
p1 0.01692 ± 0.0001288

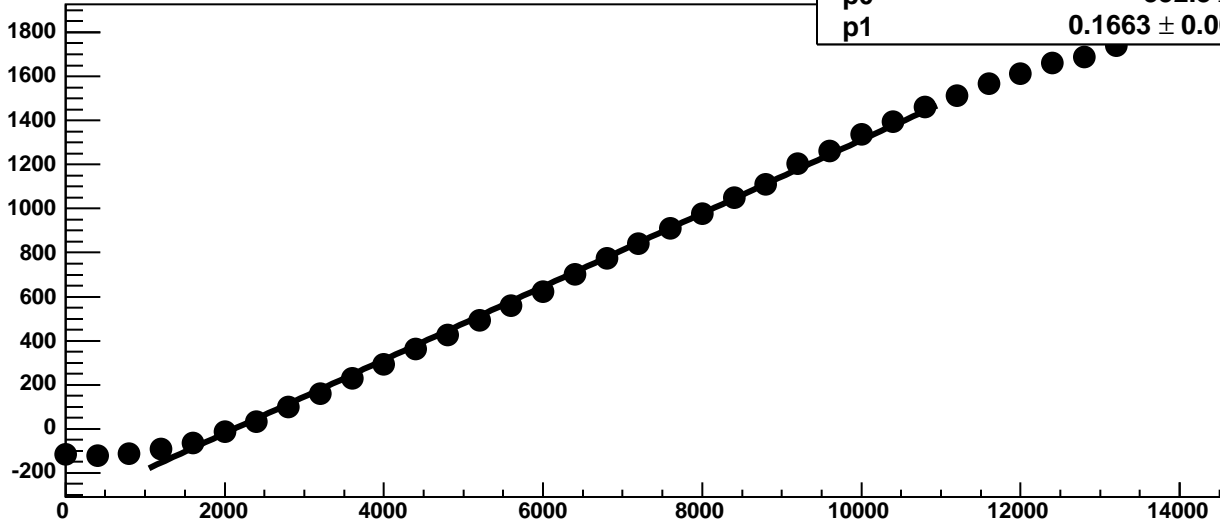
Chip 6, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



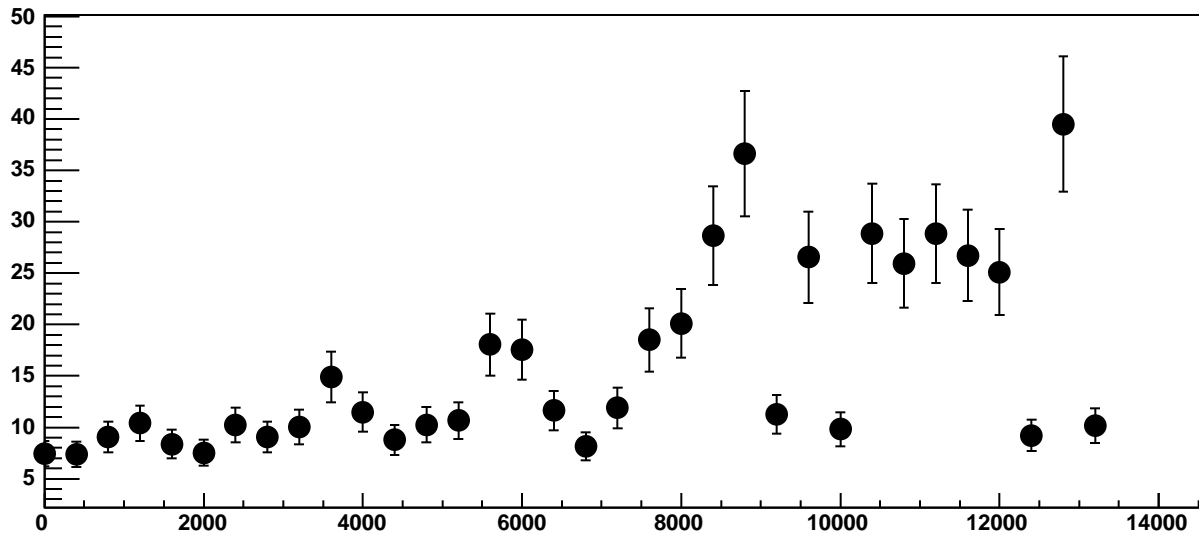
Chip 6, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



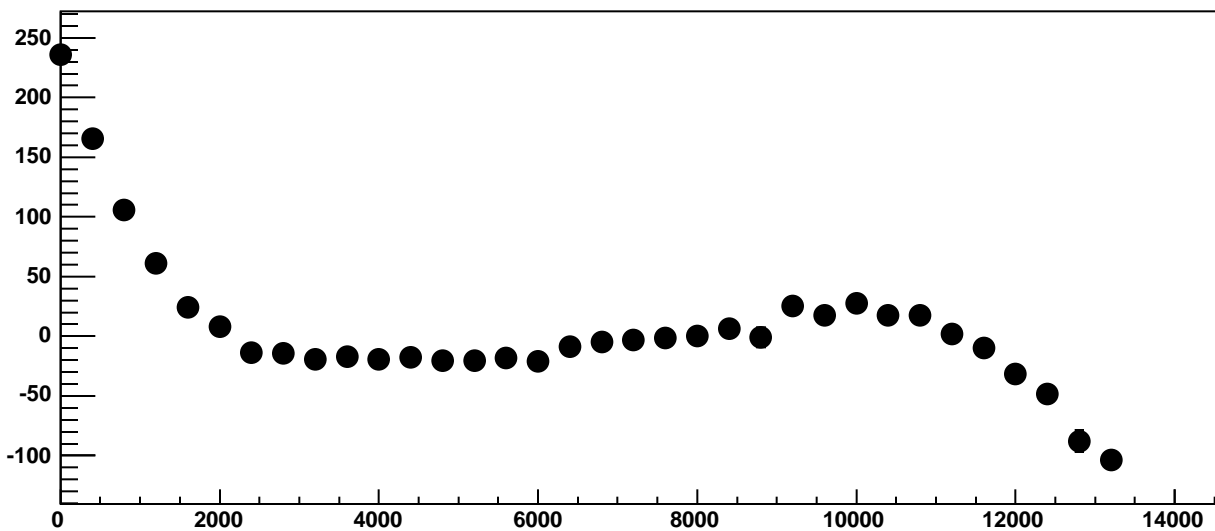
Chip 6, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



Chip 6, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC

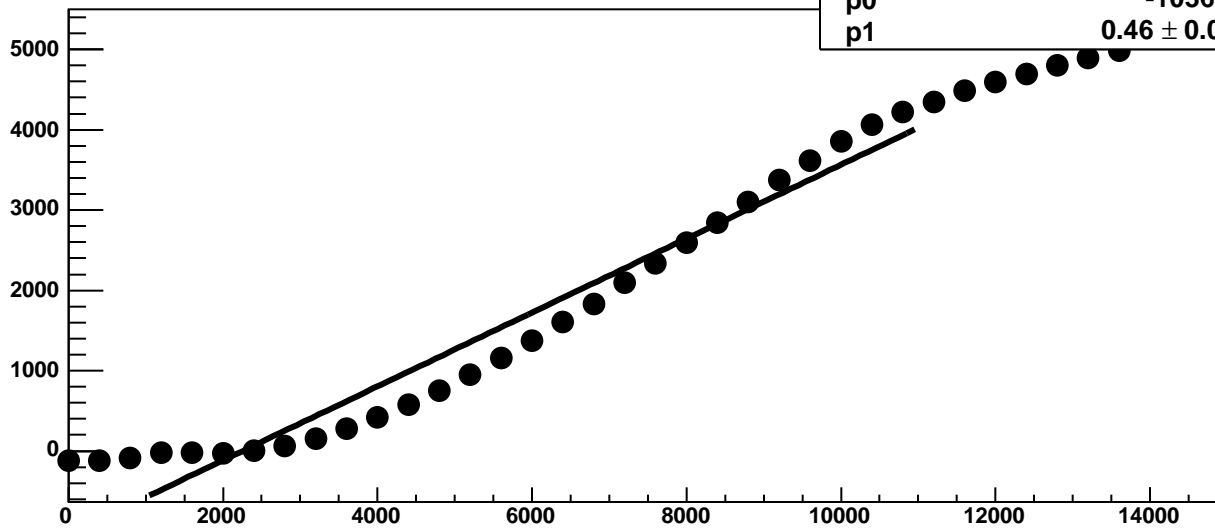


Chip 6, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

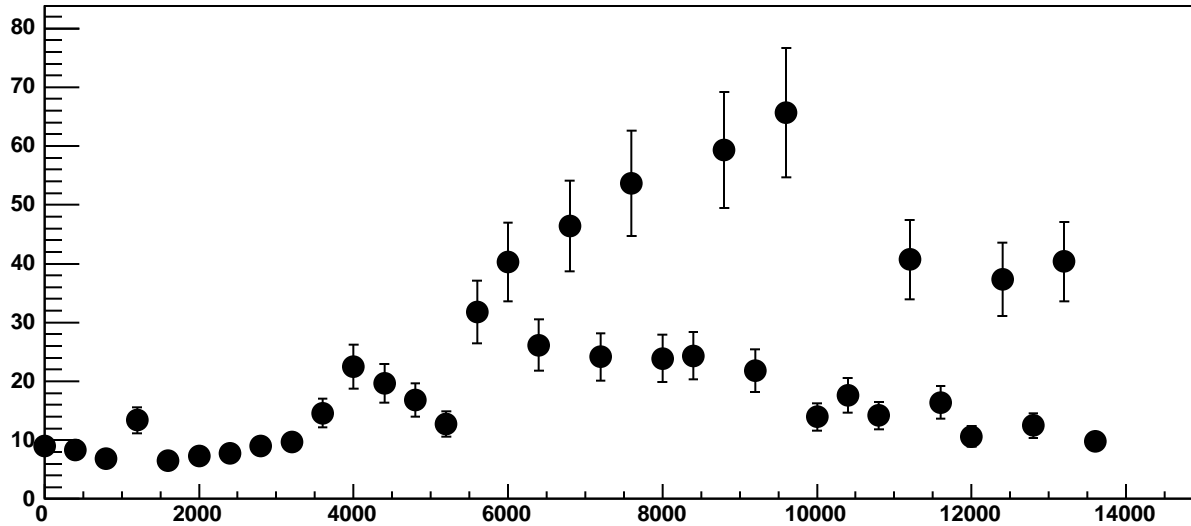


Chip 6, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

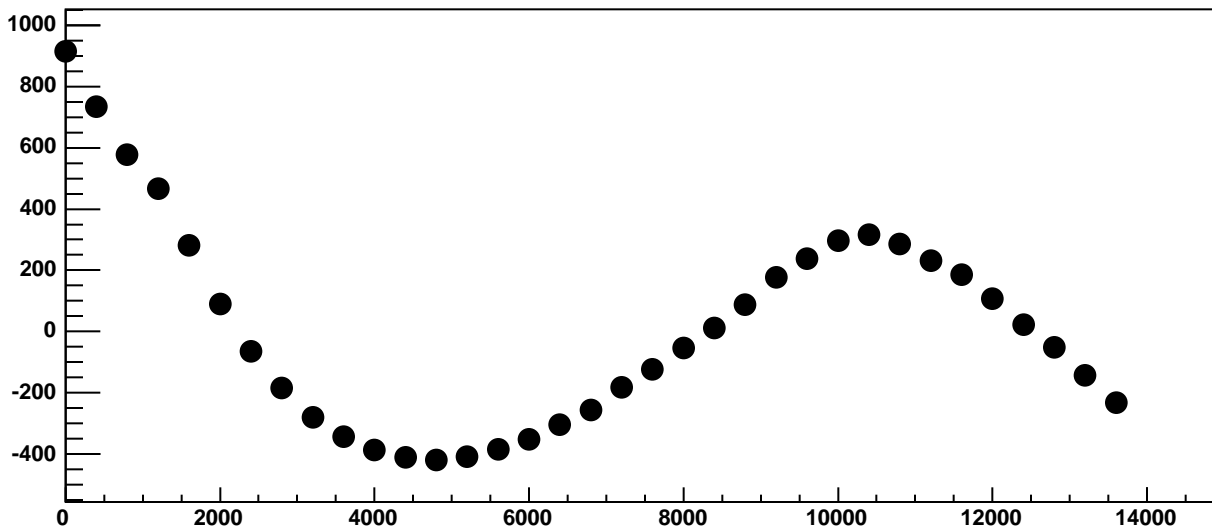
χ^2 / ndf 1.753e+05 / 23
p0 -1036 ± 1.067
p1 0.46 ± 0.0002243



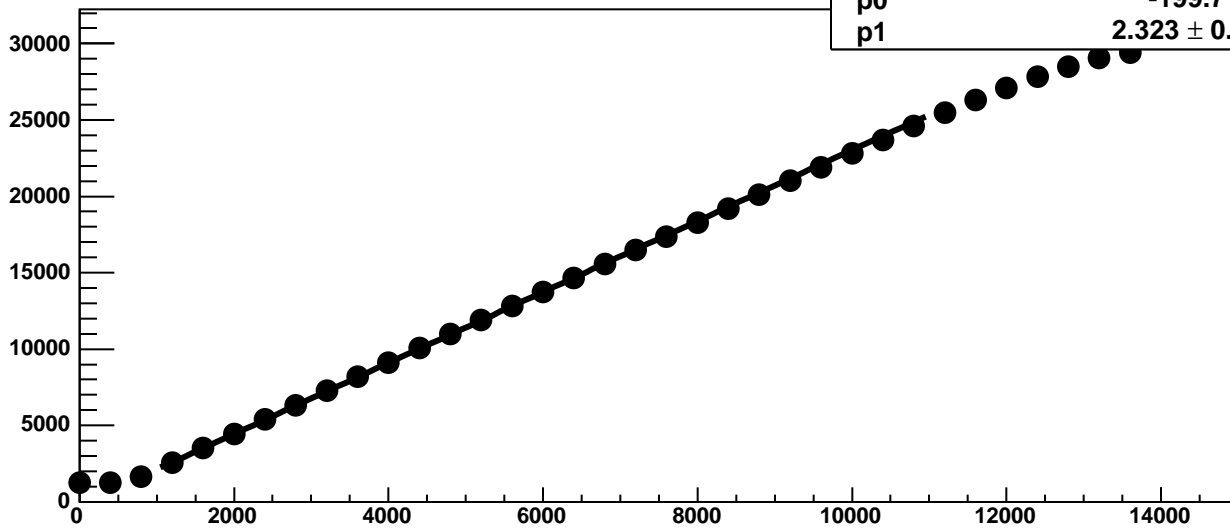
Chip 6, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

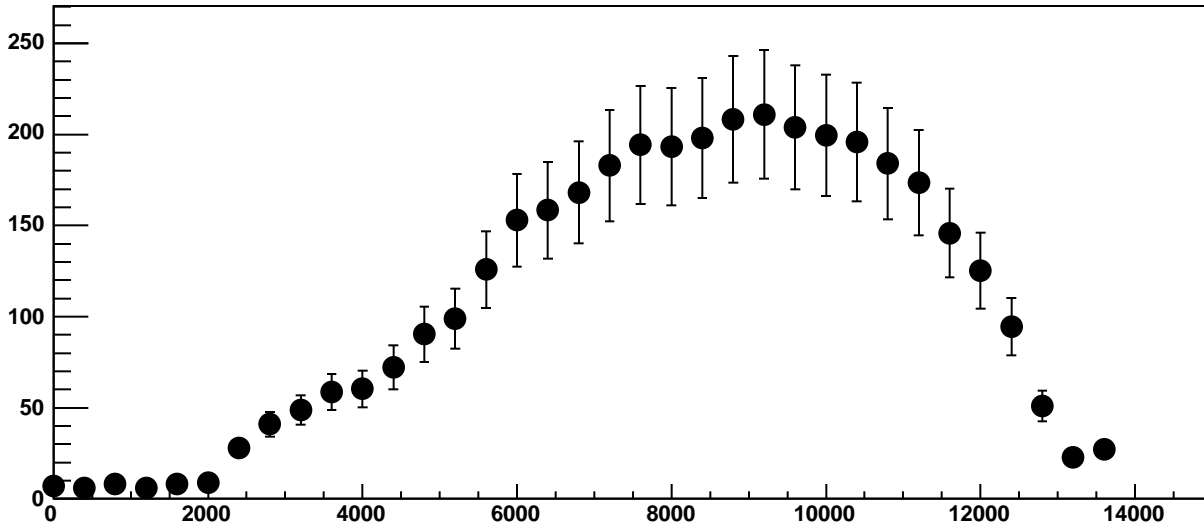


Chip 6, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC

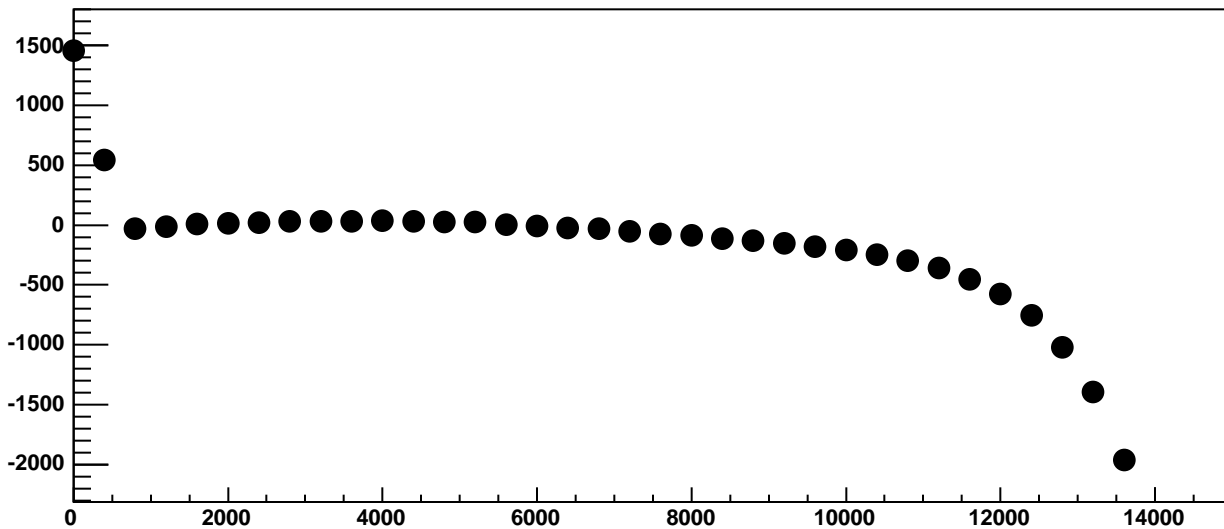


χ^2 / ndf 325.2 / 23
p0 -199.7 ± 2.259
p1 2.323 ± 0.001247

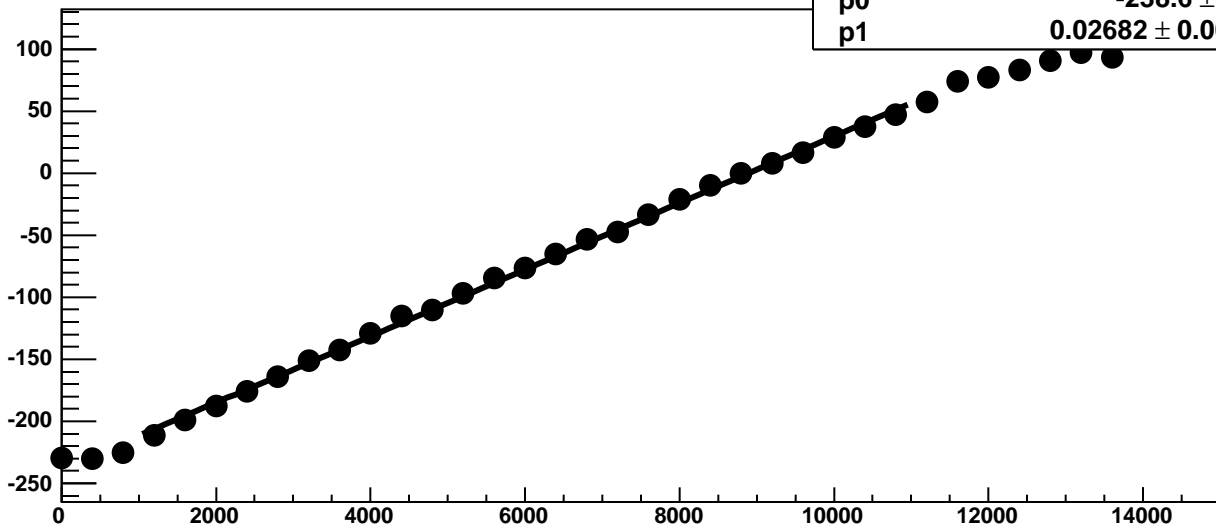
Chip 6, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



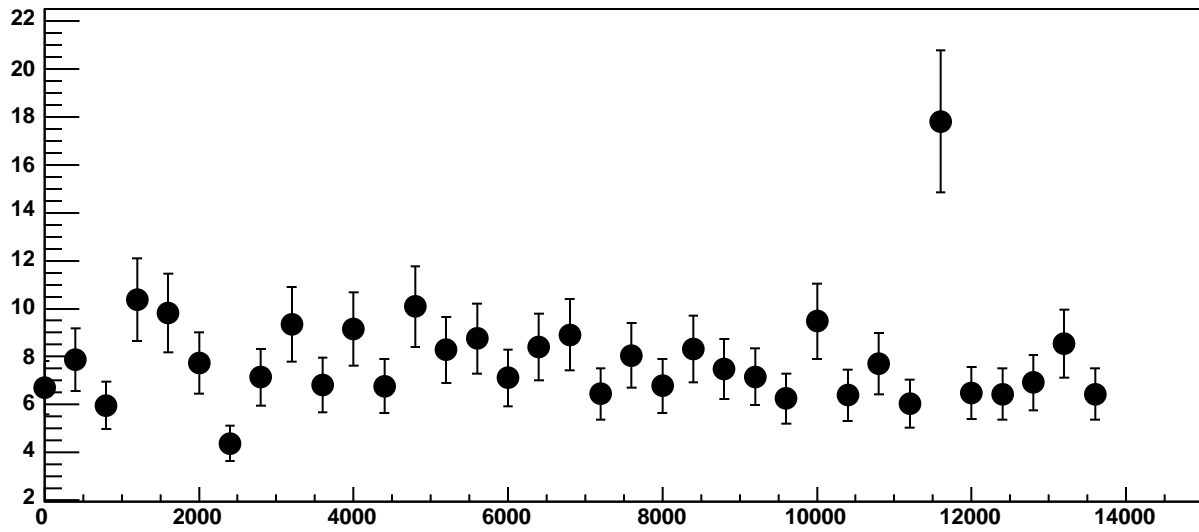
Chip 6, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC



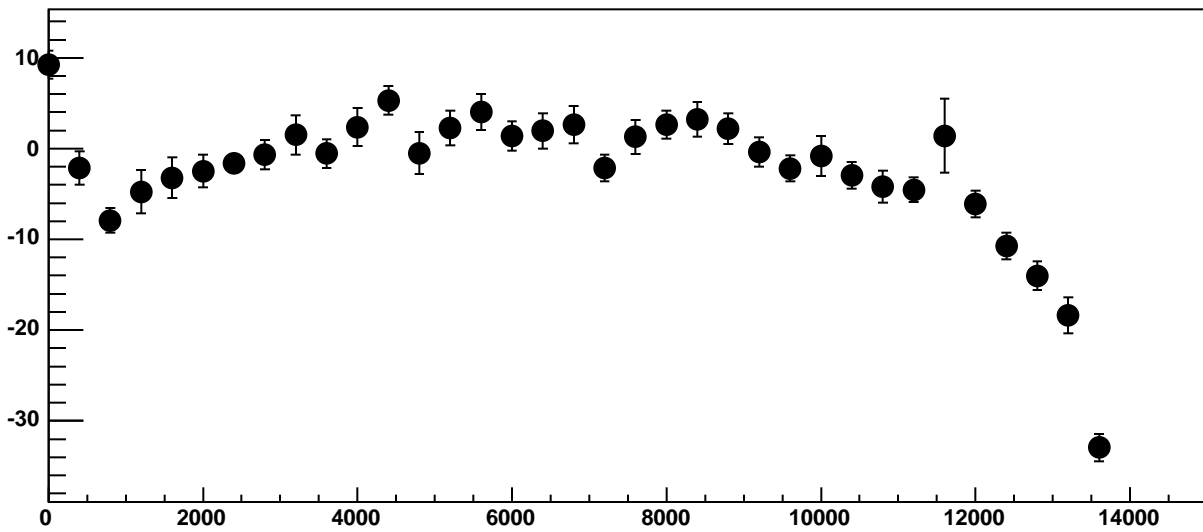
Chip 6, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



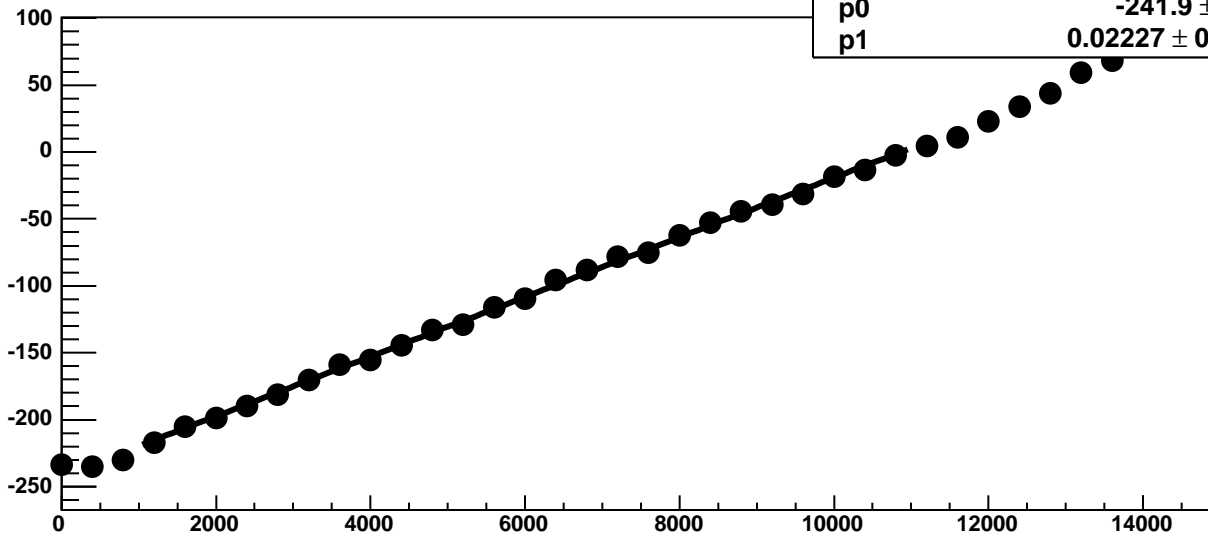
Chip 6, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 6, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

38.55 / 23

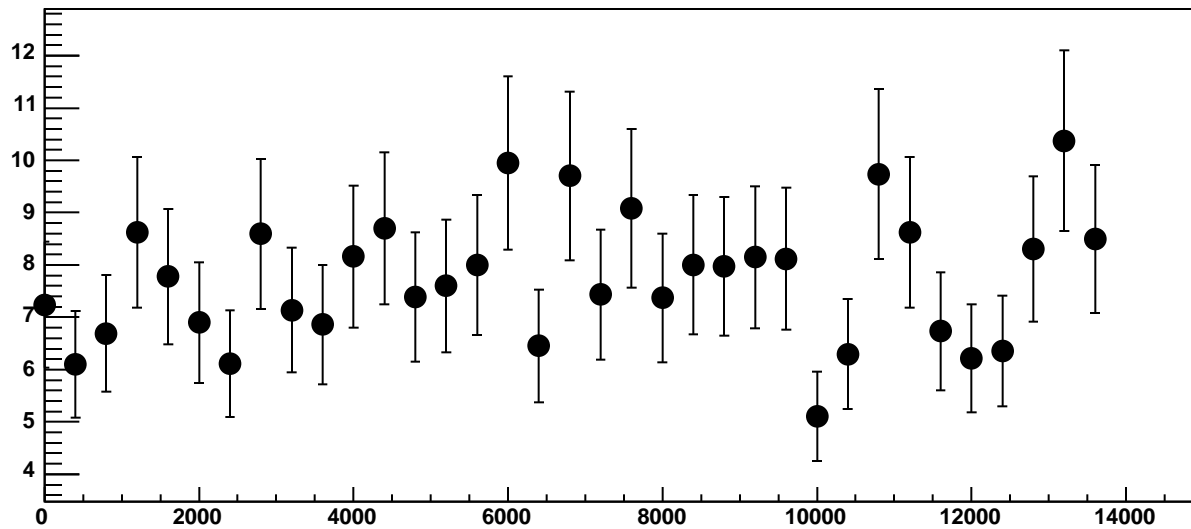
p0

-241.9 ± 0.7855

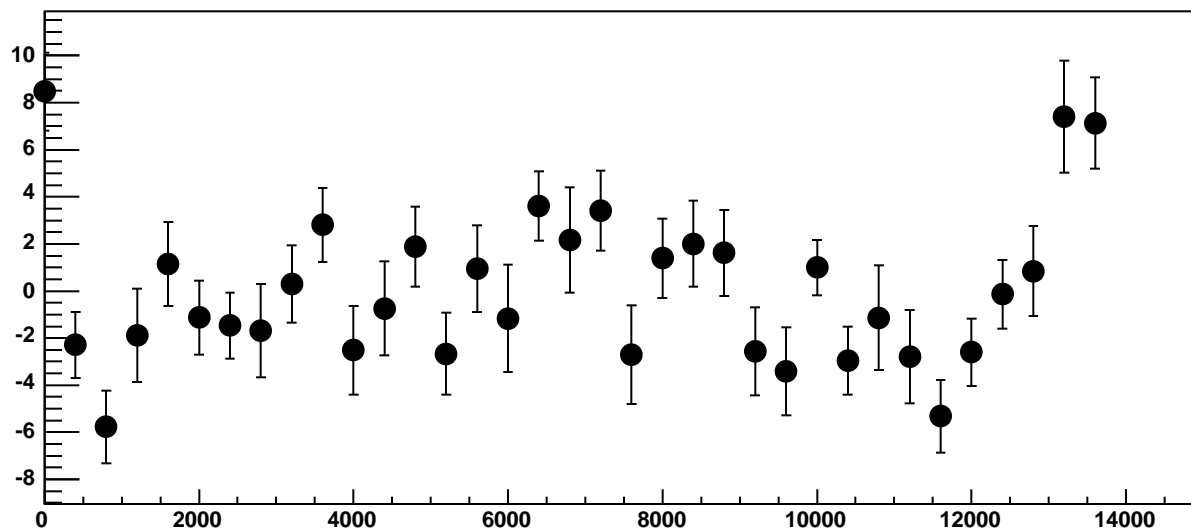
p1

0.02227 ± 0.000116

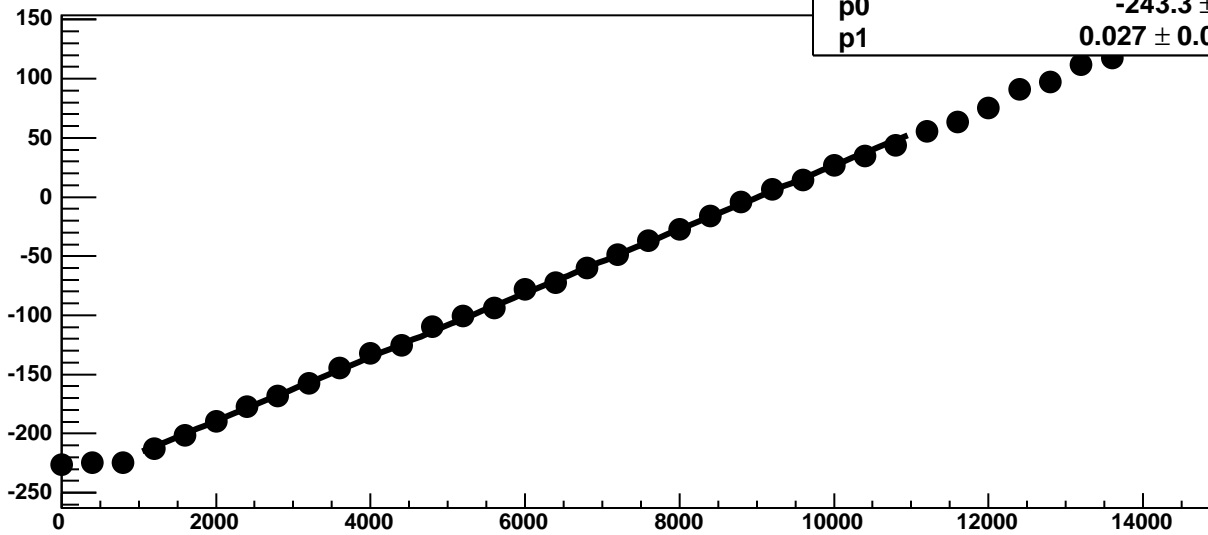
Chip 6, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

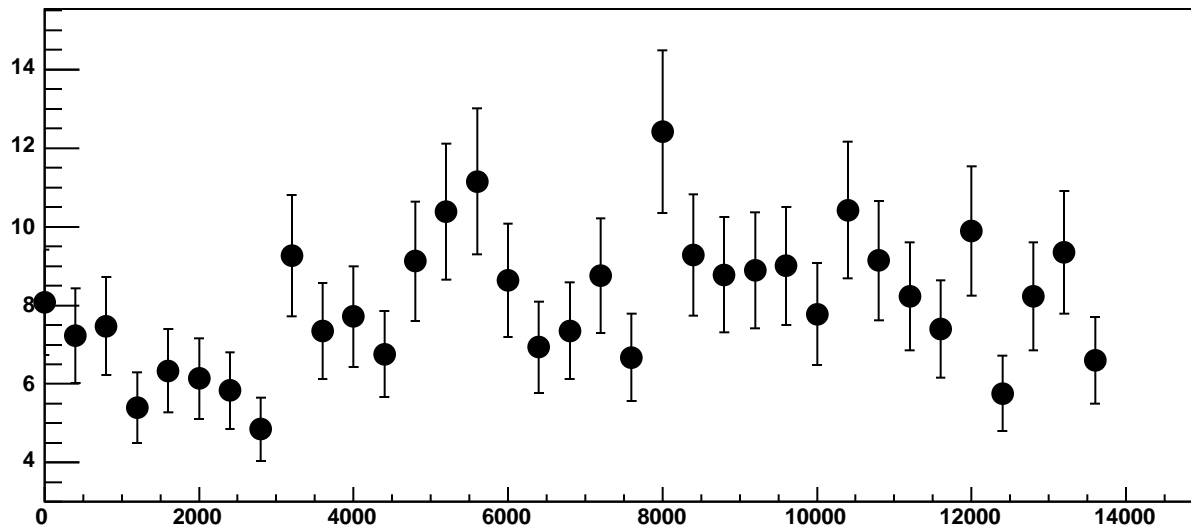


Chip 6, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

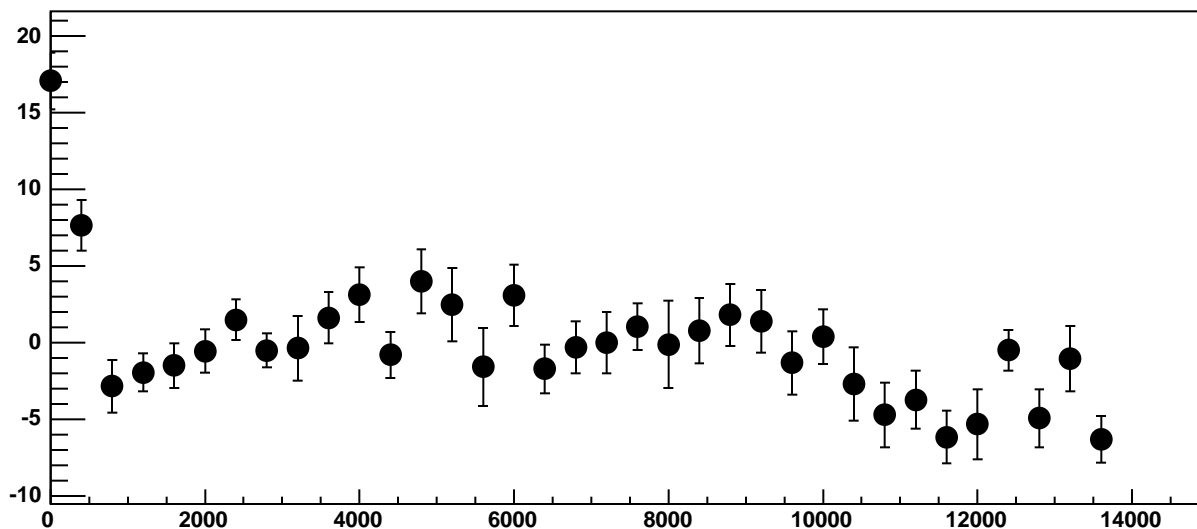


χ^2 / ndf 26.94 / 23
p0 -243.3 ± 0.6929
p1 0.027 ± 0.0001176

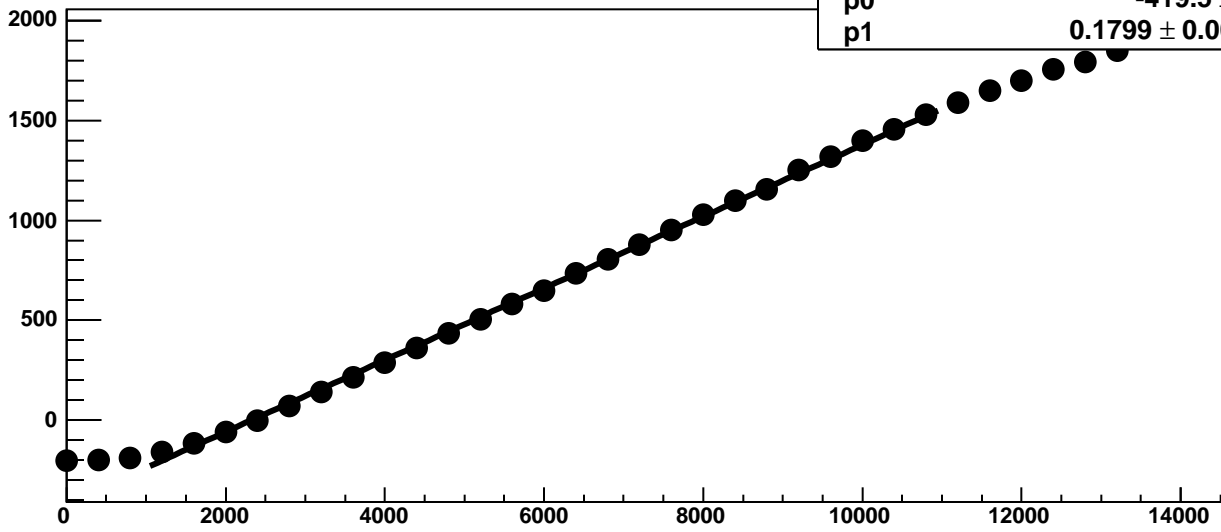
Chip 6, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



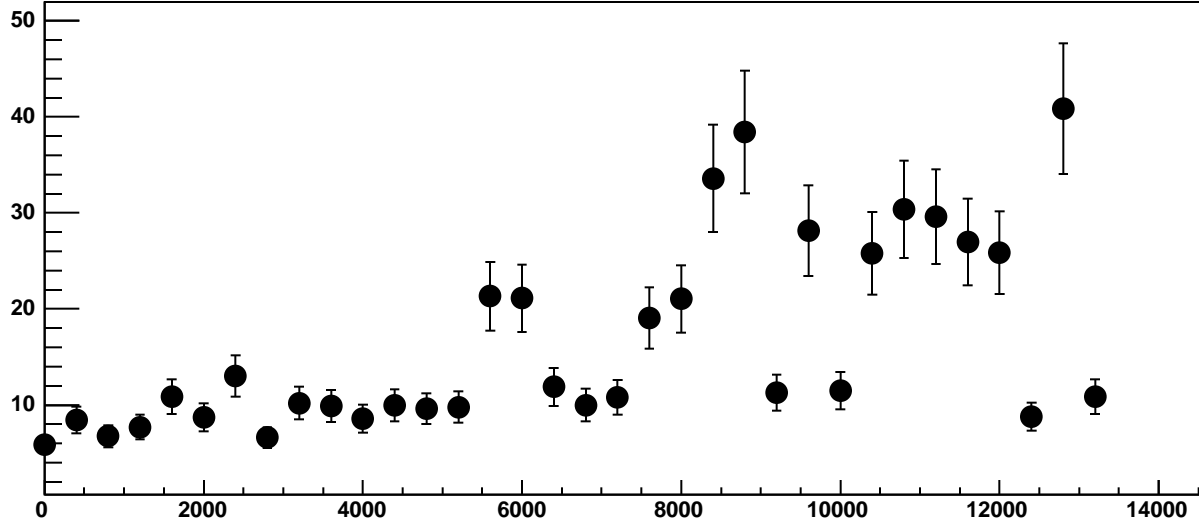
Chip 6, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC



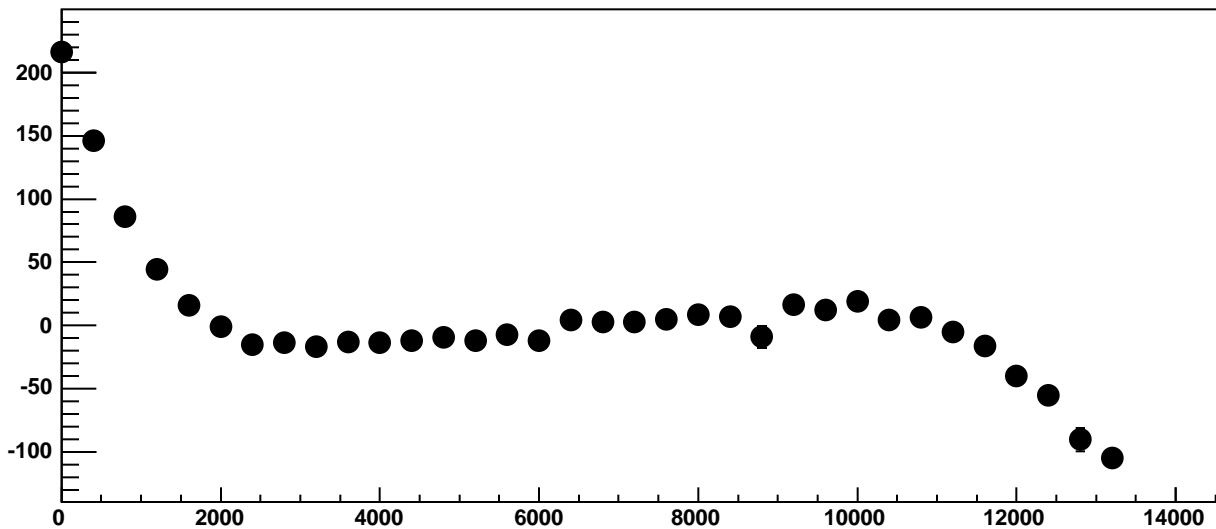
Chip 6, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



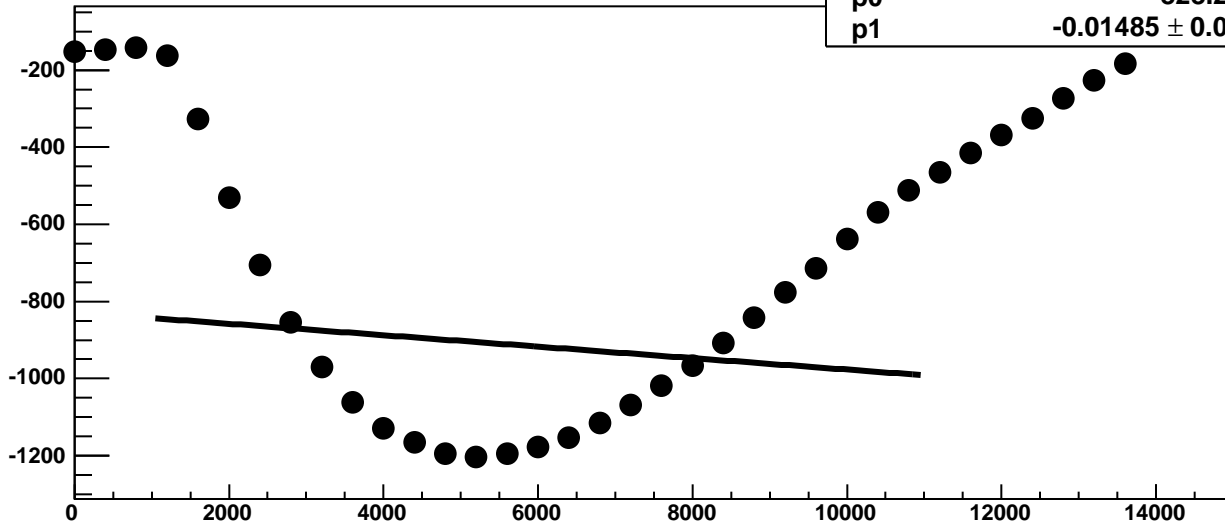
Chip 6, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

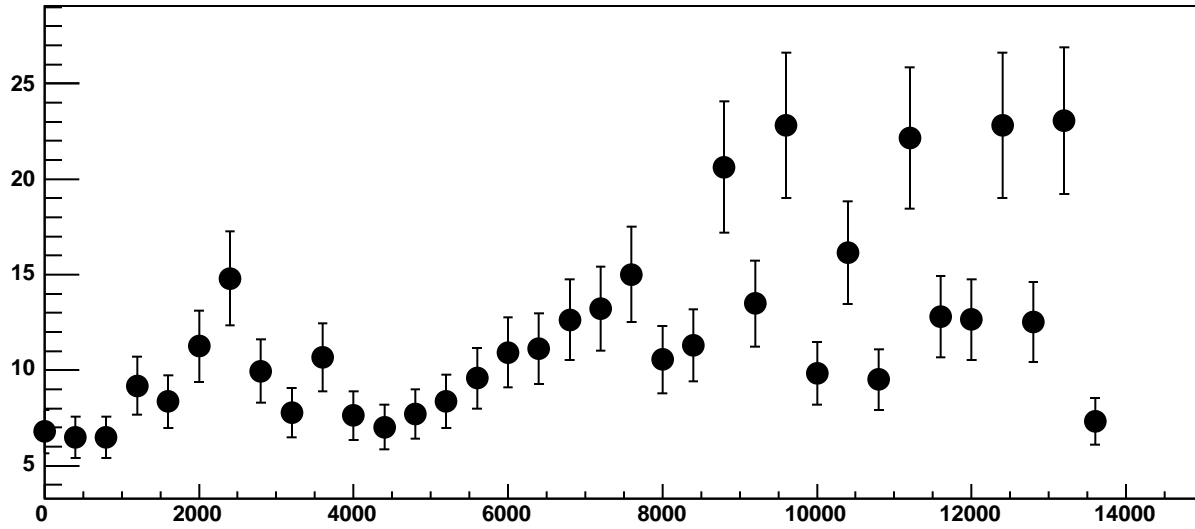


Chip 6, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

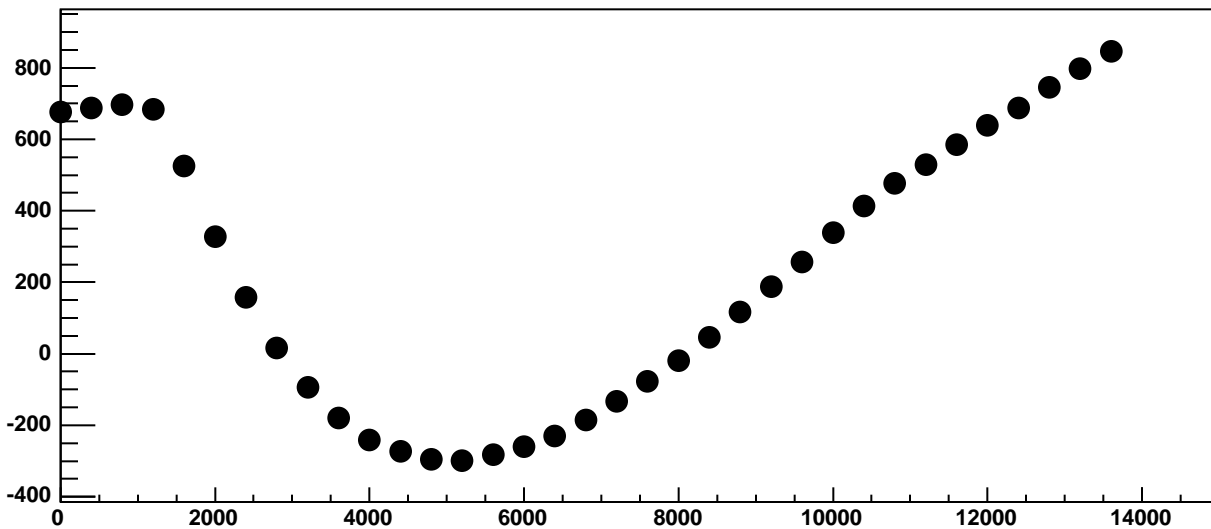


χ^2 / ndf 4.376e+05 / 23
p0 -828.2 ± 1.03
p1 -0.01485 ± 0.0001732

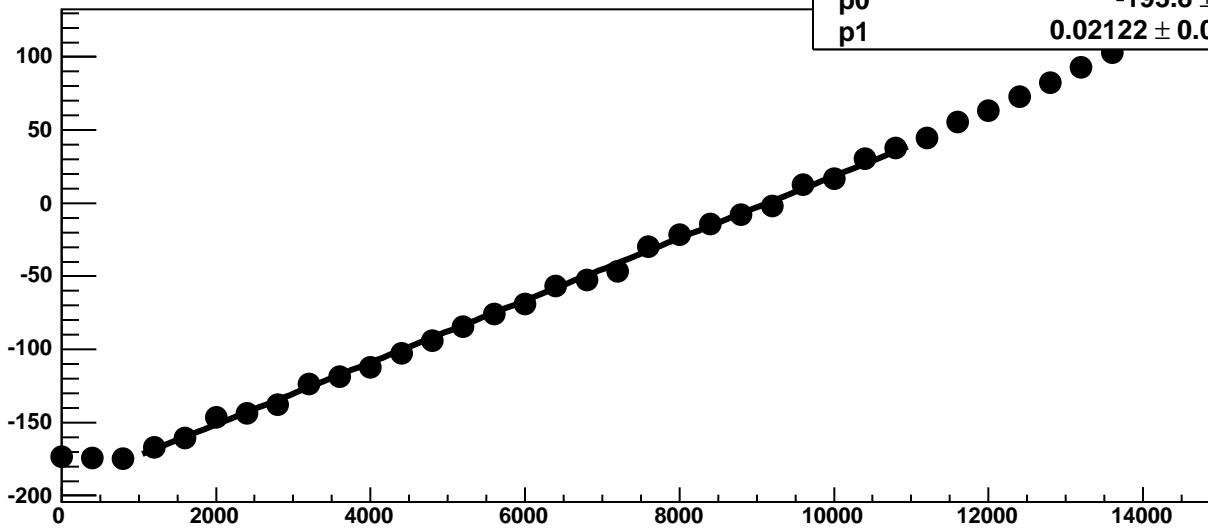
Chip 6, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

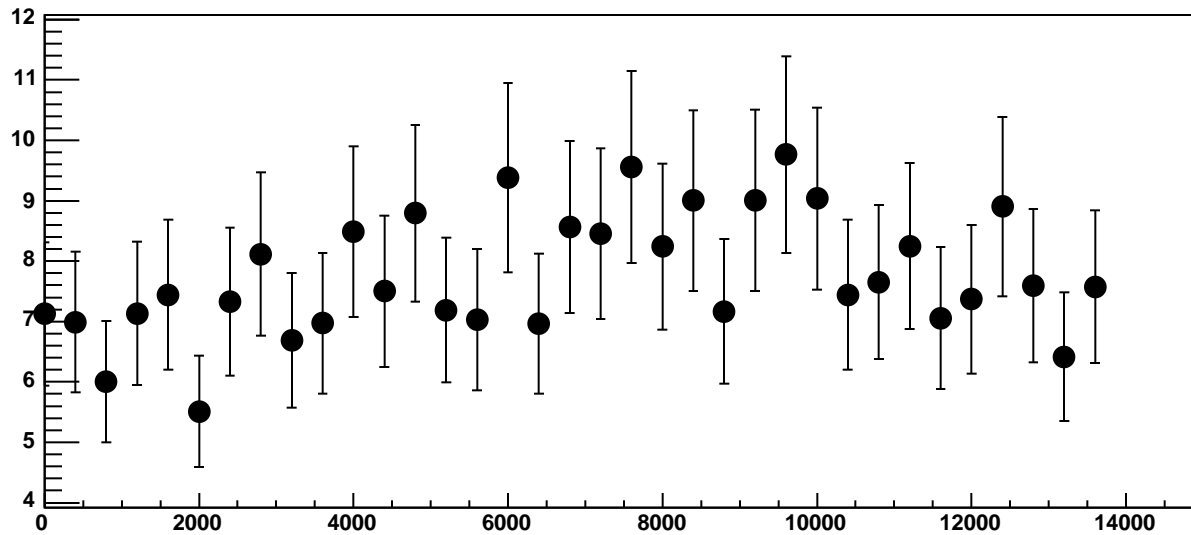


Chip 6, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

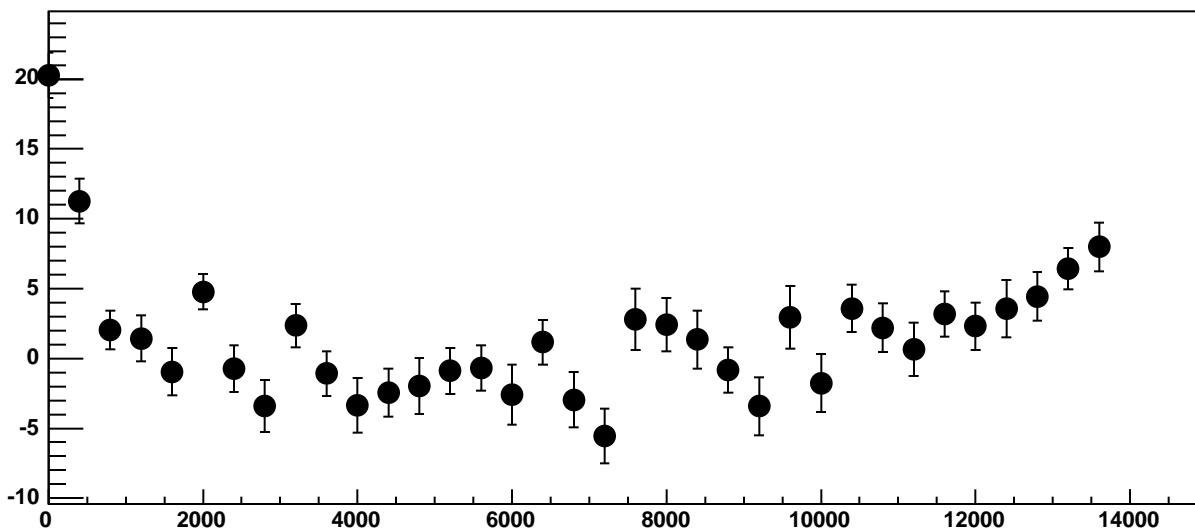


χ^2 / ndf 55.65 / 23
p0 -193.8 ± 0.7605
p1 0.02122 ± 0.0001205

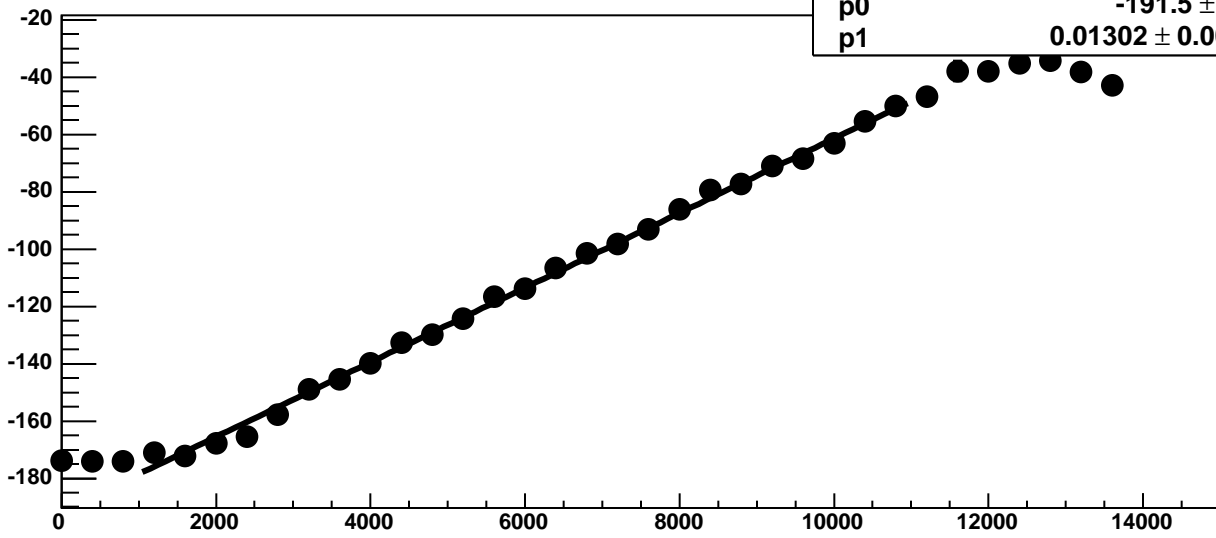
Chip 6, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



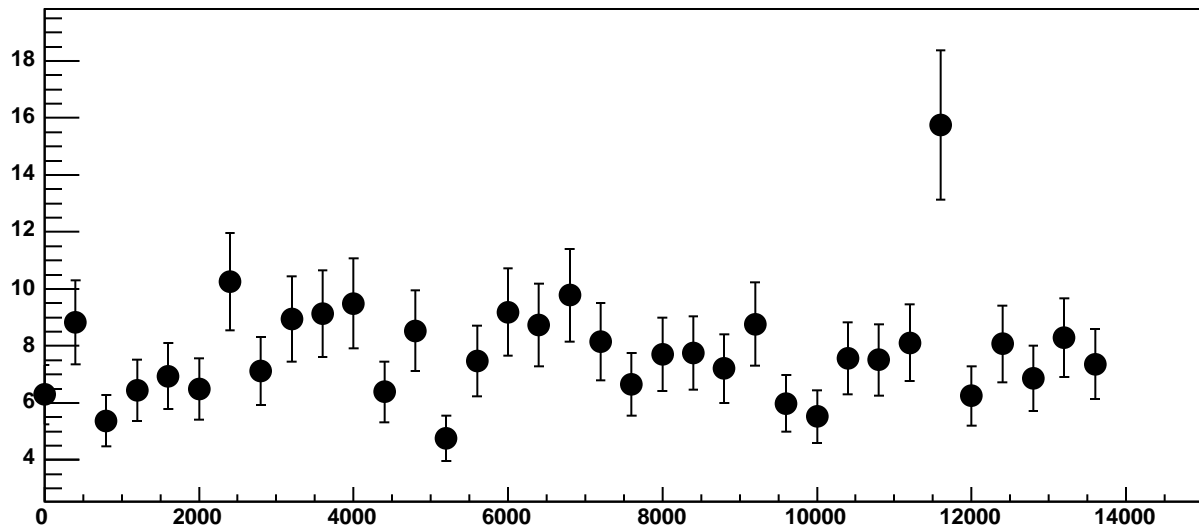
Chip 6, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC



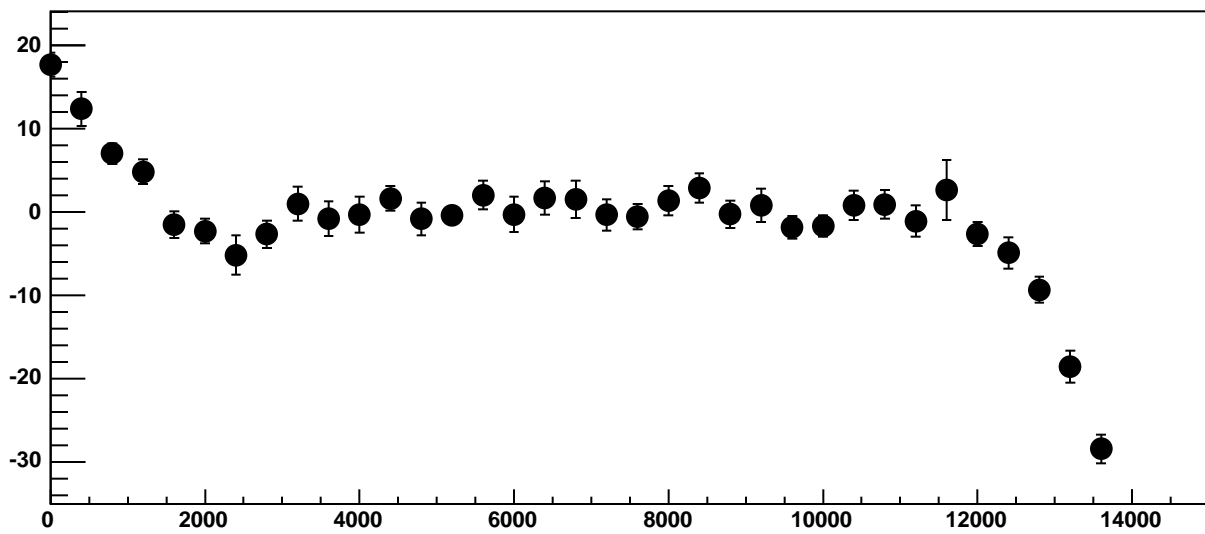
Chip 6, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



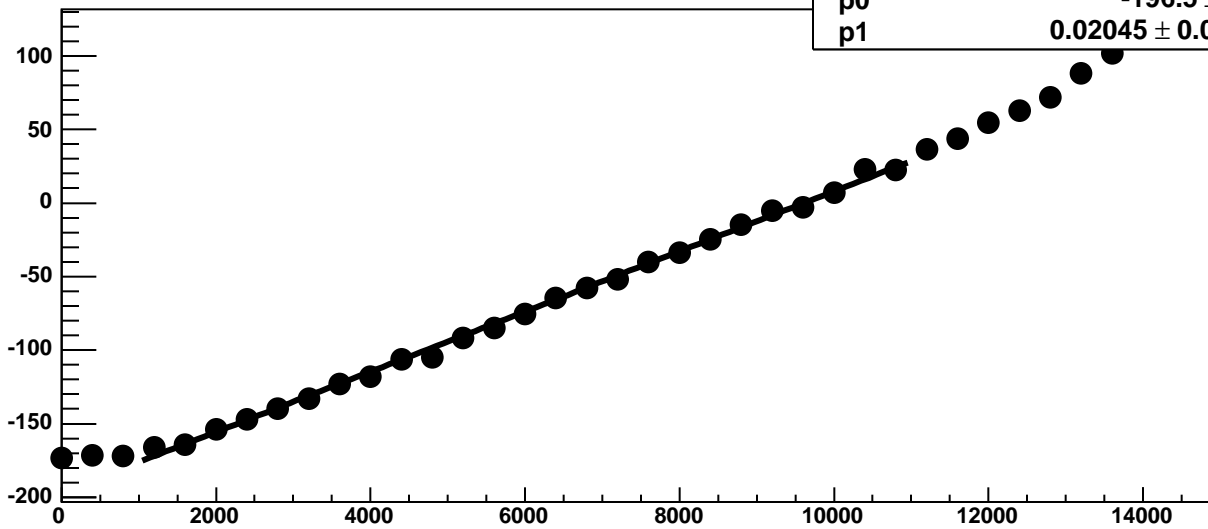
Chip 6, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

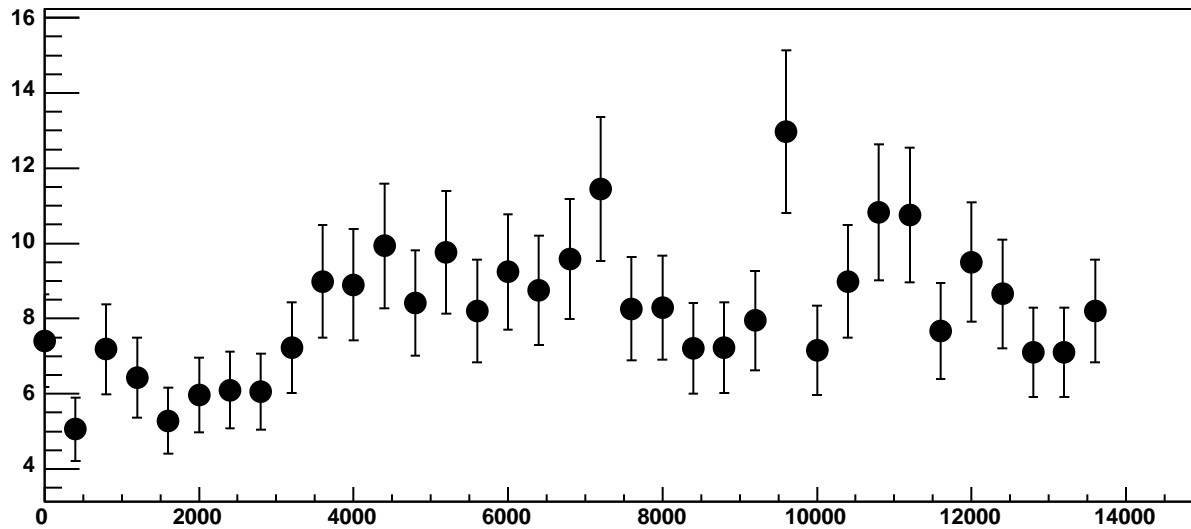


Chip 6, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

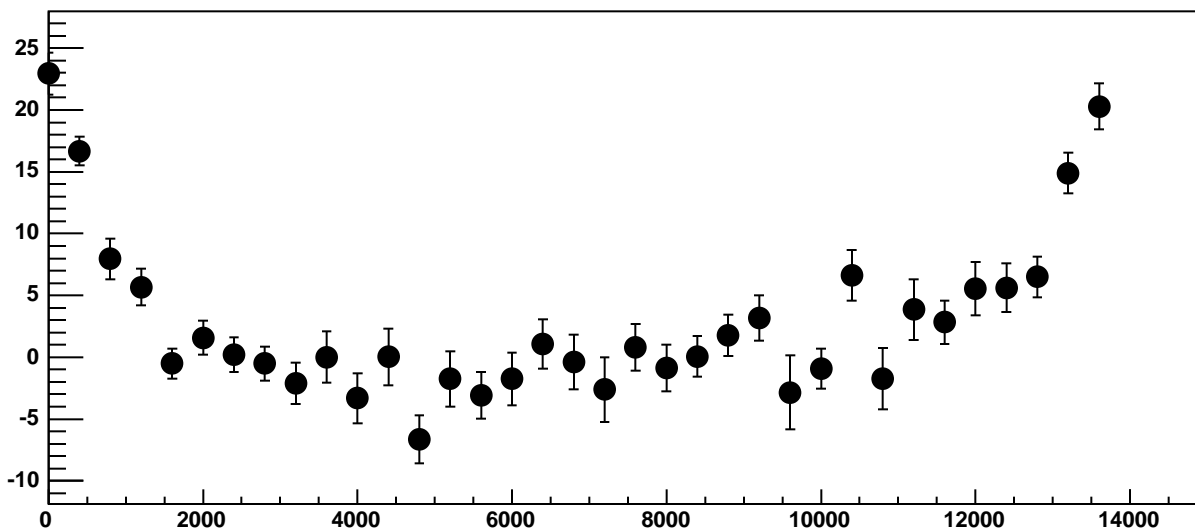


χ^2 / ndf 54.55 / 23
p0 -196.5 ± 0.7151
p1 0.02045 ± 0.0001178

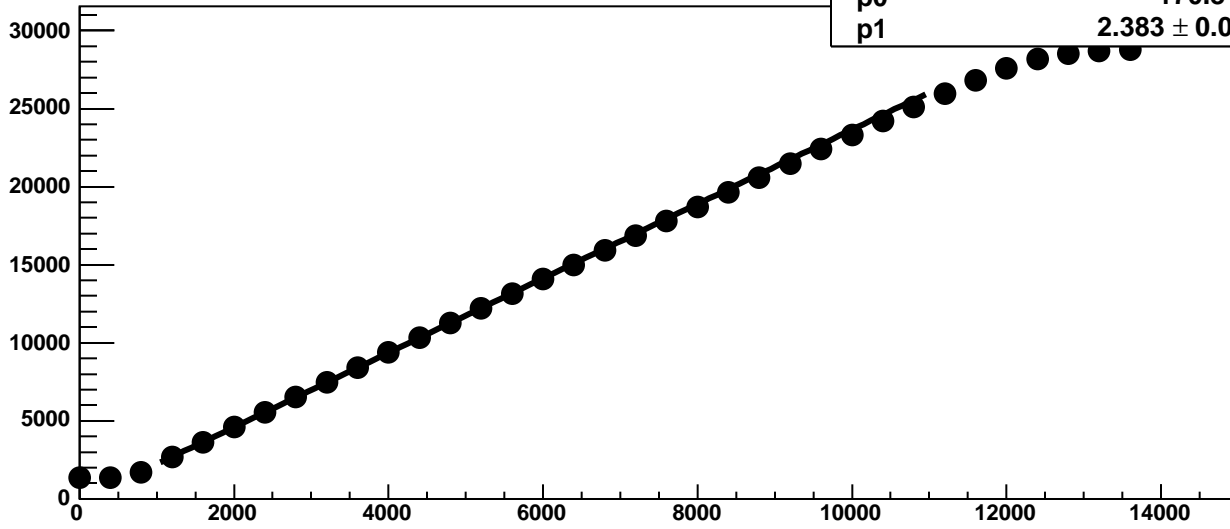
Chip 6, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



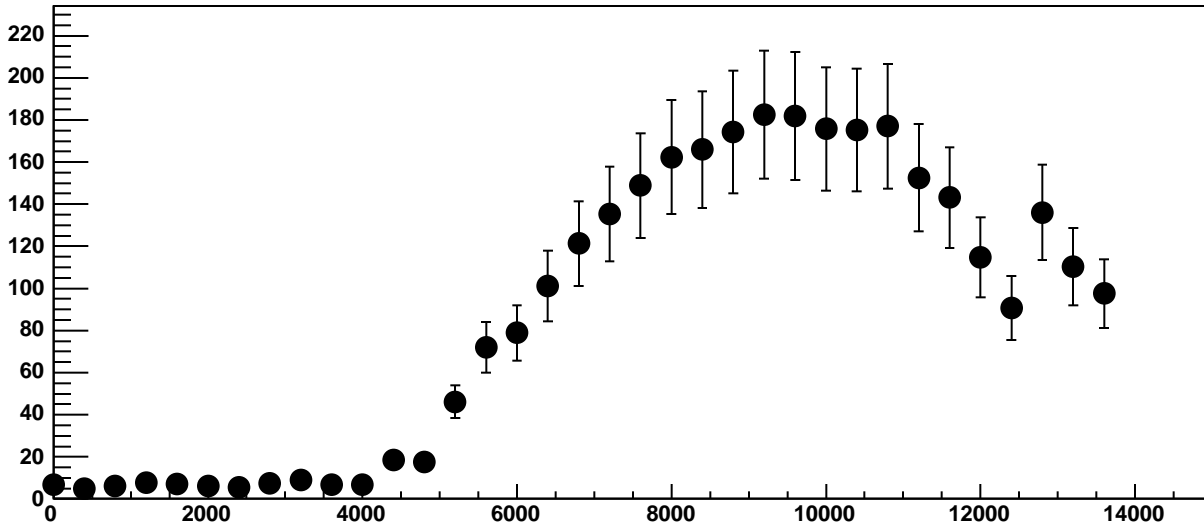
Chip 6, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



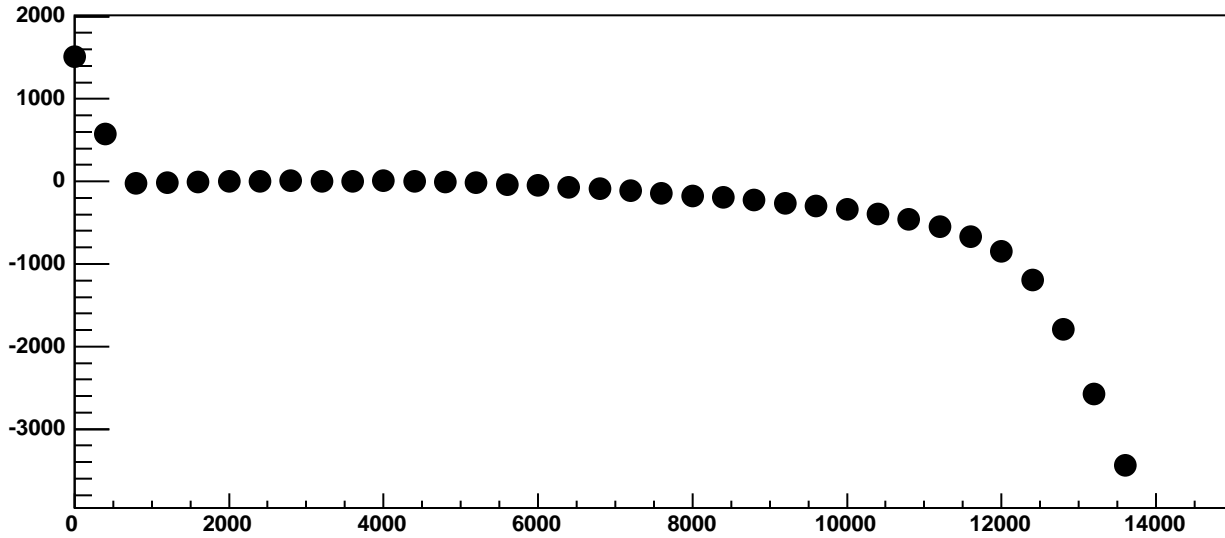
Chip 6, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC



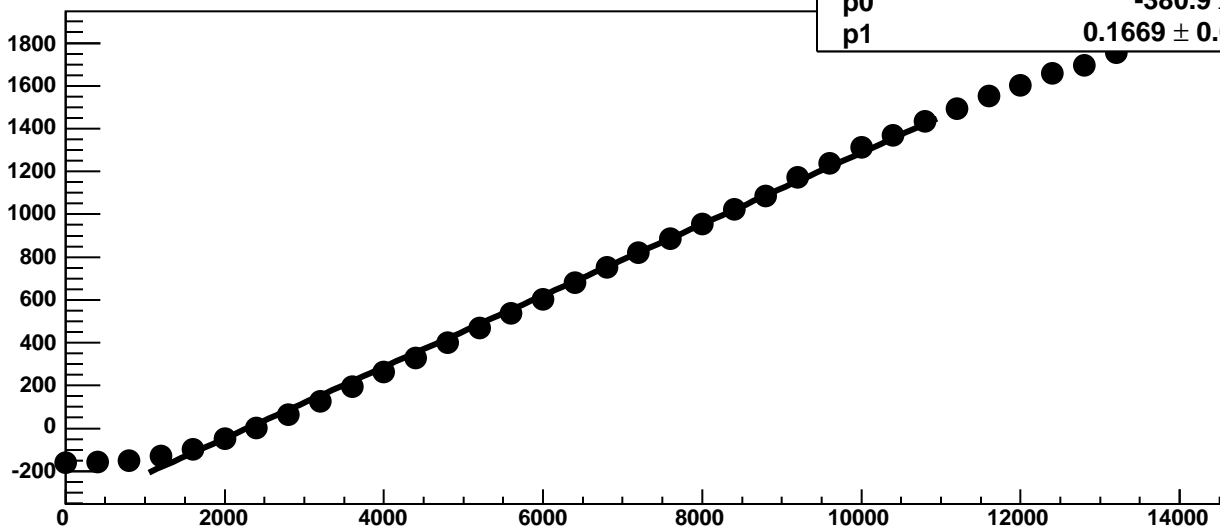
Chip 6, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



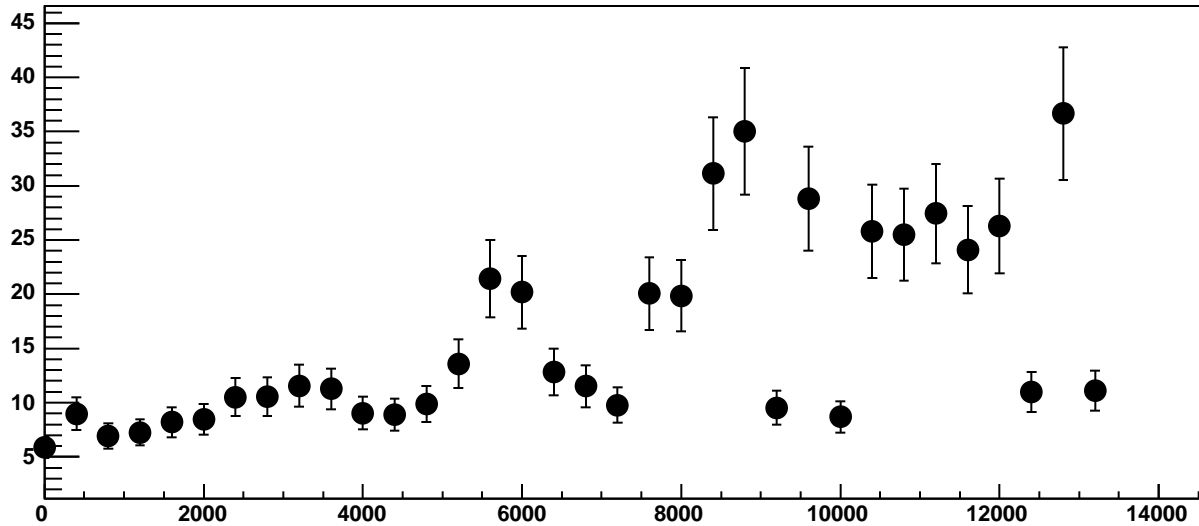
Chip 6, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC



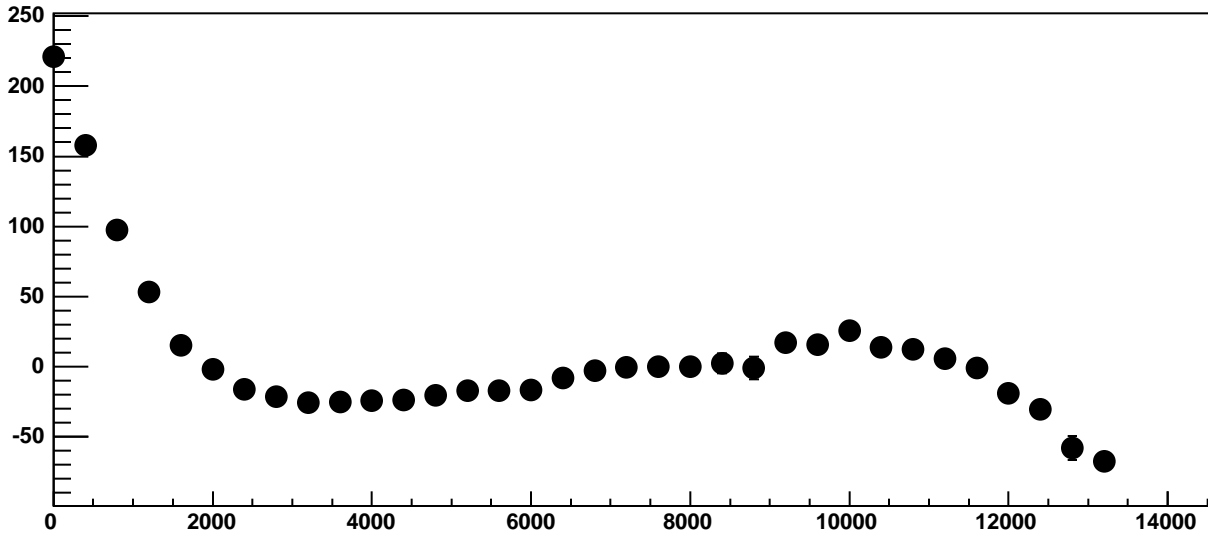
Chip 6, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



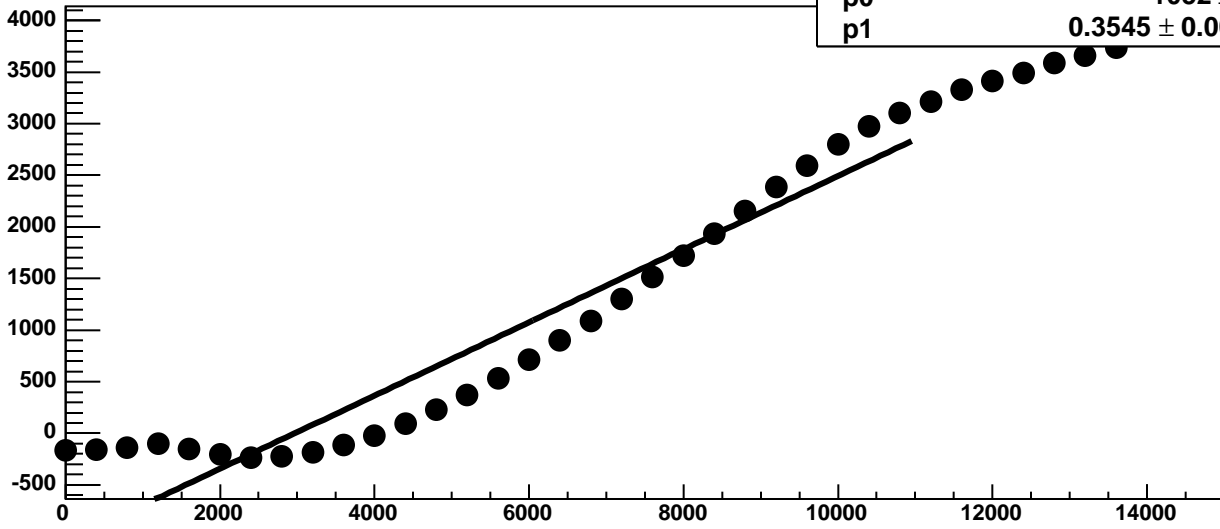
Chip 6, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



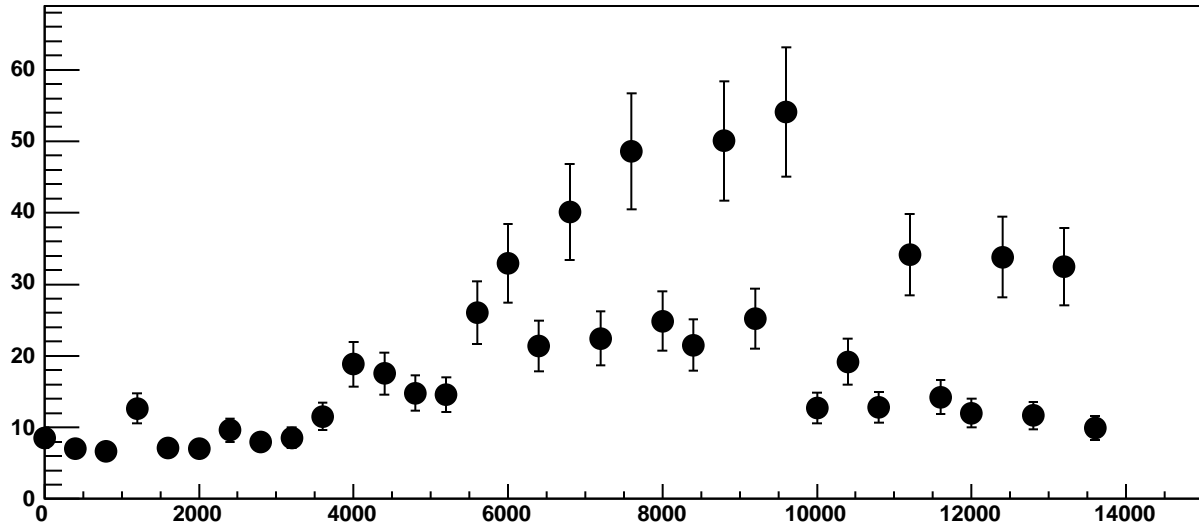
Chip 6, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



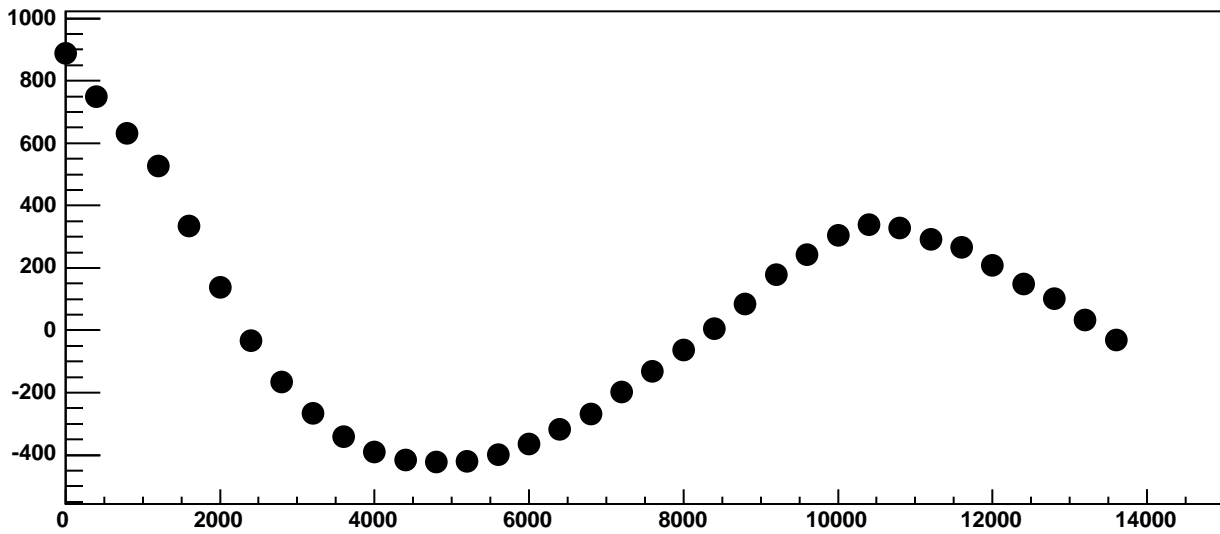
Chip 6, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



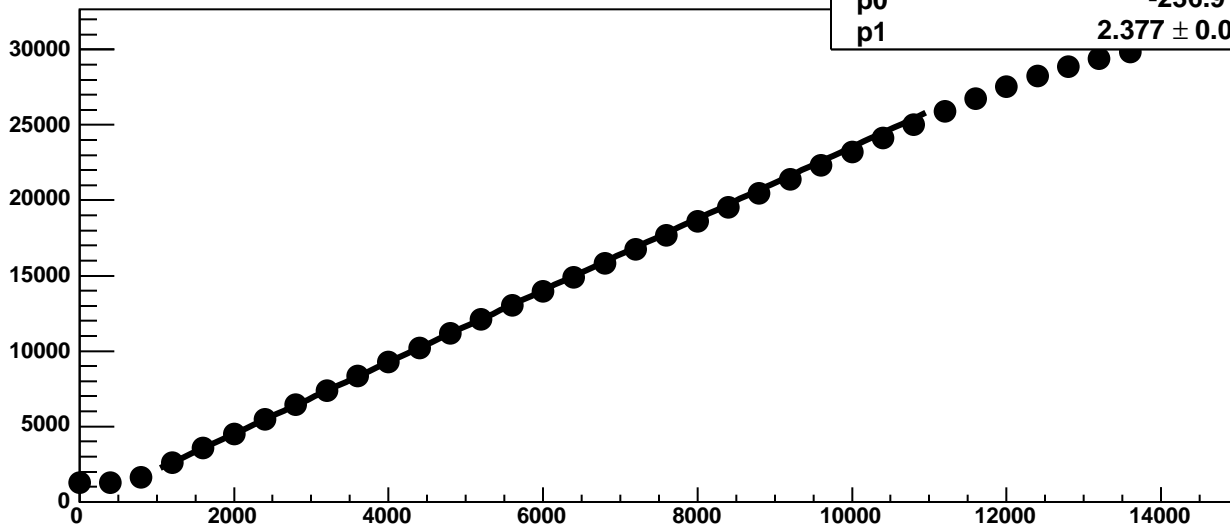
Chip 6, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

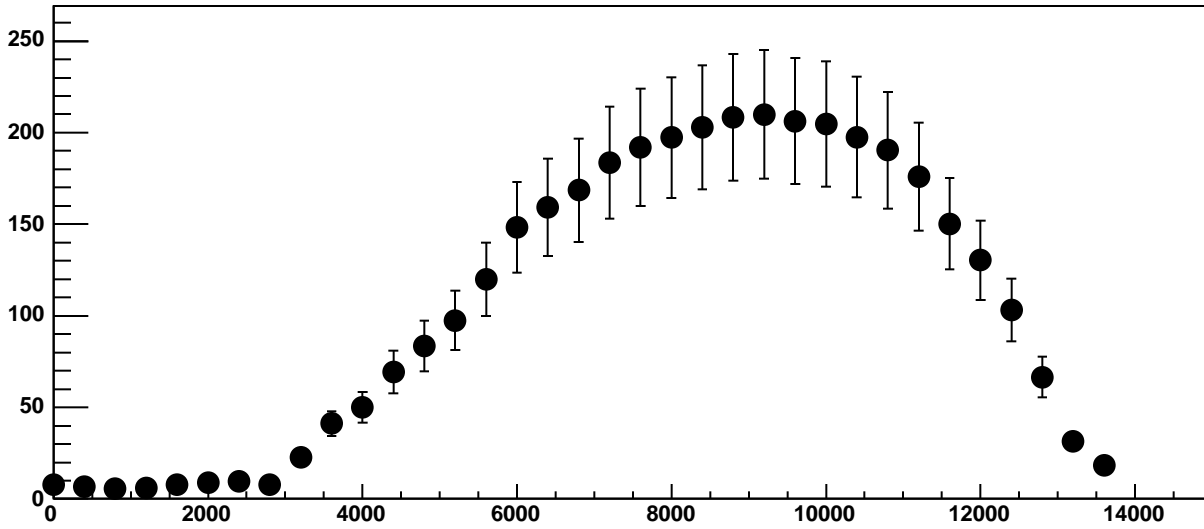


Chip 6, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC

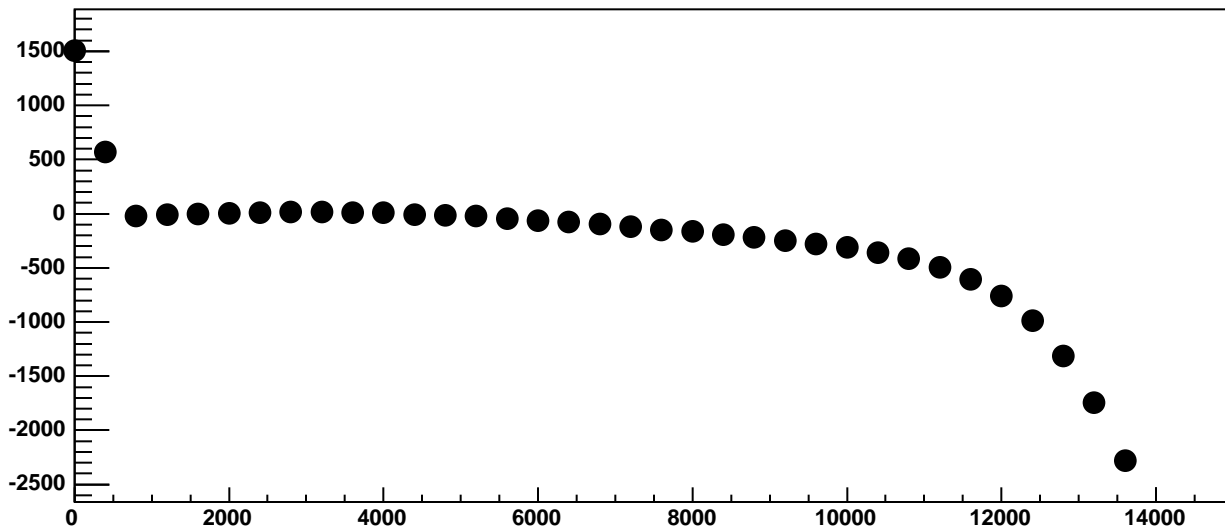


χ^2 / ndf 505.9 / 23
p0 -236.9 ± 2.016
p1 2.377 ± 0.0009548

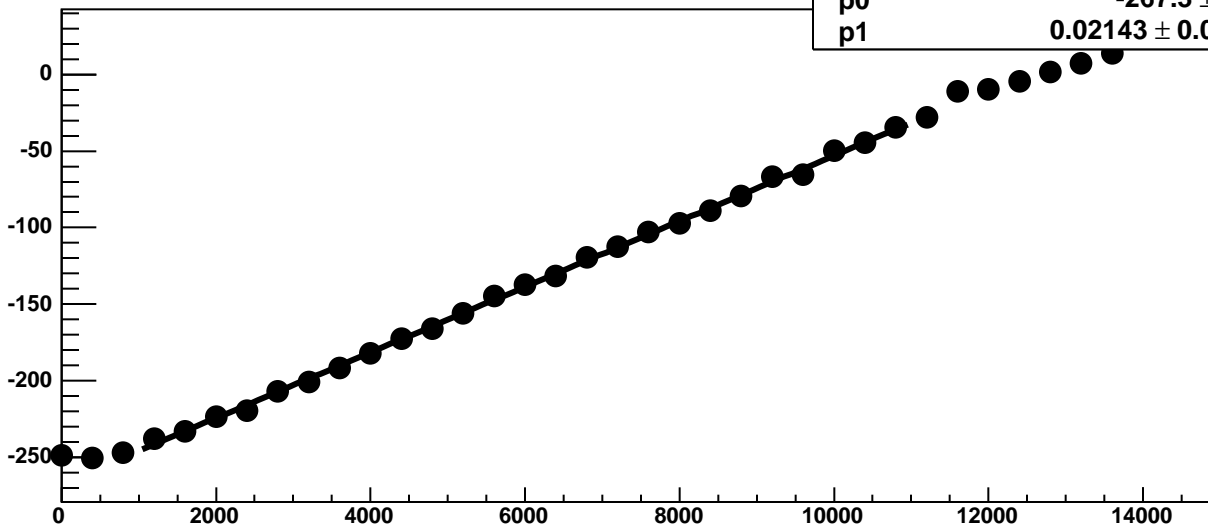
Chip 6, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



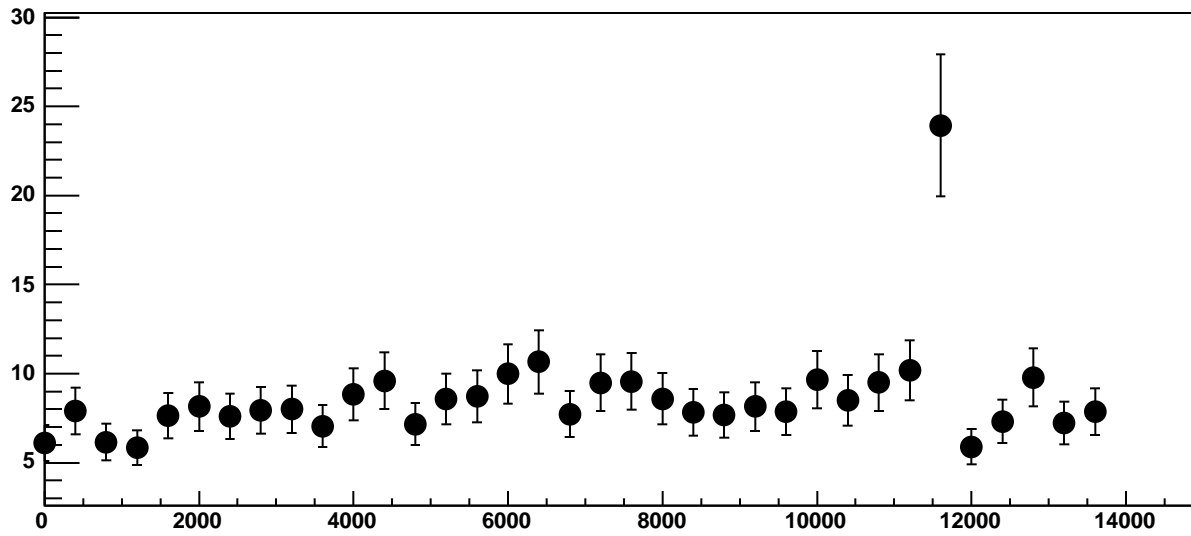
Chip 6, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC



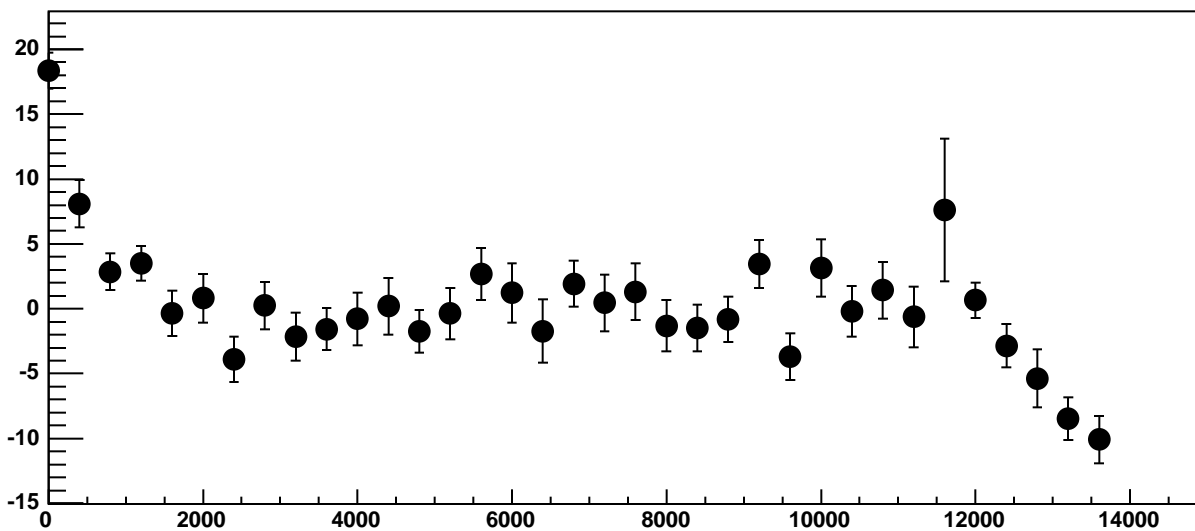
Chip 6, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



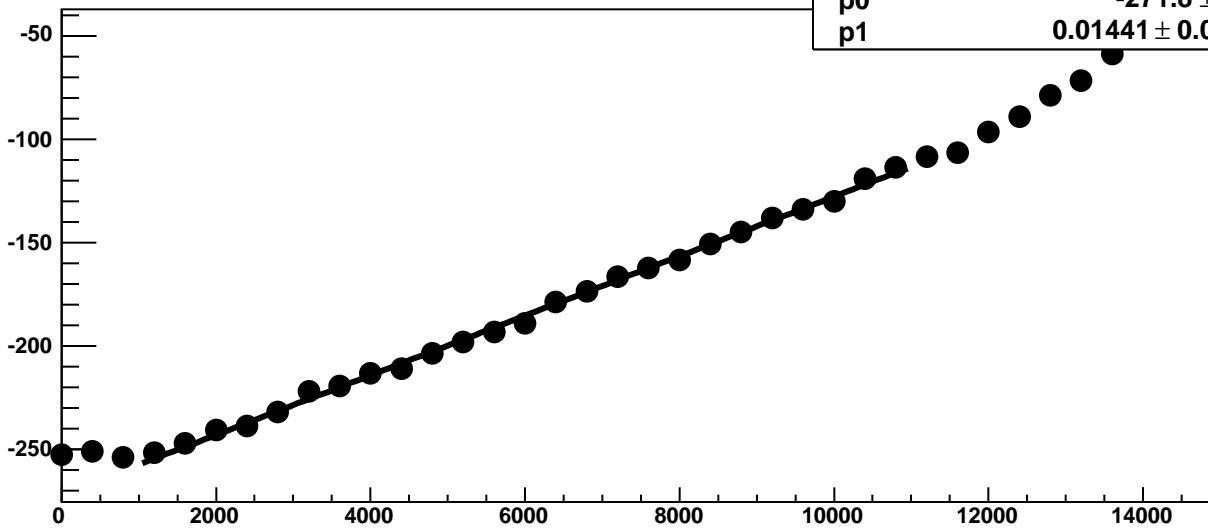
Chip 6, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



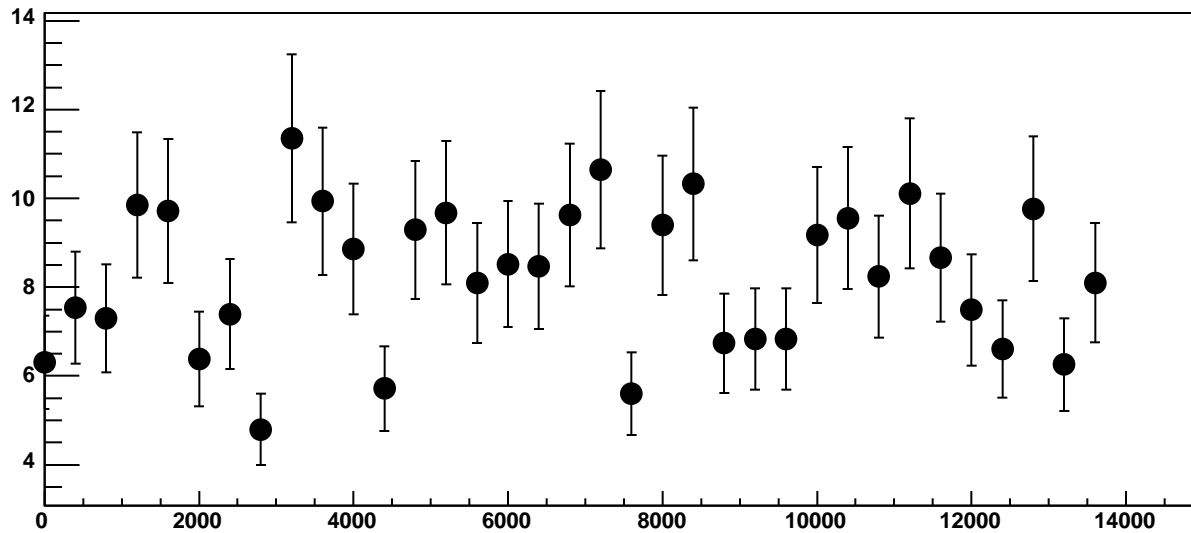
Chip 6, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



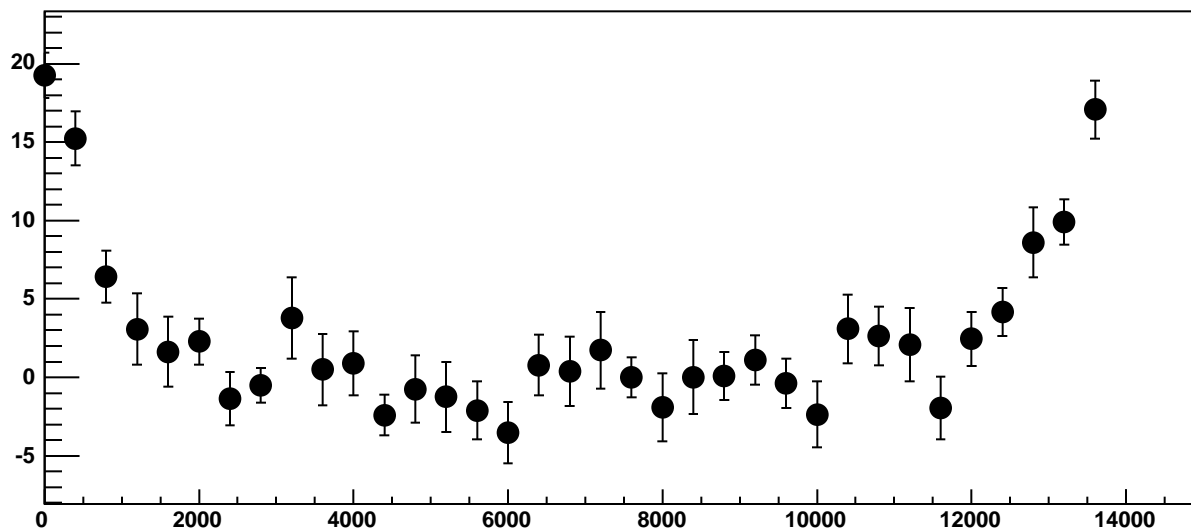
Chip 6, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



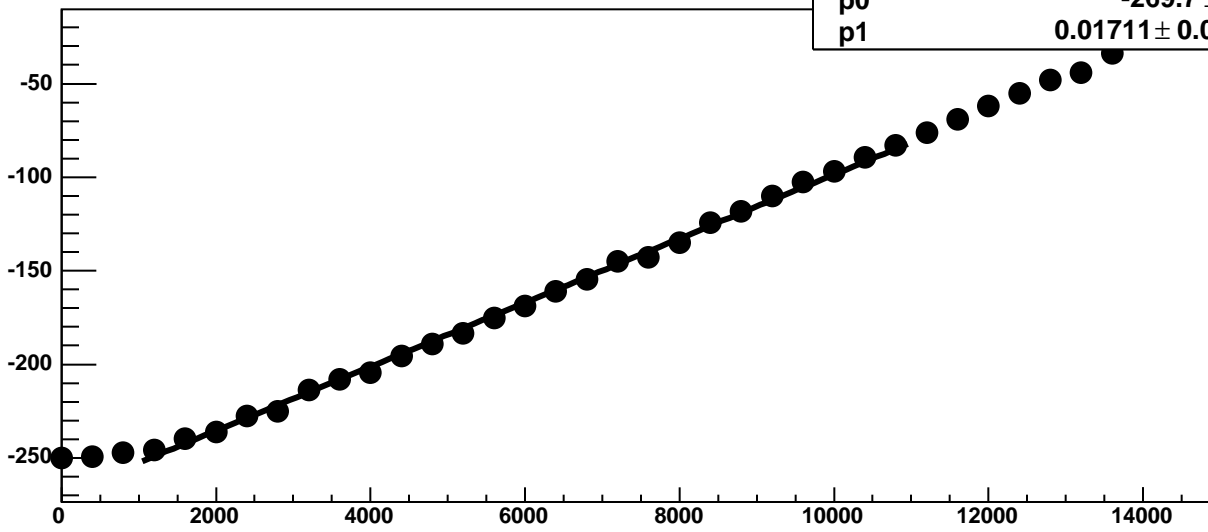
Chip 6, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

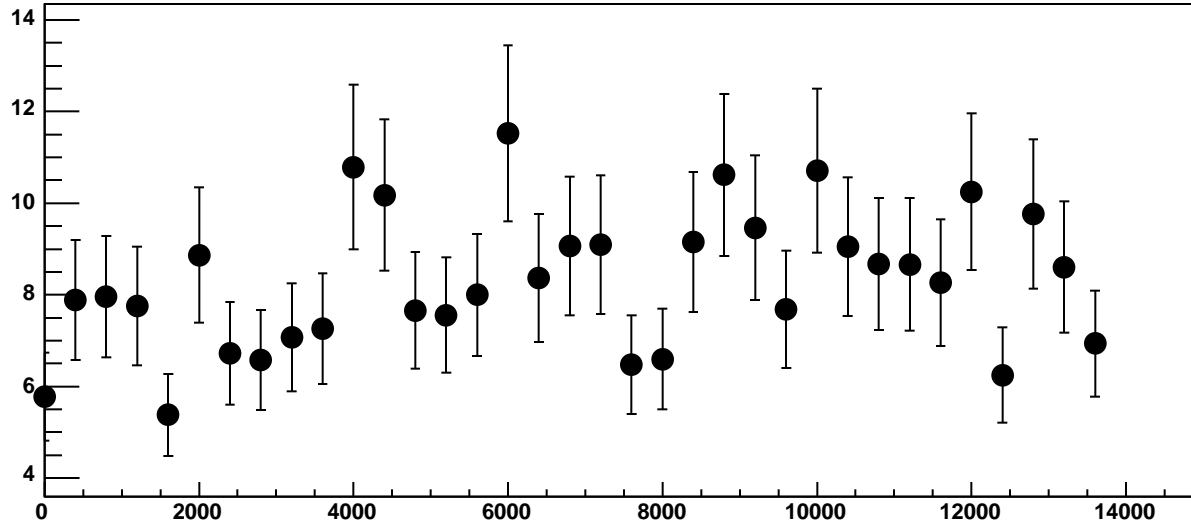


Chip 6, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

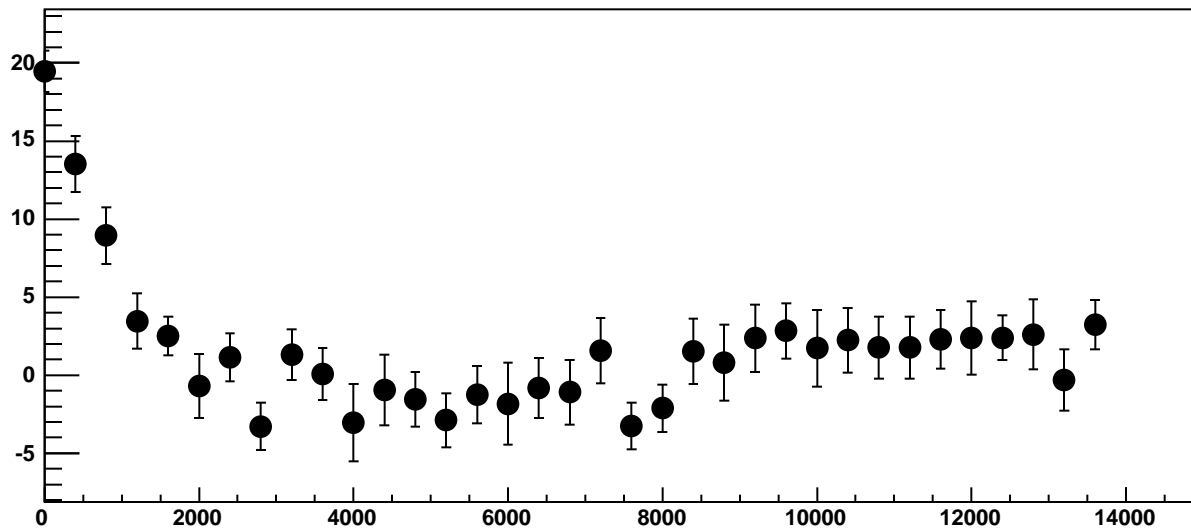


χ^2 / ndf 34.78 / 23
p0 -269.7 ± 0.7781
p1 0.01711 ± 0.0001248

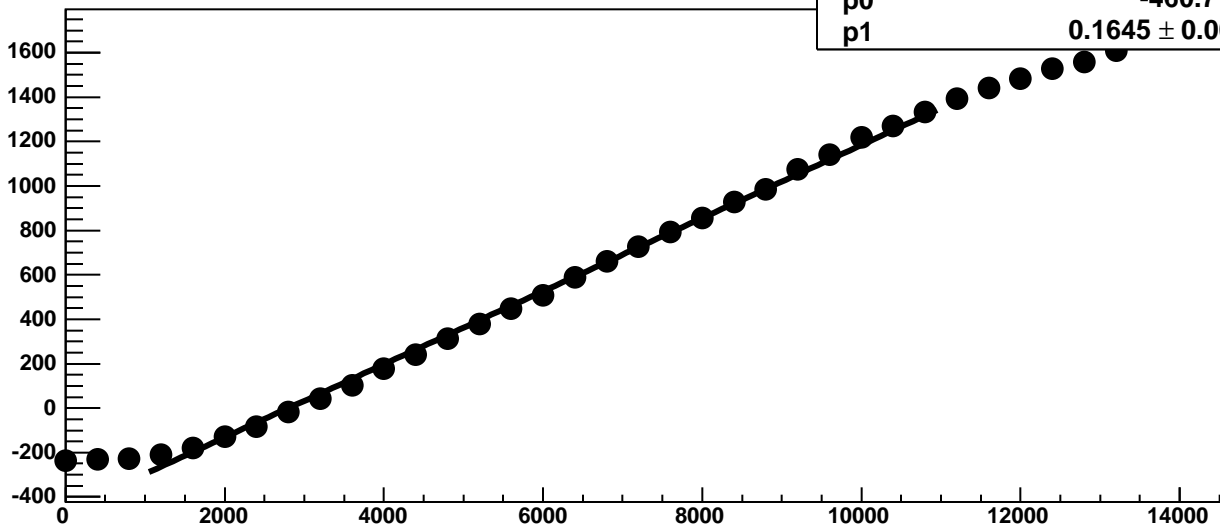
Chip 6, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



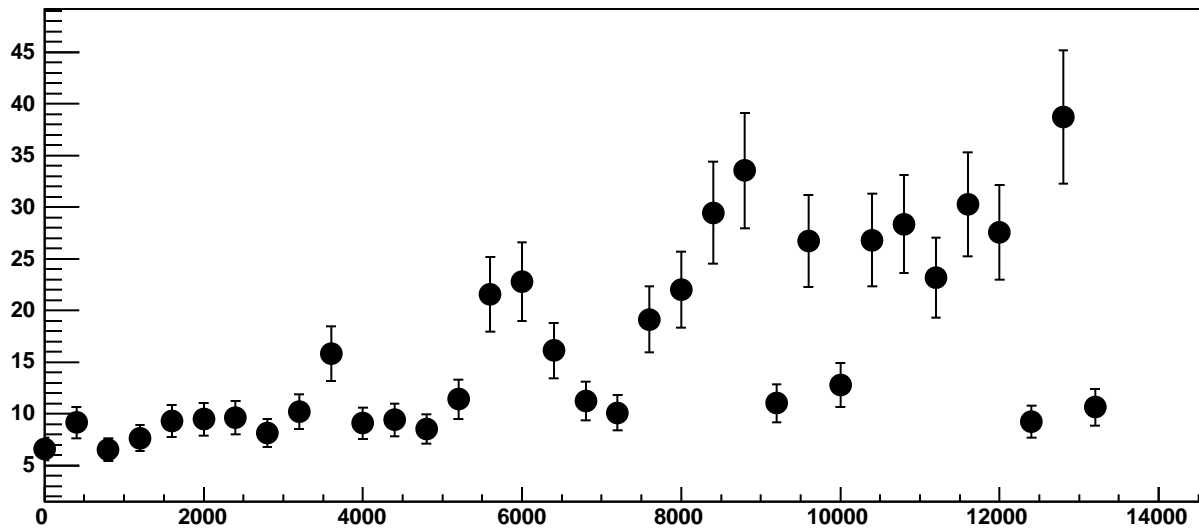
Chip 6, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



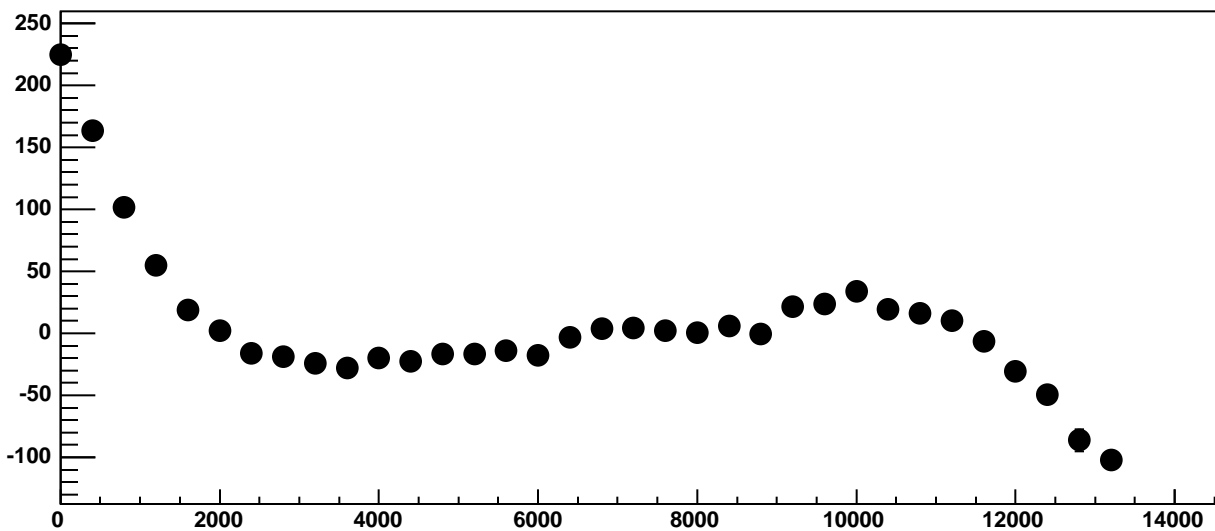
Chip 6, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



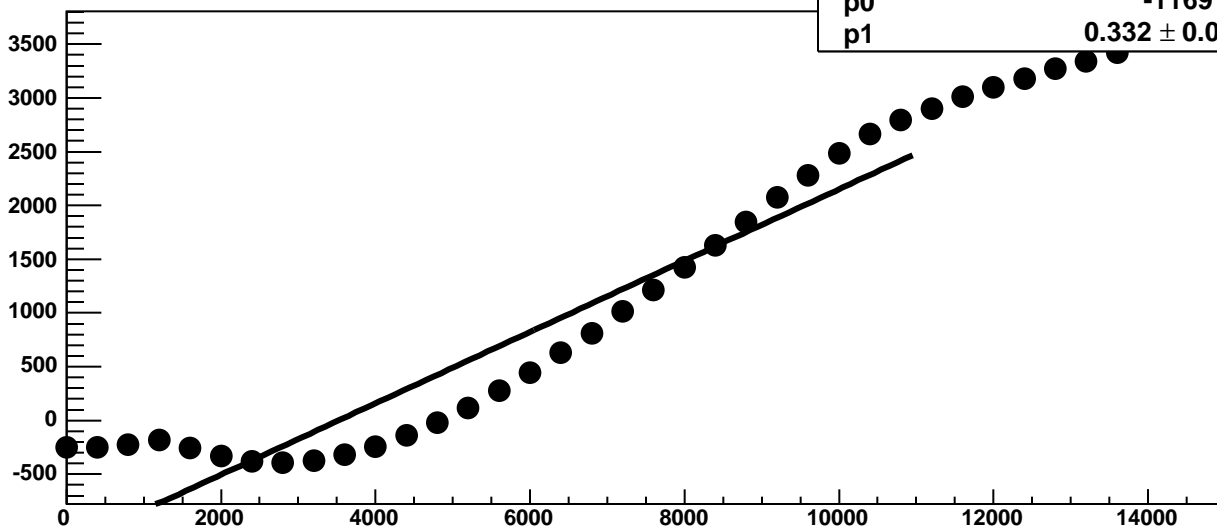
Chip 6, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



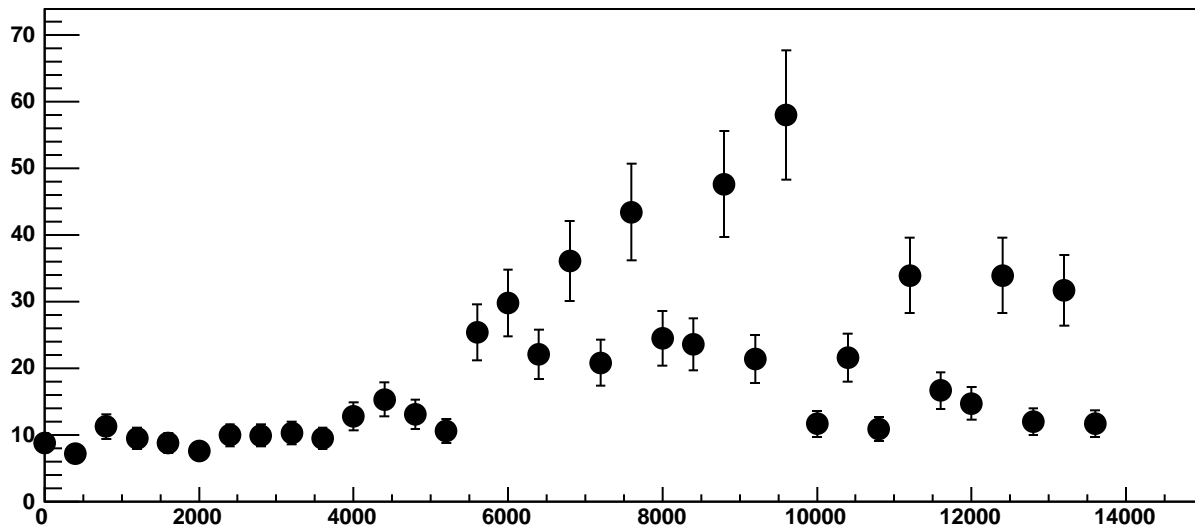
Chip 6, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



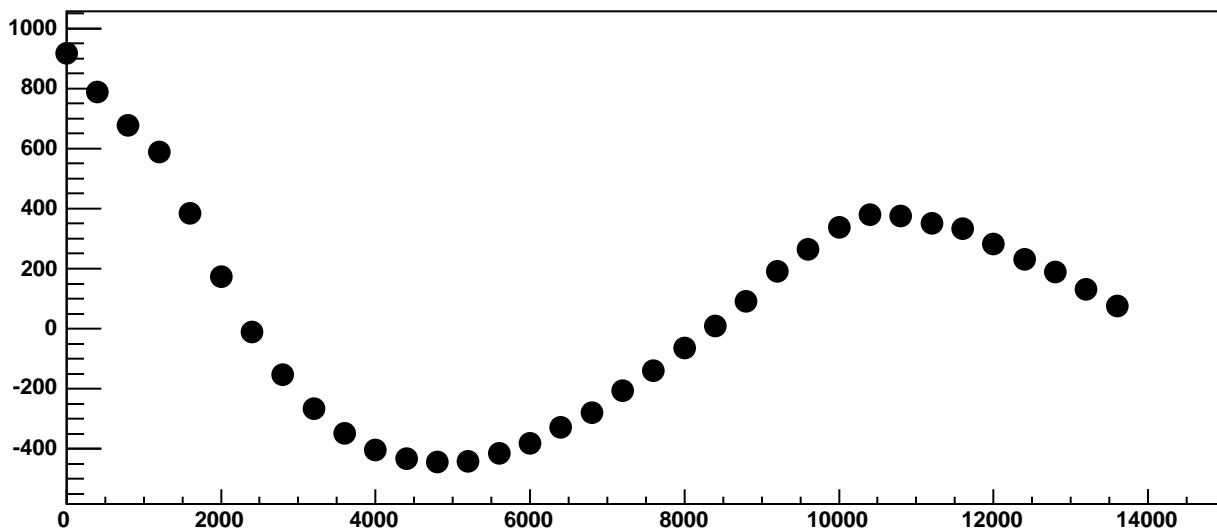
Chip 6, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



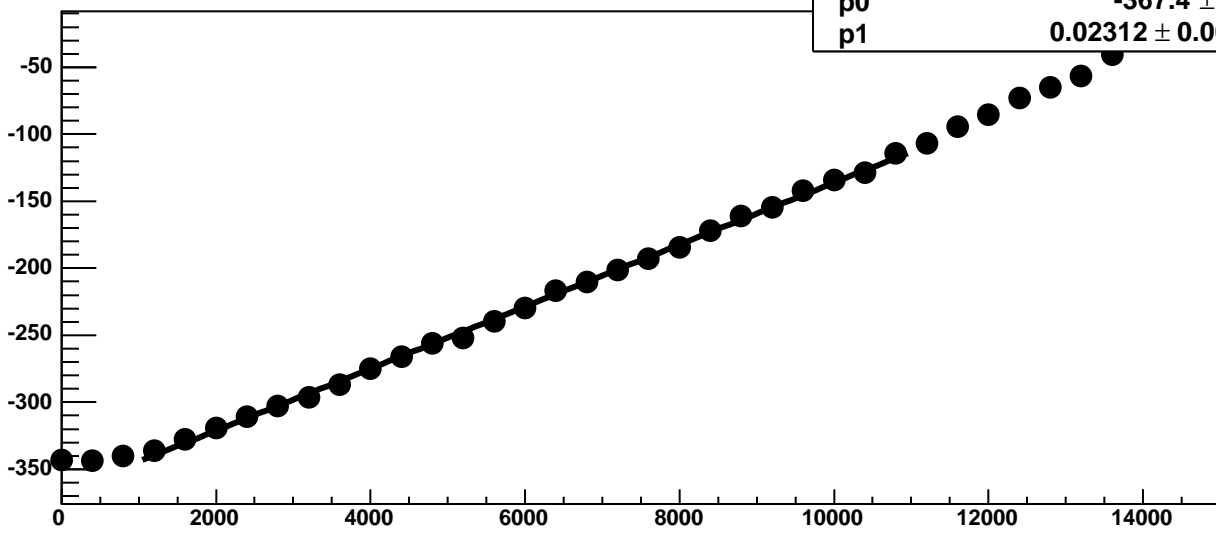
Chip 6, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

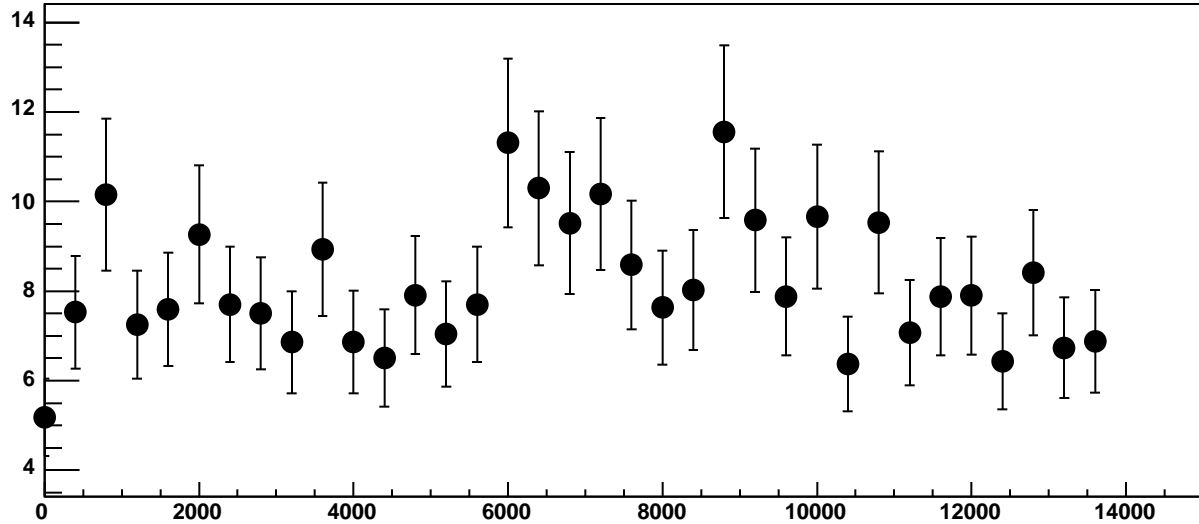


Chip 6, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

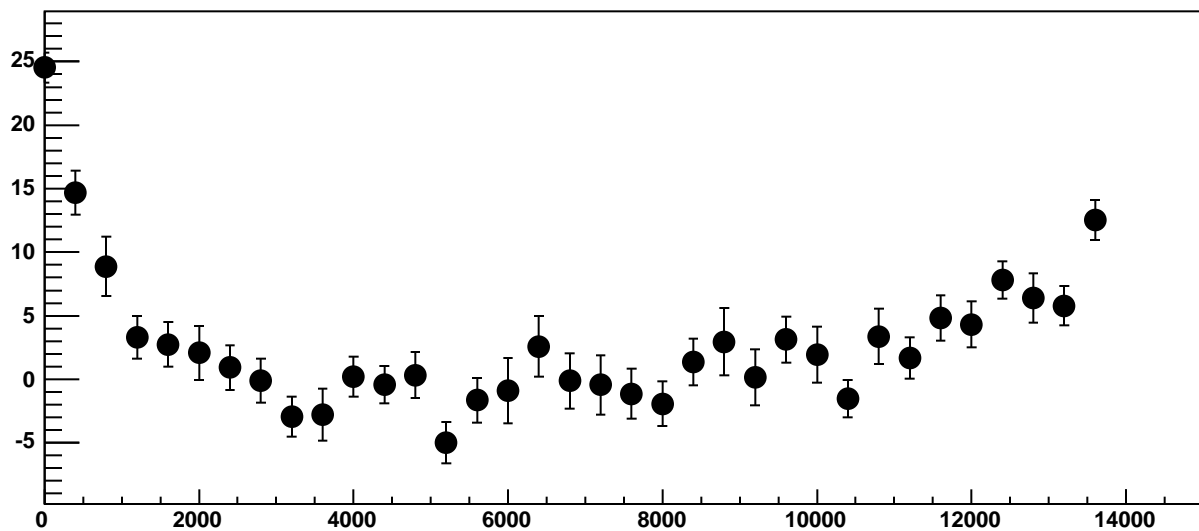


χ^2 / ndf 35.37 / 23
p0 -367.4 ± 0.8172
p1 0.02312 ± 0.0001273

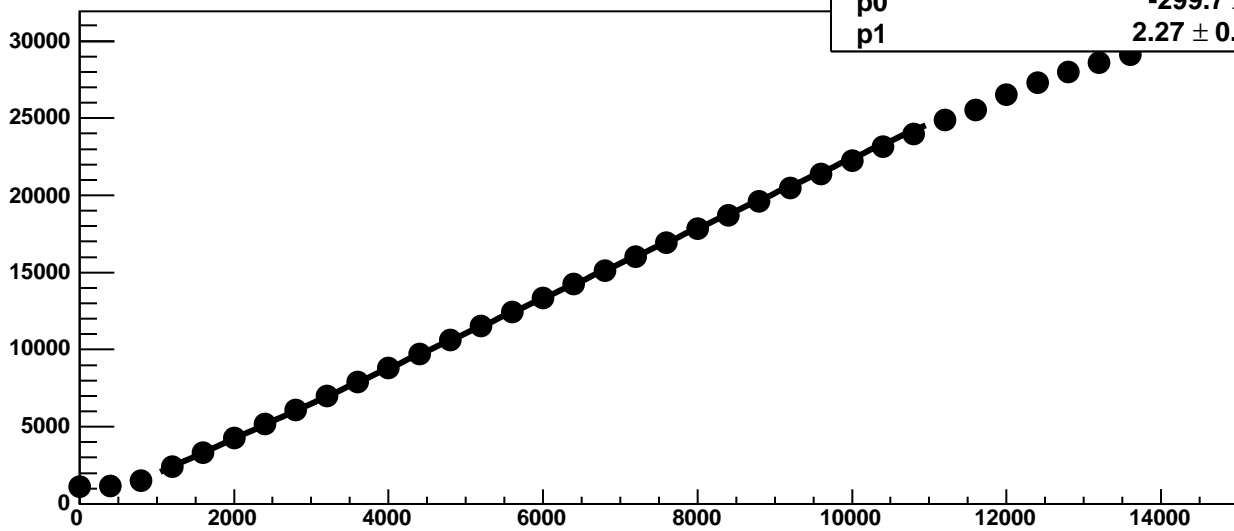
Chip 6, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



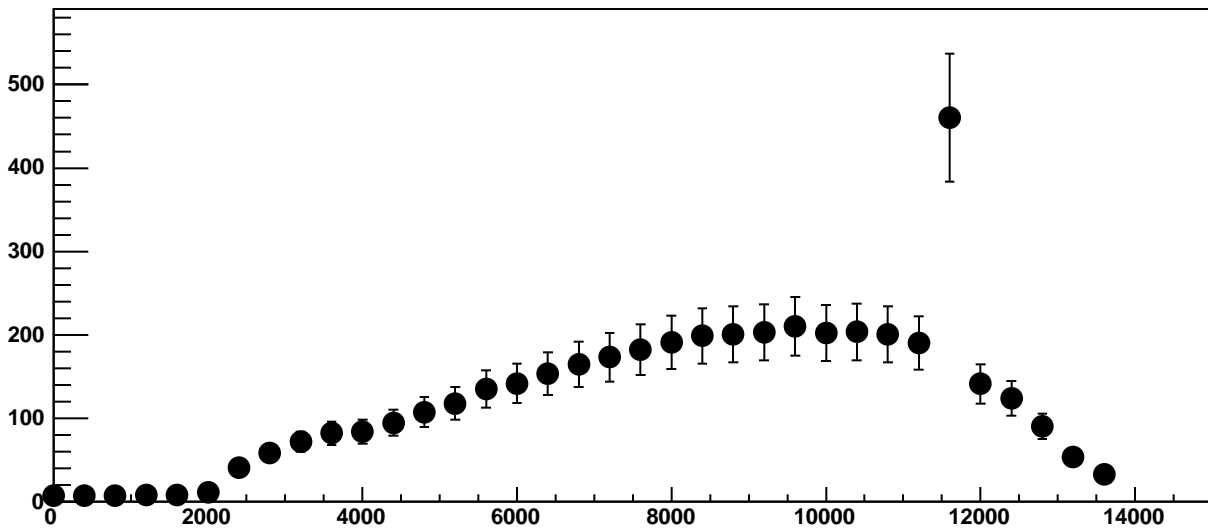
Chip 6, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



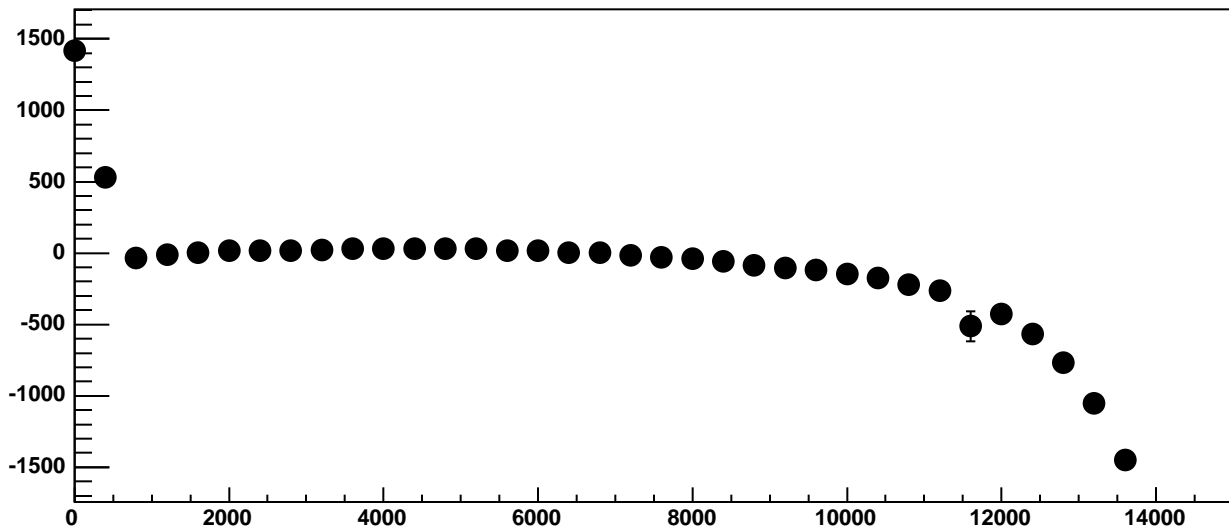
Chip 6, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



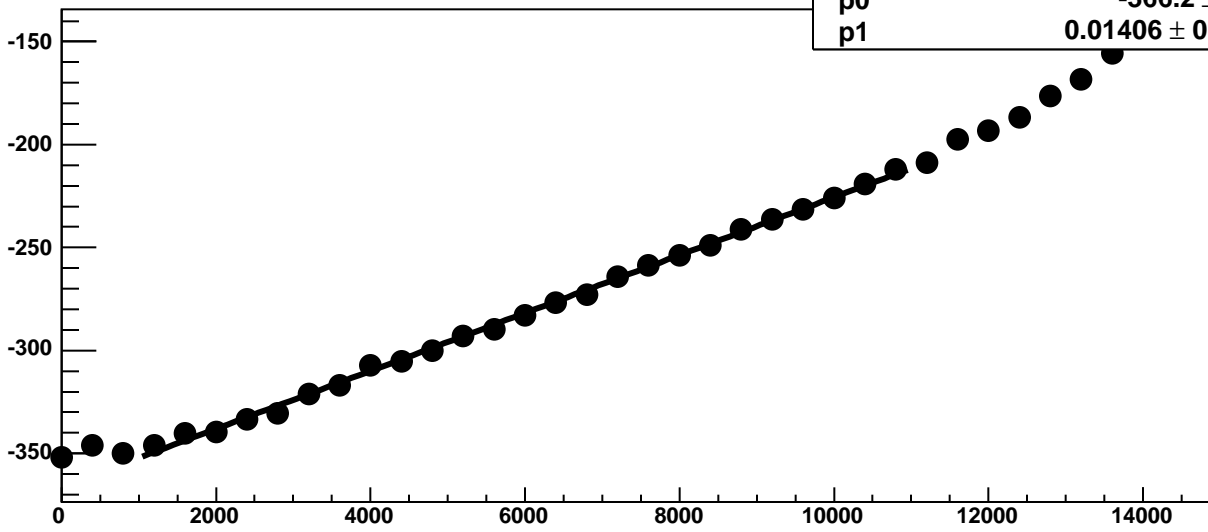
Chip 6, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



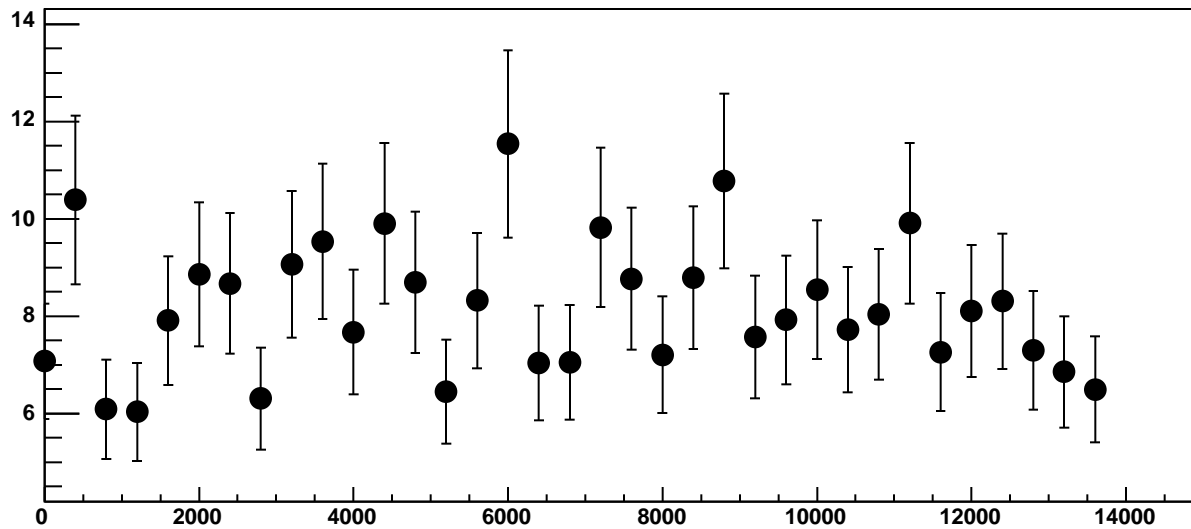
Chip 6, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC



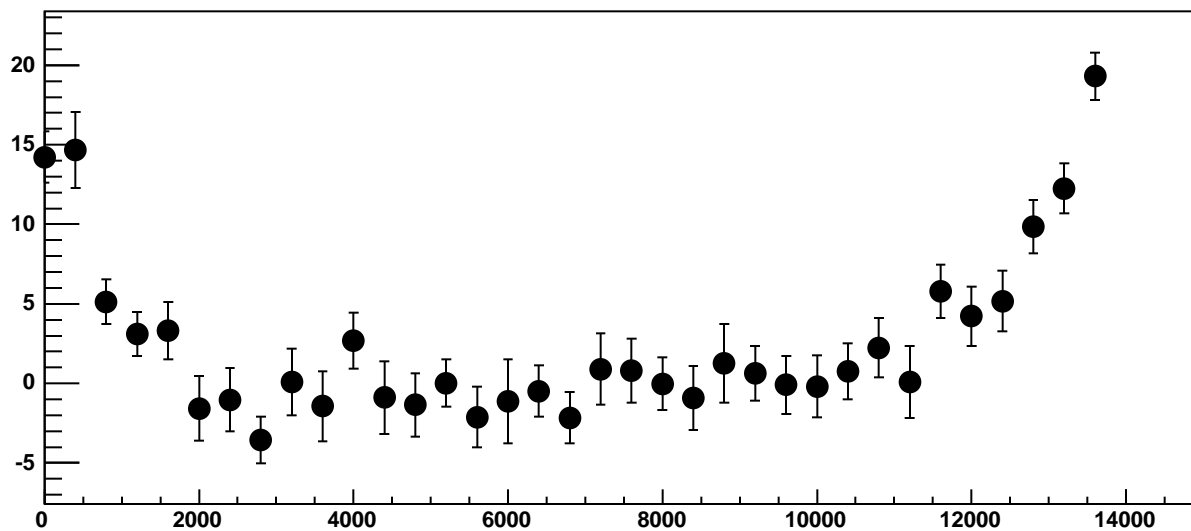
Chip 6, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



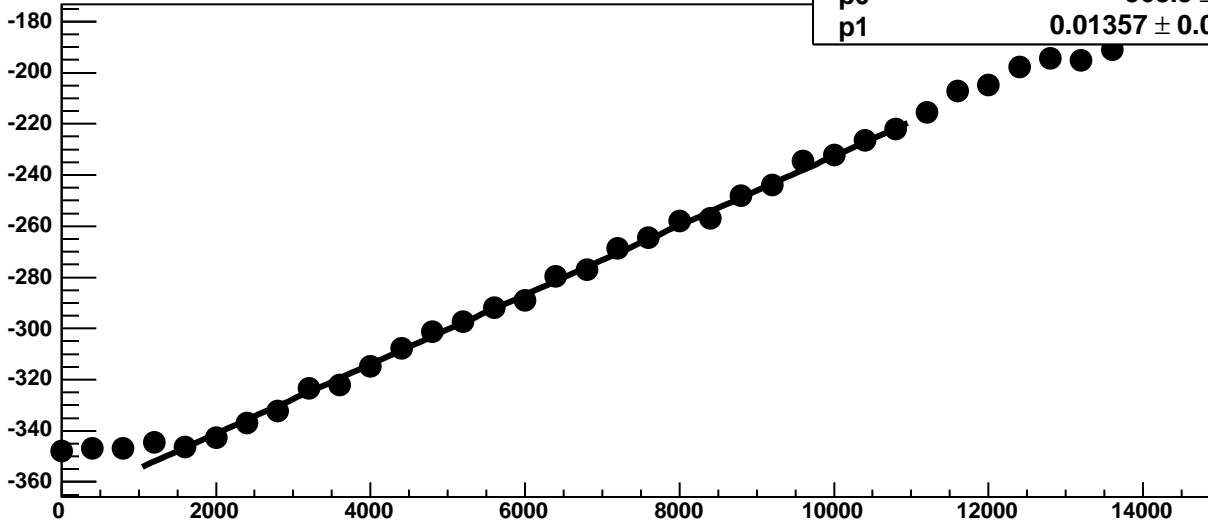
Chip 6, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

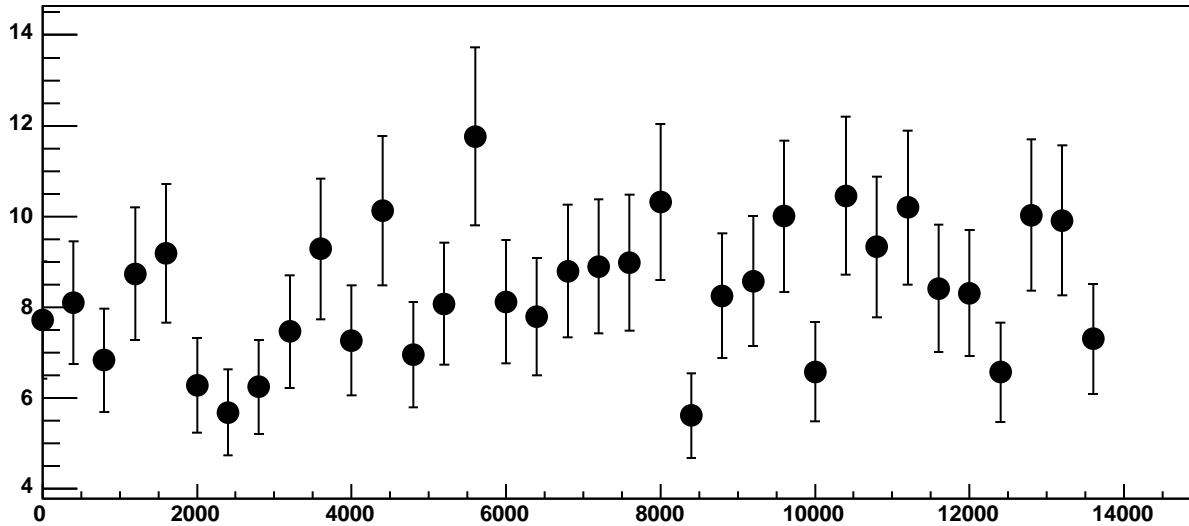


Chip 6, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

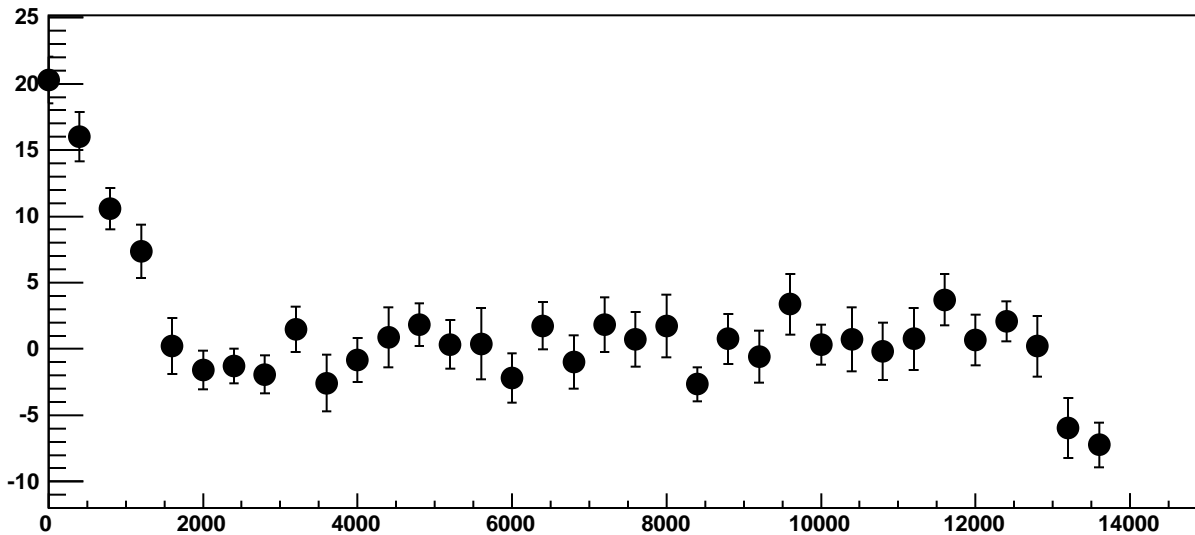


χ^2 / ndf 32.36 / 23
p0 -368.3 ± 0.7954
p1 0.01357 ± 0.0001246

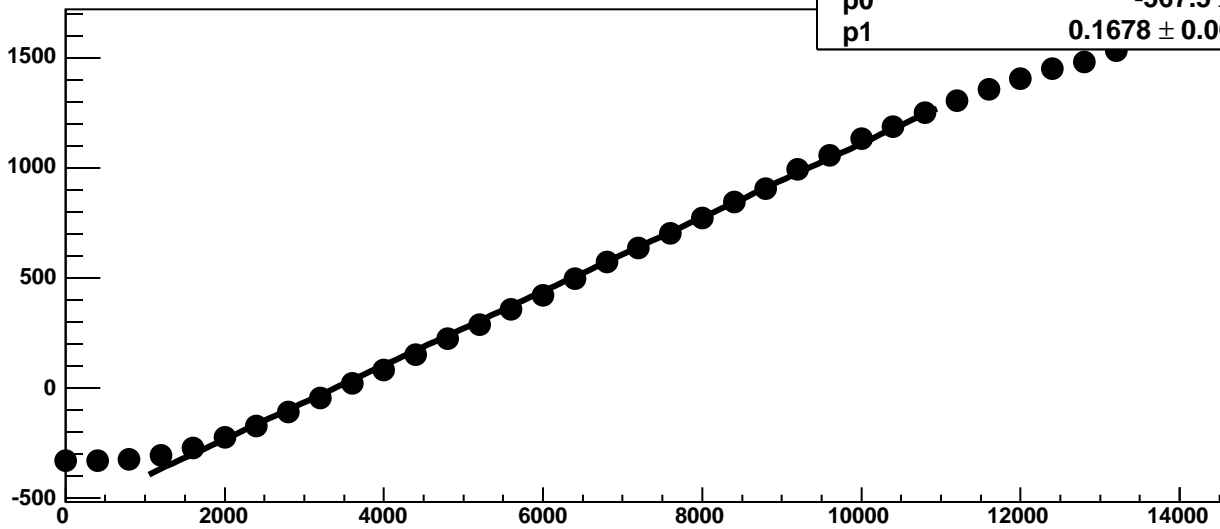
Chip 6, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



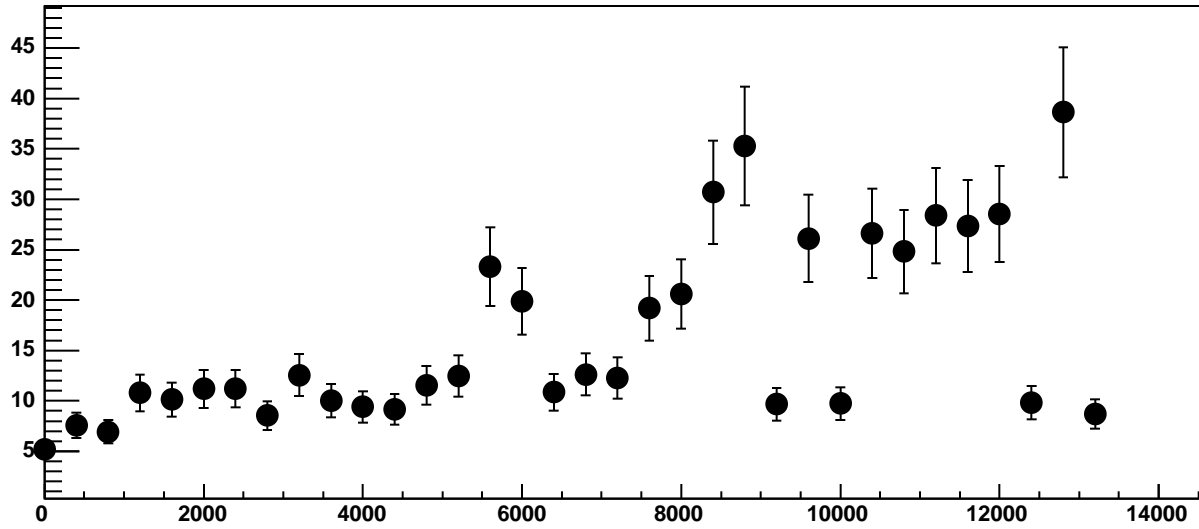
Chip 6, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



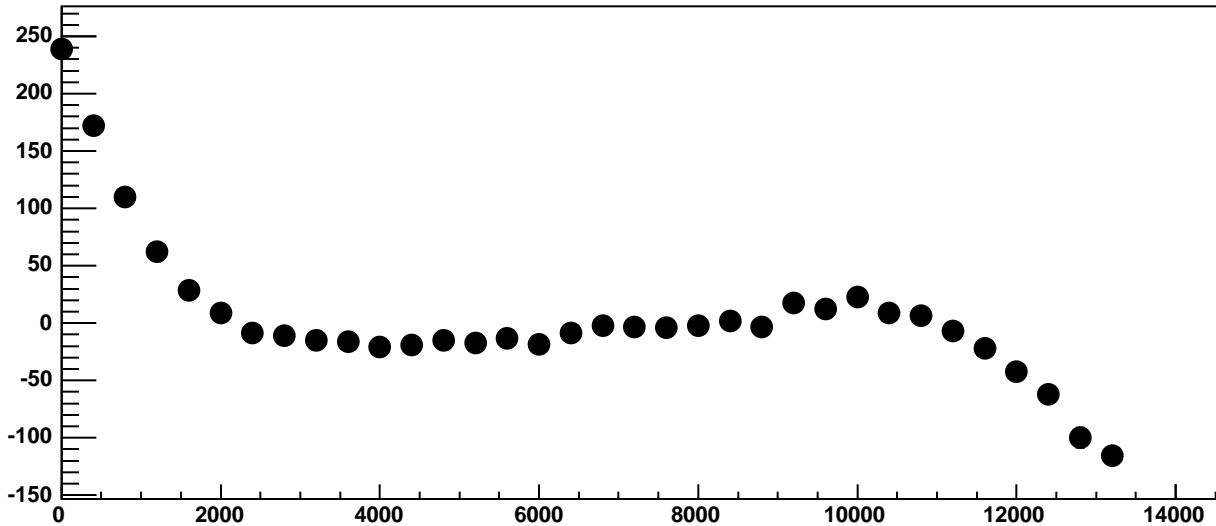
Chip 6, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC



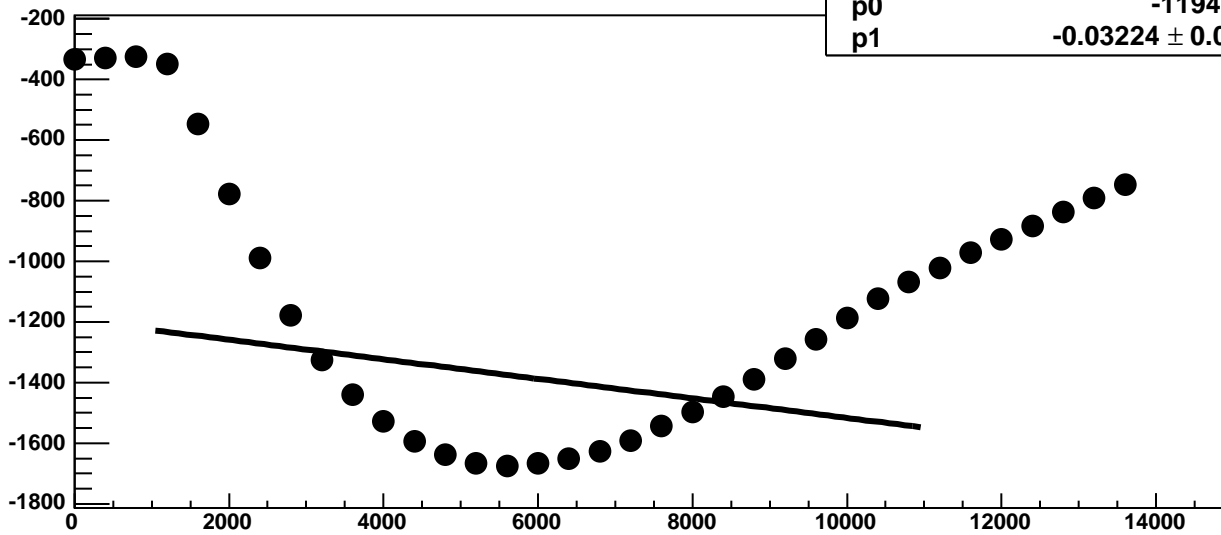
Chip 6, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC

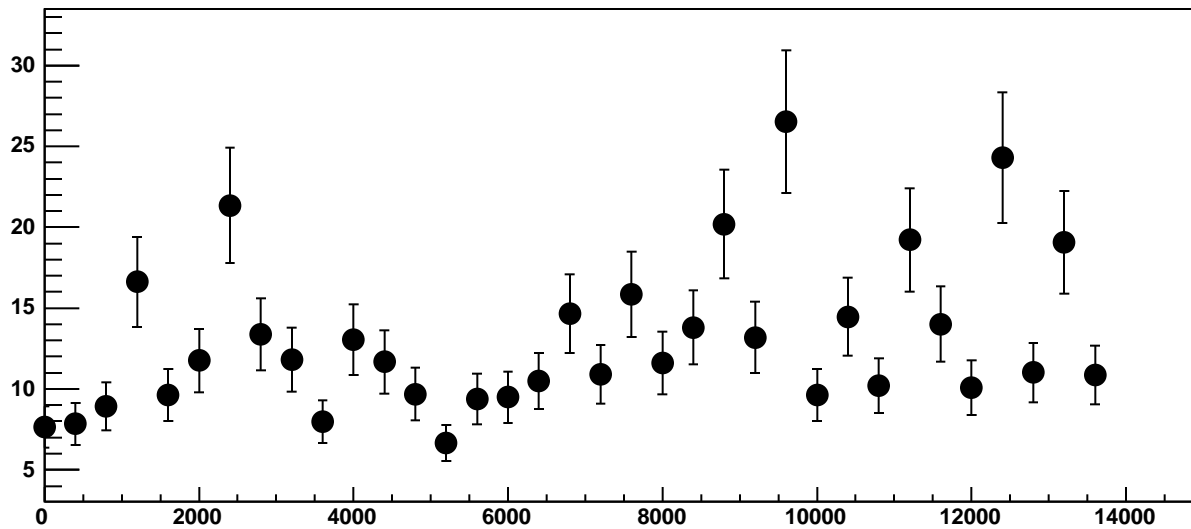


Chip 6, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

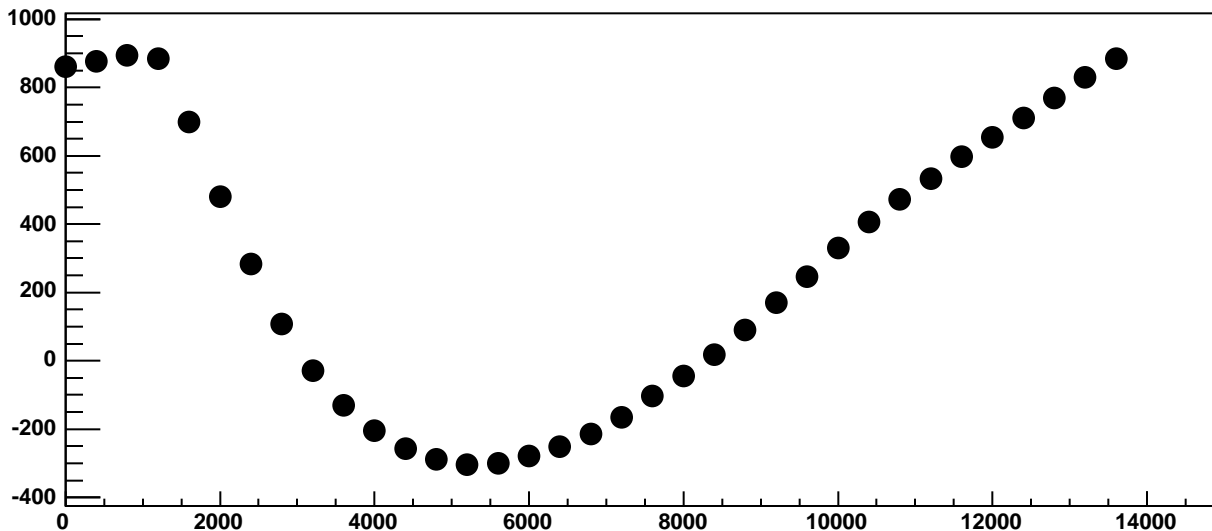


χ^2 / ndf 4.059e+05 / 23
p0 -1194 ± 1.244
p1 -0.03224 ± 0.0001957

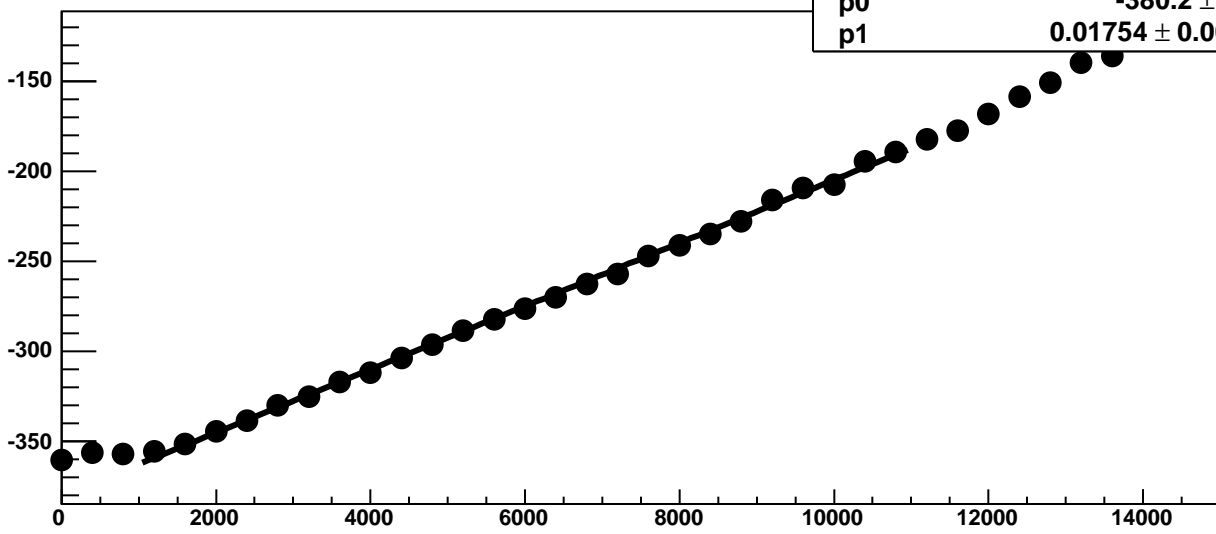
Chip 6, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

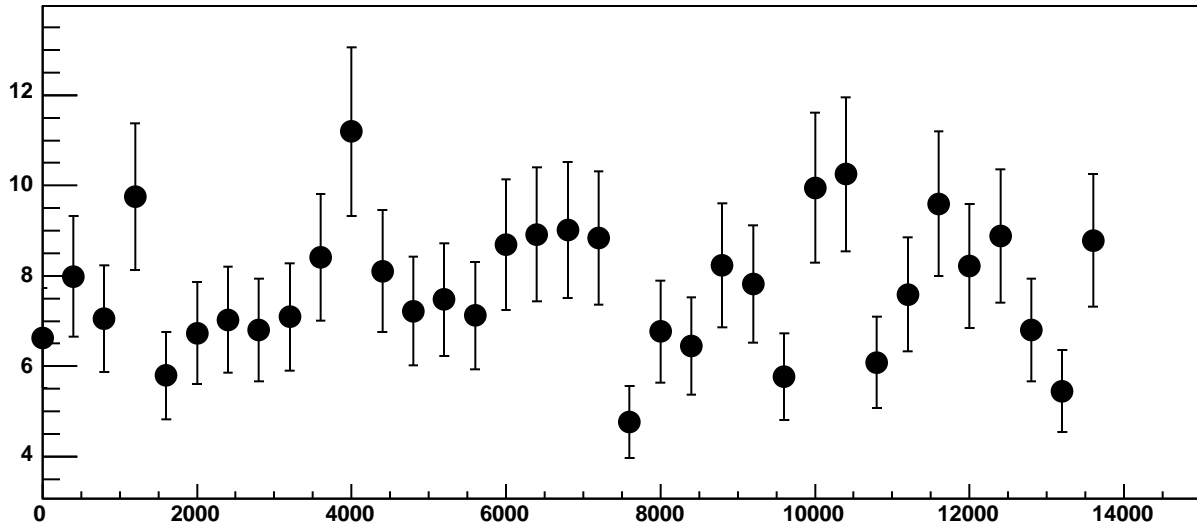


Chip 6, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

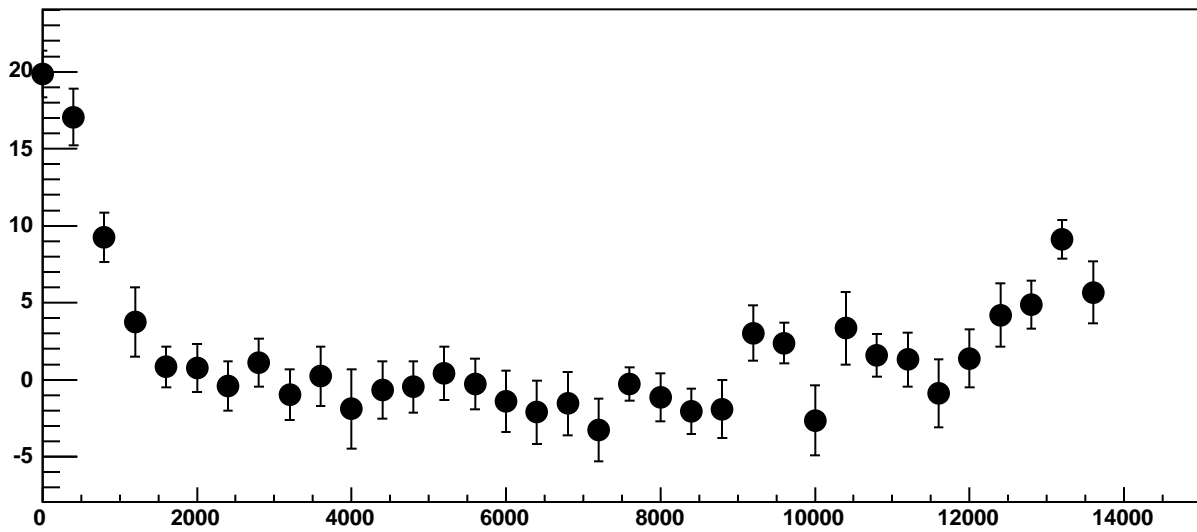


χ^2 / ndf 24.17 / 23
p0 -380.2 ± 0.7712
p1 0.01754 ± 0.0001145

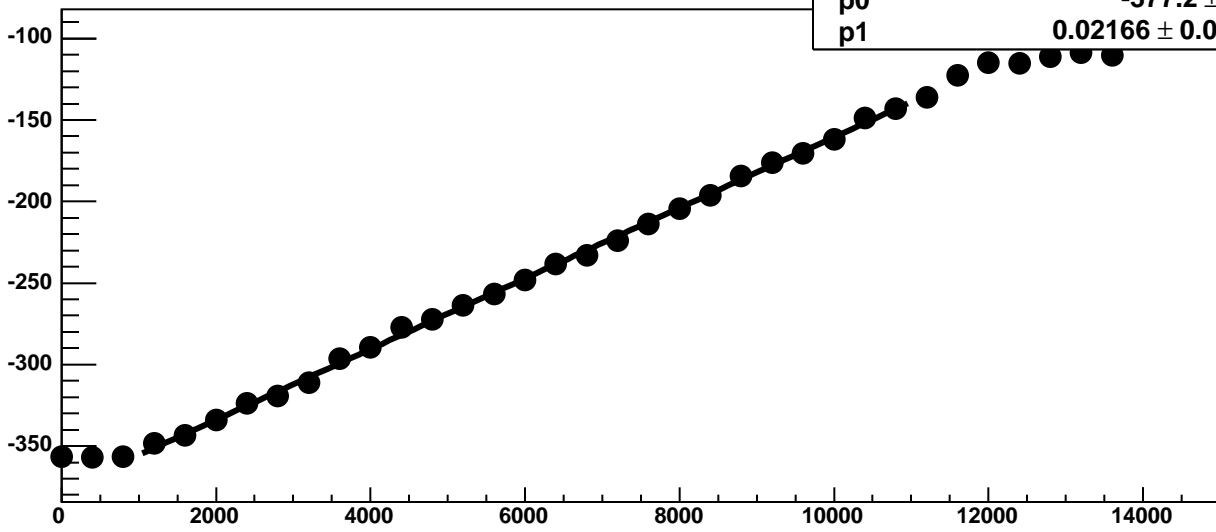
Chip 6, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



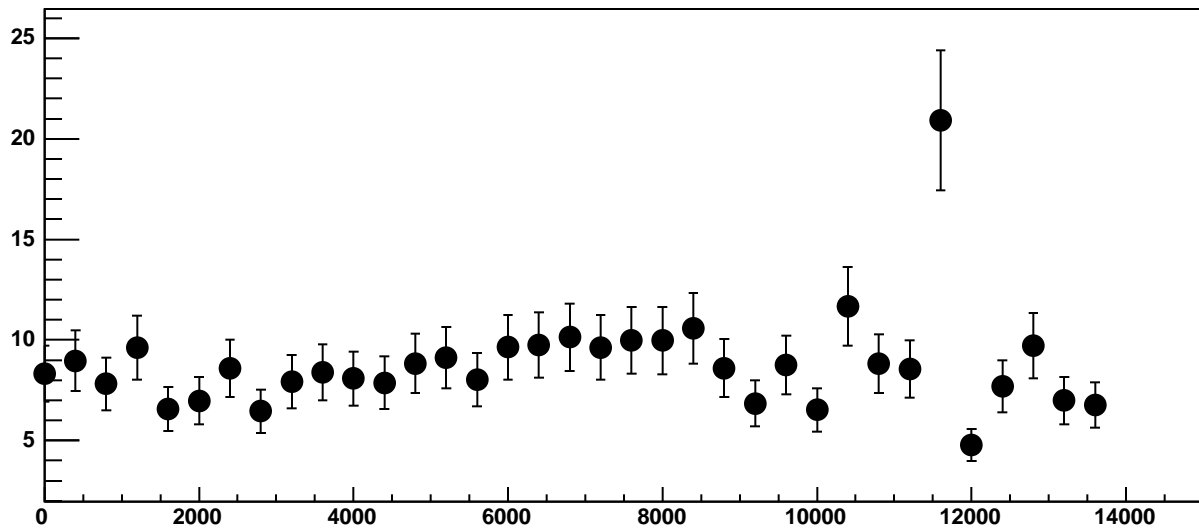
Chip 6, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



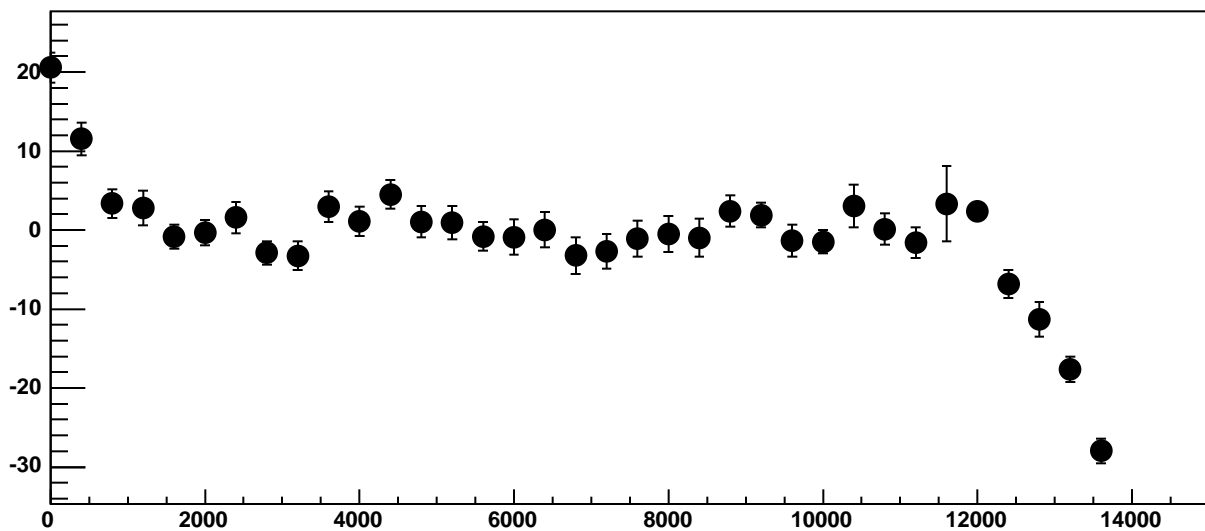
Chip 6, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC



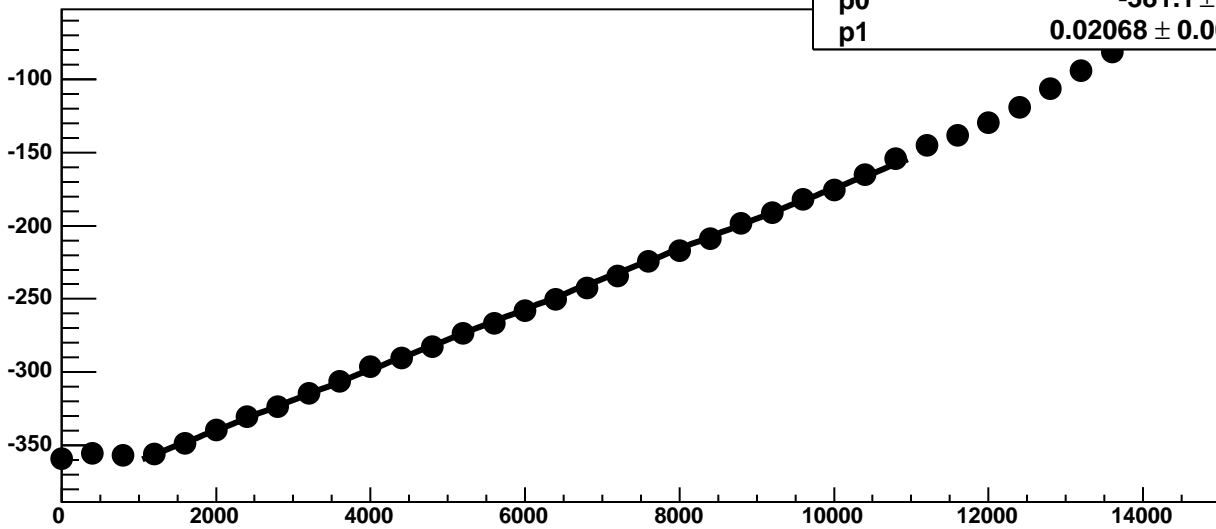
Chip 6, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

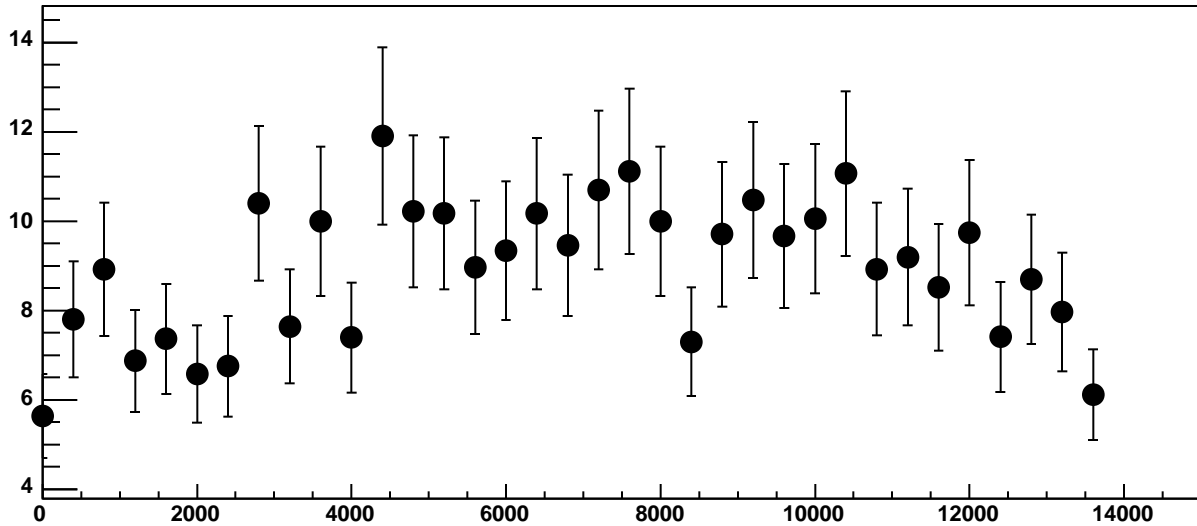


Chip 6, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

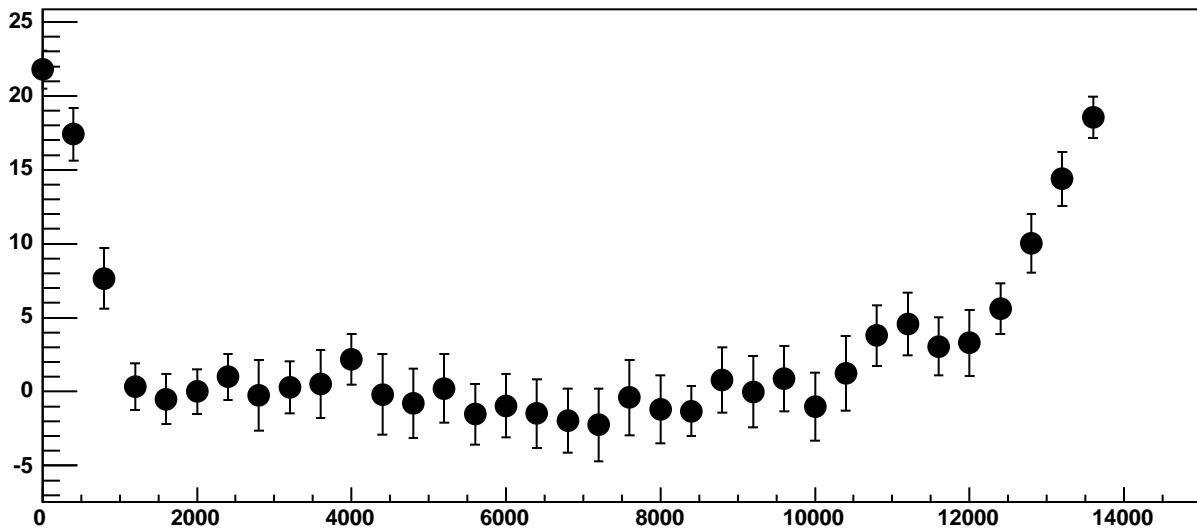


χ^2 / ndf 10.32 / 23
p0 -381.1 ± 0.8337
p1 0.02068 ± 0.0001349

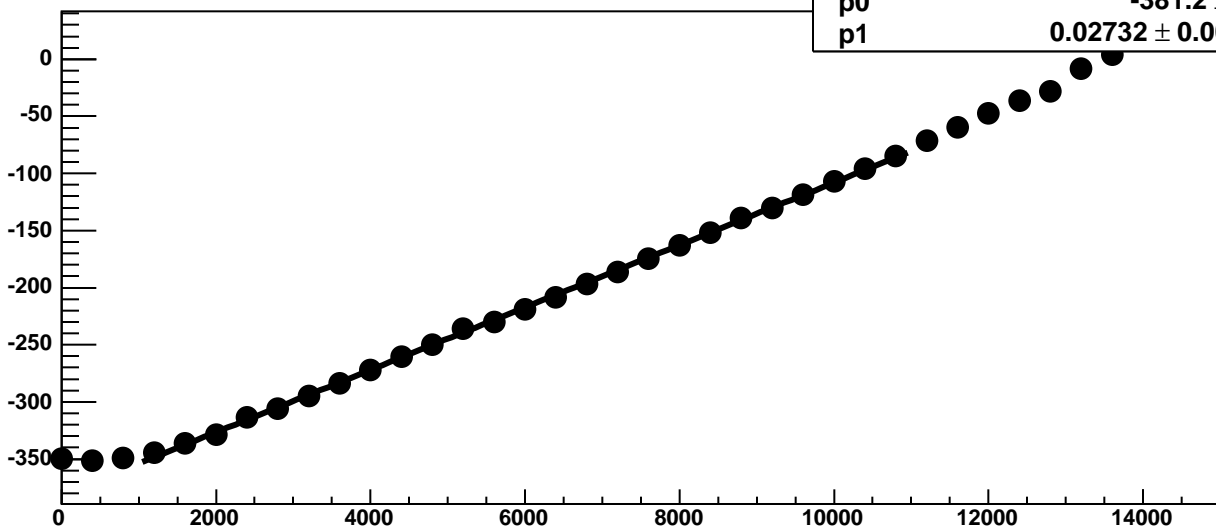
Chip 6, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

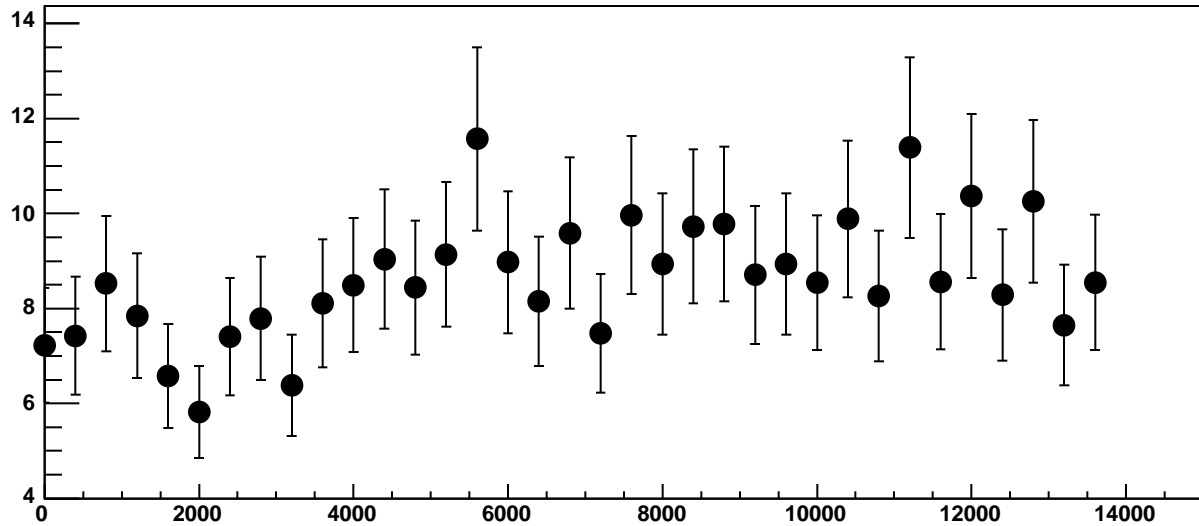


Chip 6, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

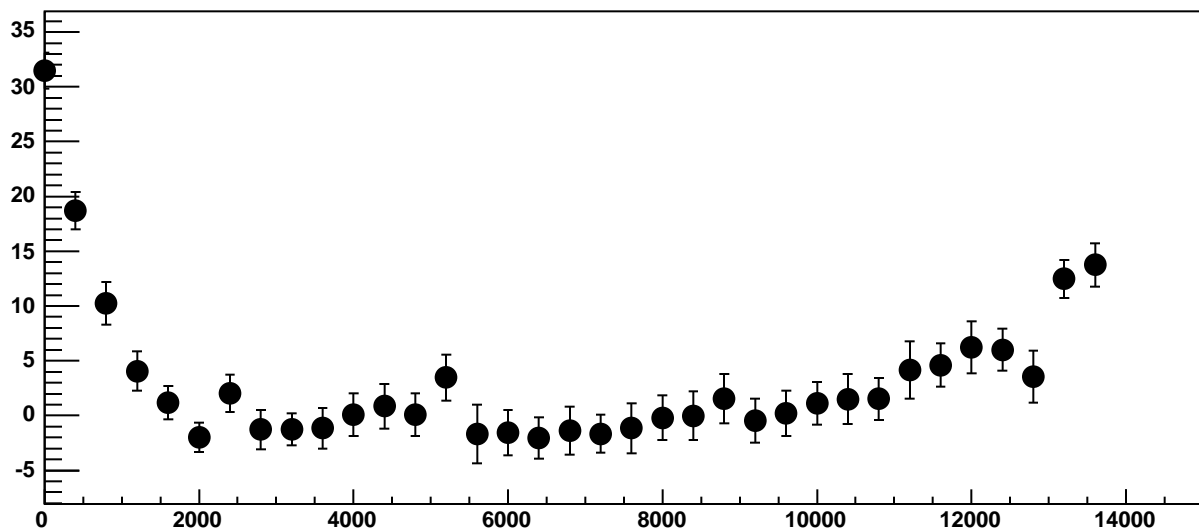


χ^2 / ndf 19.59 / 23
p0 -381.2 ± 0.785
p1 0.02732 ± 0.0001266

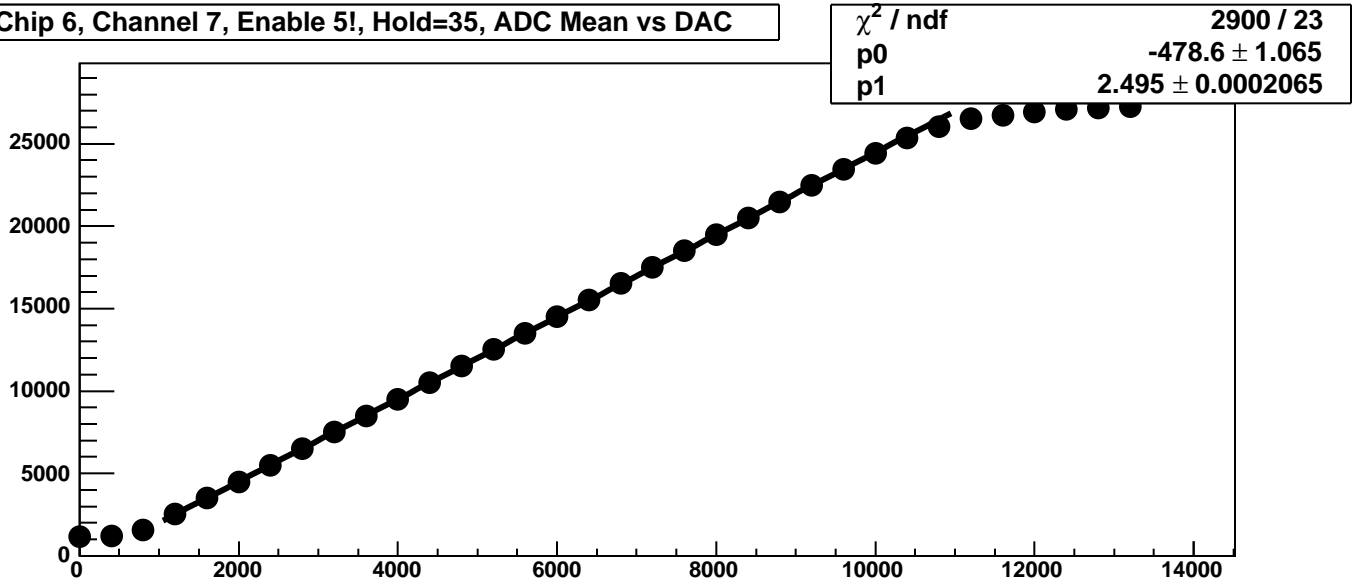
Chip 6, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



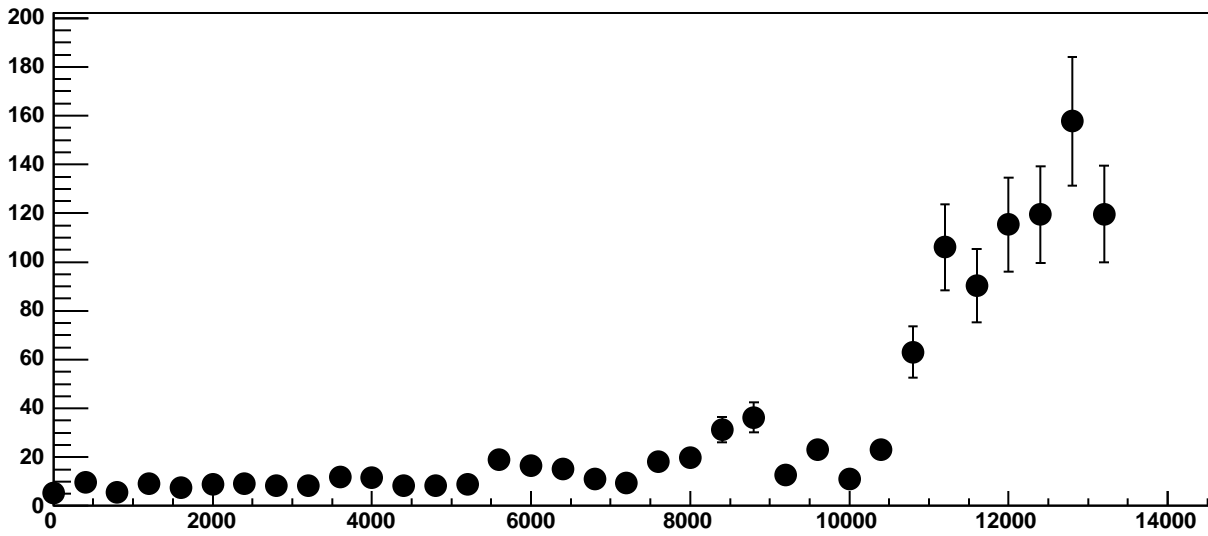
Chip 6, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



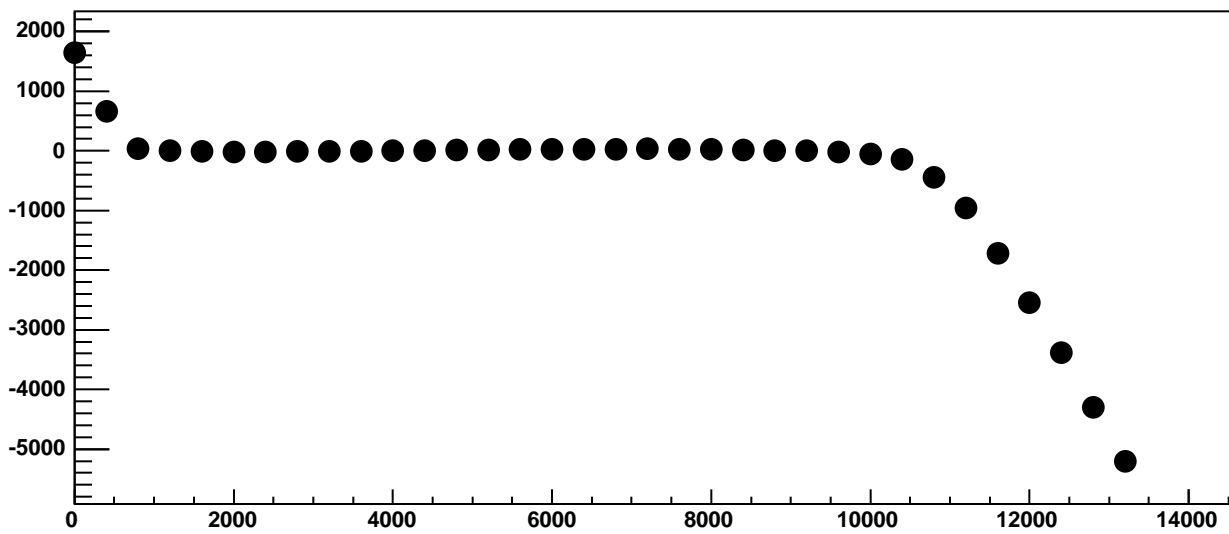
Chip 6, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC



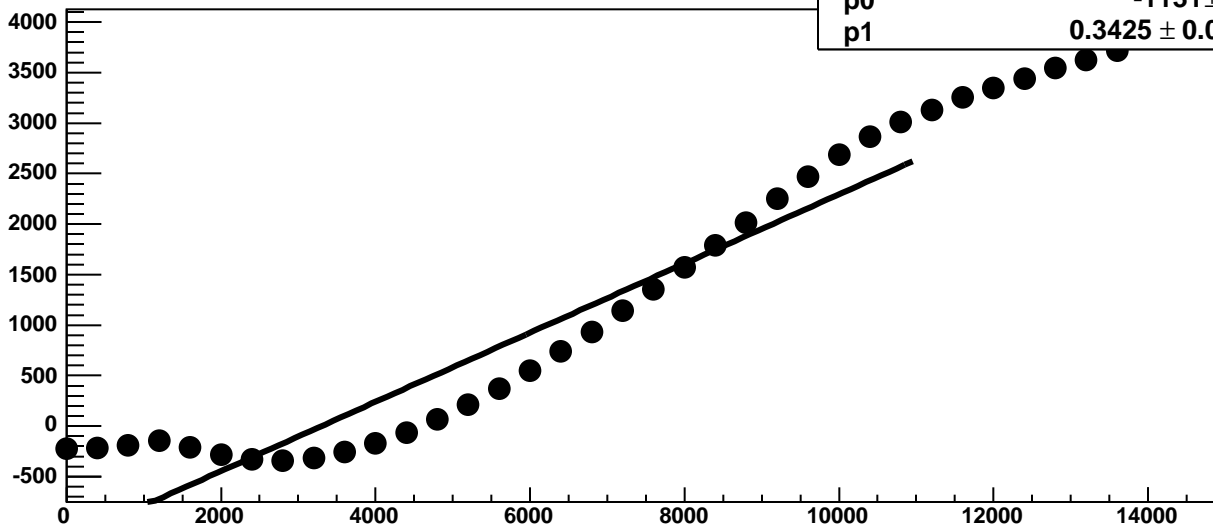
Chip 6, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



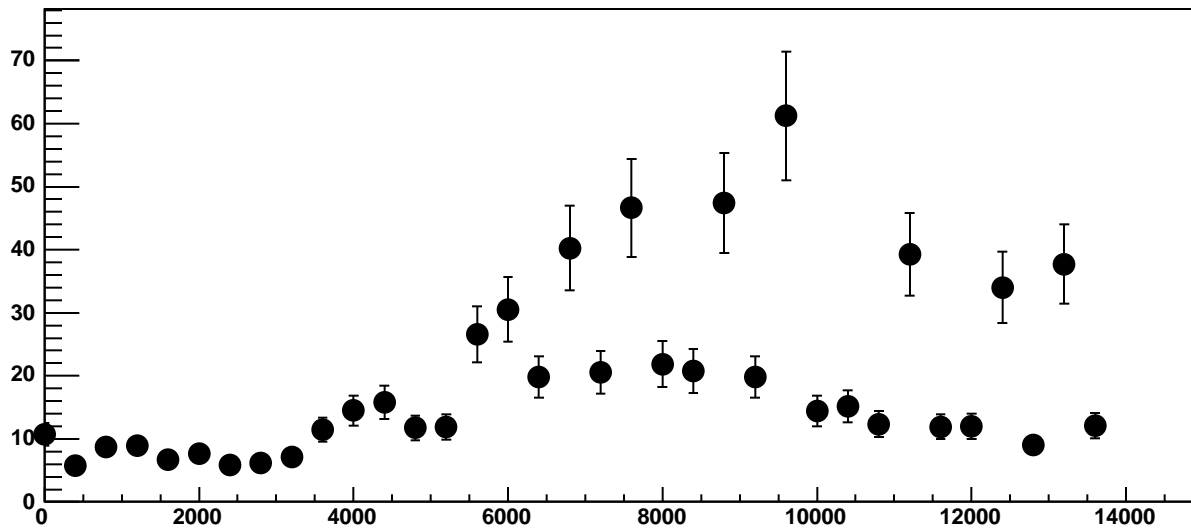
Chip 6, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC



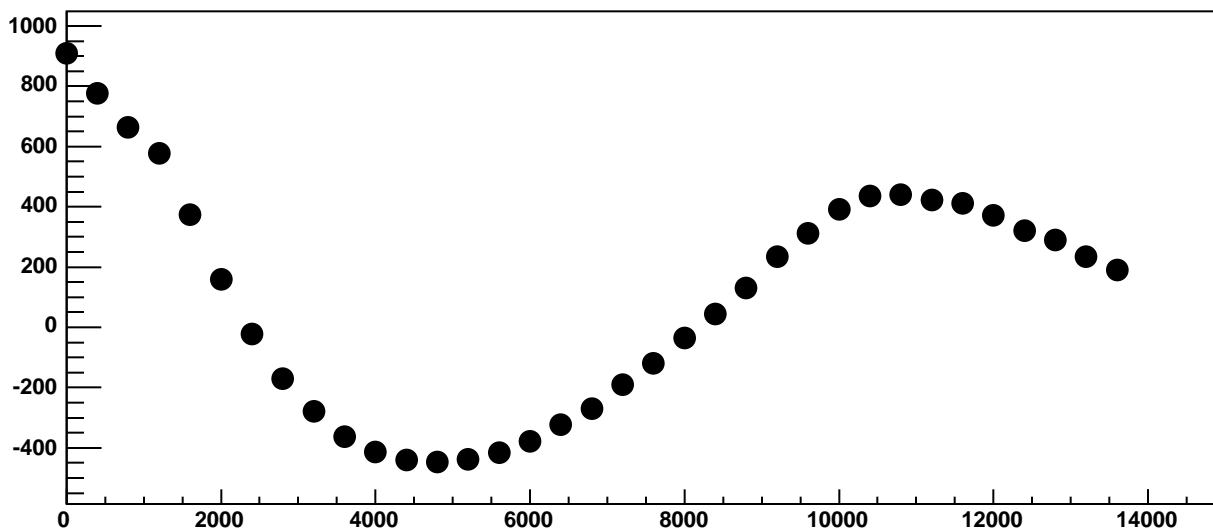
Chip 6, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



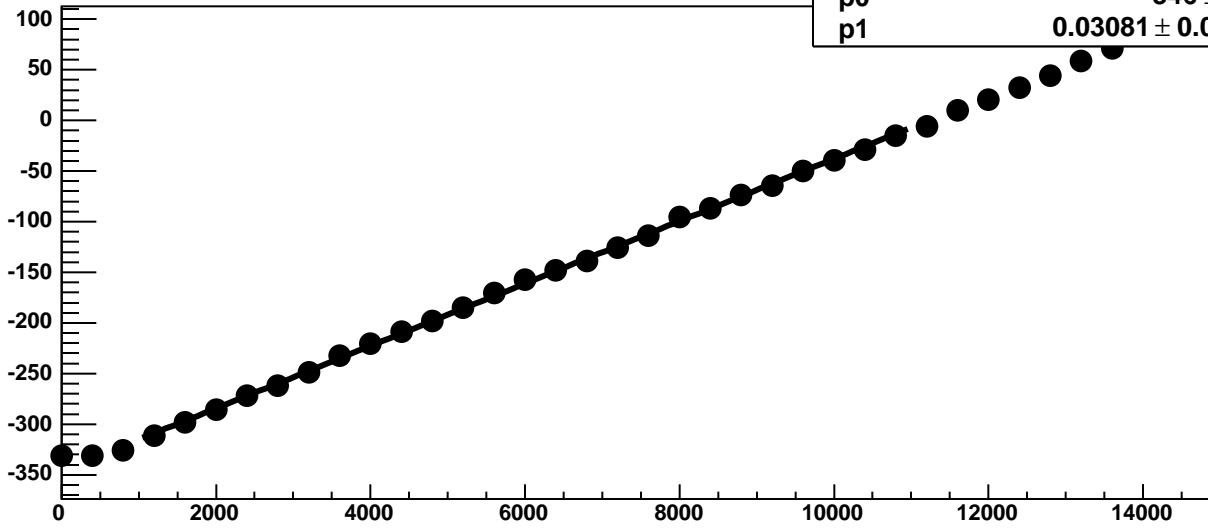
Chip 6, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

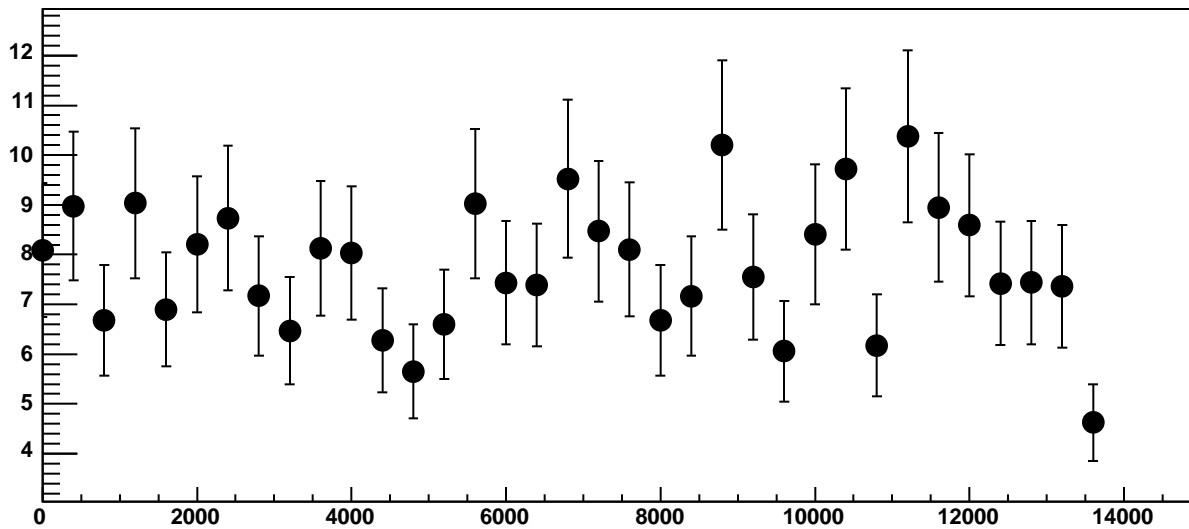


Chip 6, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

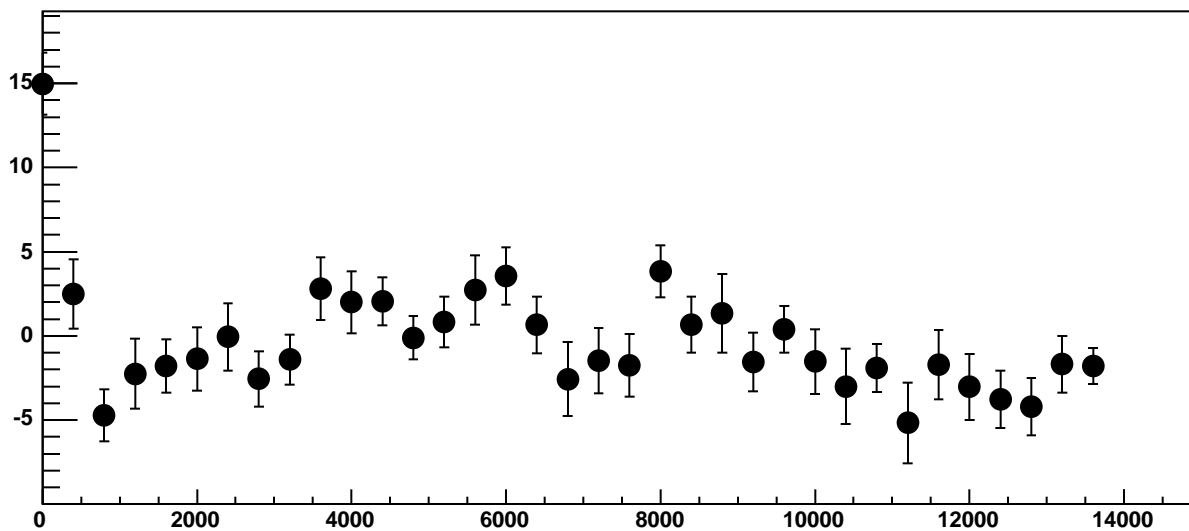


χ^2 / ndf 32.97 / 23
p0 -346 ± 0.7961
p1 0.03081 ± 0.0001197

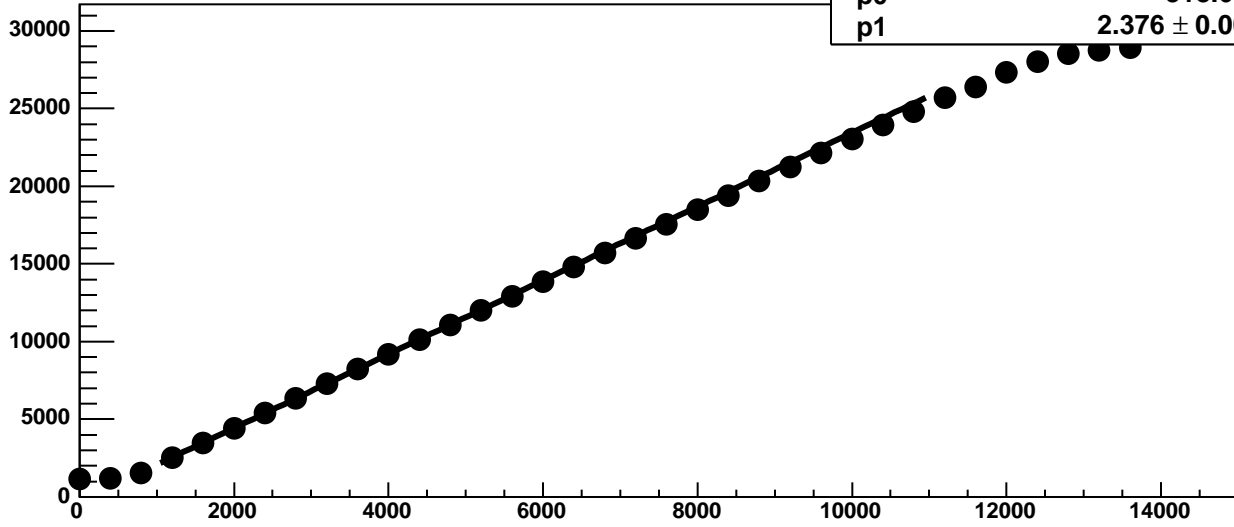
Chip 6, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 6, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

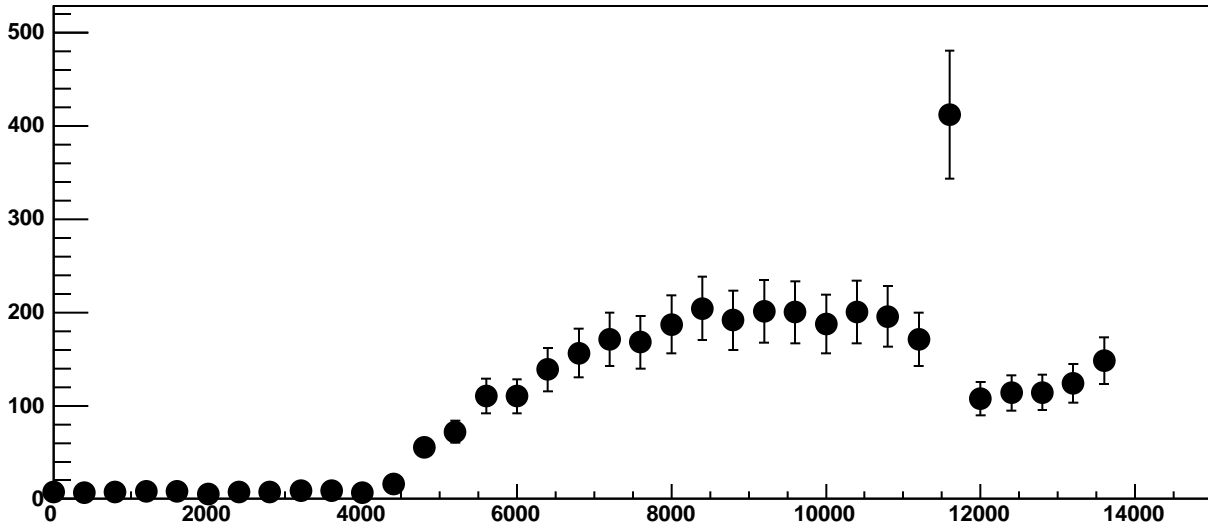


Chip 6, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC

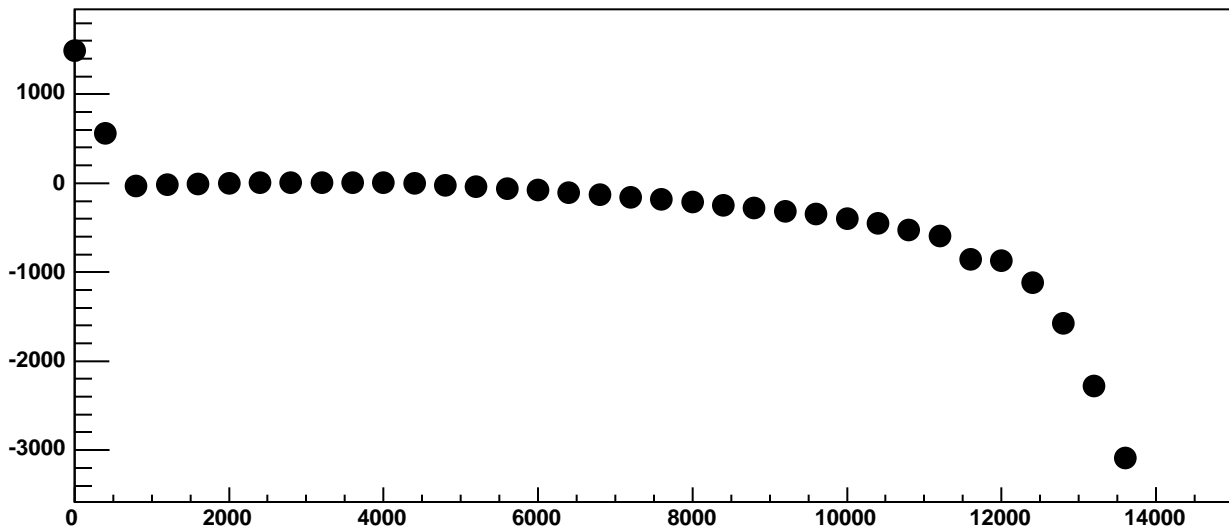


χ^2 / ndf 746 / 23
p0 -318.6 ± 1.66
p1 2.376 ± 0.0005887

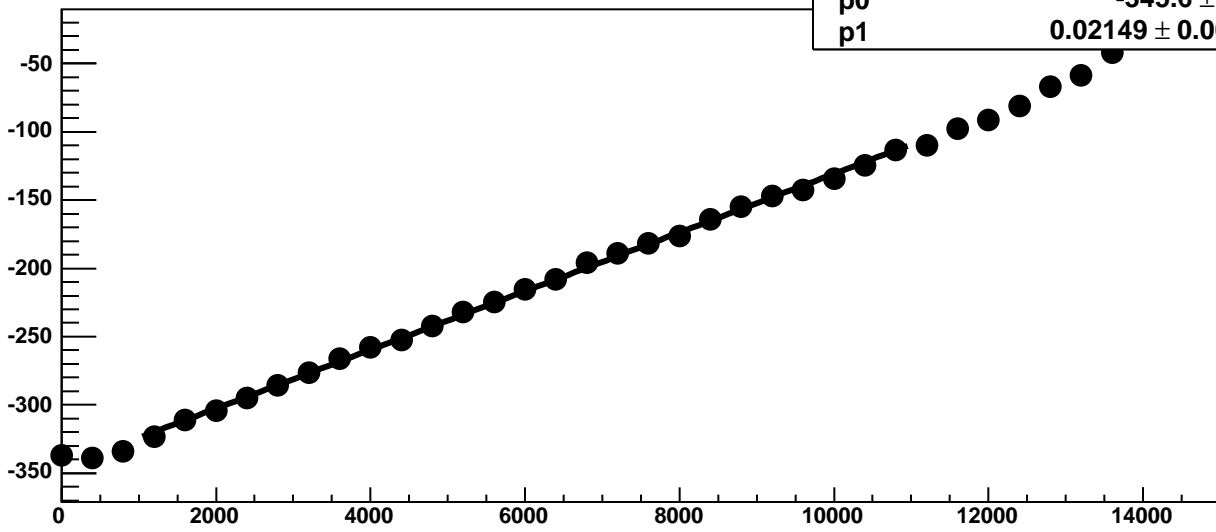
Chip 6, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC

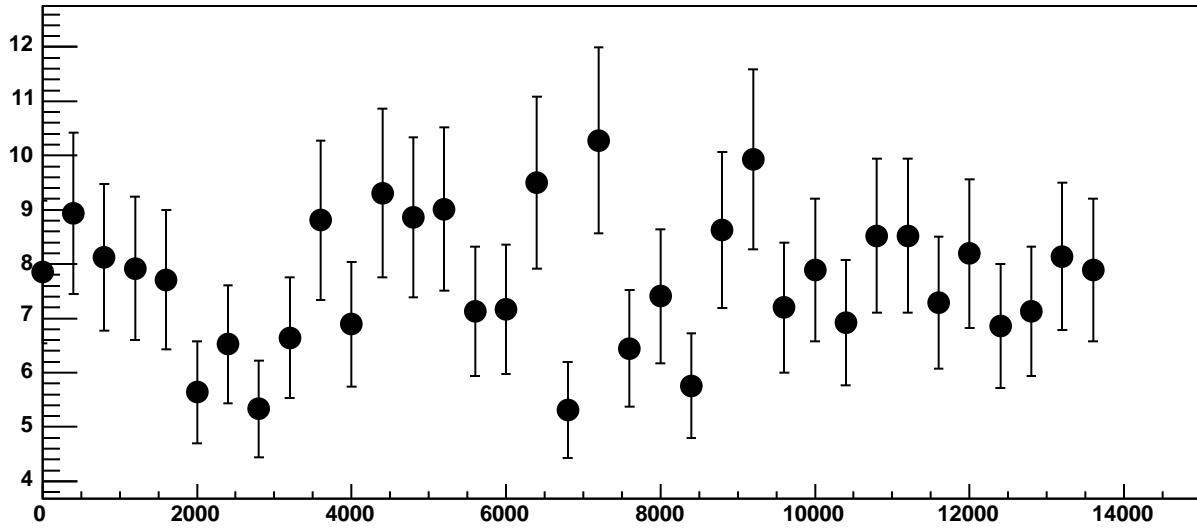


Chip 6, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

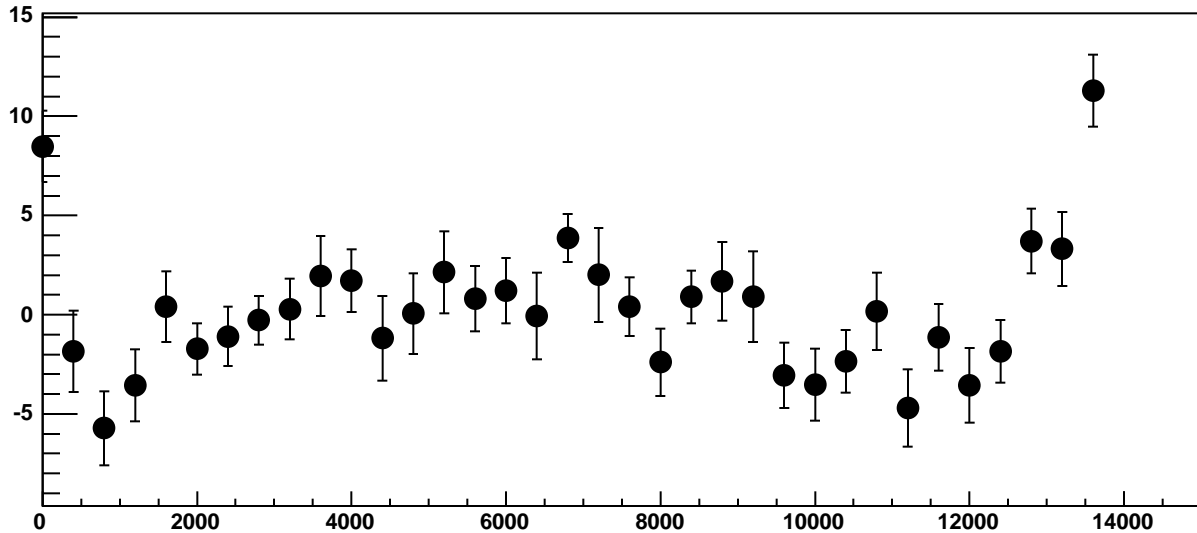


χ^2 / ndf 34.2 / 23
p0 -345.6 ± 0.7397
p1 0.02149 ± 0.0001145

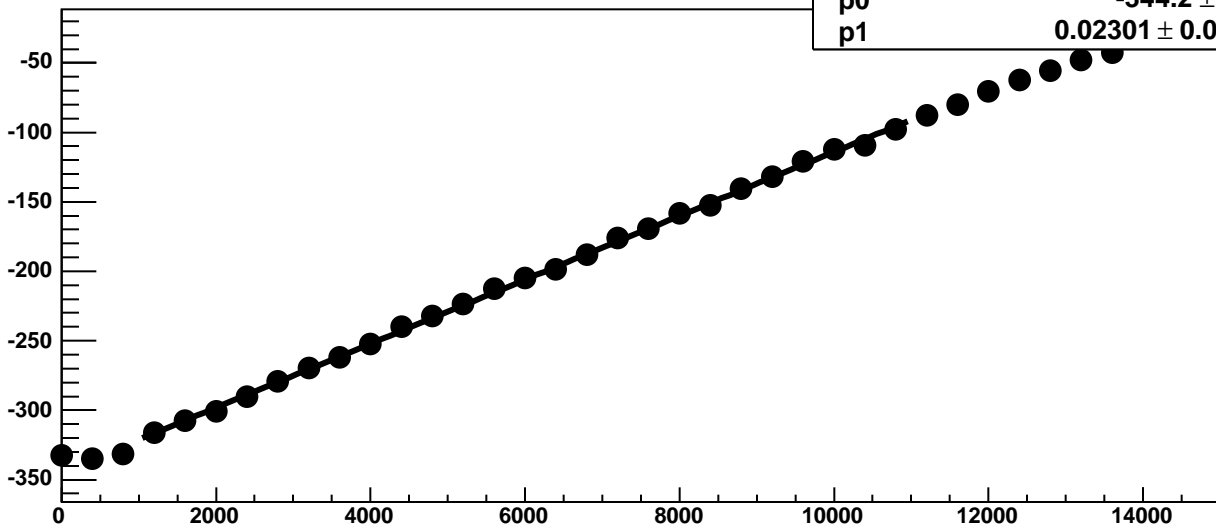
Chip 6, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

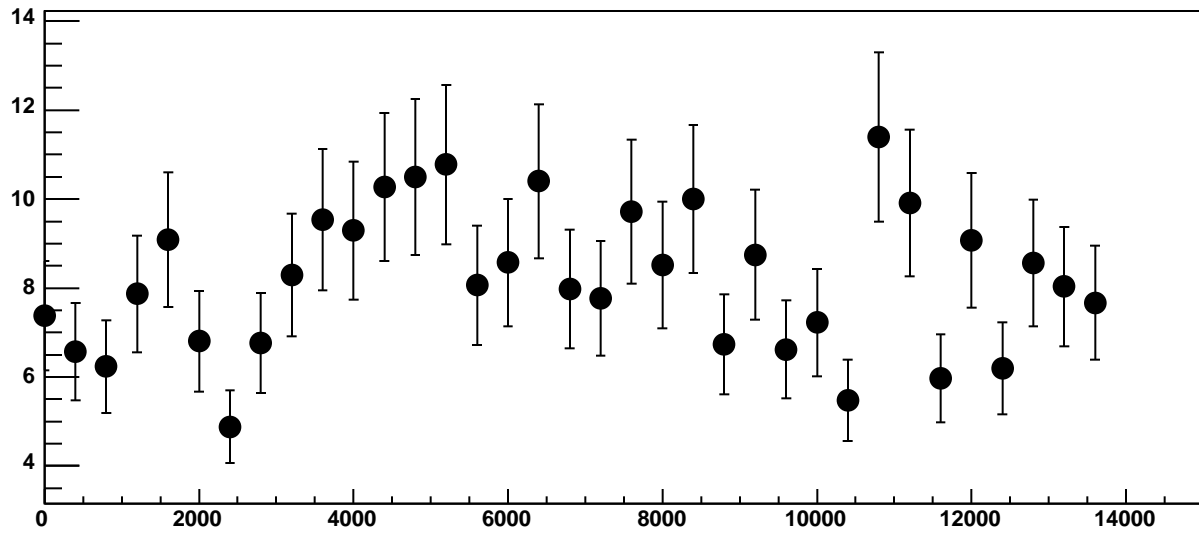


Chip 6, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

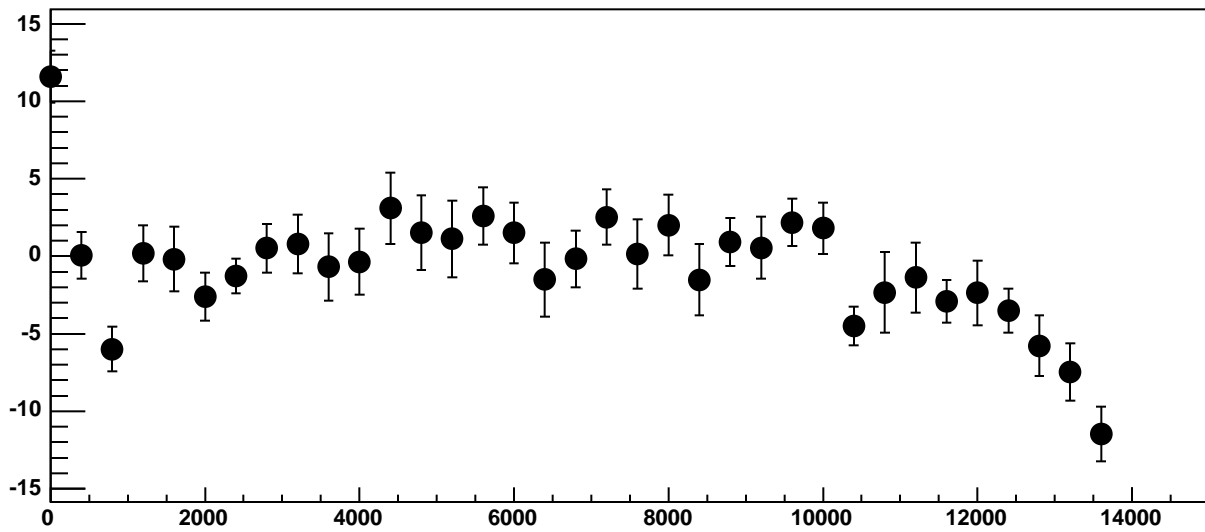


χ^2 / ndf 30.66 / 23
p0 -344.2 ± 0.7812
p1 0.02301 ± 0.0001162

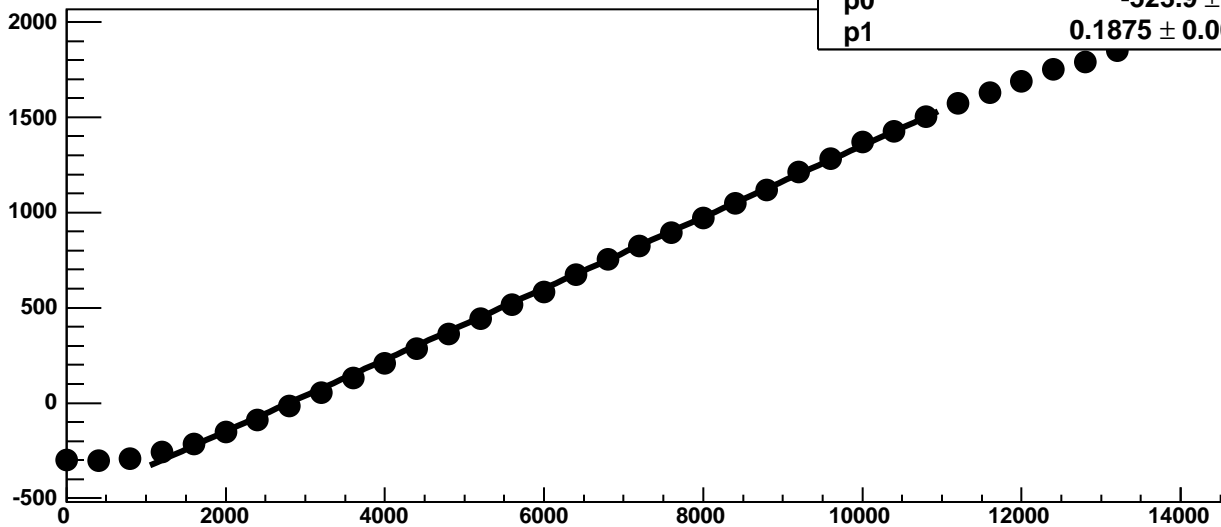
Chip 6, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



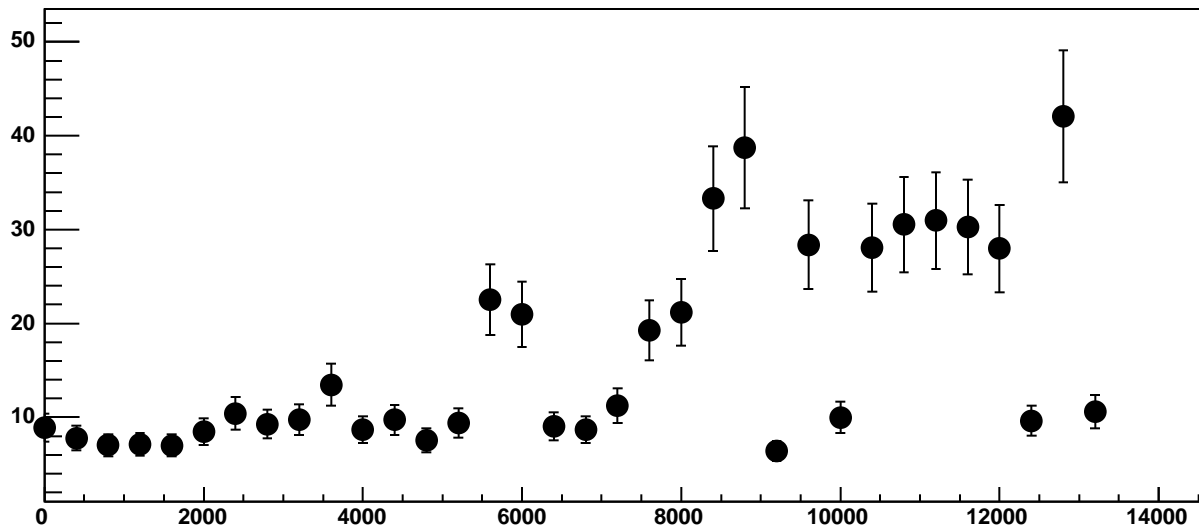
Chip 6, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



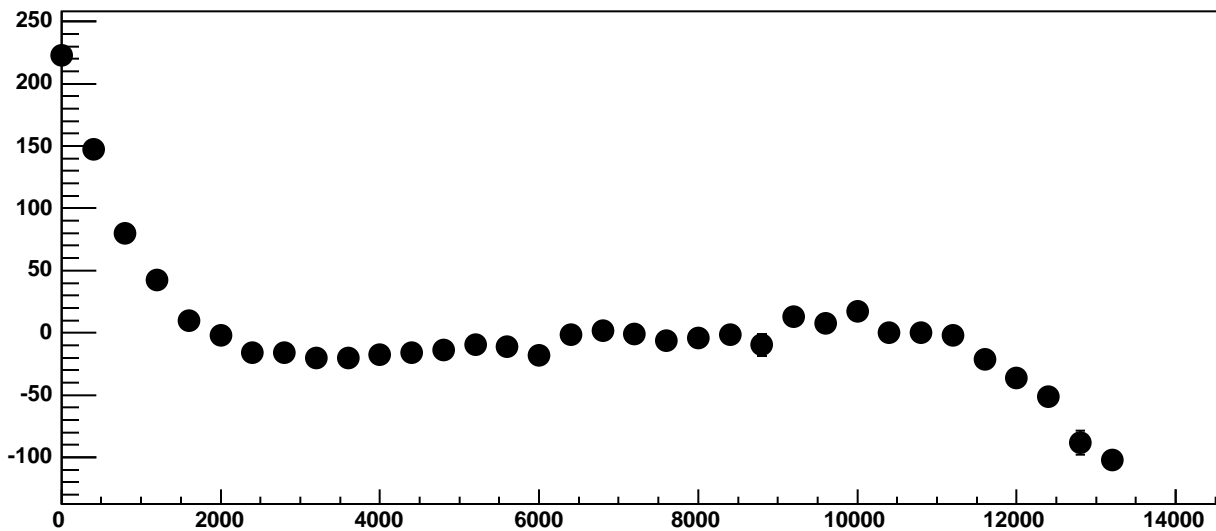
Chip 6, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC



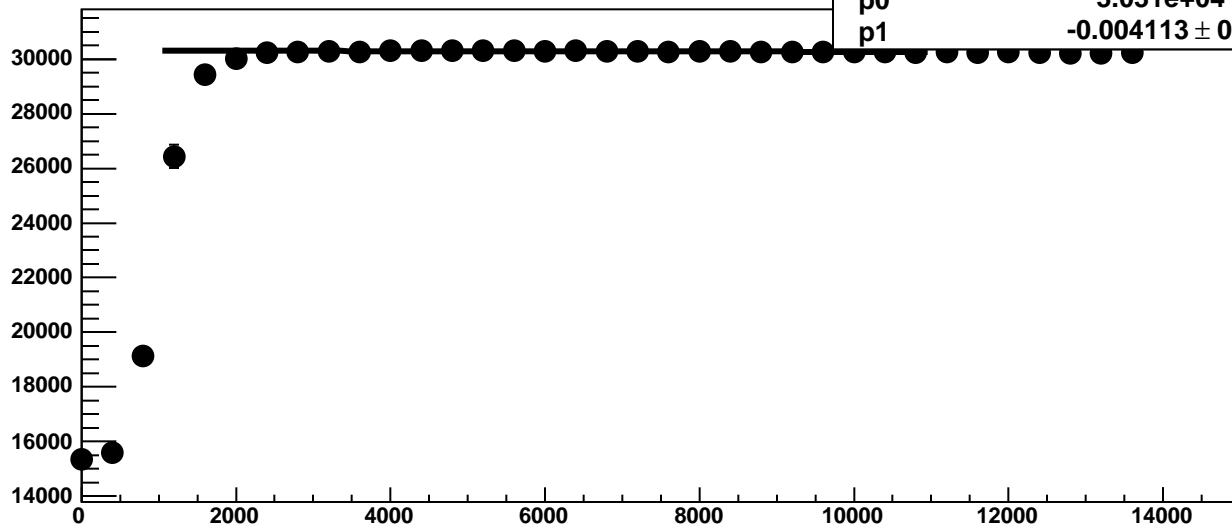
Chip 6, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



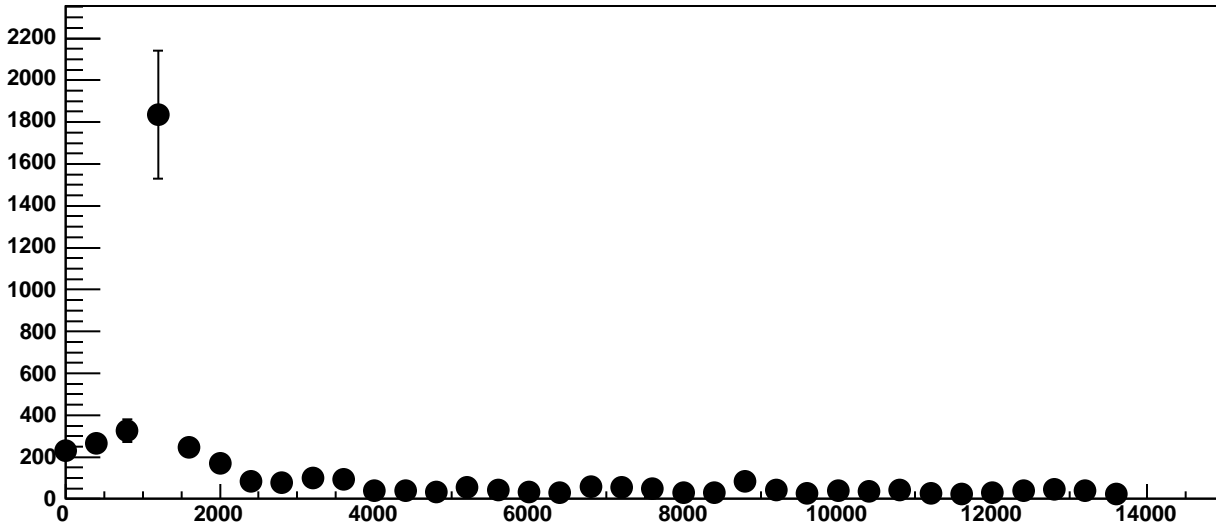
Chip 6, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



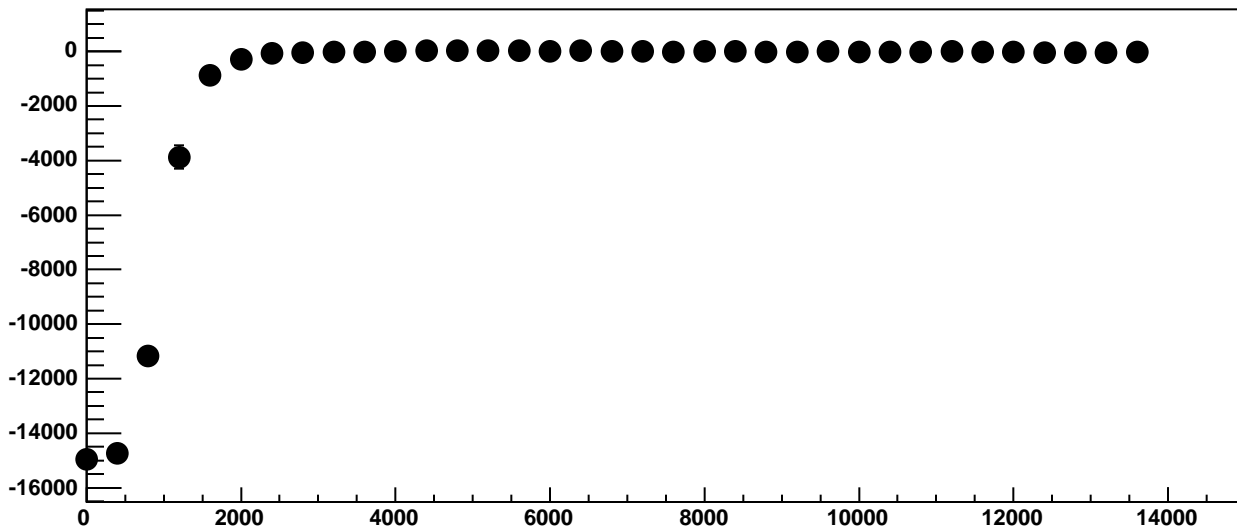
Chip 6, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



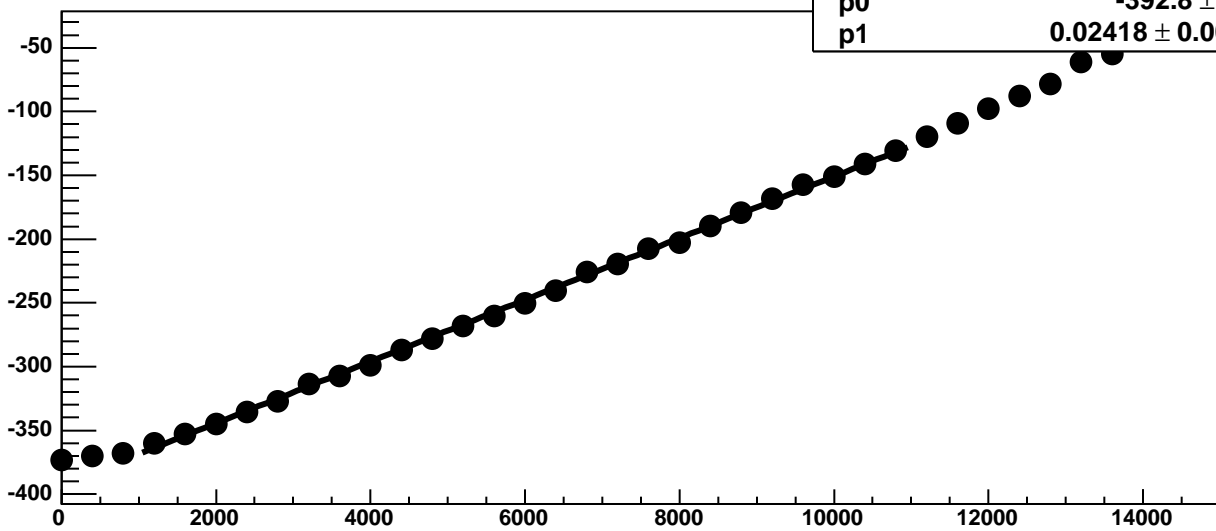
Chip 6, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC

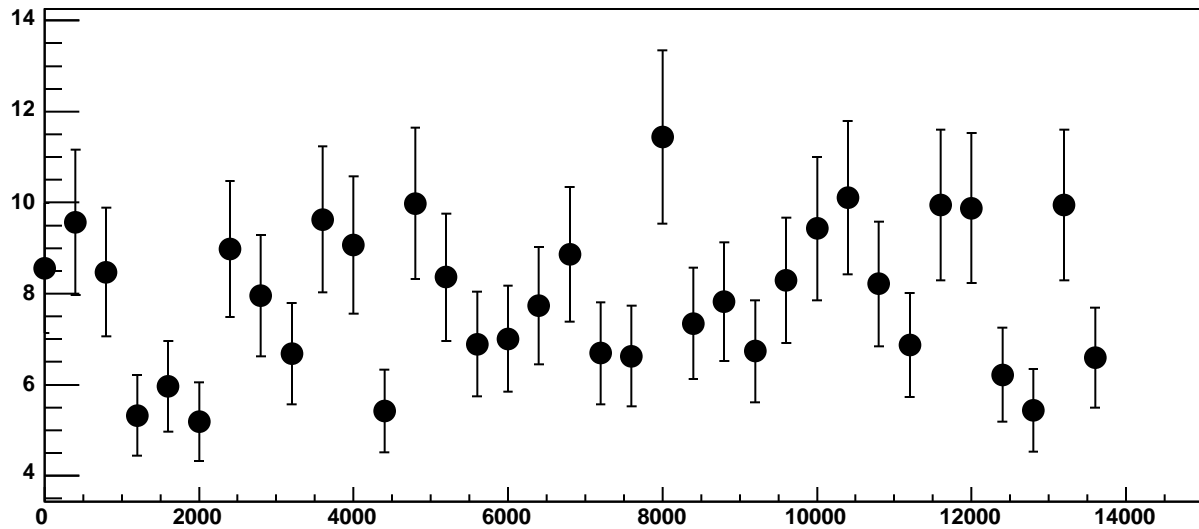


Chip 6, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

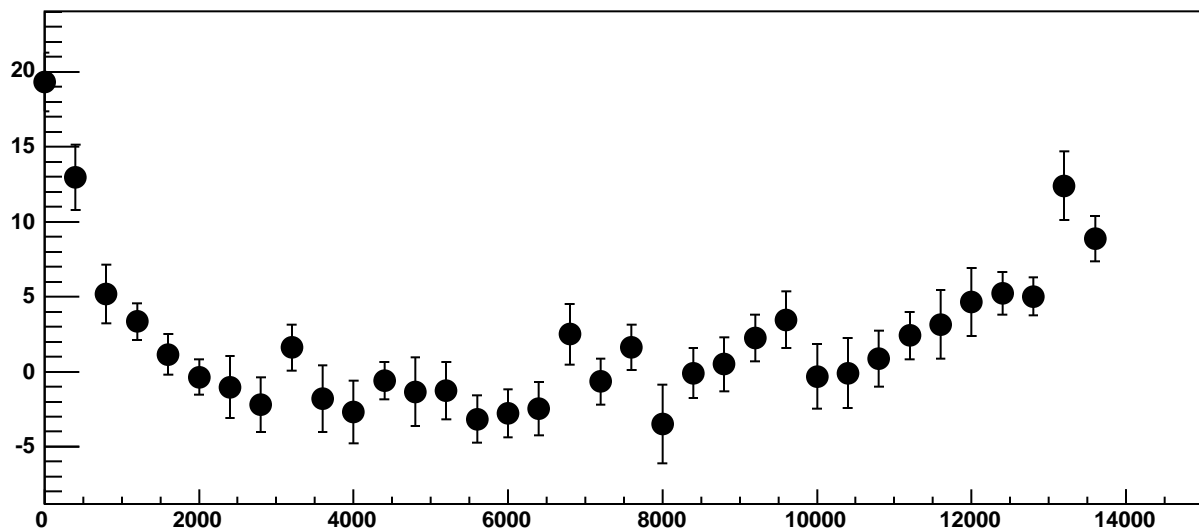


χ^2 / ndf 33.72 / 23
p0 -392.8 ± 0.7068
p1 0.02418 ± 0.0001146

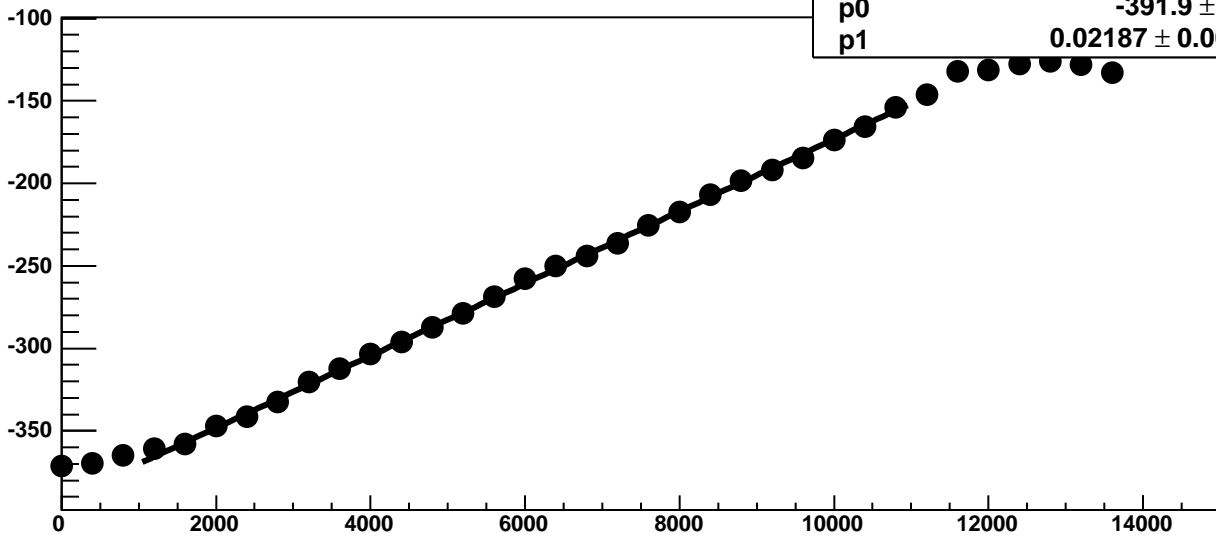
Chip 6, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



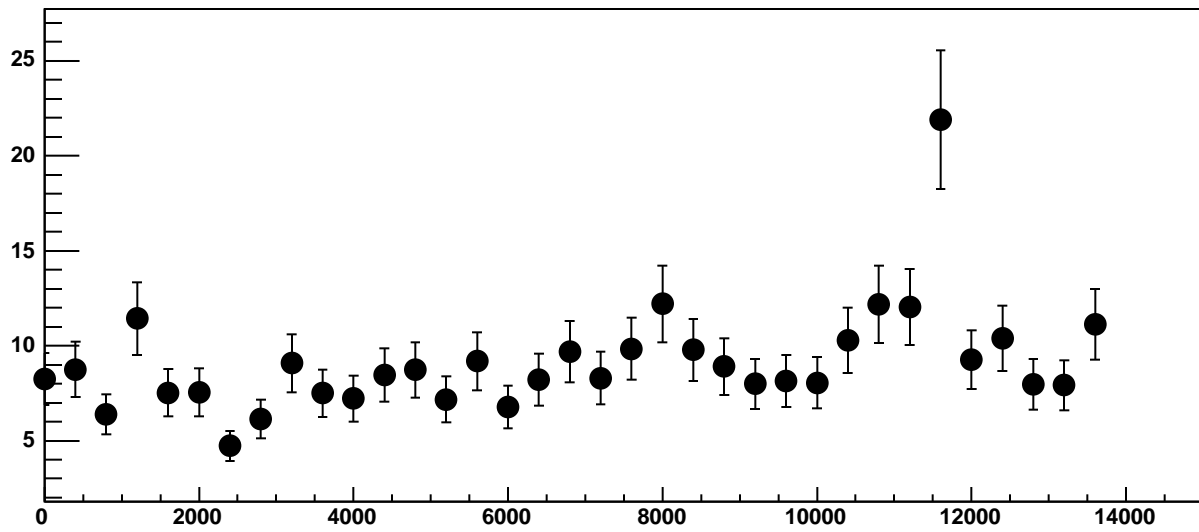
Chip 6, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



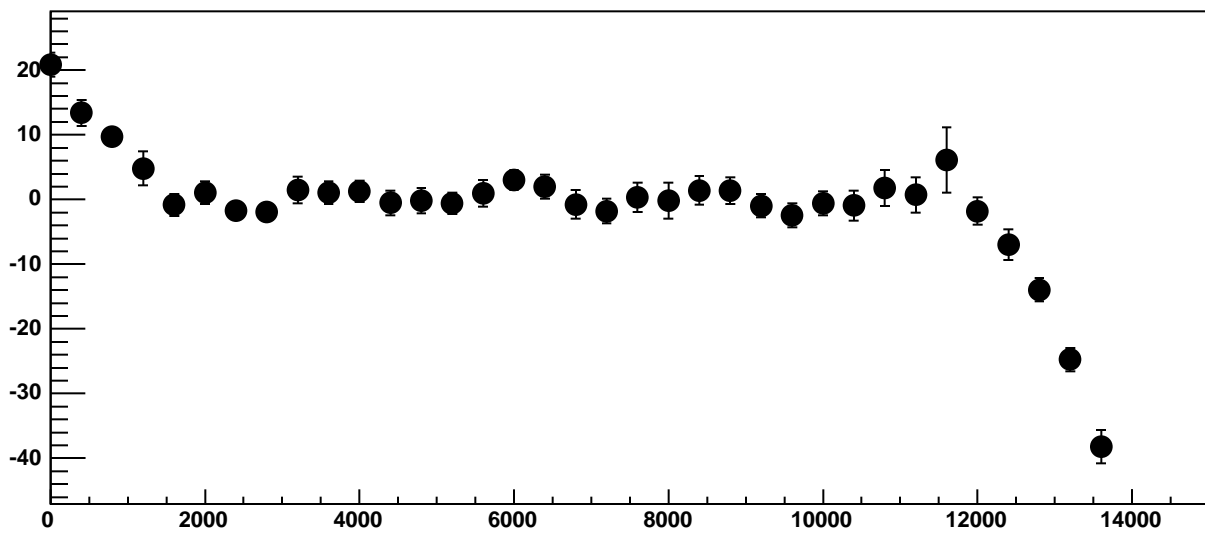
Chip 6, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



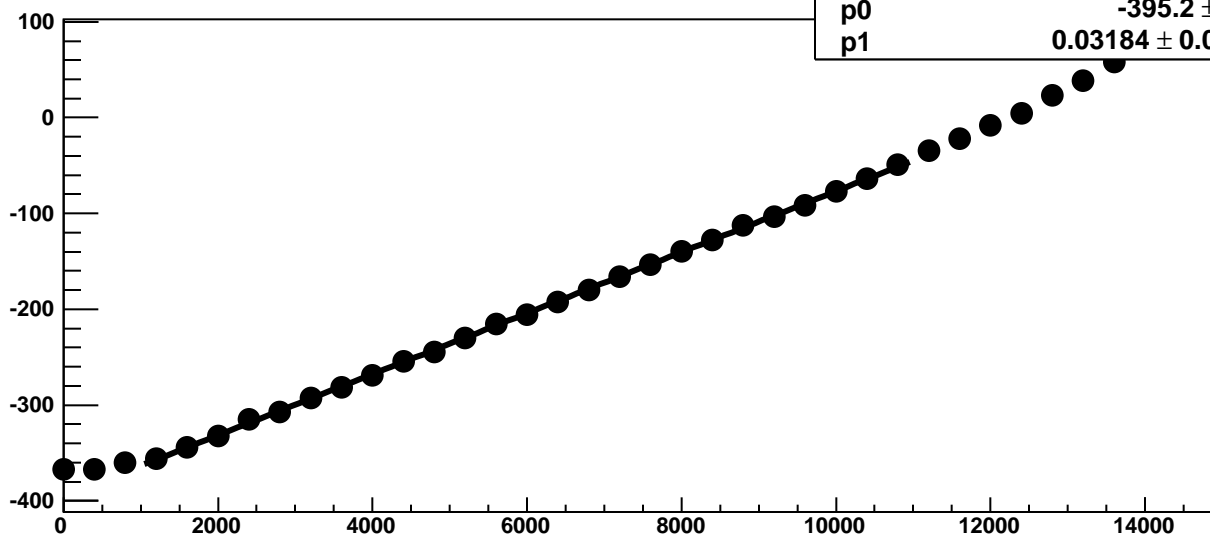
Chip 6, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

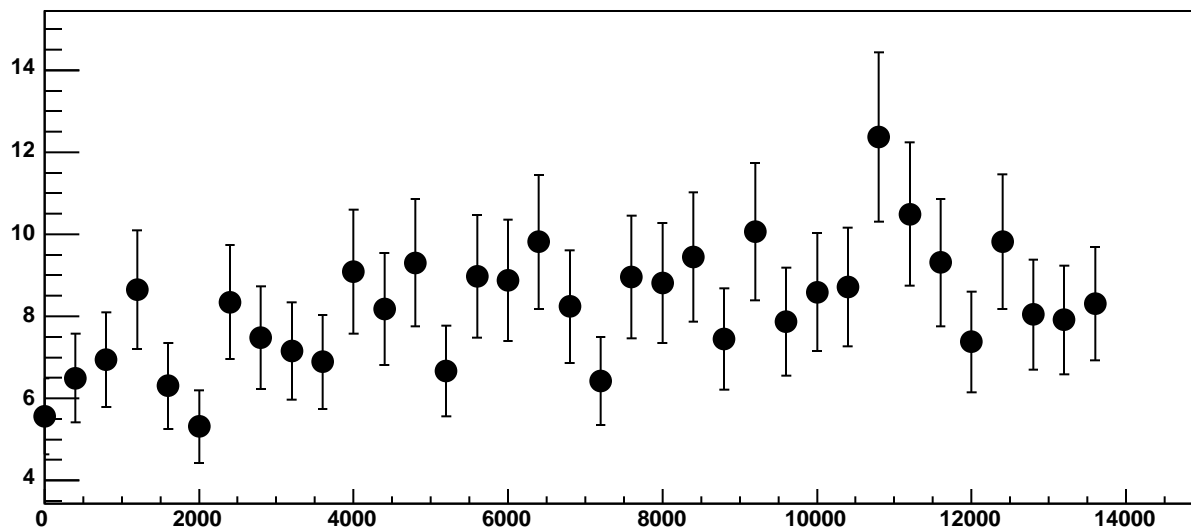


Chip 6, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

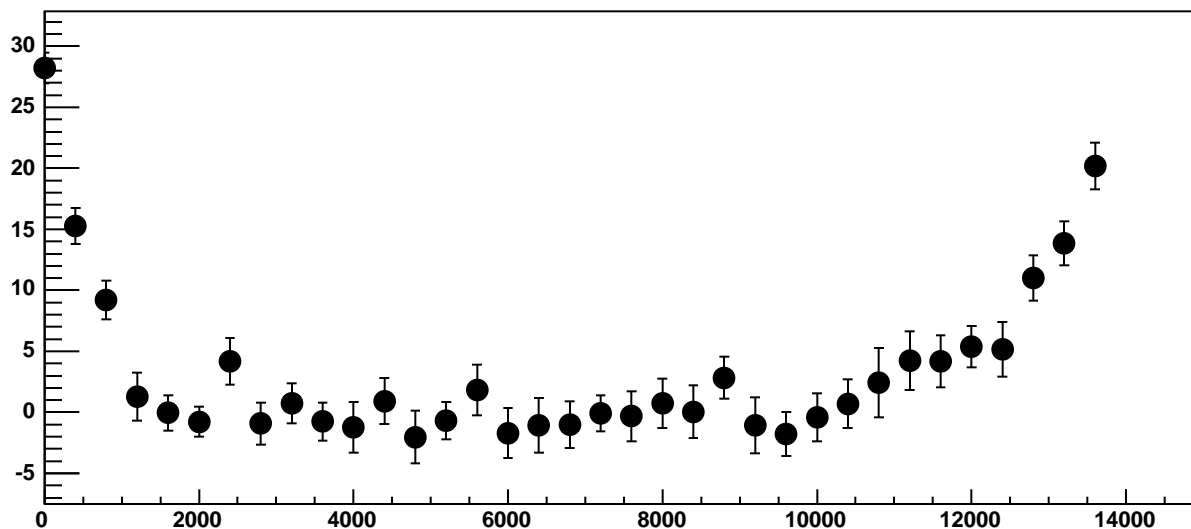


χ^2 / ndf 15.02 / 23
p0 -395.2 ± 0.7818
p1 0.03184 ± 0.0001267

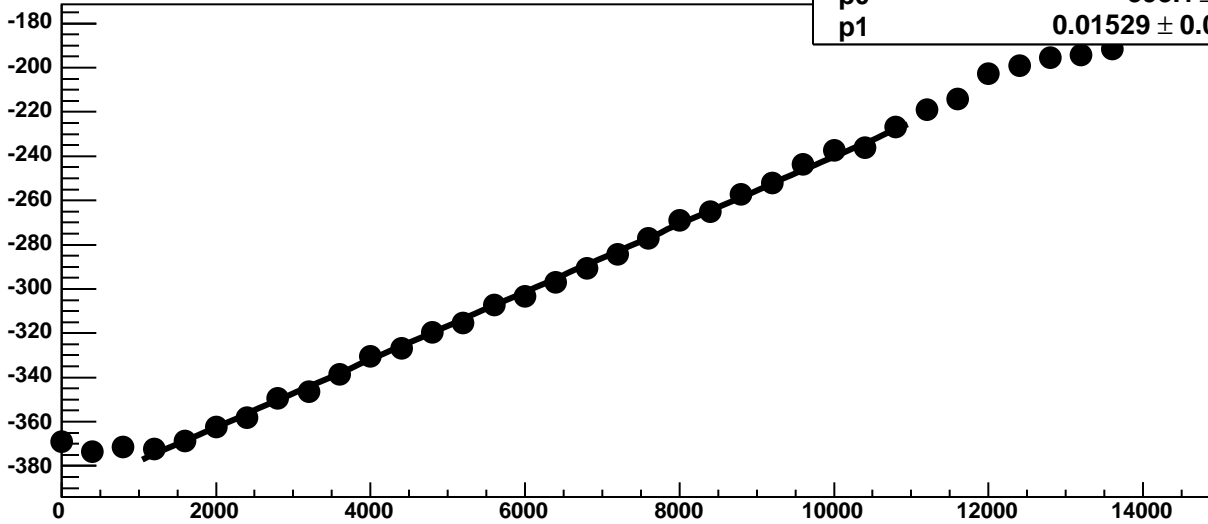
Chip 6, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

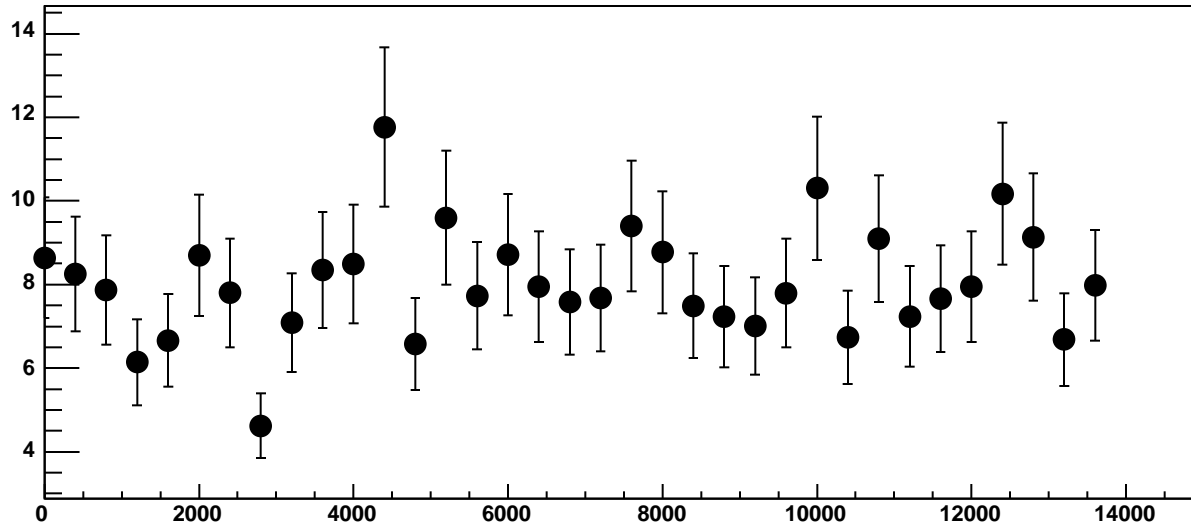


Chip 6, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

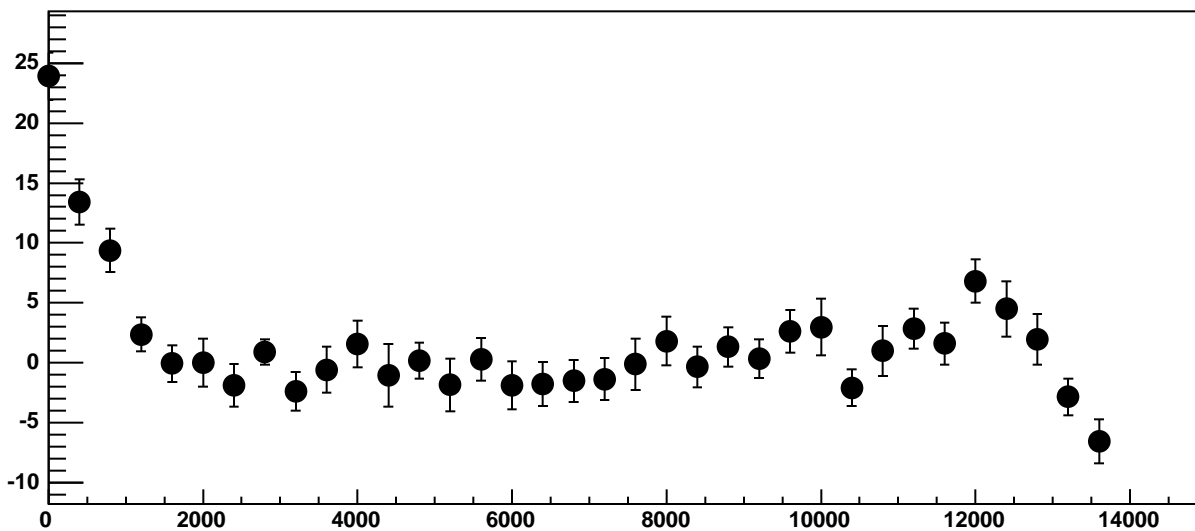


χ^2 / ndf 18.92 / 23
p0 -393.1 ± 0.7403
p1 0.01529 ± 0.0001162

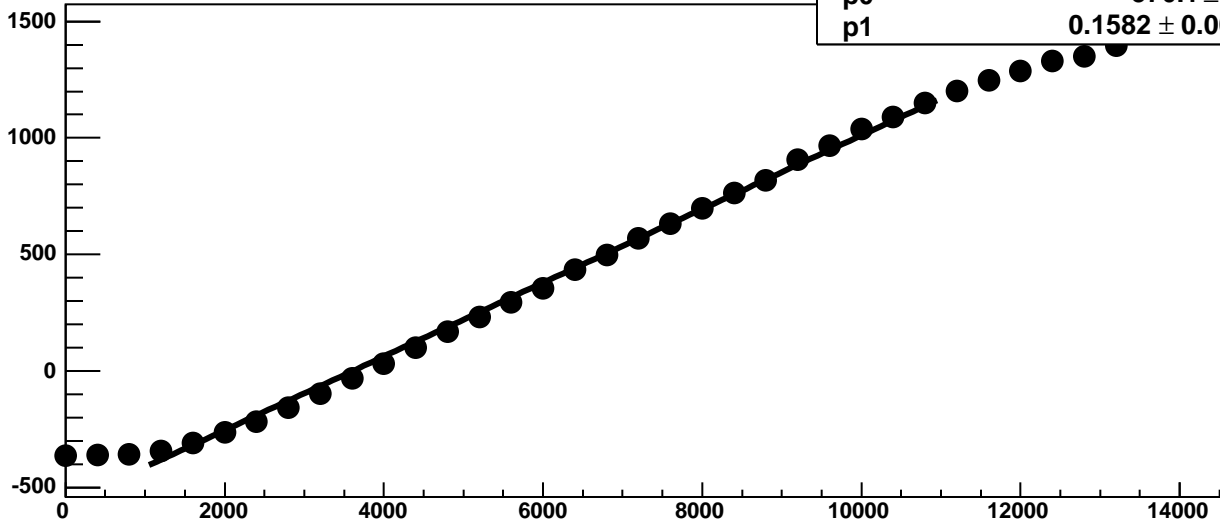
Chip 6, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



Chip 6, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

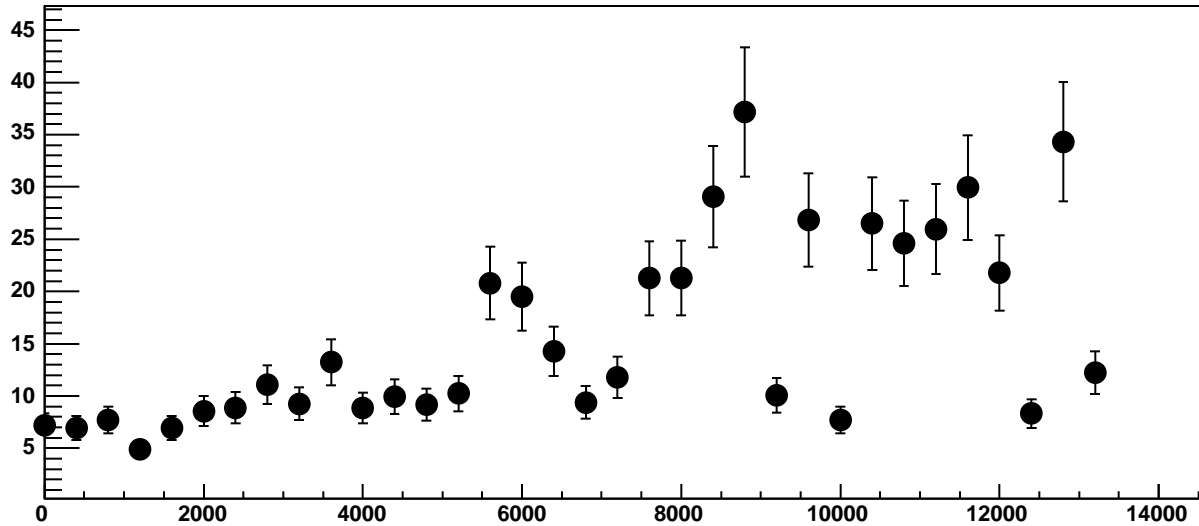


Chip 6, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

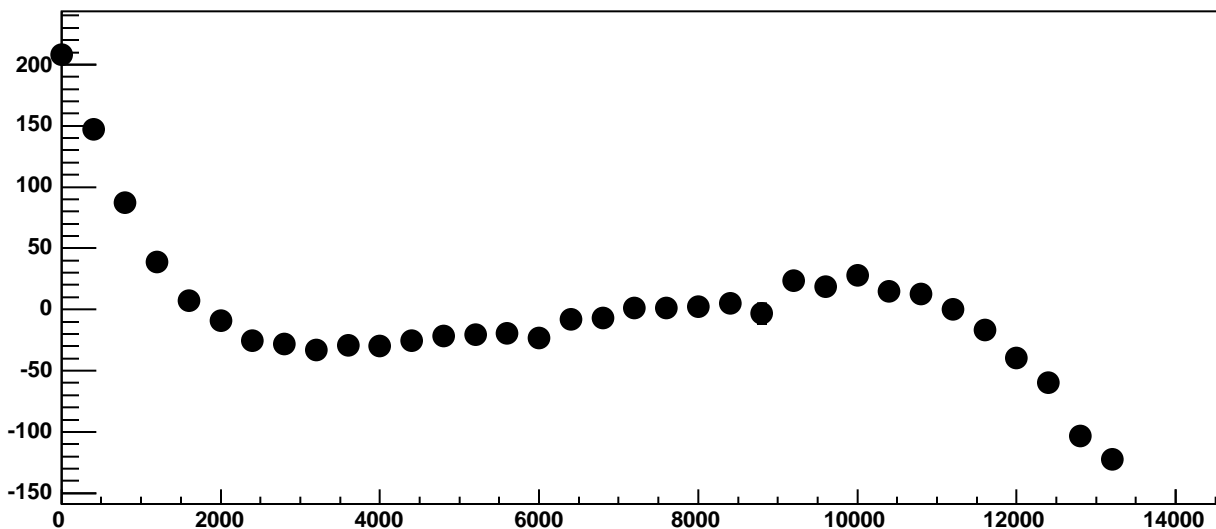


χ^2 / ndf 2794 / 23
p0 -570.1 ± 0.8636
p1 0.1582 ± 0.0001658

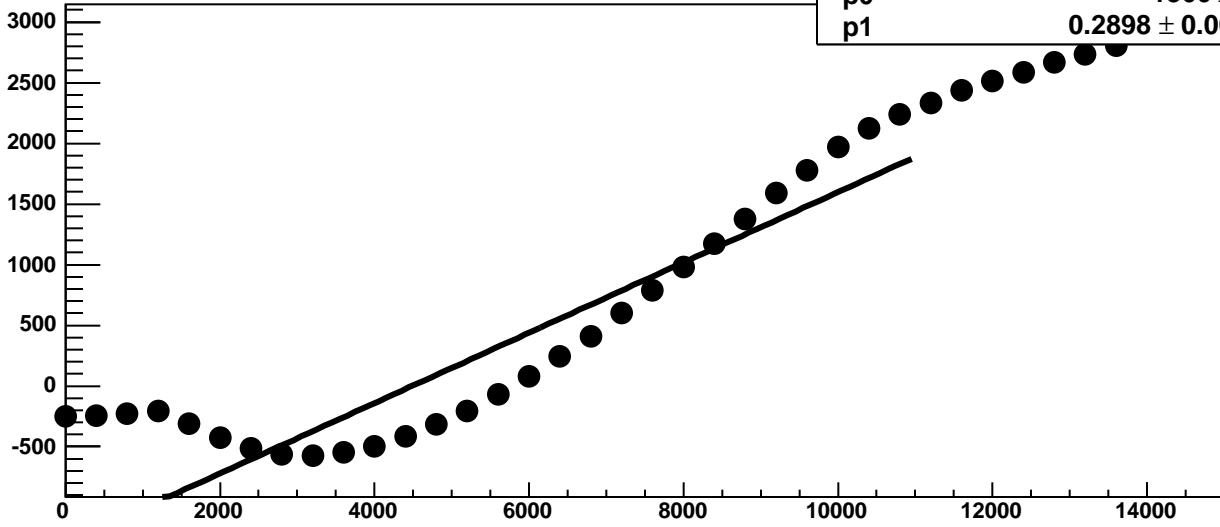
Chip 6, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



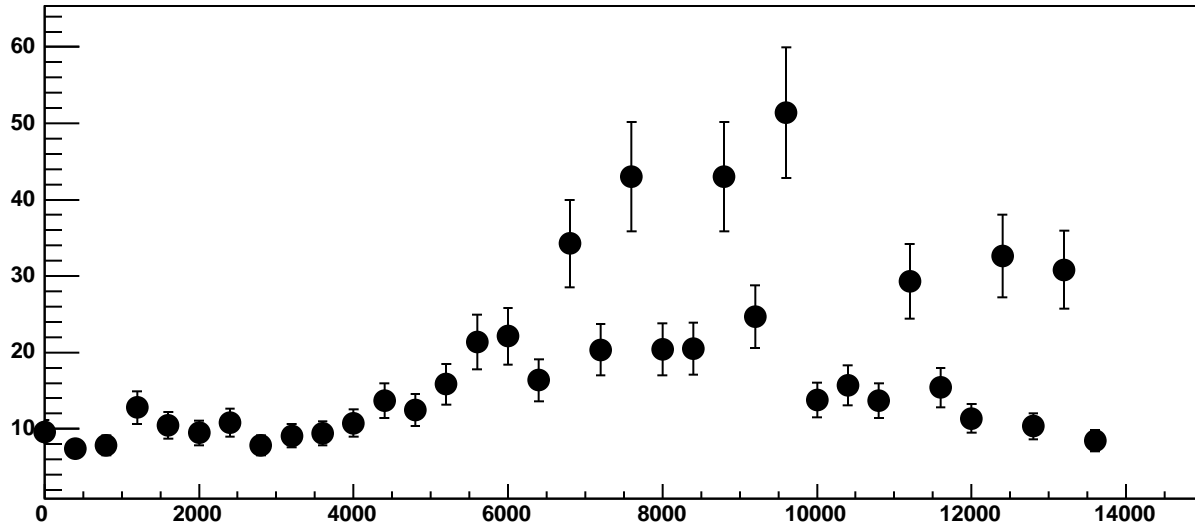
Chip 6, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



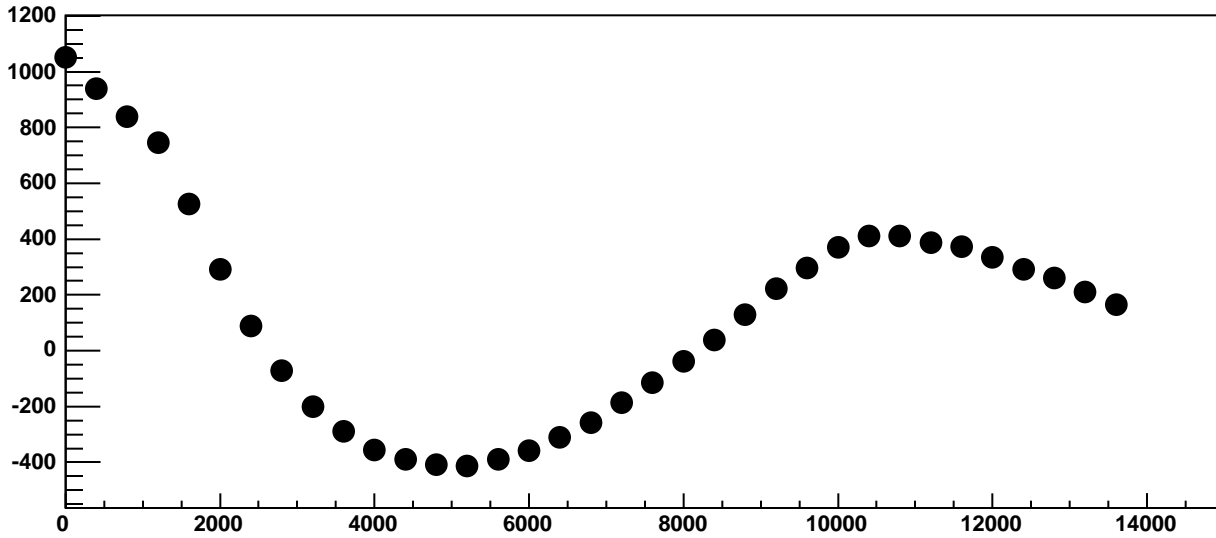
Chip 6, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC



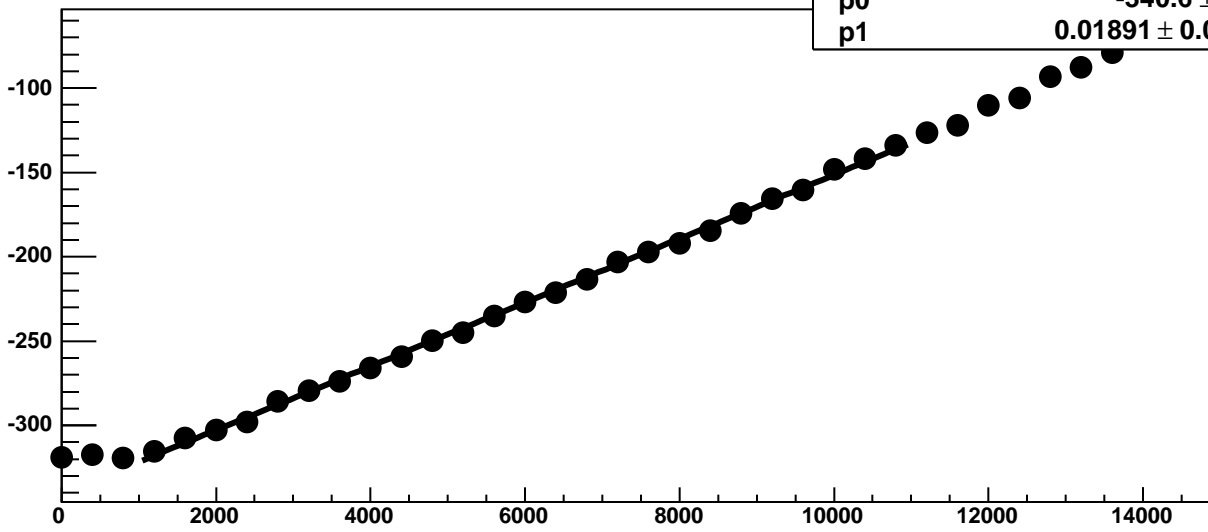
Chip 6, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC

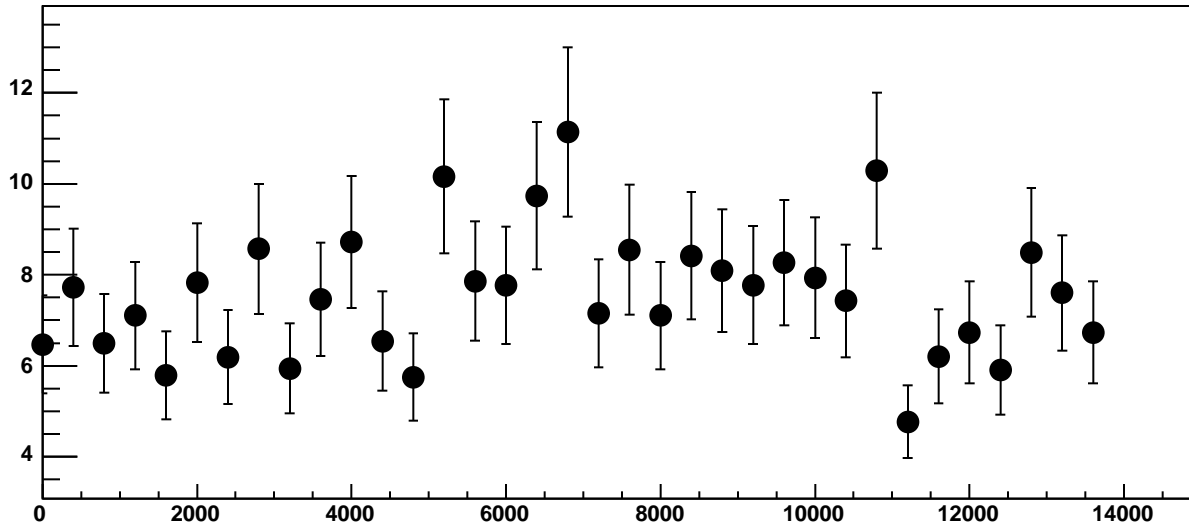


Chip 6, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

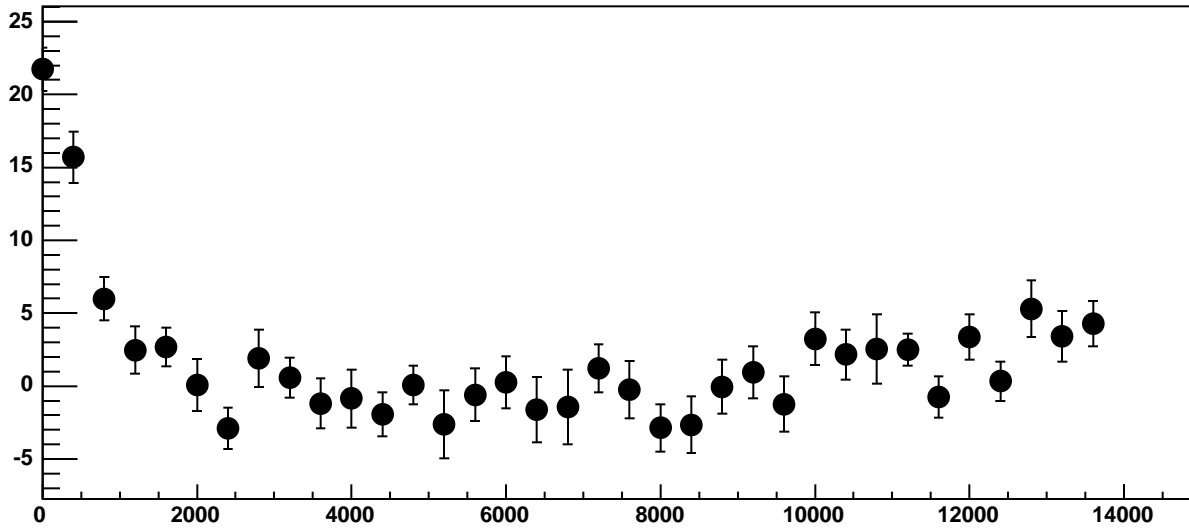


χ^2 / ndf 28.48 / 23
p0 -340.6 ± 0.7465
p1 0.01891 ± 0.0001192

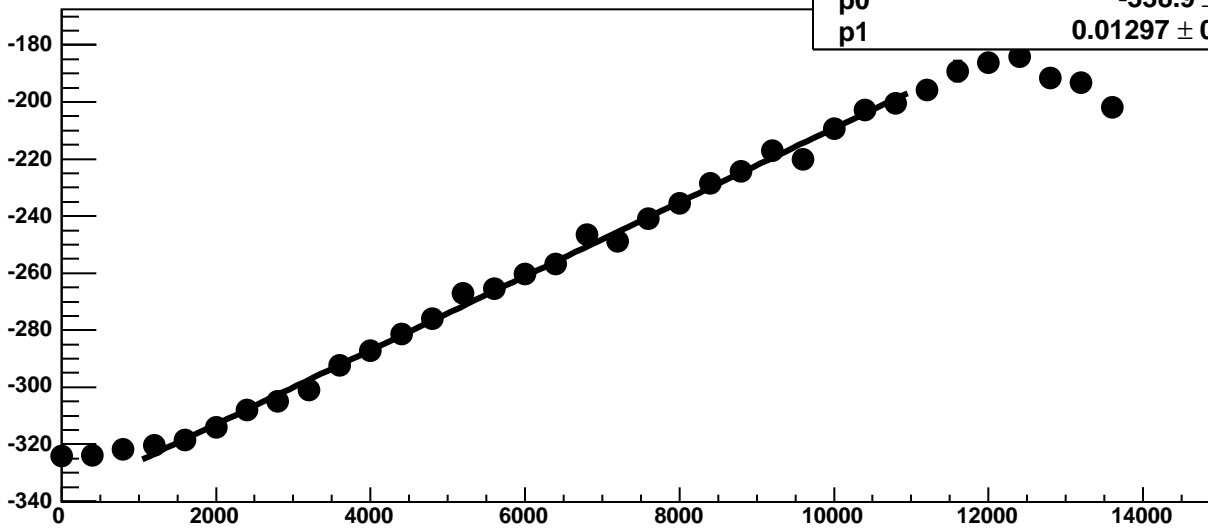
Chip 6, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



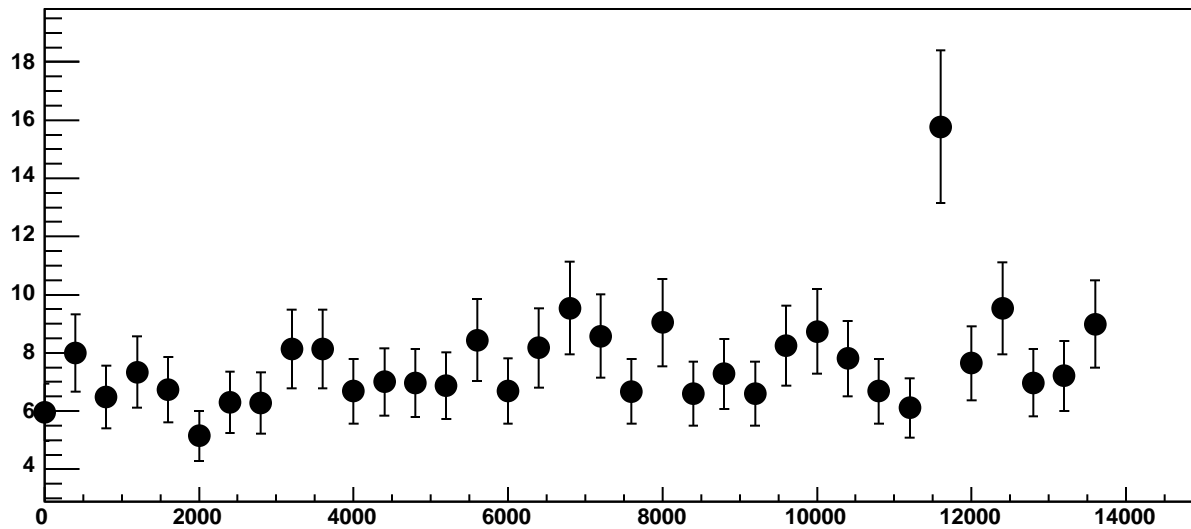
Chip 6, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



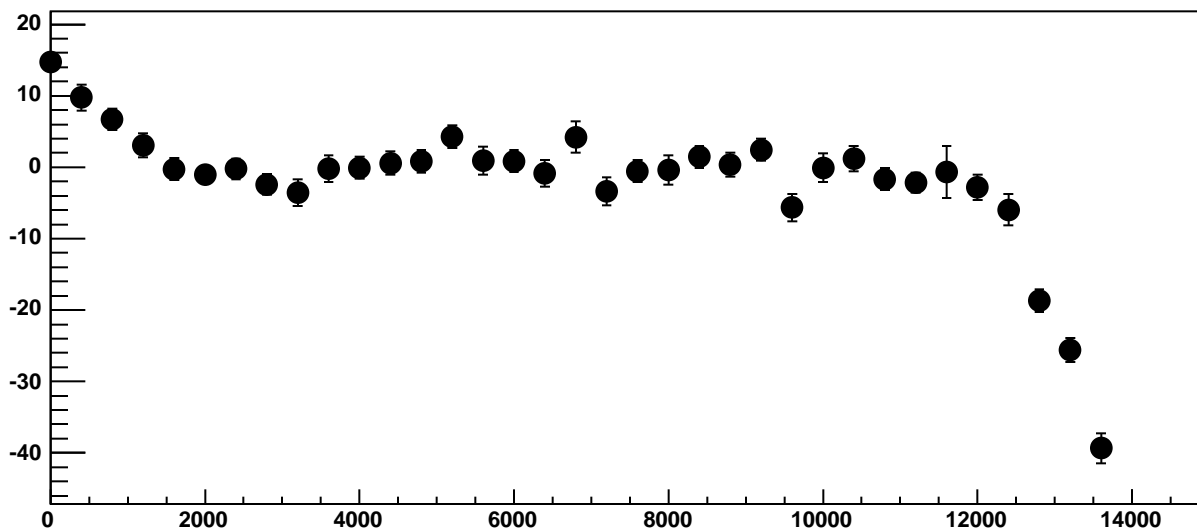
Chip 6, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



Chip 6, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

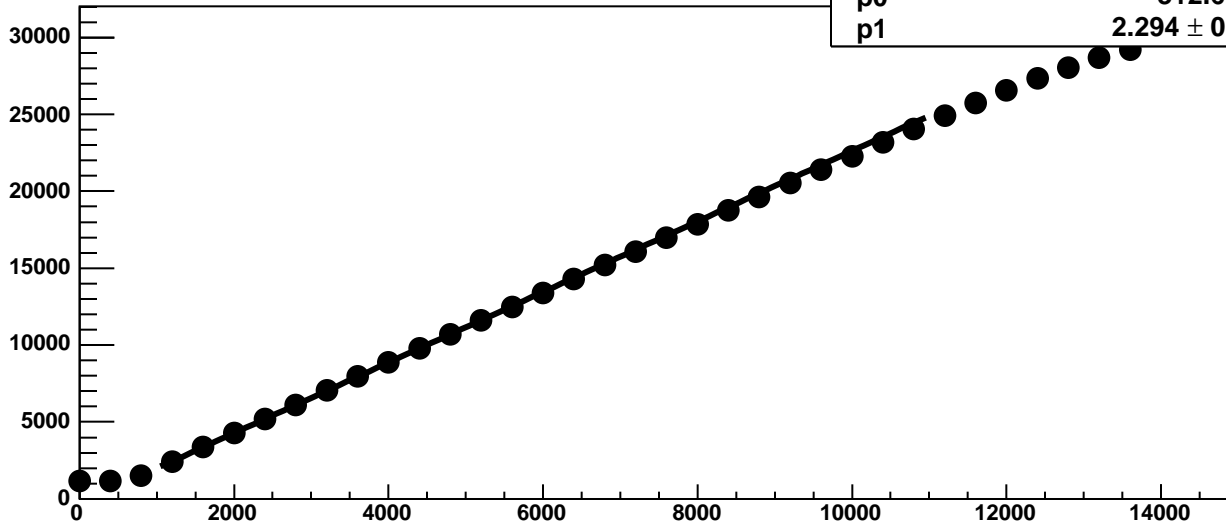


Chip 6, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

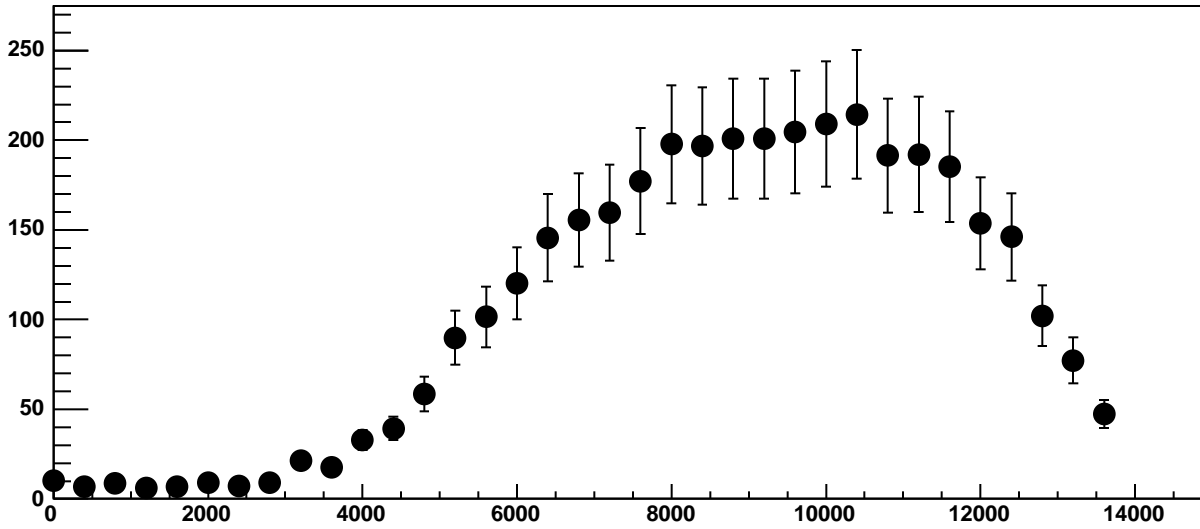


Chip 6, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC

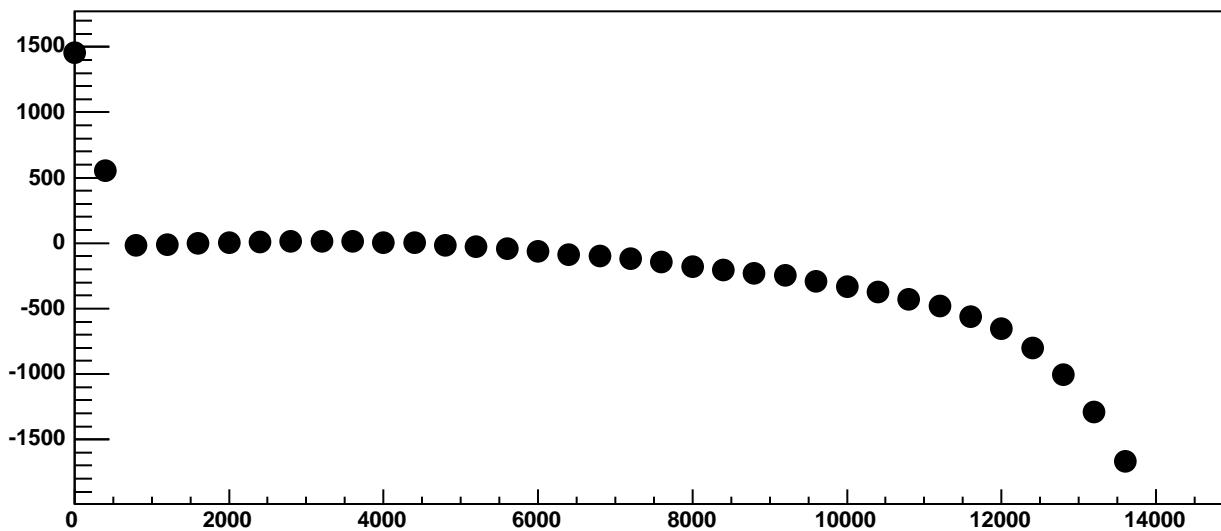
χ^2 / ndf 527.4 / 23
p0 -312.6 ± 1.878
p1 2.294 ± 0.000856



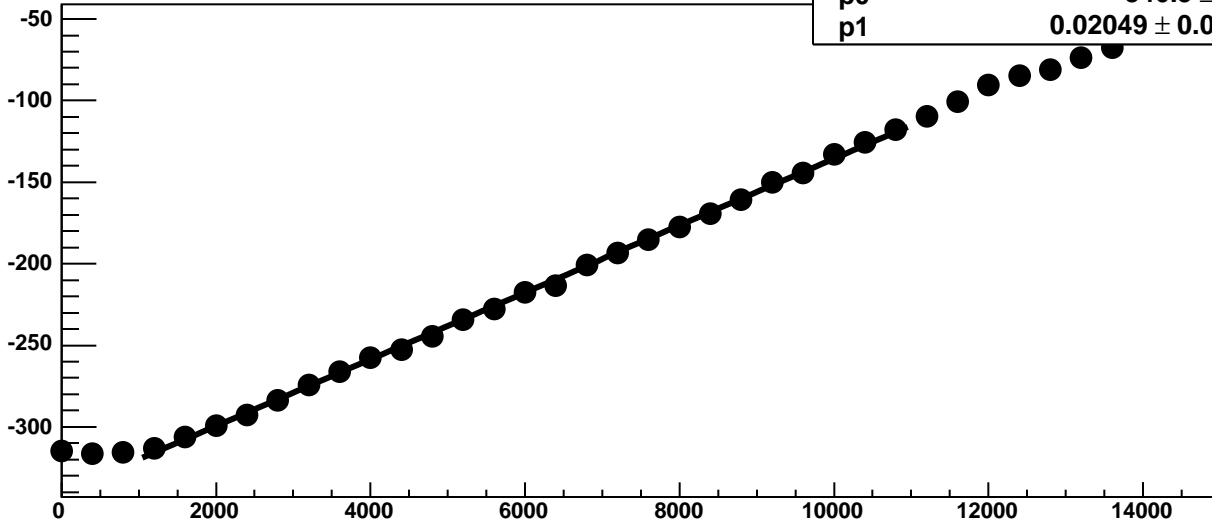
Chip 6, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC

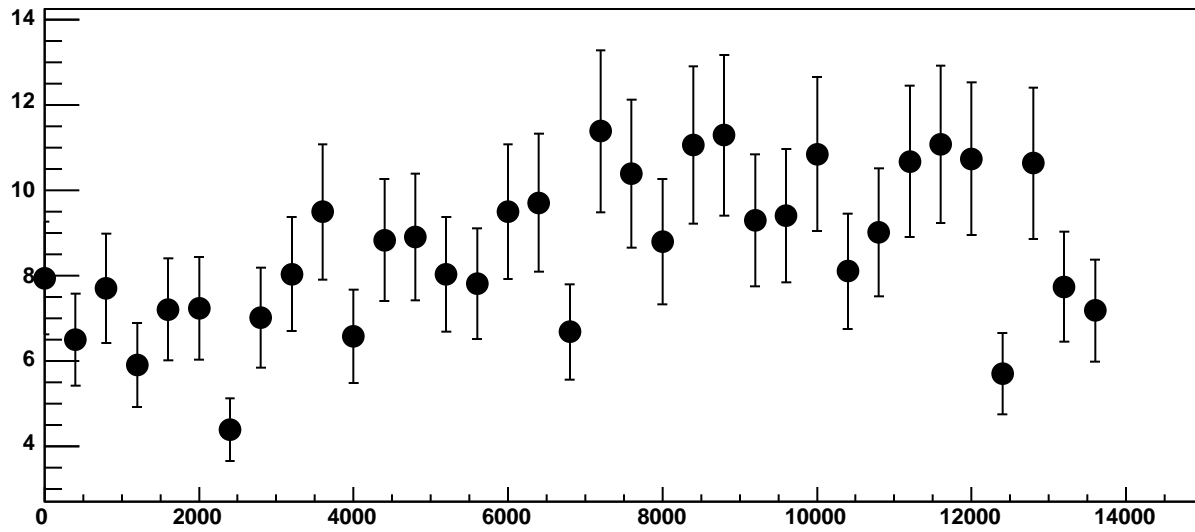


Chip 6, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

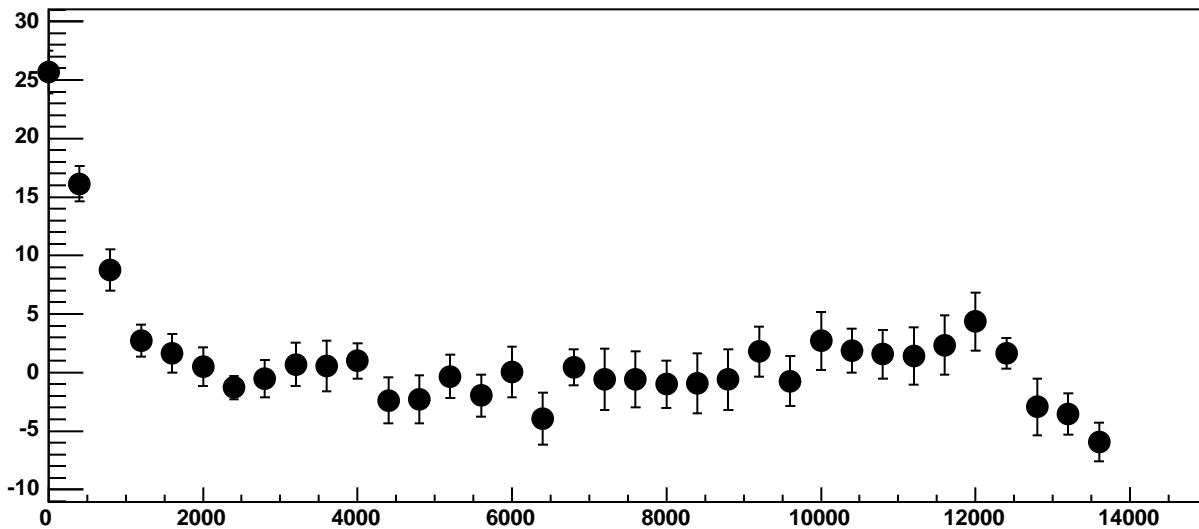


χ^2 / ndf 18.8 / 23
p0 -340.5 ± 0.7275
p1 0.02049 ± 0.0001244

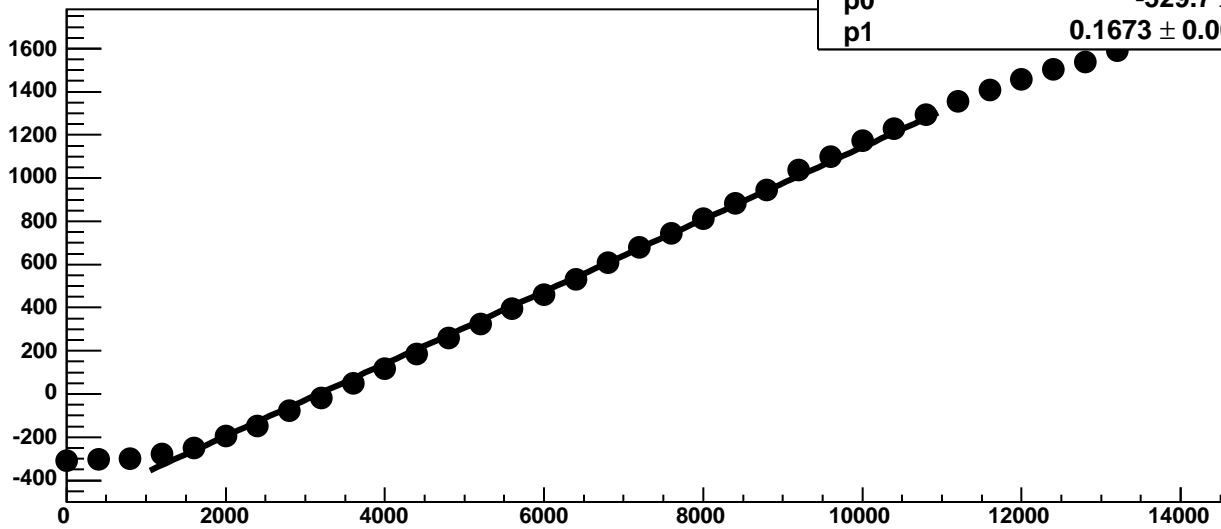
Chip 6, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



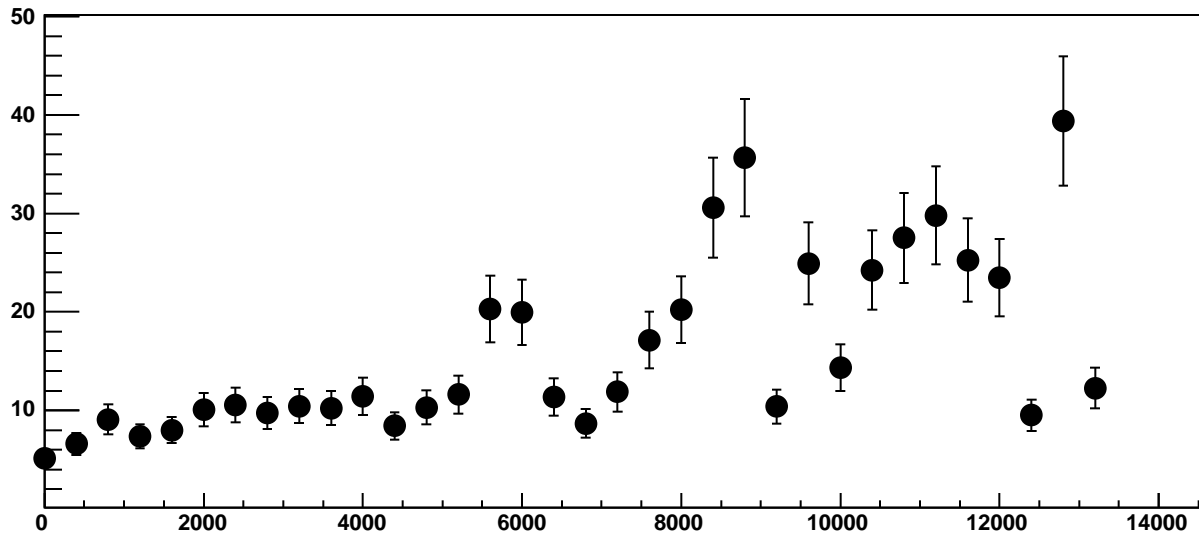
Chip 6, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



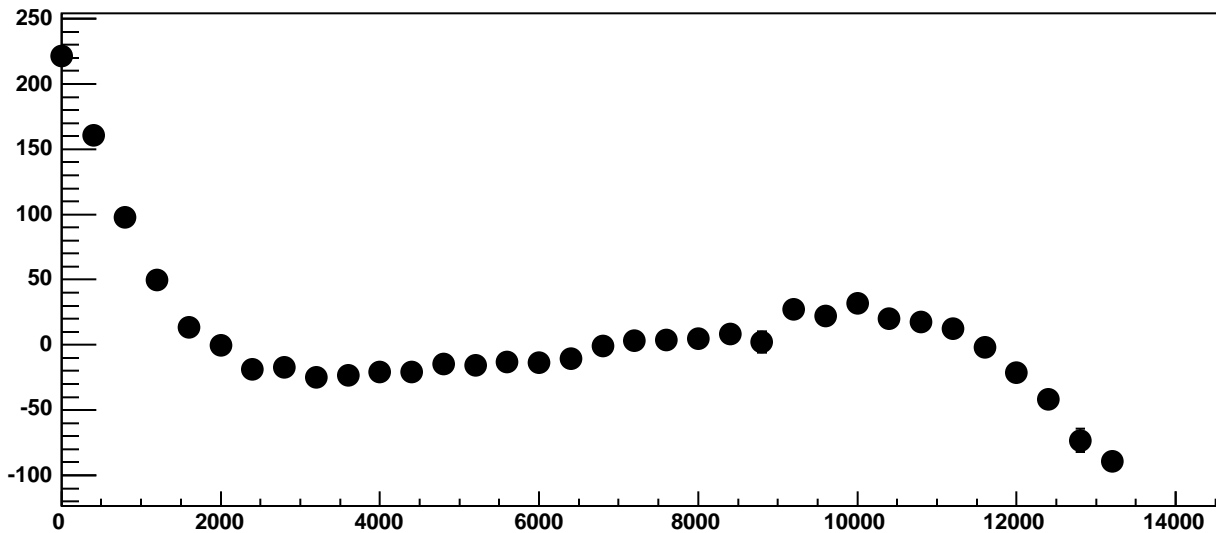
Chip 6, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



Chip 6, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

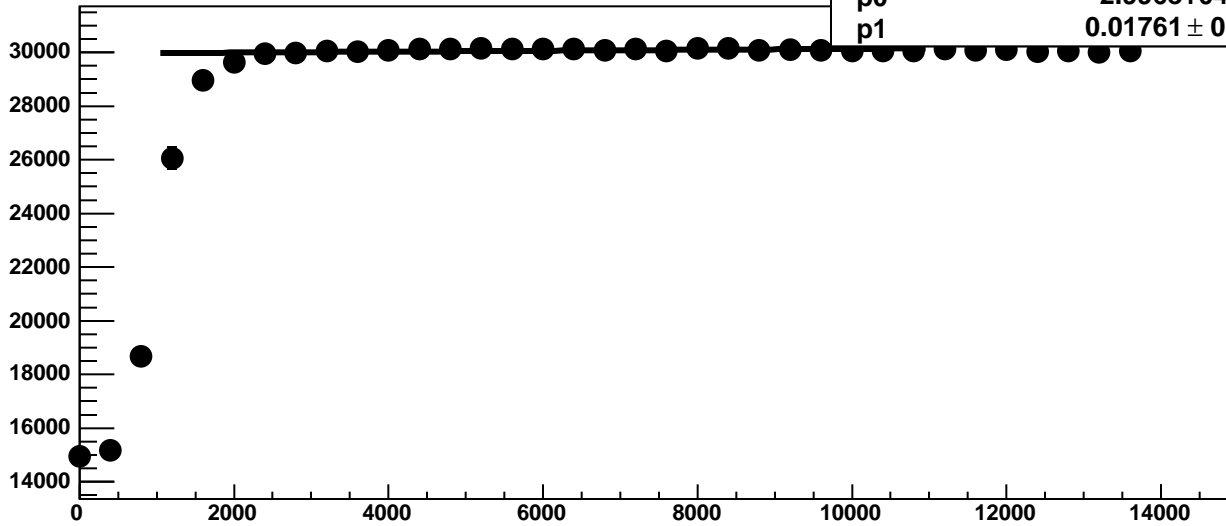


Chip 6, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

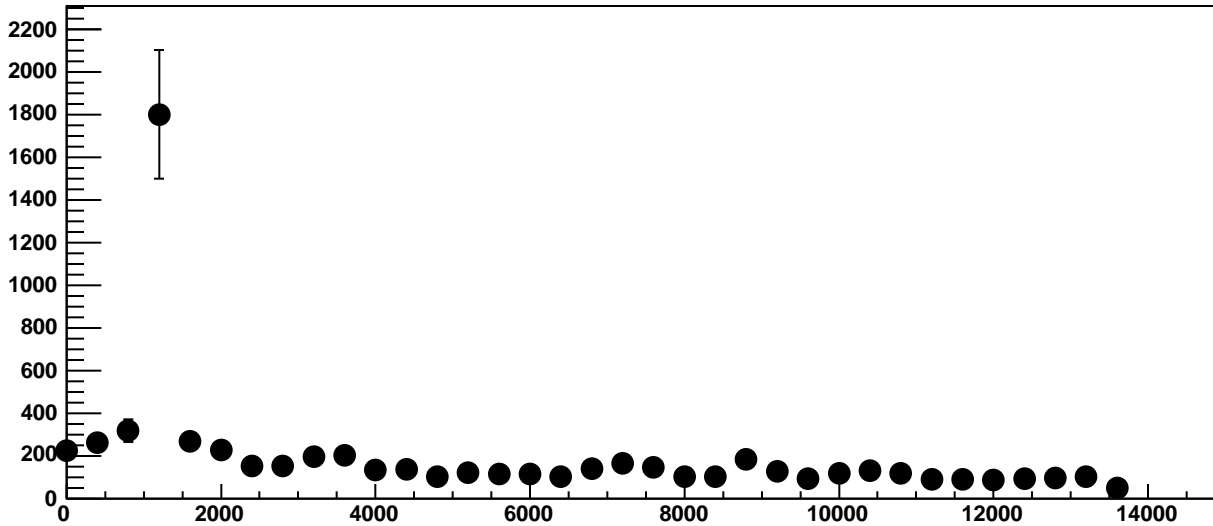


Chip 6, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC

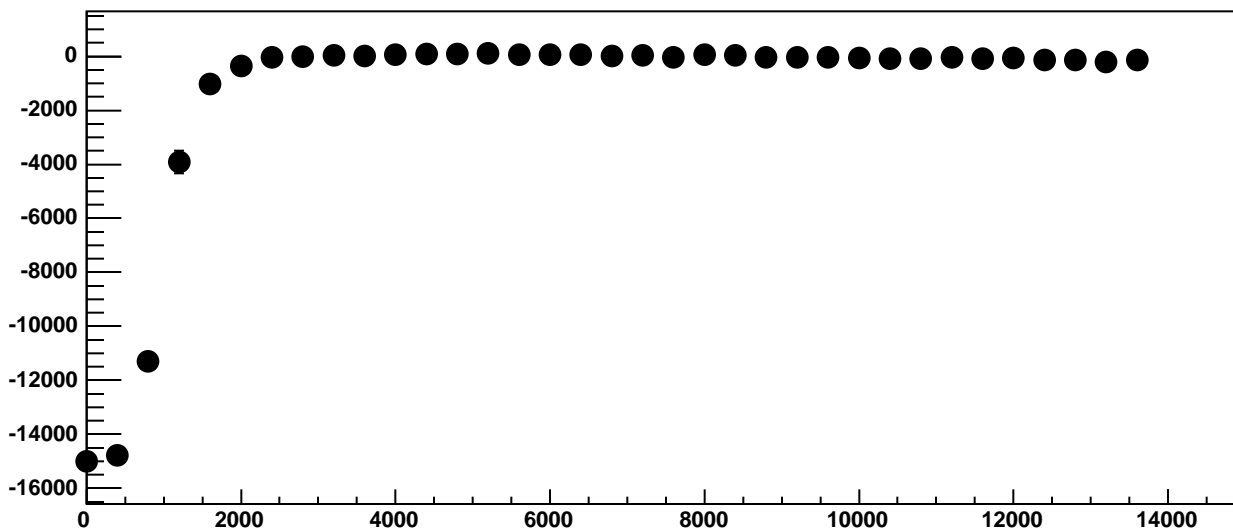
χ^2 / ndf 509.6 / 23
p0 $2.996\text{e}+04 \pm 18.11$
p1 0.01761 ± 0.002473



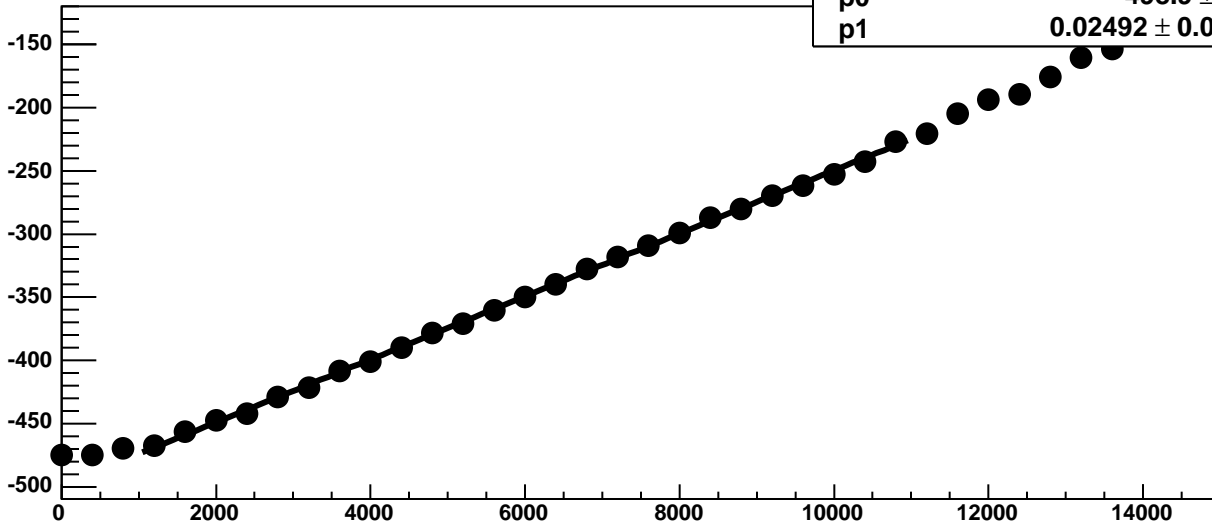
Chip 6, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC

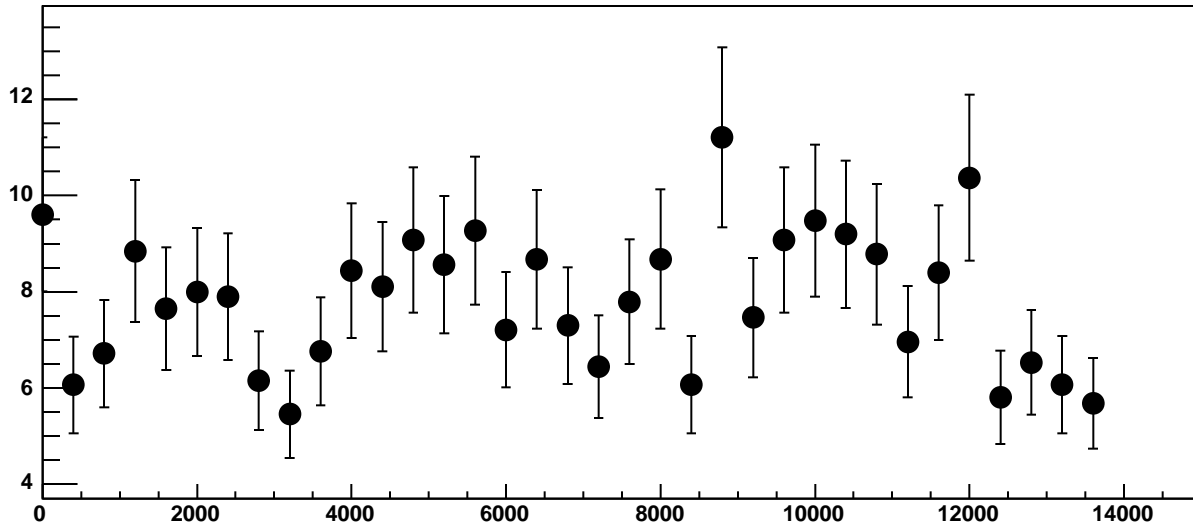


Chip 6, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

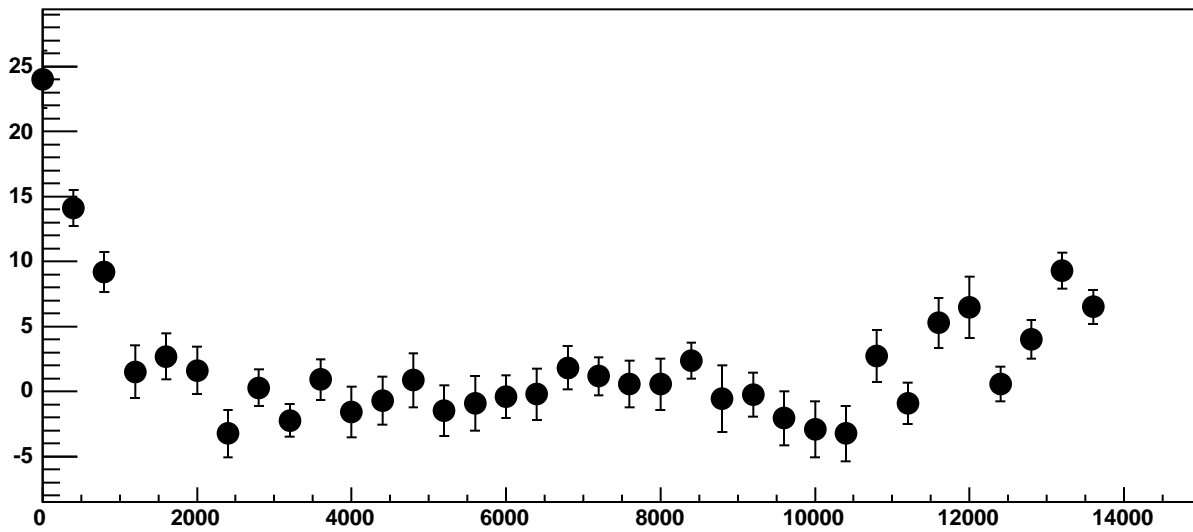


χ^2 / ndf 24.15 / 23
p0 -498.9 ± 0.8097
p1 0.02492 ± 0.0001274

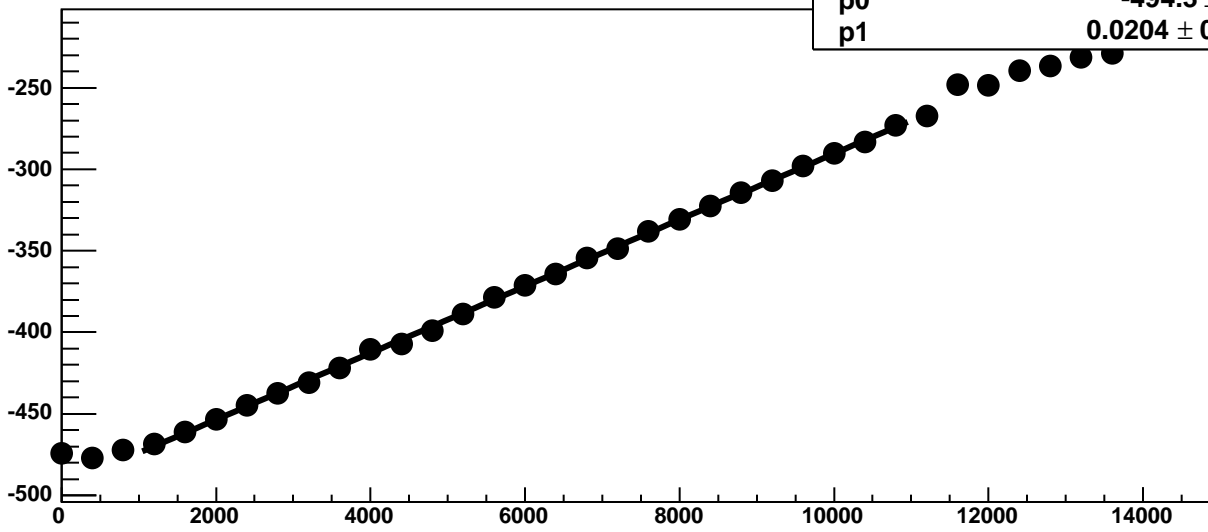
Chip 6, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



Chip 6, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

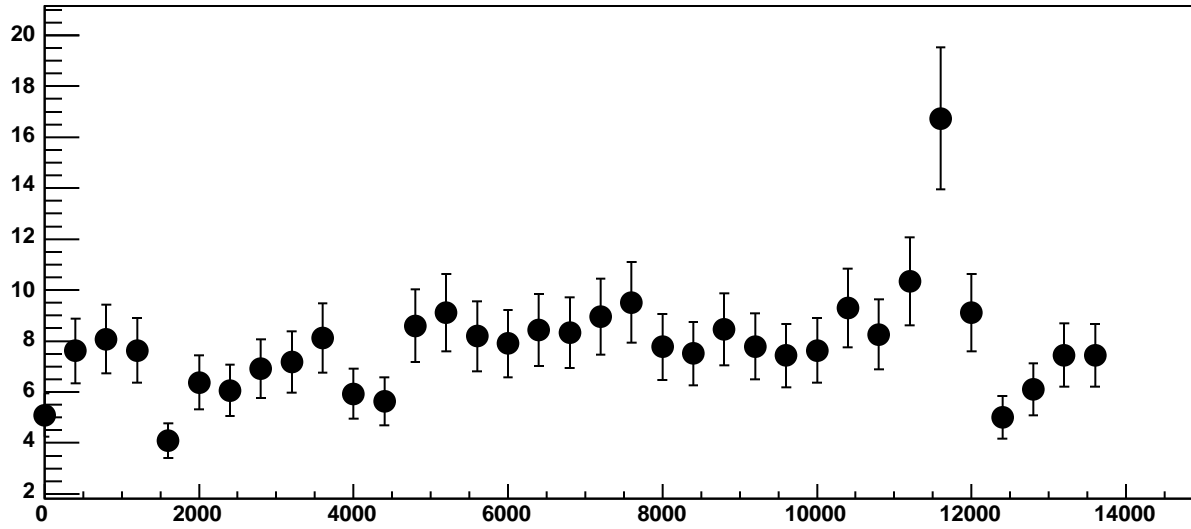


Chip 6, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

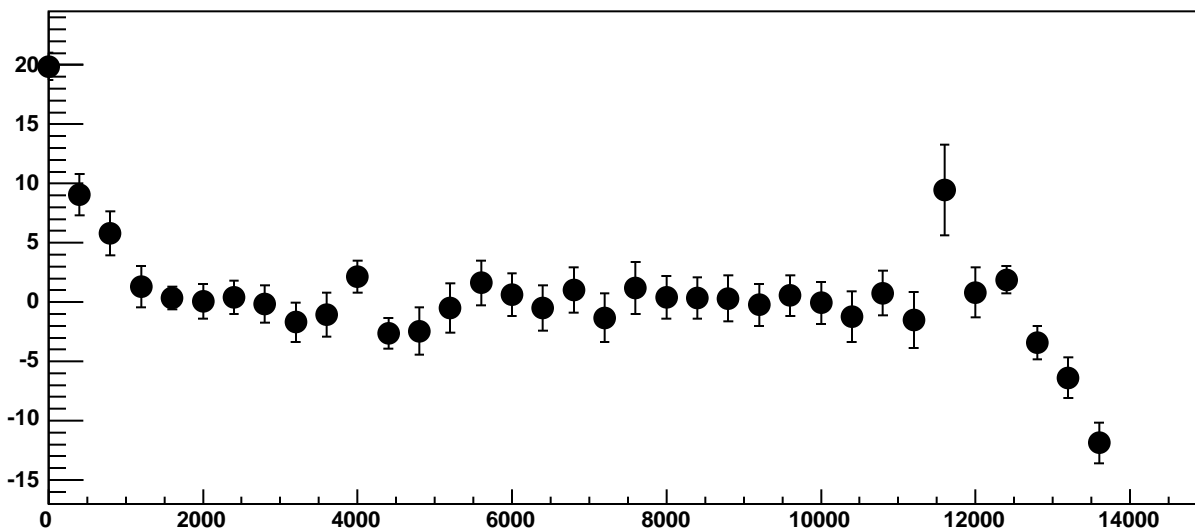


χ^2 / ndf 13.13 / 23
p0 -494.3 ± 0.6691
p1 0.0204 ± 0.000111

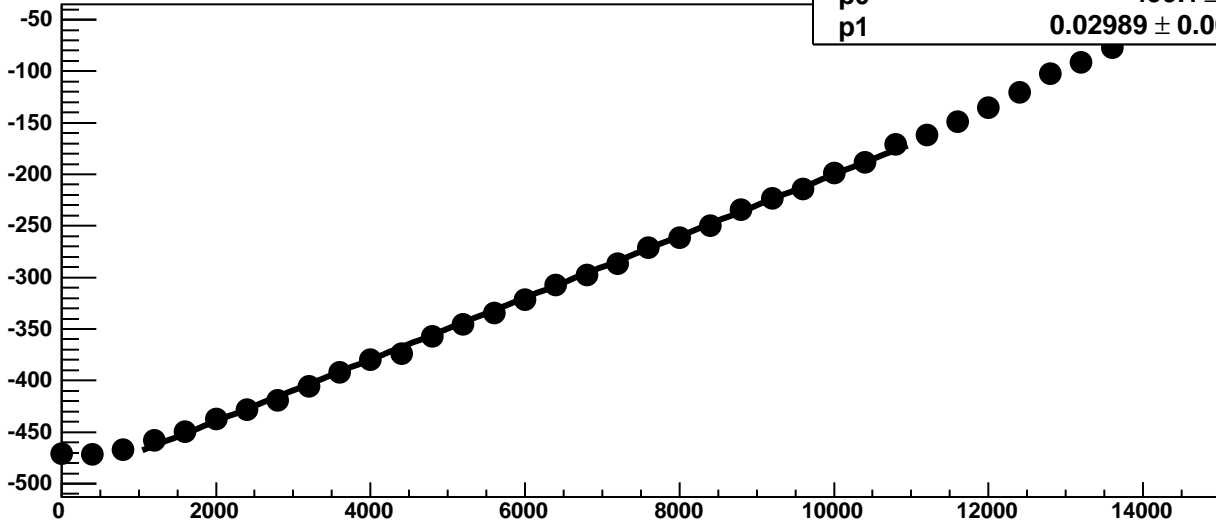
Chip 6, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

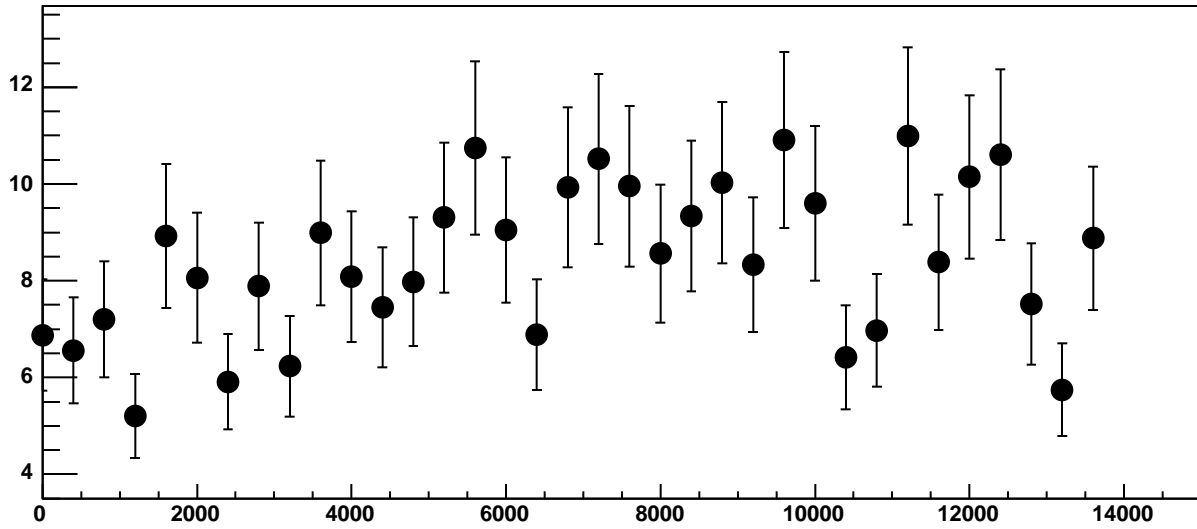


Chip 6, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

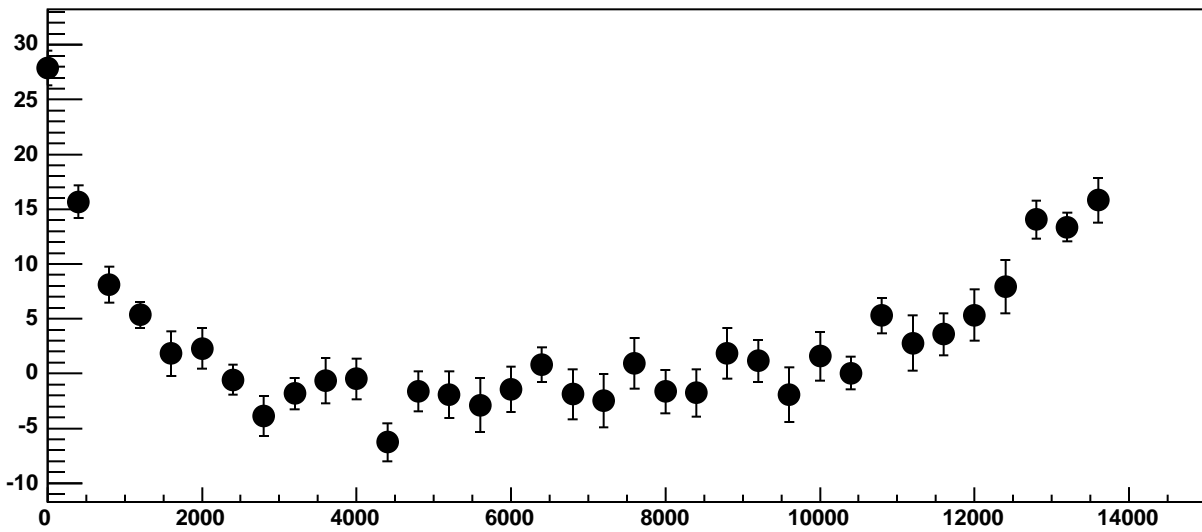


χ^2 / ndf 62.63 / 23
p0 -499.1 ± 0.7441
p1 0.02989 ± 0.0001174

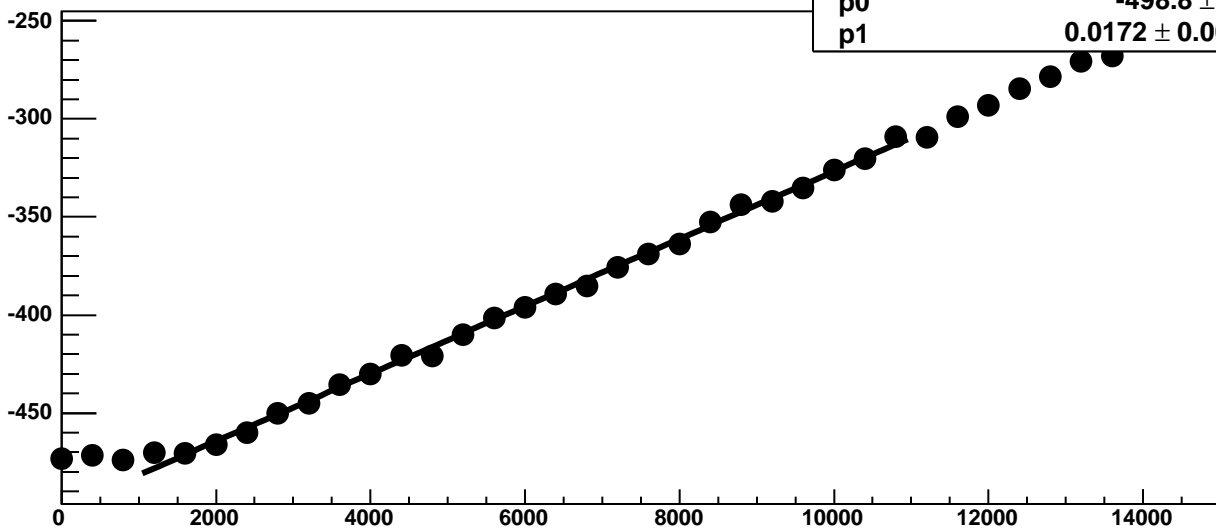
Chip 6, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



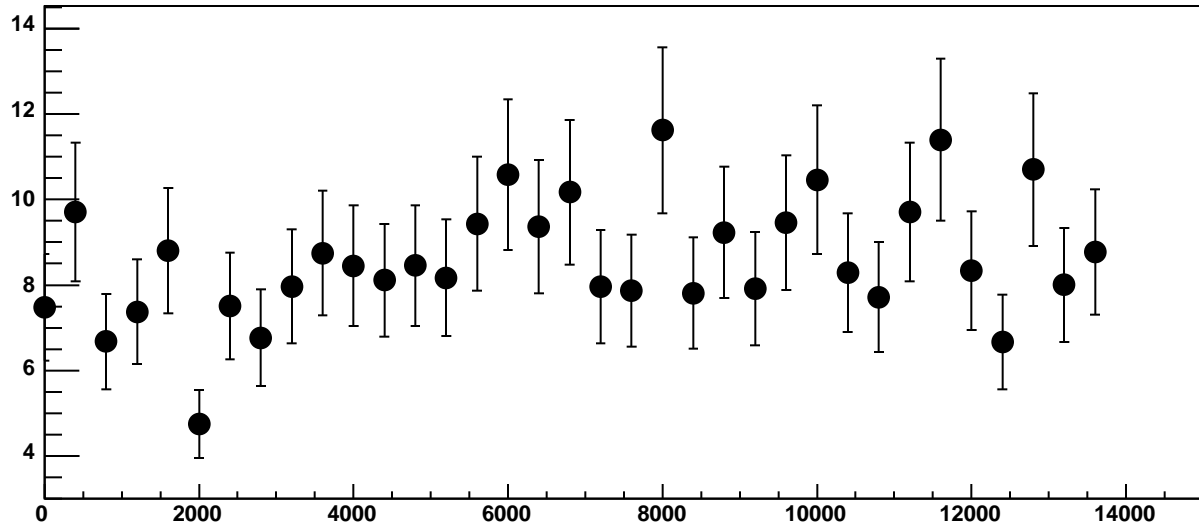
Chip 6, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC



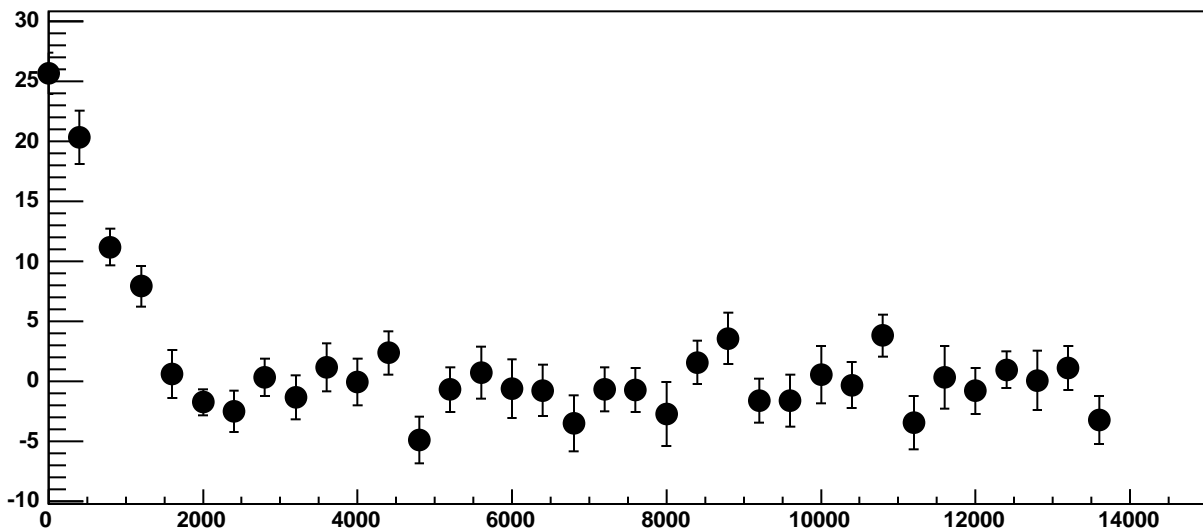
Chip 6, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



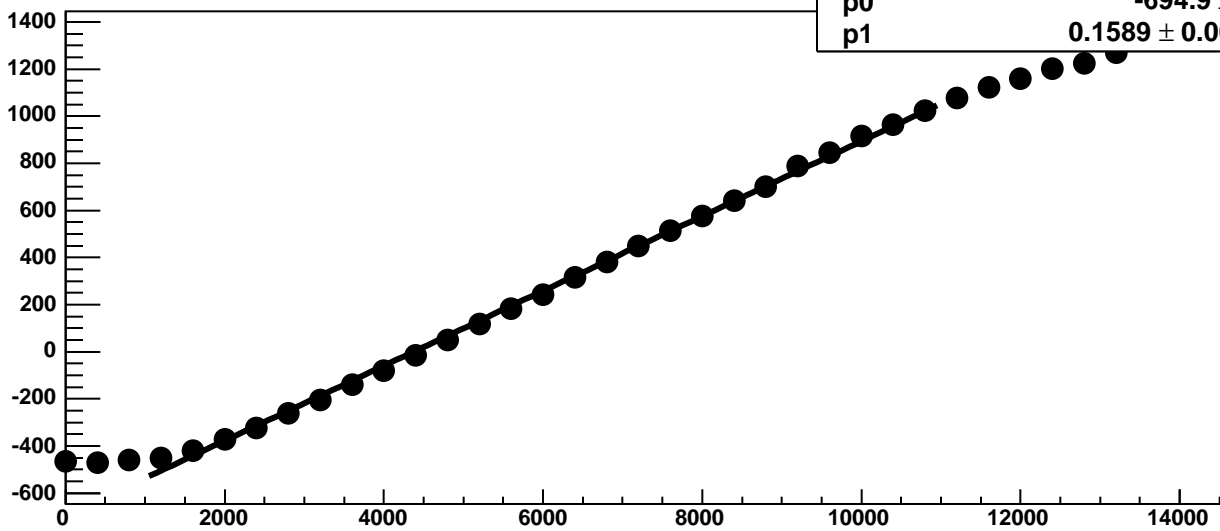
Chip 6, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



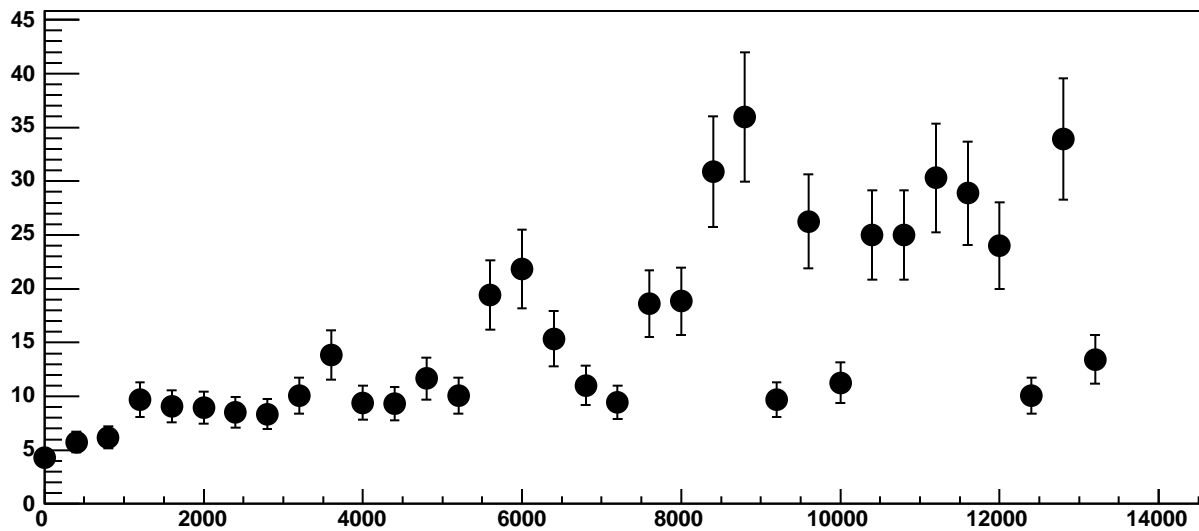
Chip 6, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



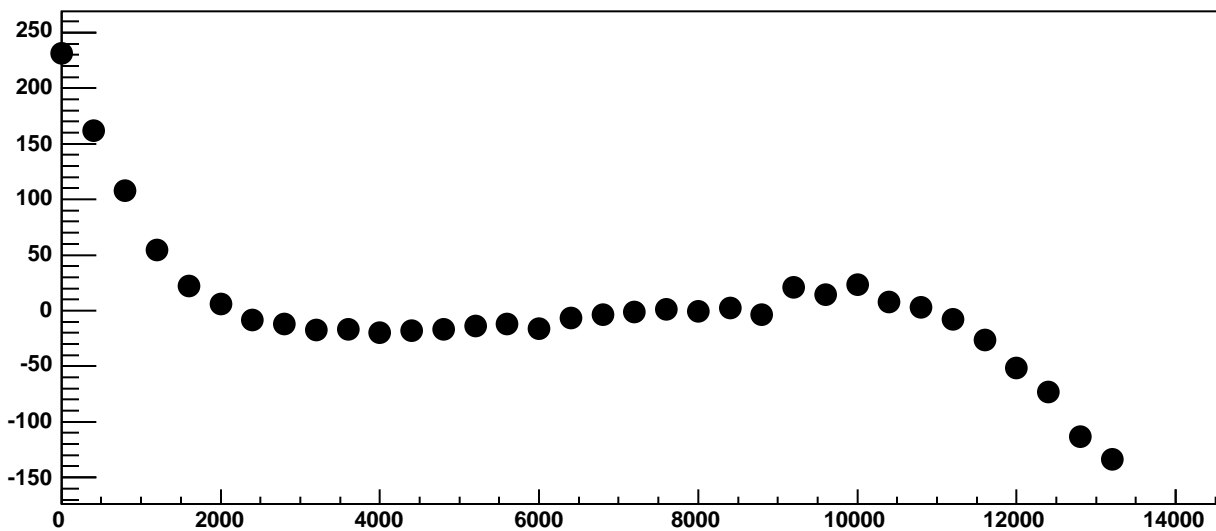
Chip 6, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



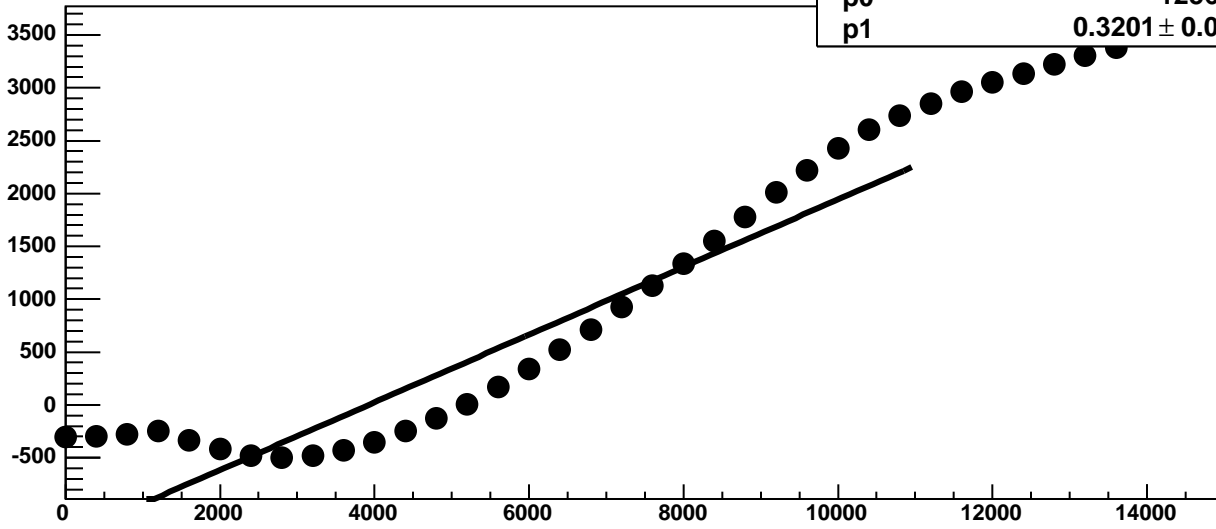
Chip 6, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

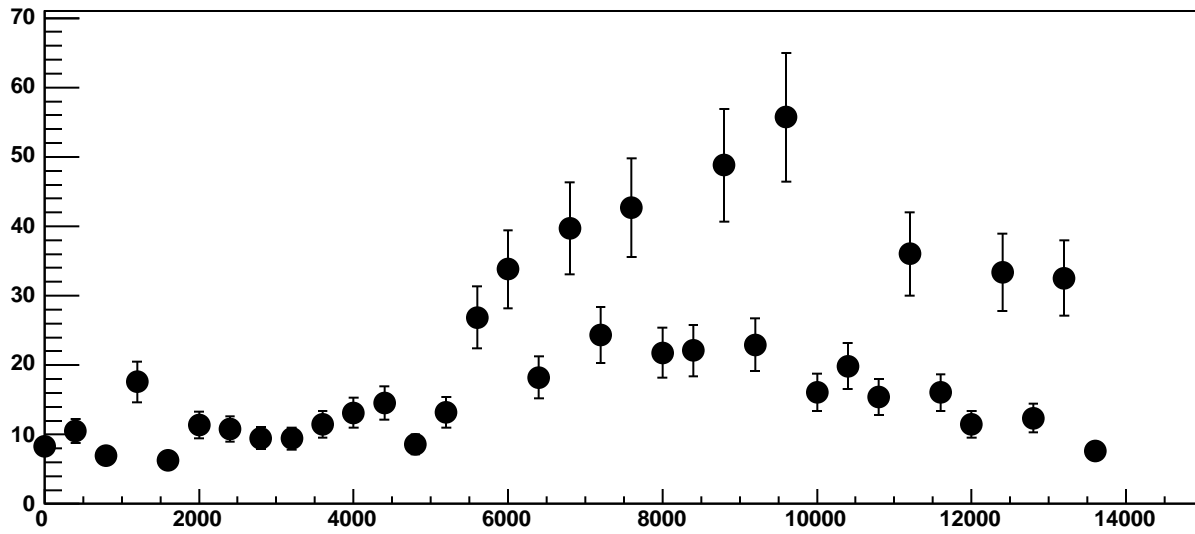


Chip 6, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

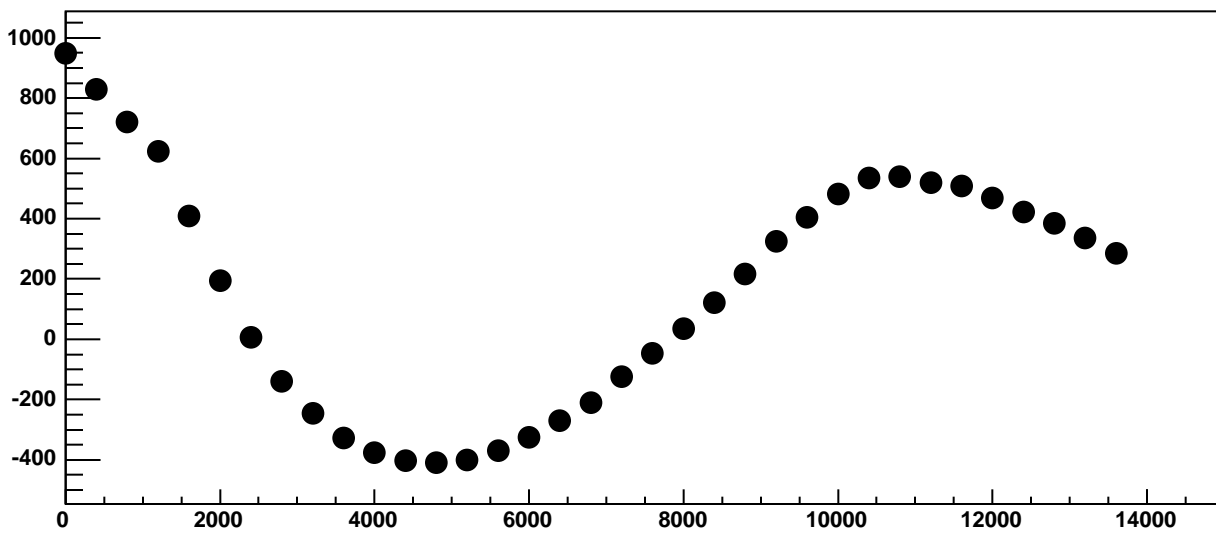


χ^2 / ndf 3.036e+05 / 23
p0 -1256 ± 1.18
p1 0.3201 ± 0.0002416

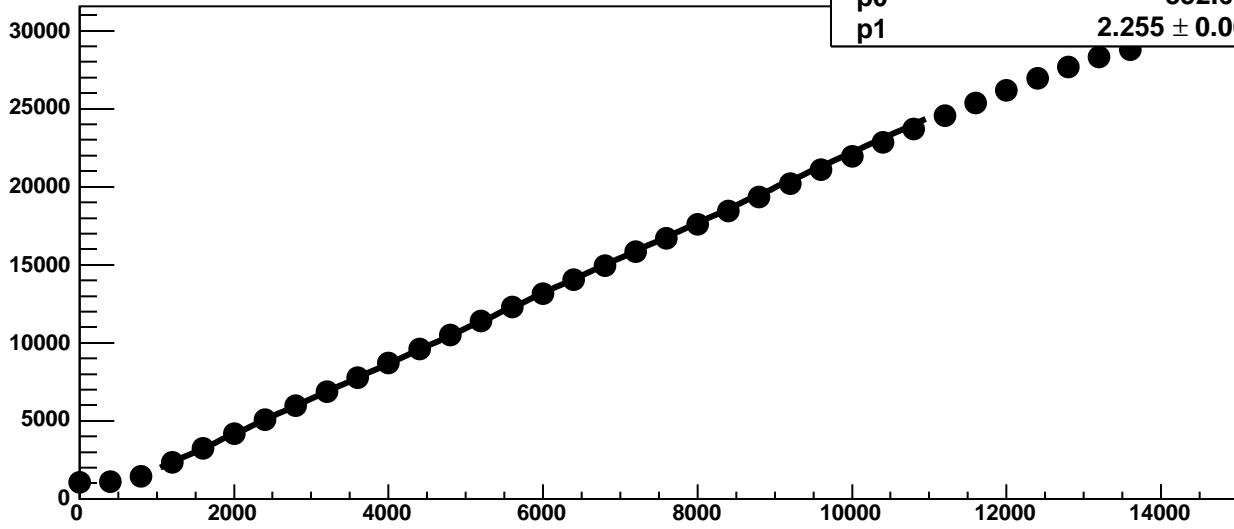
Chip 6, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 6, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

407 / 23

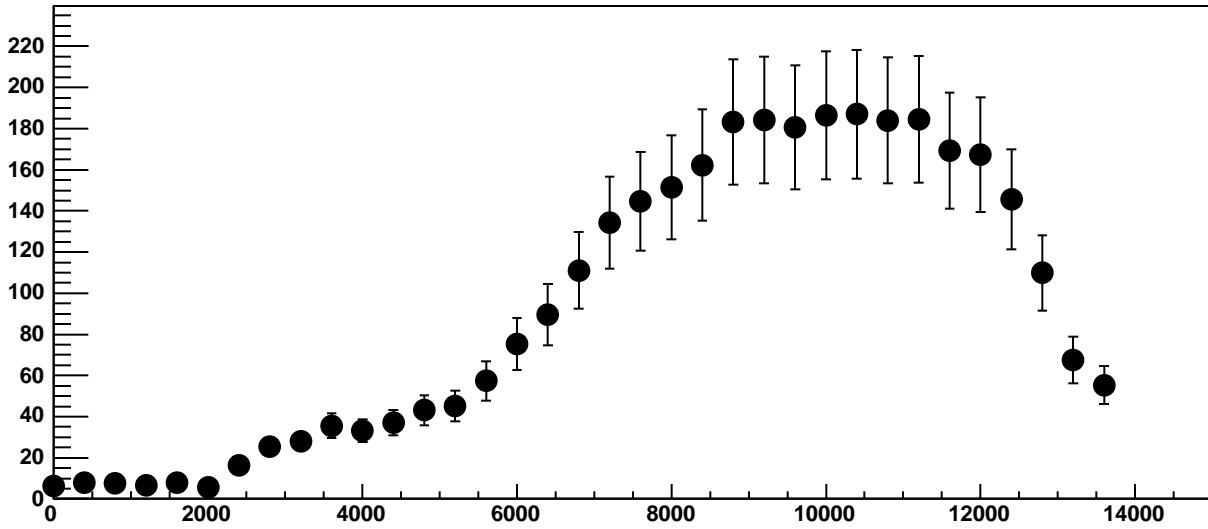
p0

-352.6 ± 1.88

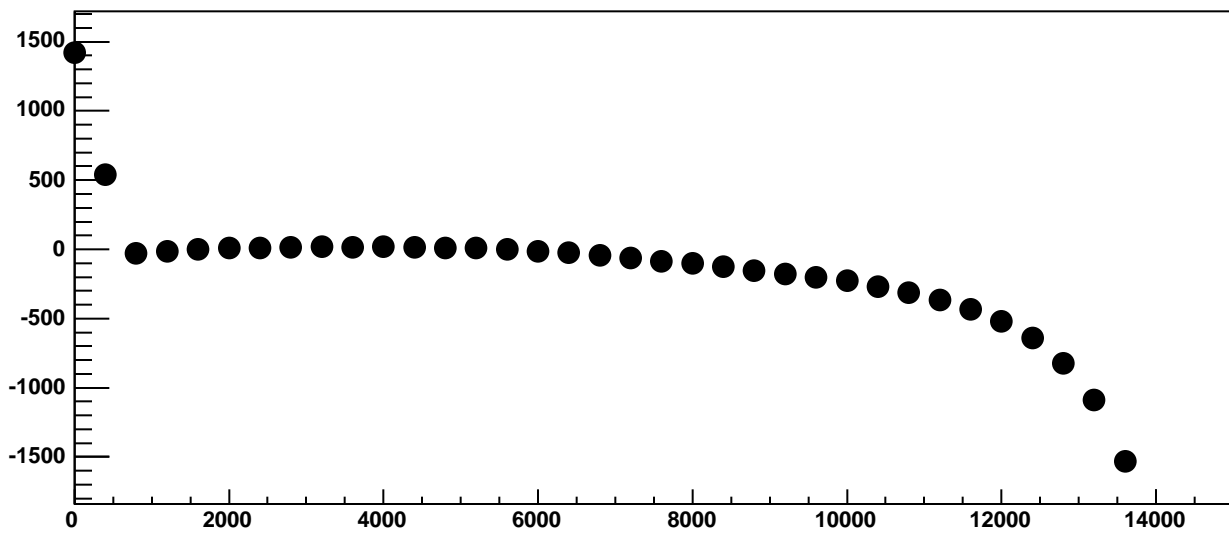
p1

2.255 ± 0.0008867

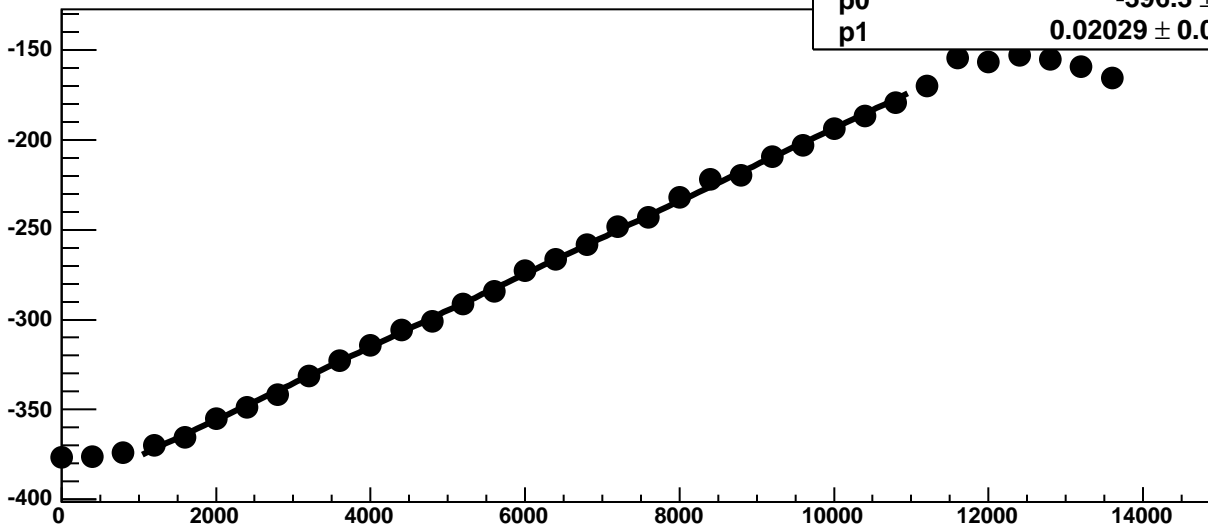
Chip 6, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



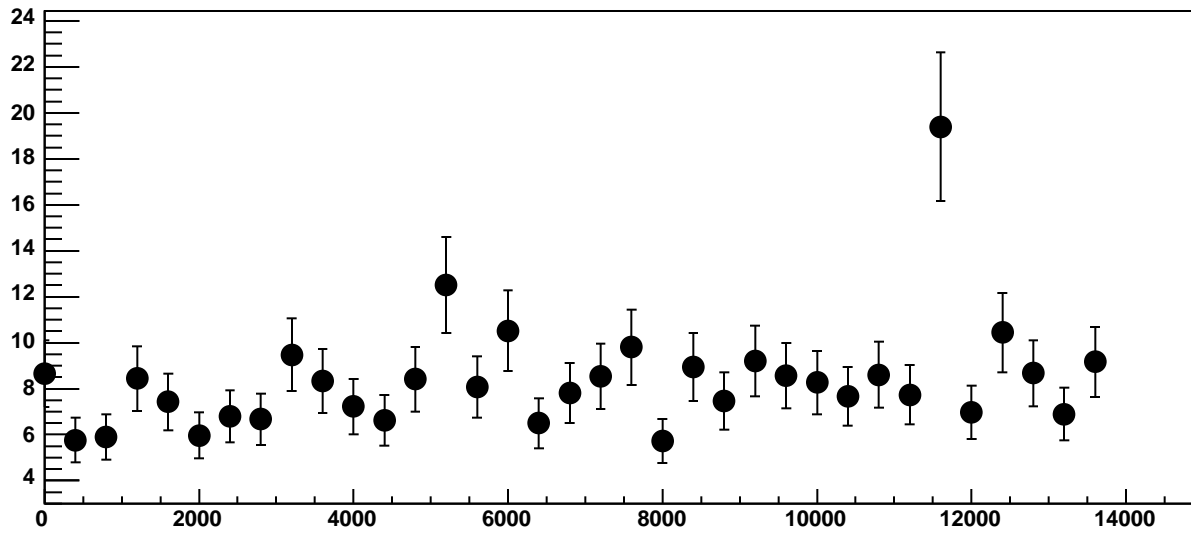
Chip 6, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC



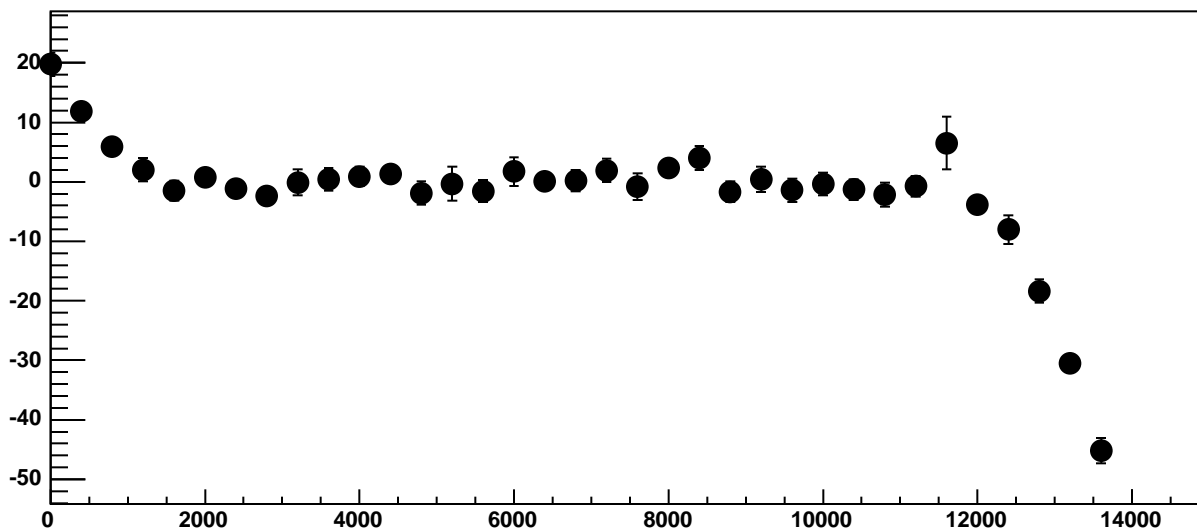
Chip 6, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



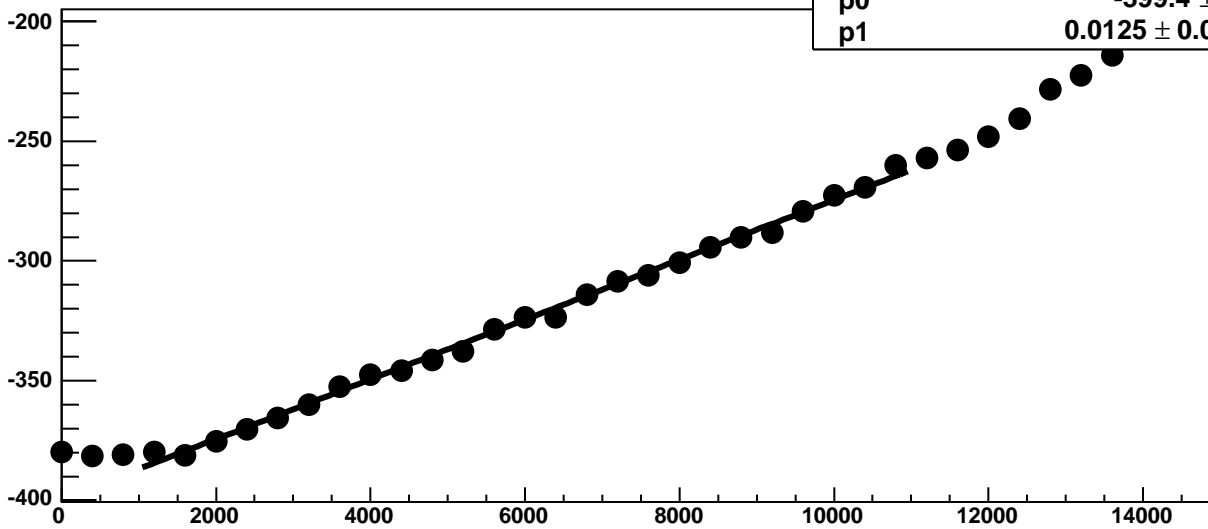
Chip 6, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



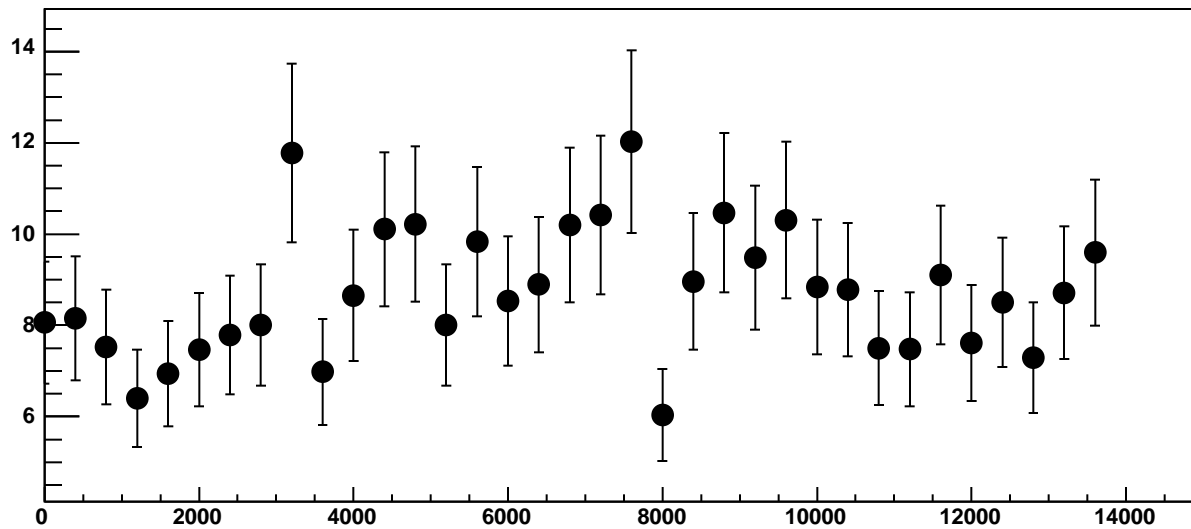
Chip 6, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



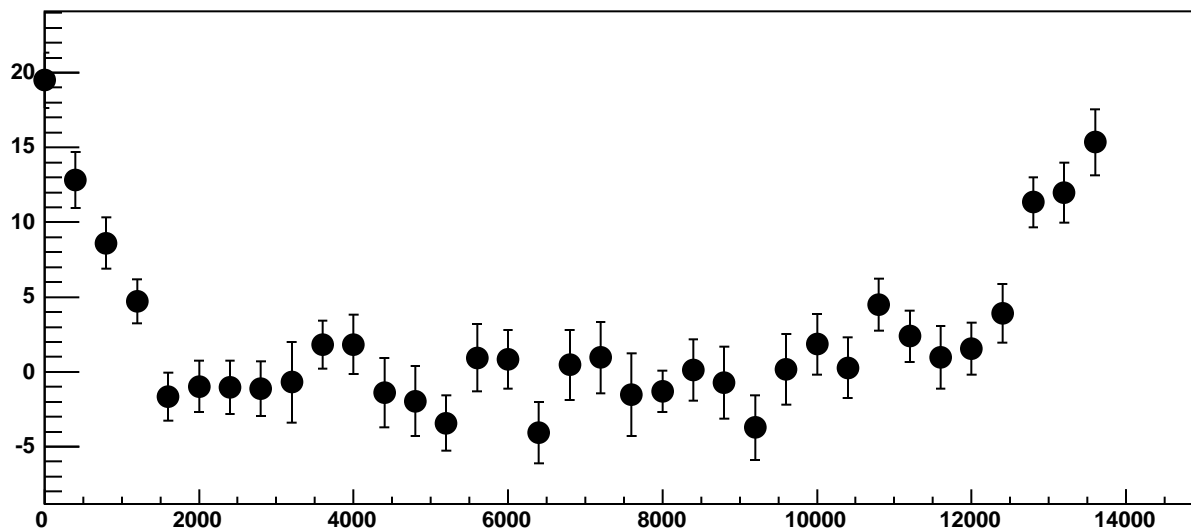
Chip 6, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



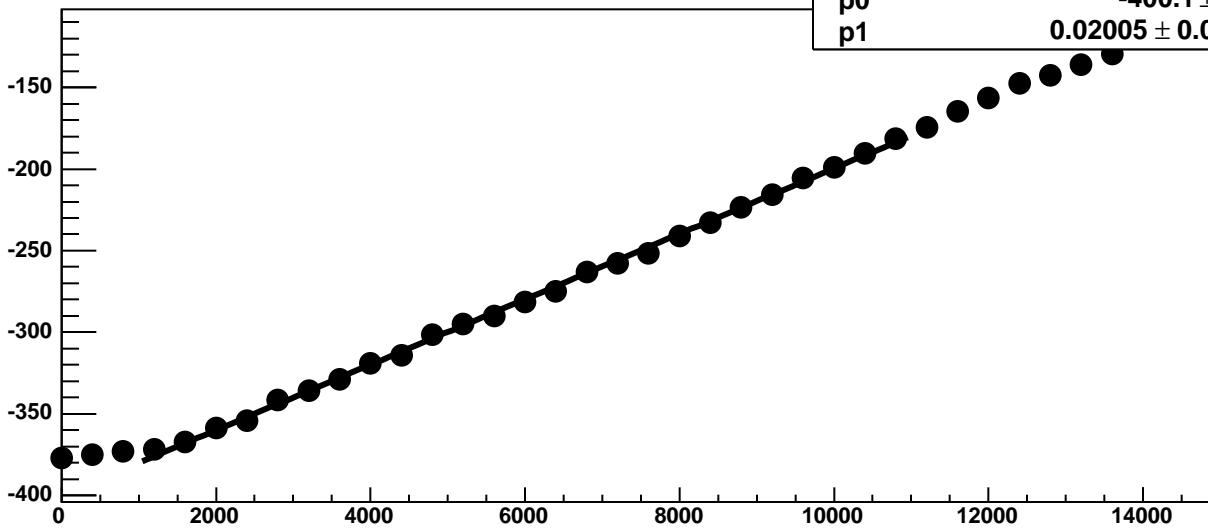
Chip 6, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

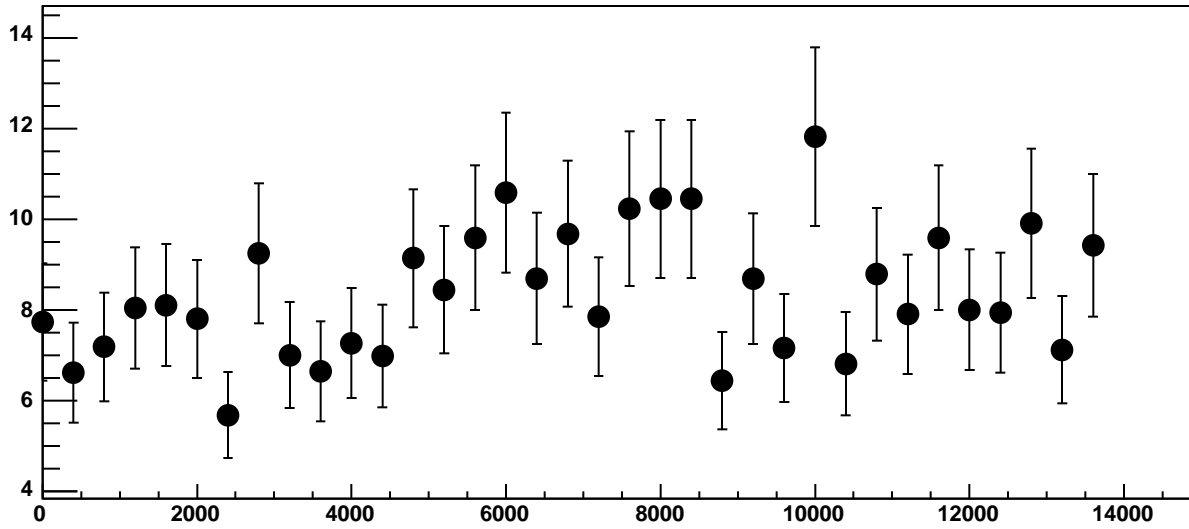


Chip 6, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC

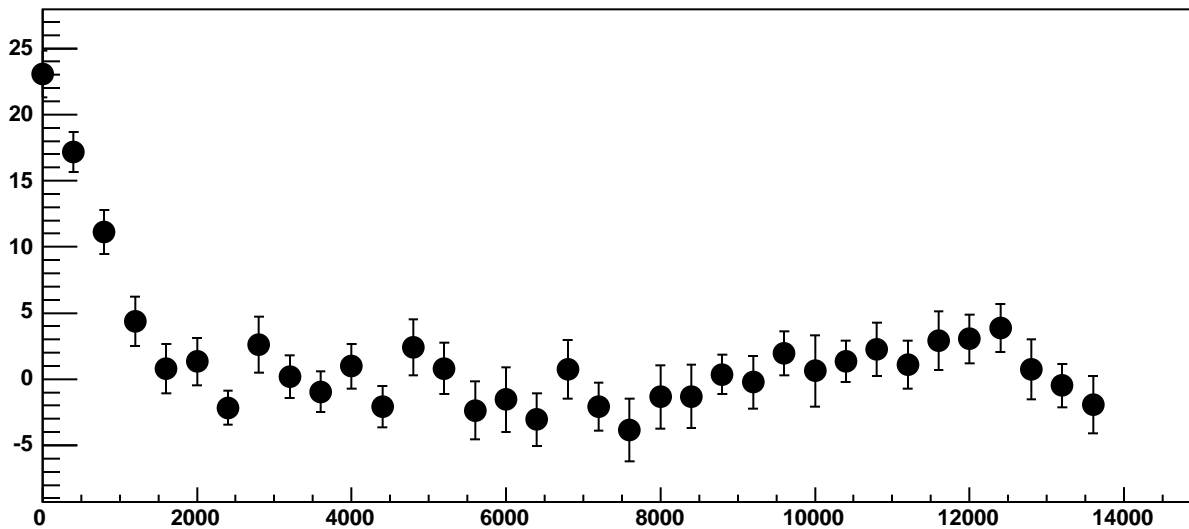


χ^2 / ndf 26.79 / 23
p0 -400.1 ± 0.7996
p1 0.02005 ± 0.0001244

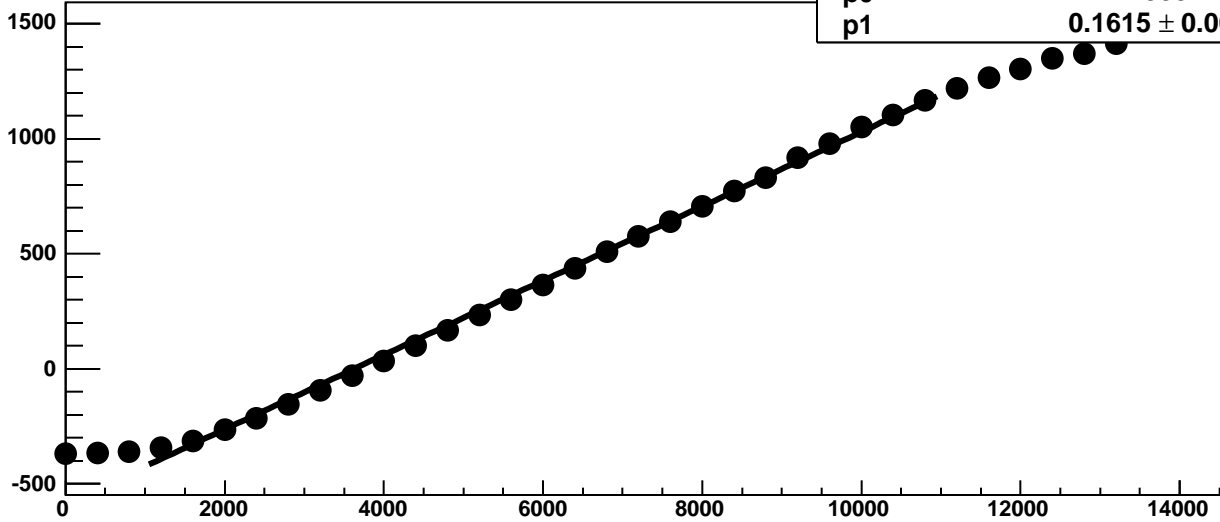
Chip 6, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



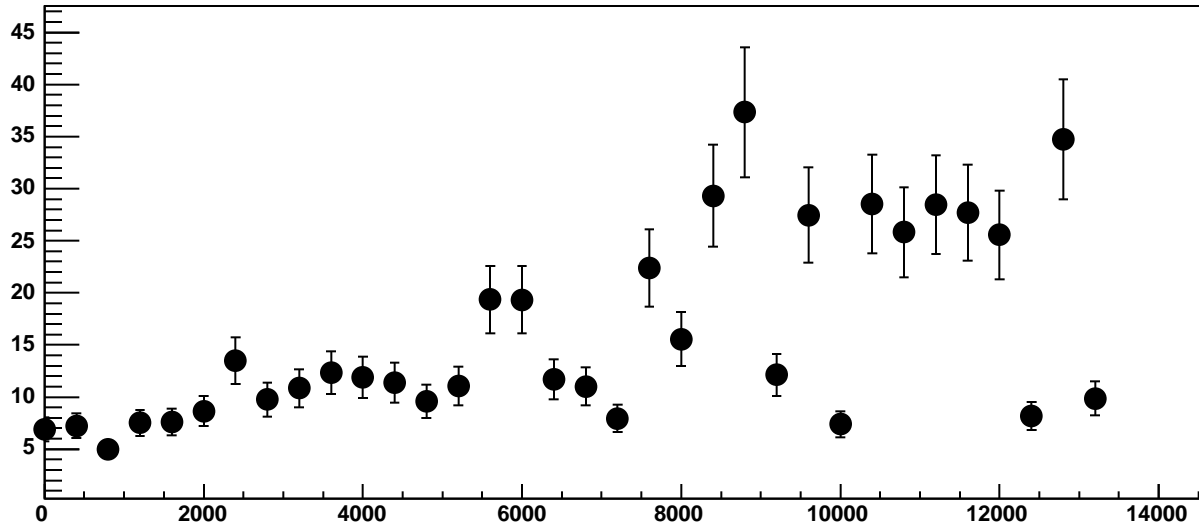
Chip 6, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC



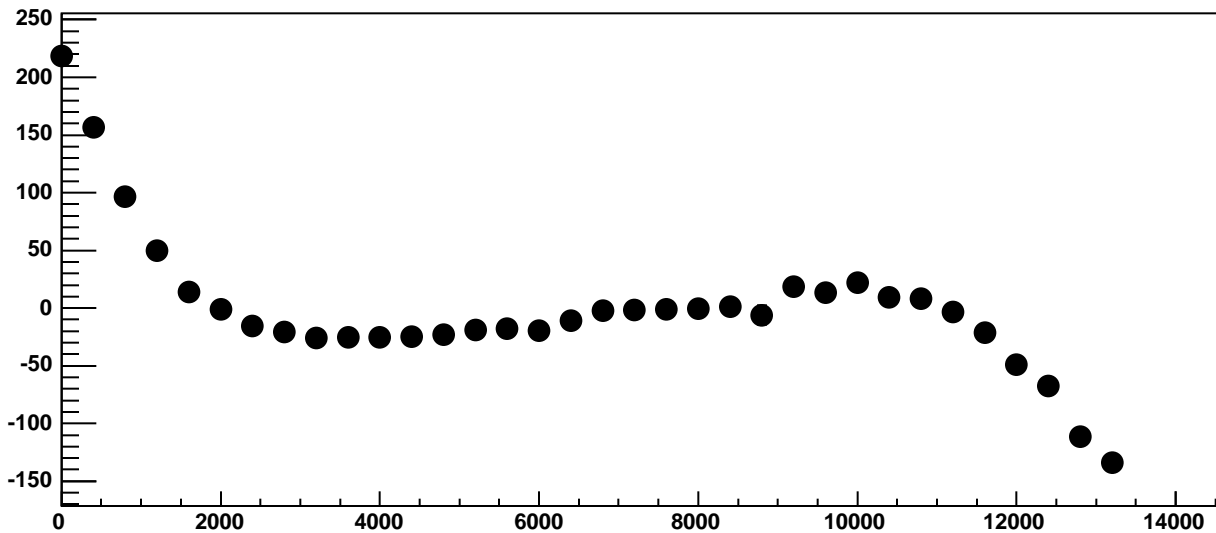
Chip 6, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC



Chip 6, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

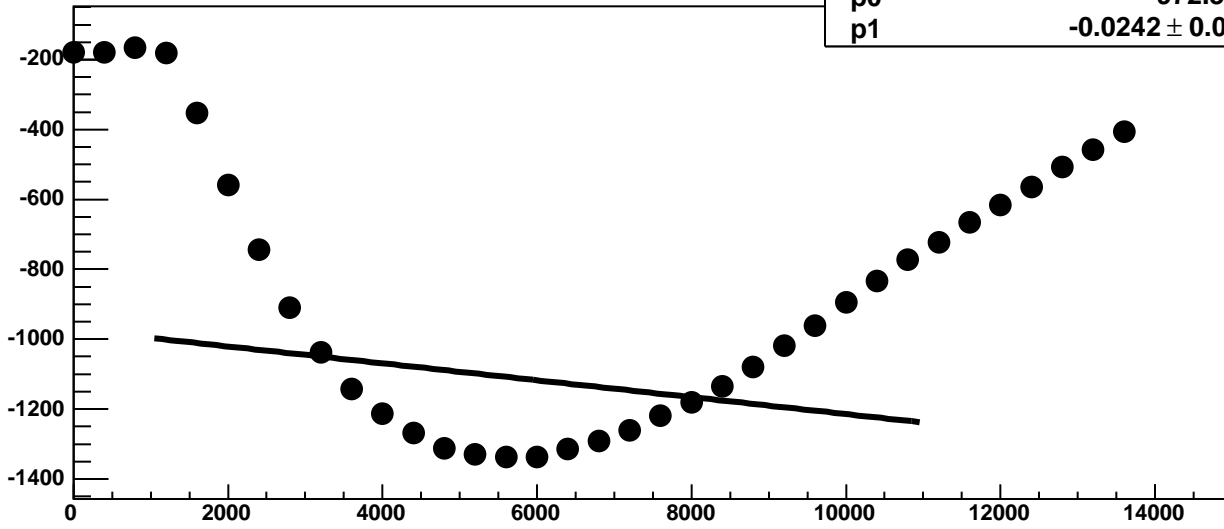


Chip 6, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

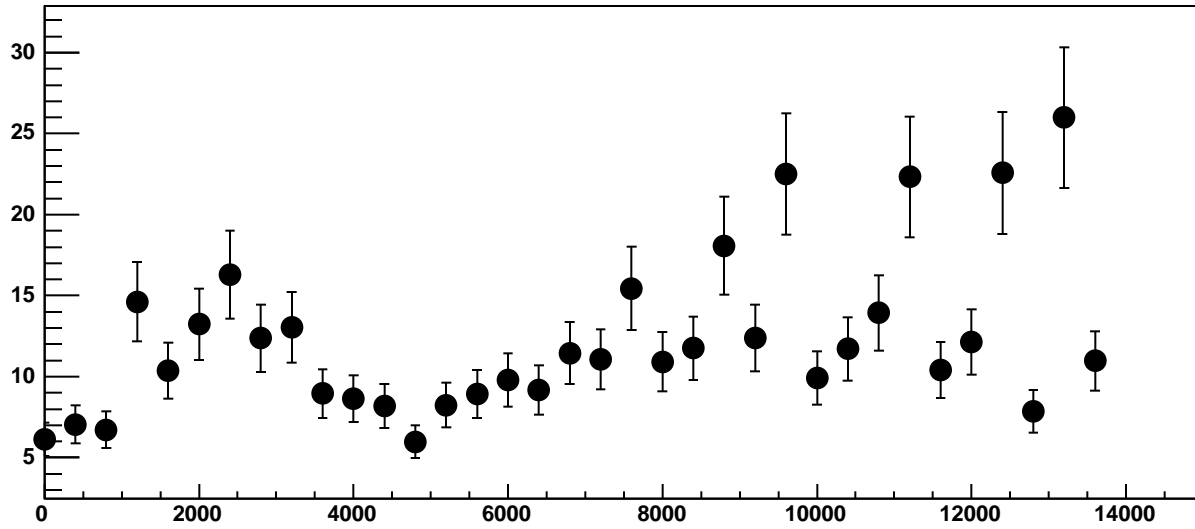


Chip 6, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

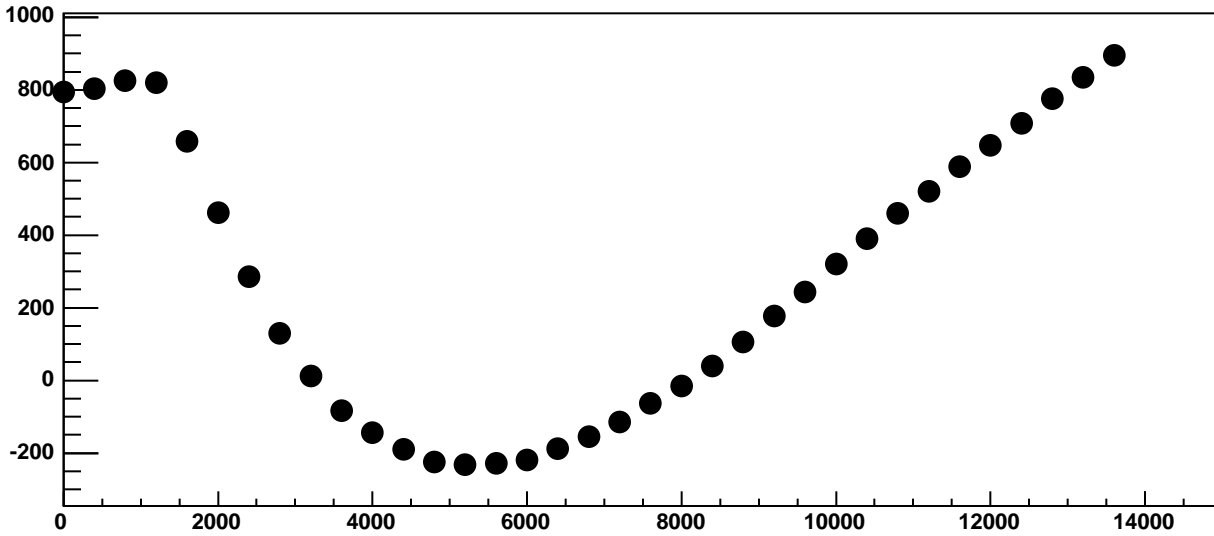
χ^2 / ndf 3.305e+05 / 23
p0 -972.3 ± 1.211
p1 -0.0242 ± 0.0001955



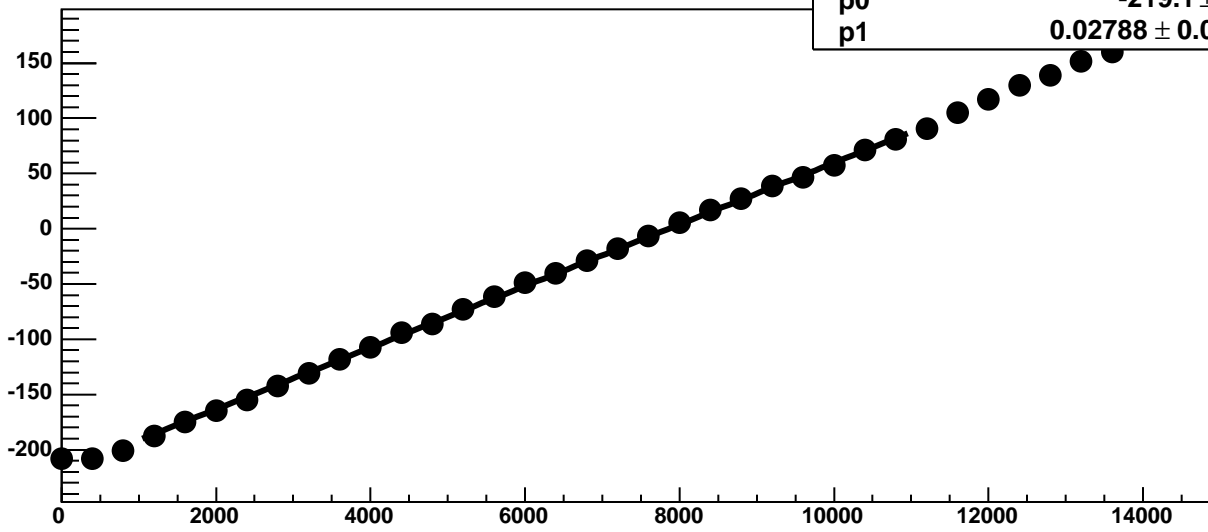
Chip 6, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

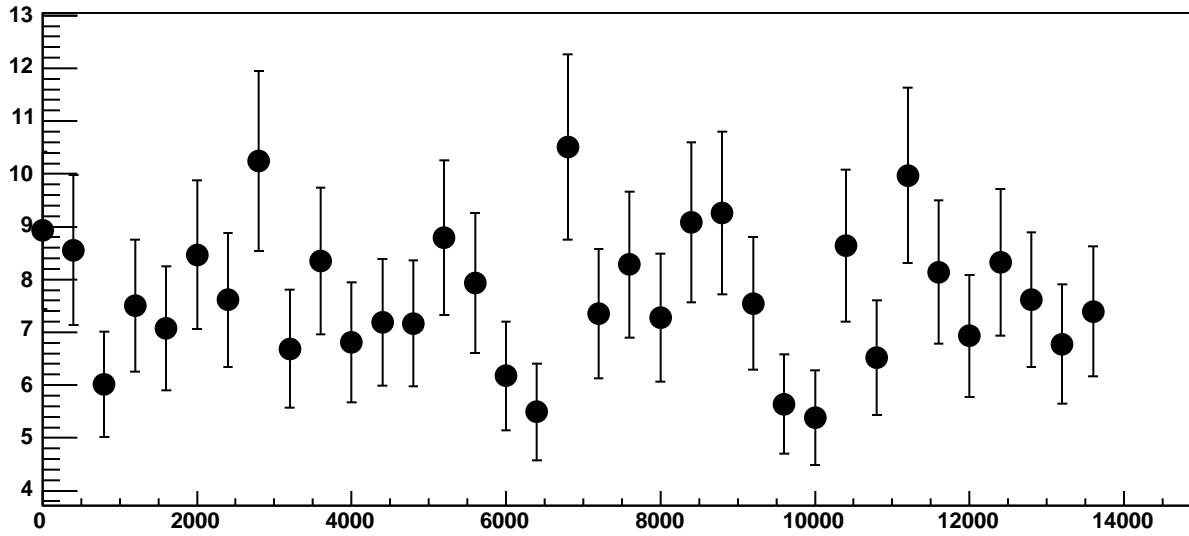


Chip 6, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC

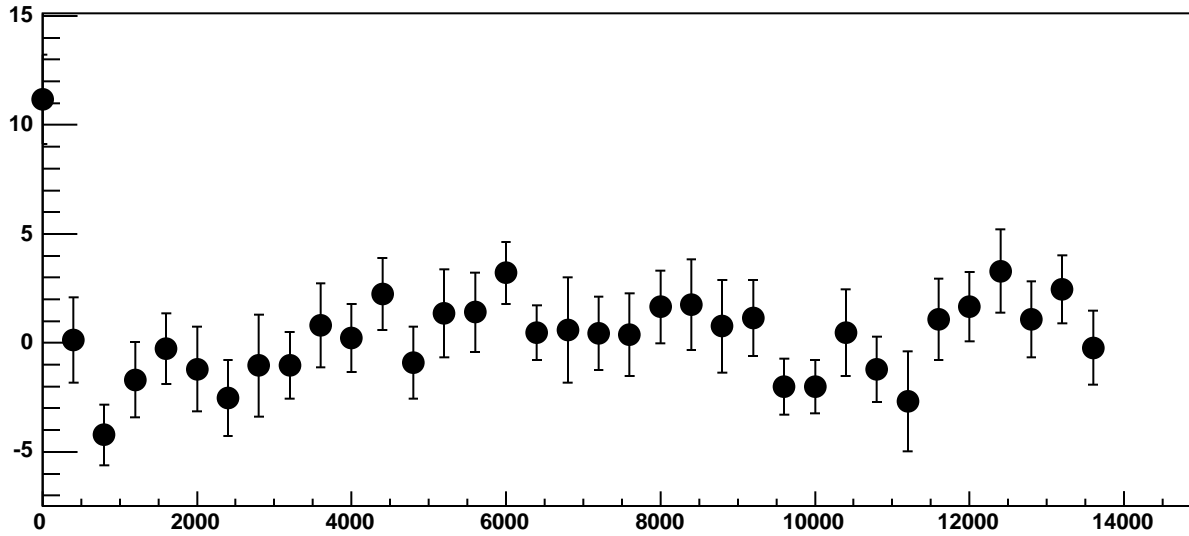


χ^2 / ndf 20.99 / 23
p0 -219.1 ± 0.7903
p1 0.02788 ± 0.0001147

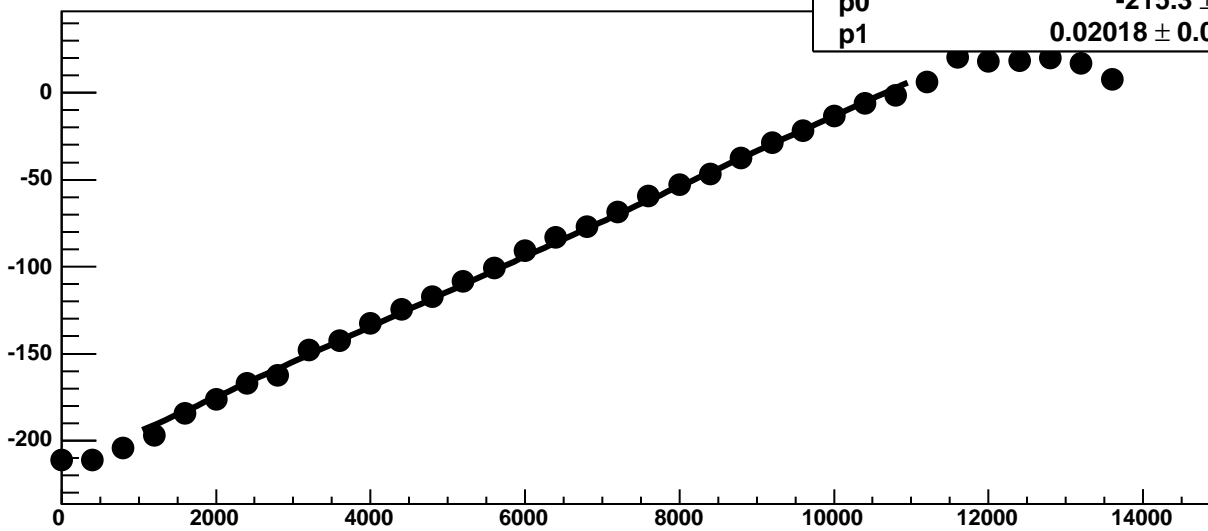
Chip 6, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC



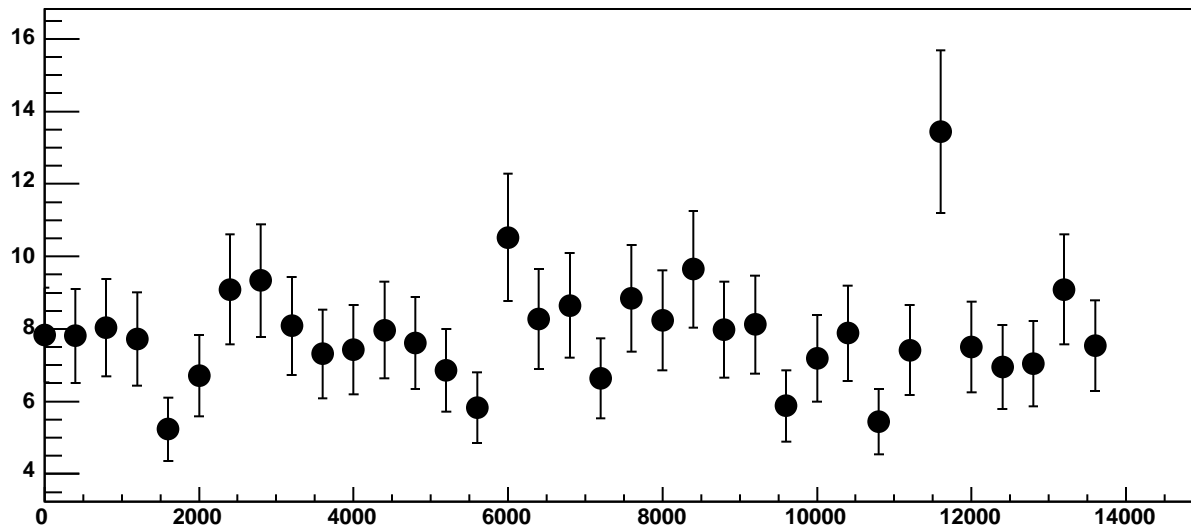
Chip 6, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC



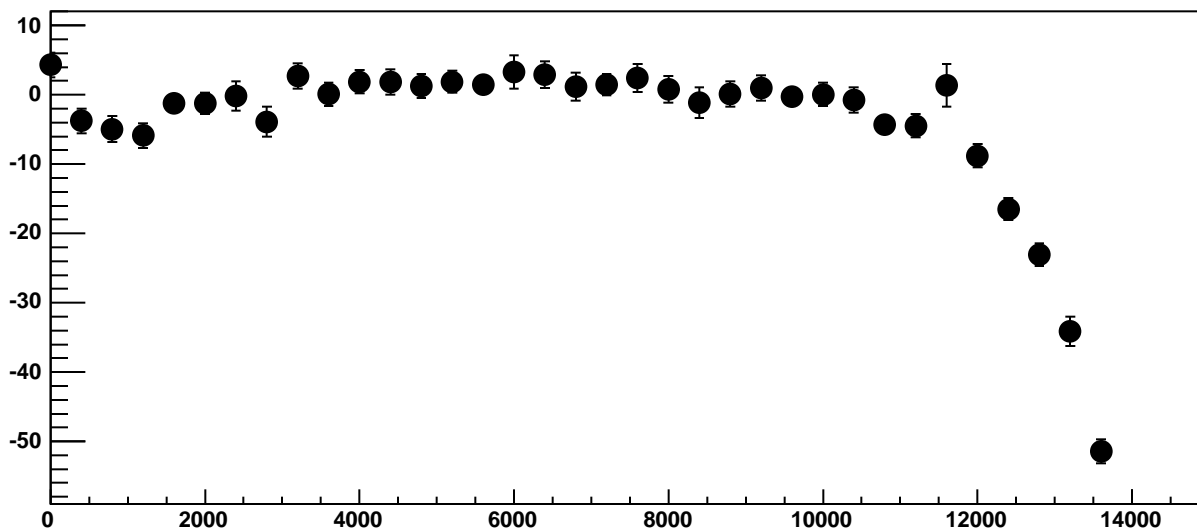
Chip 6, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



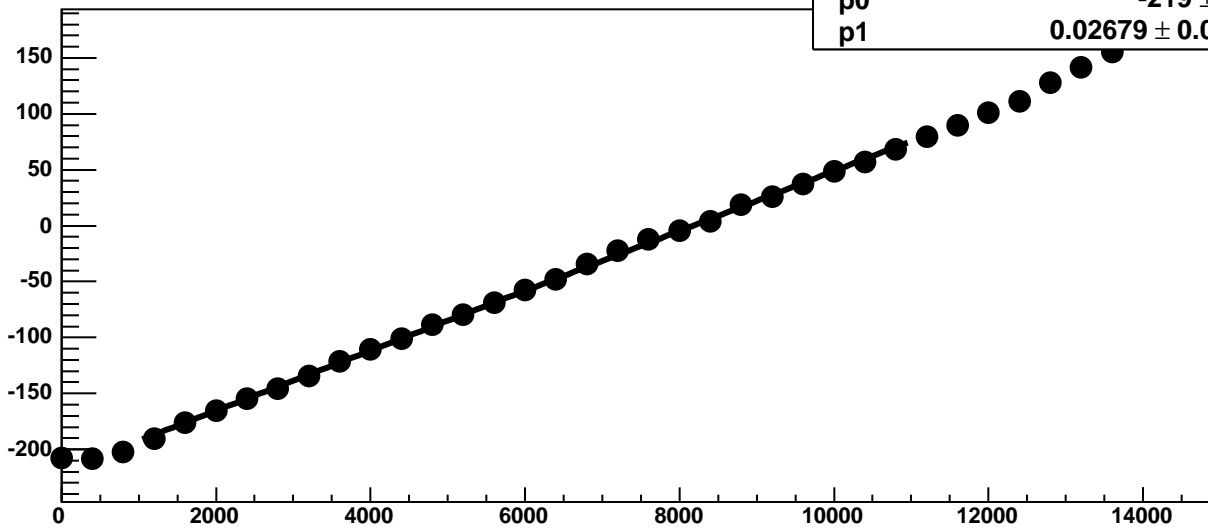
Chip 6, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

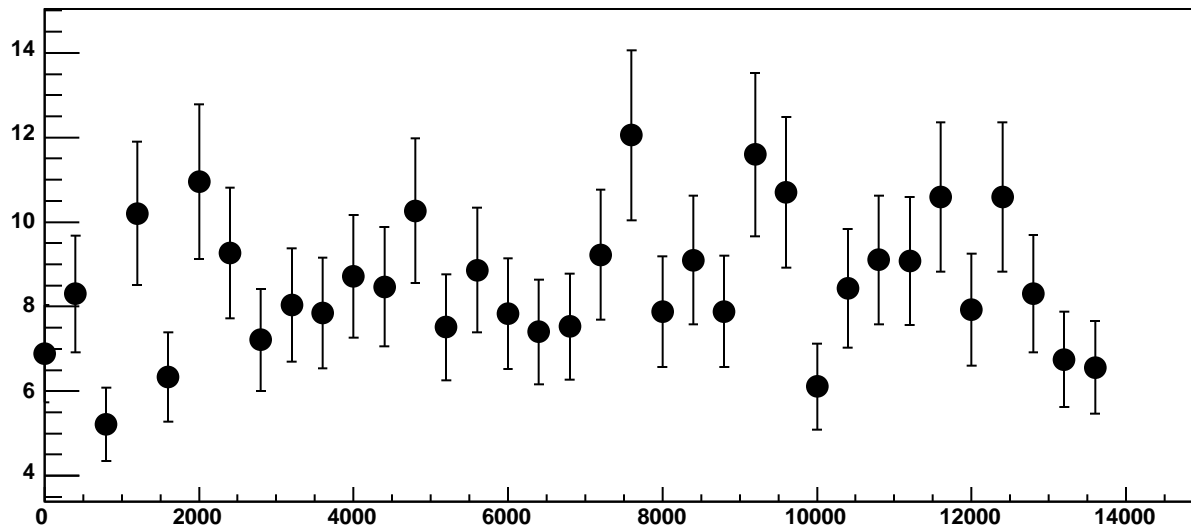


Chip 6, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

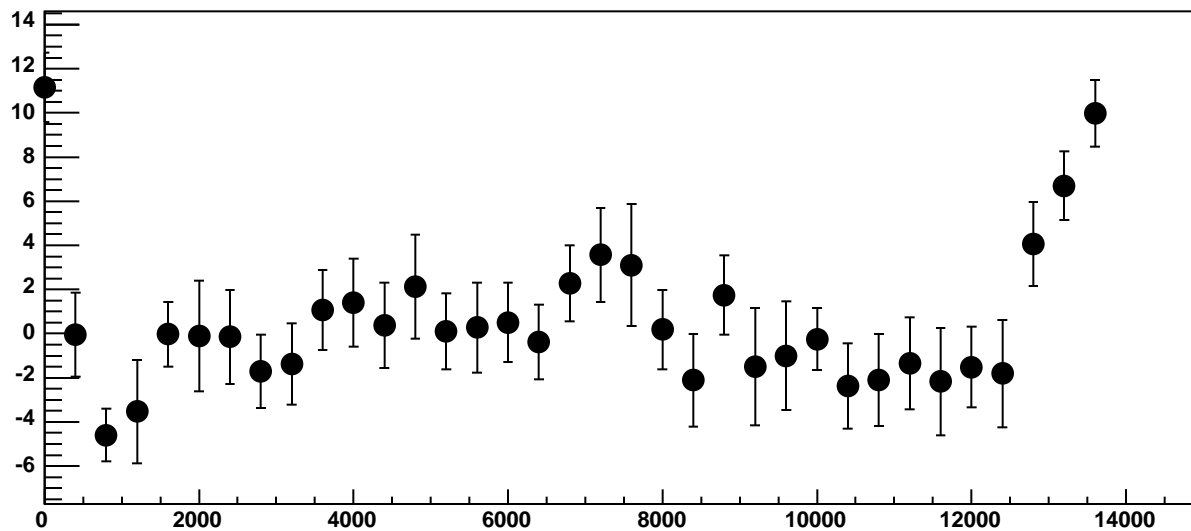


χ^2 / ndf 16.6 / 23
p0 -219 ± 0.8773
p1 0.02679 ± 0.0001326

Chip 6, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

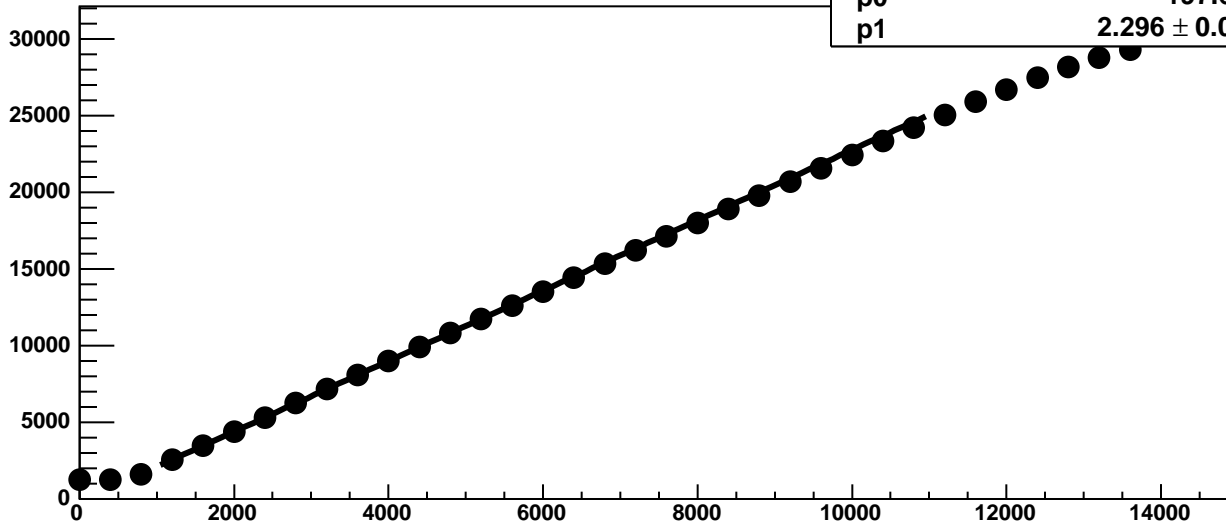


Chip 6, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

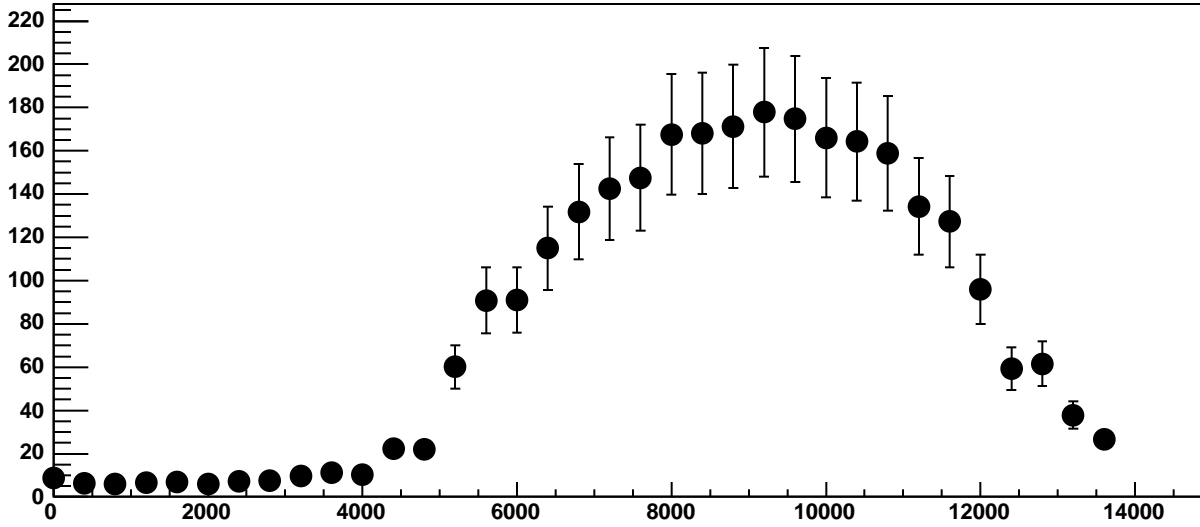


Chip 6, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

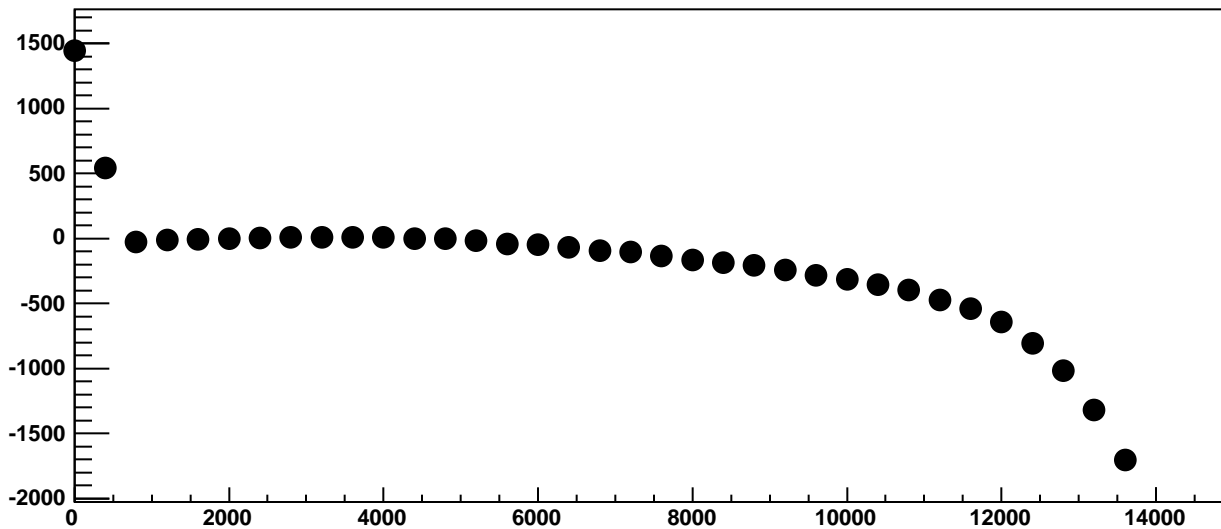
χ^2 / ndf 592.9 / 23
p0 -197.6 ± 1.59
p1 2.296 ± 0.0006133



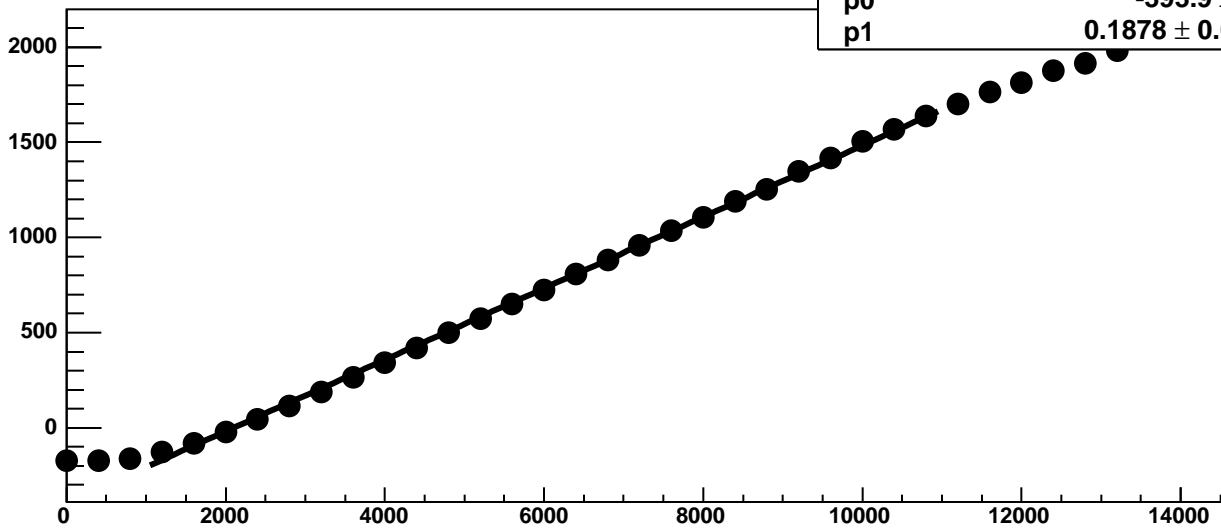
Chip 6, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC



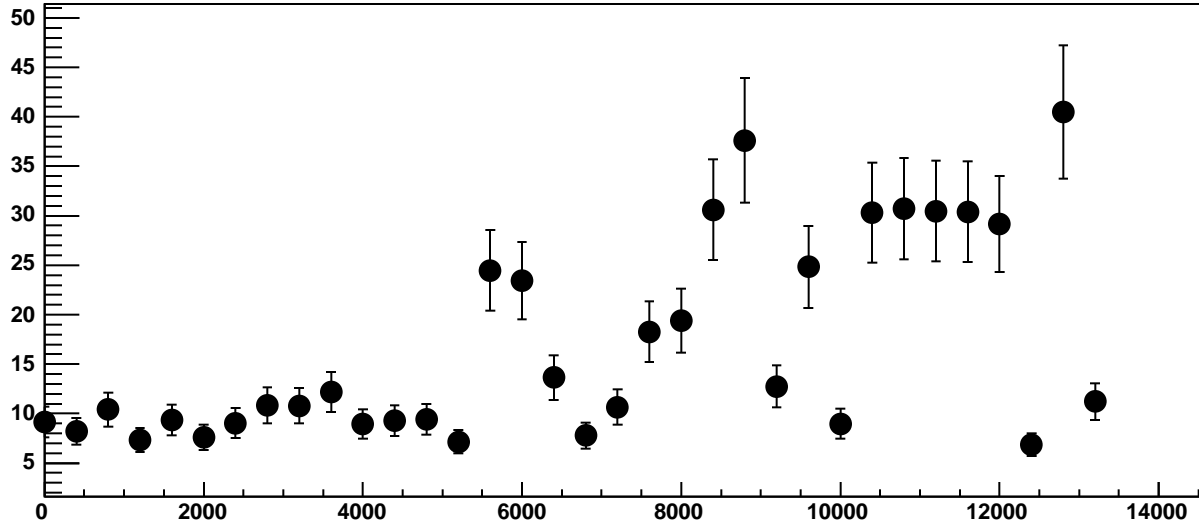
Chip 6, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC



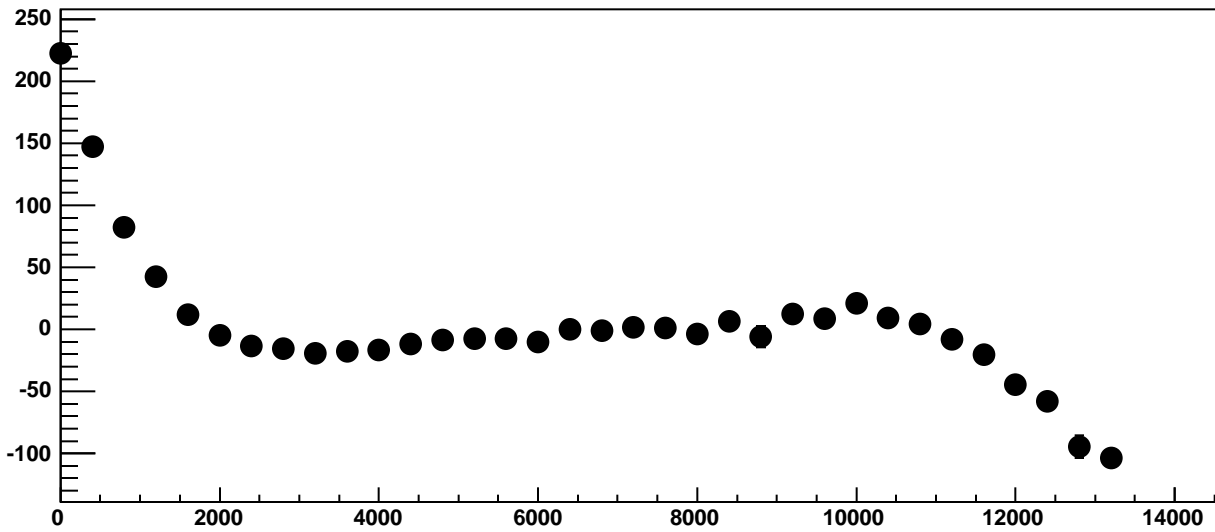
Chip 6, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



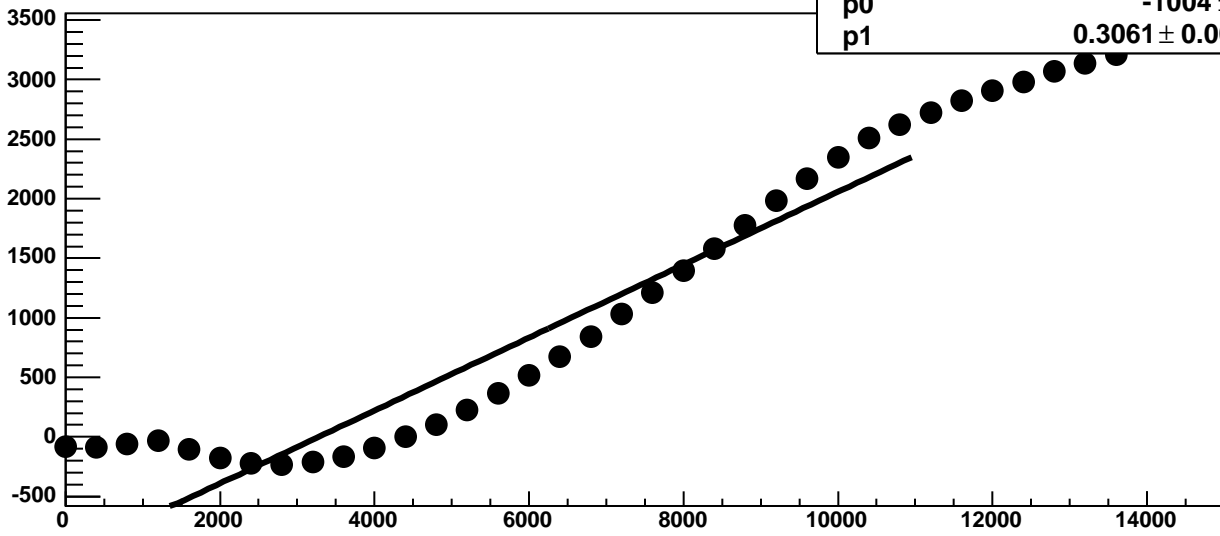
Chip 6, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



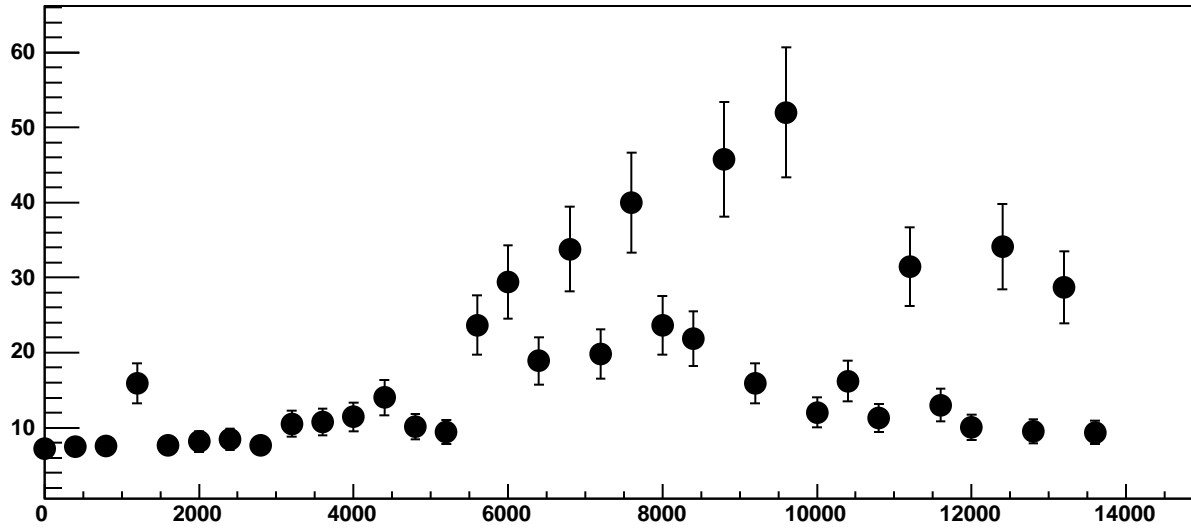
Chip 6, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



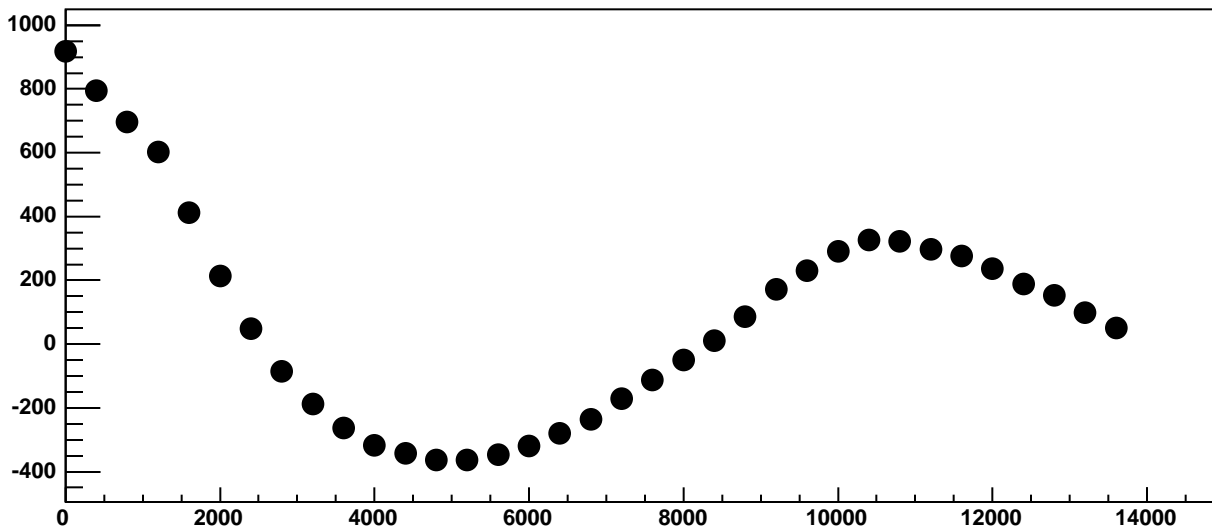
Chip 6, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



Chip 6, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

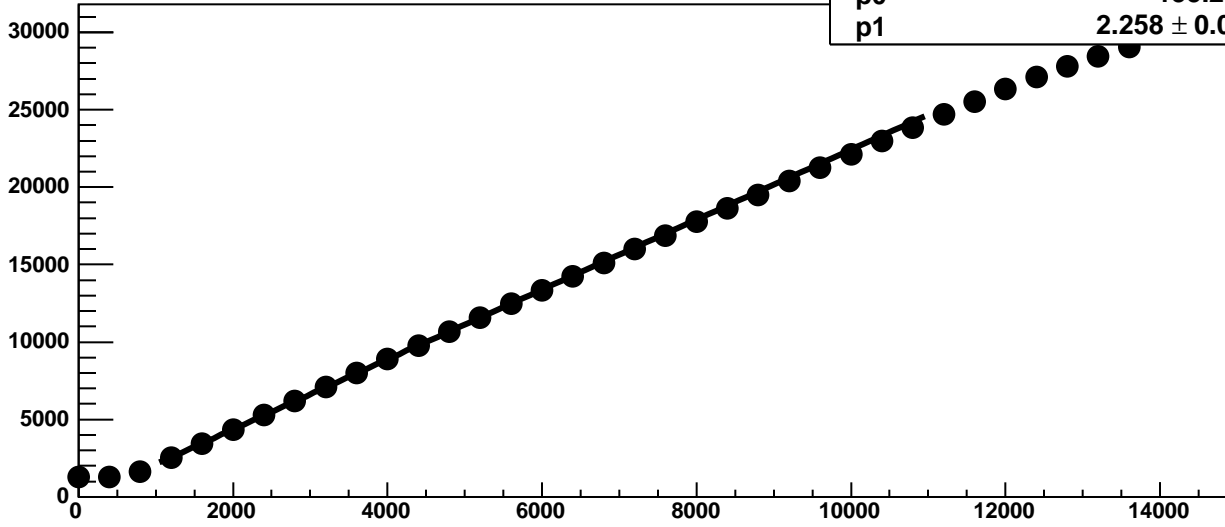


Chip 6, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

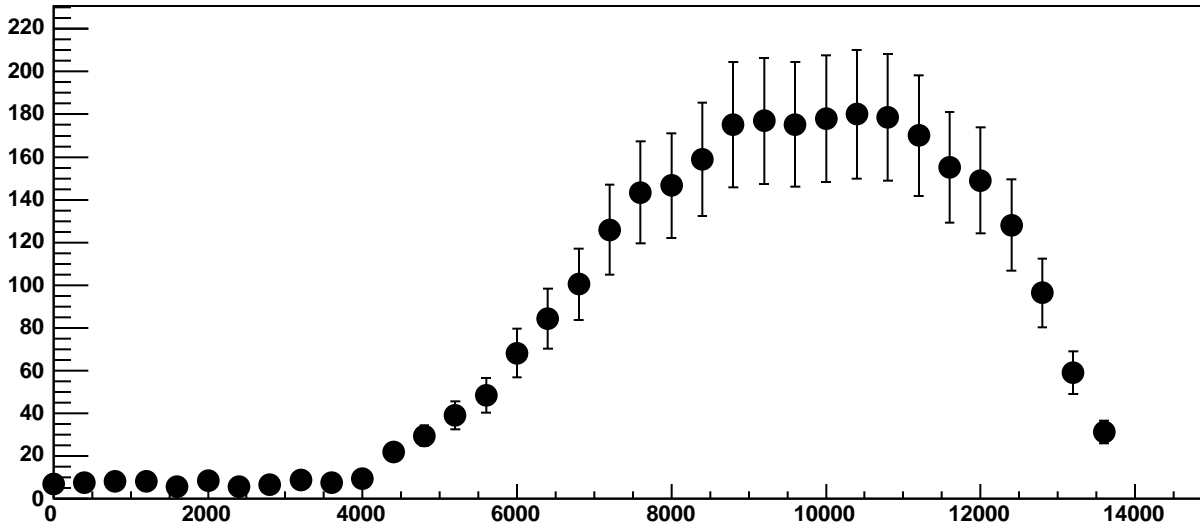


Chip 6, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC

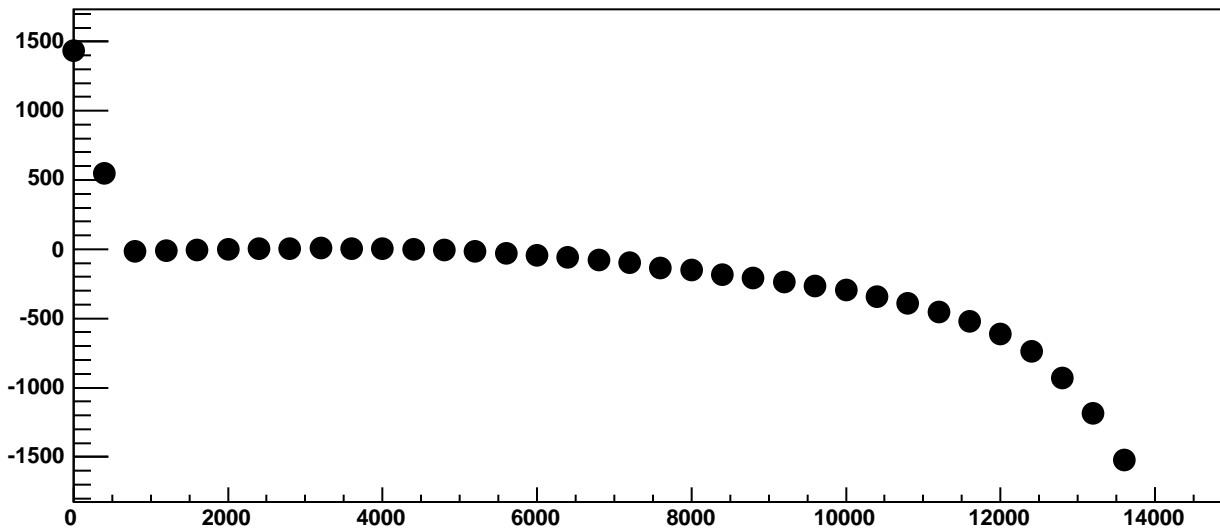
χ^2 / ndf 553.7 / 23
p0 -155.2 ± 1.602
p1 2.258 ± 0.0005866



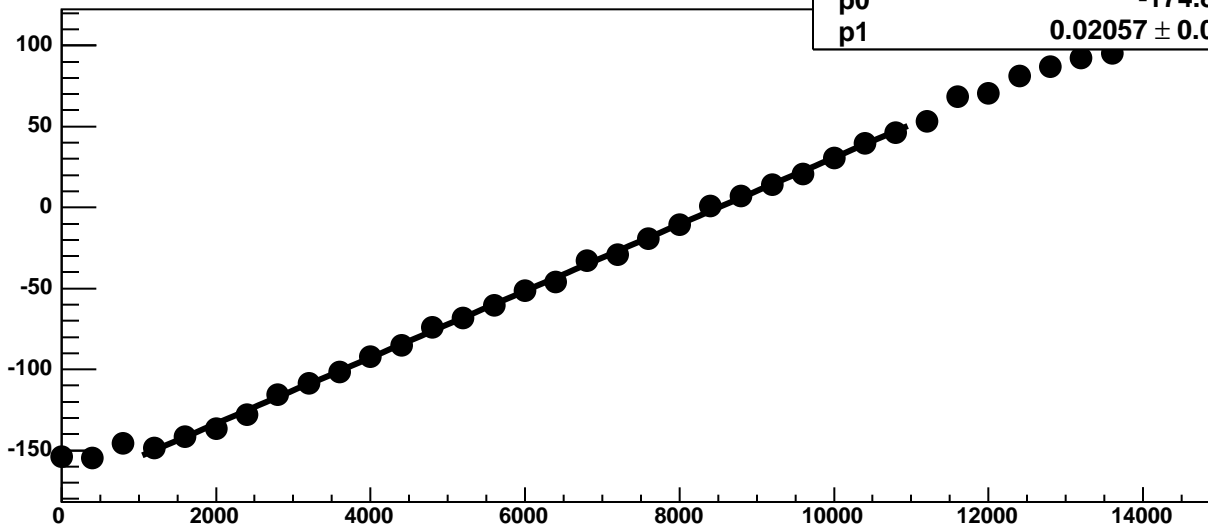
Chip 6, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



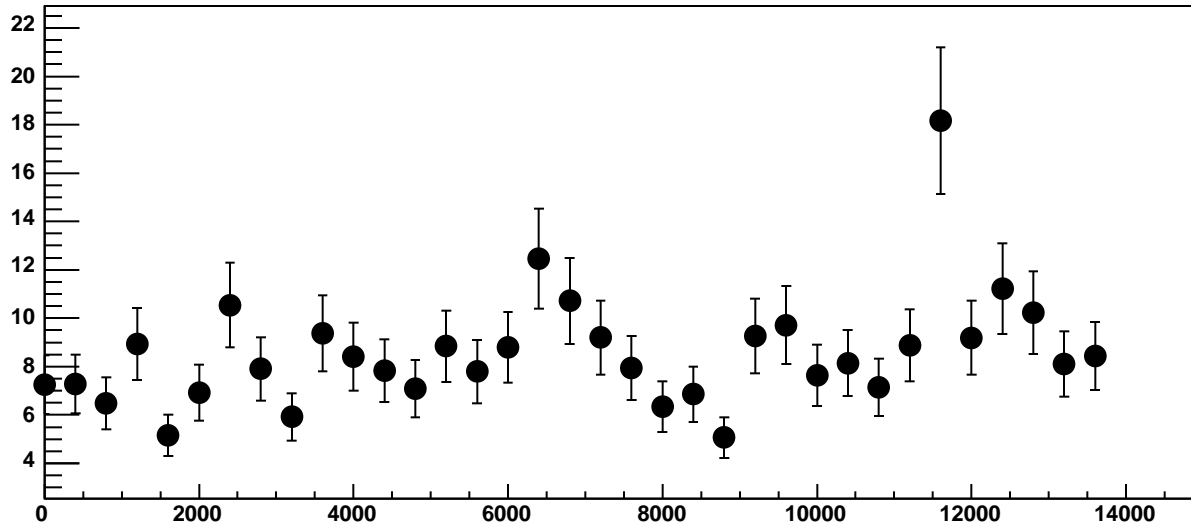
Chip 6, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC



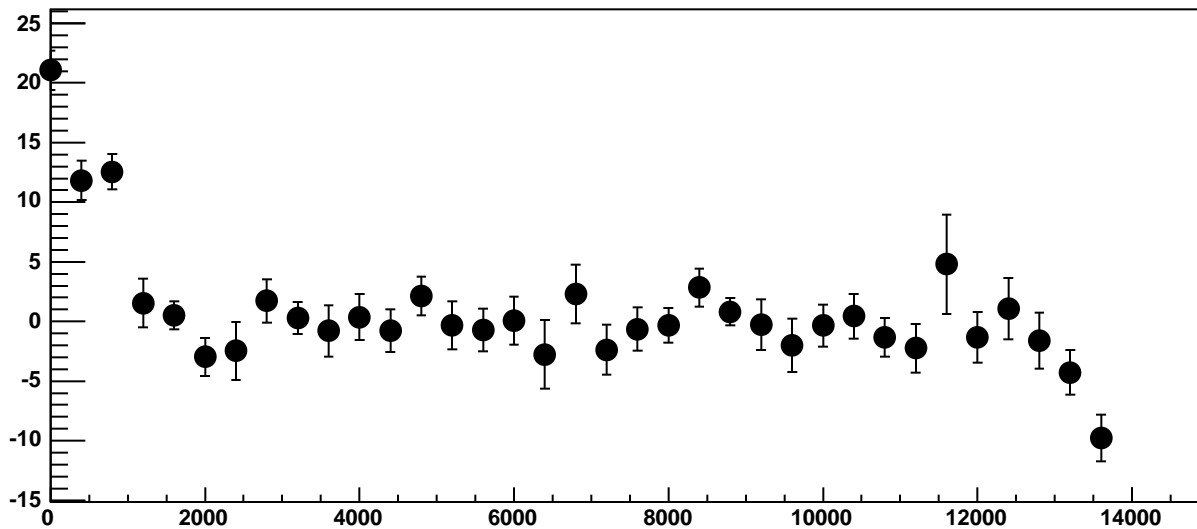
Chip 6, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



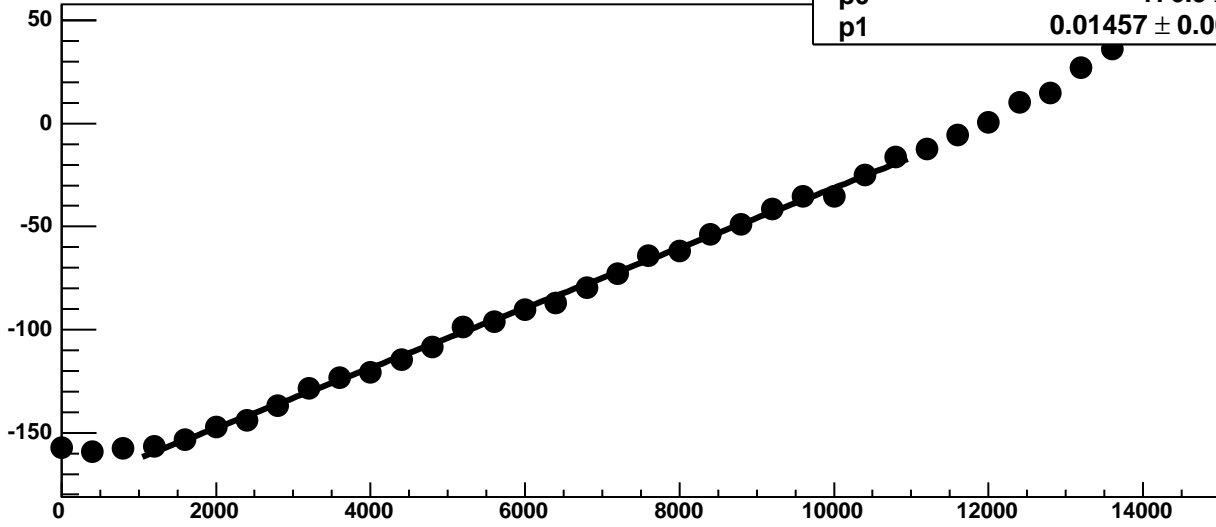
Chip 6, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



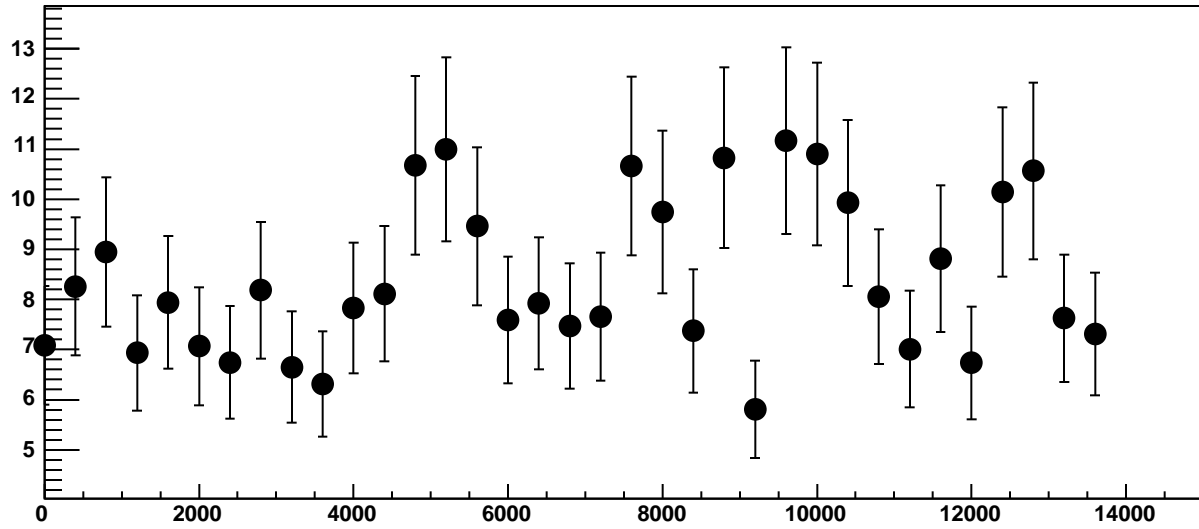
Chip 6, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



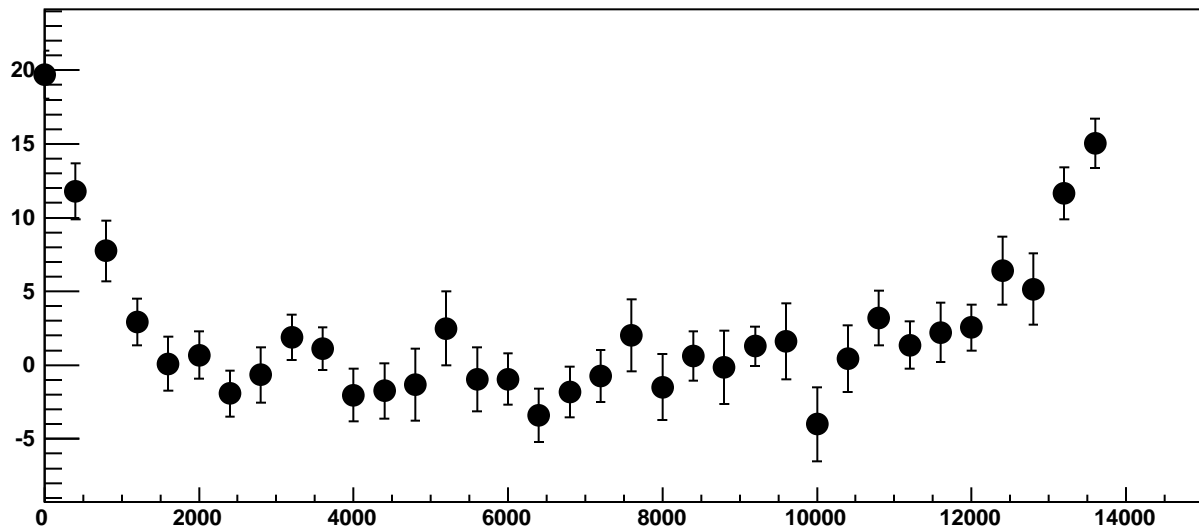
Chip 6, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



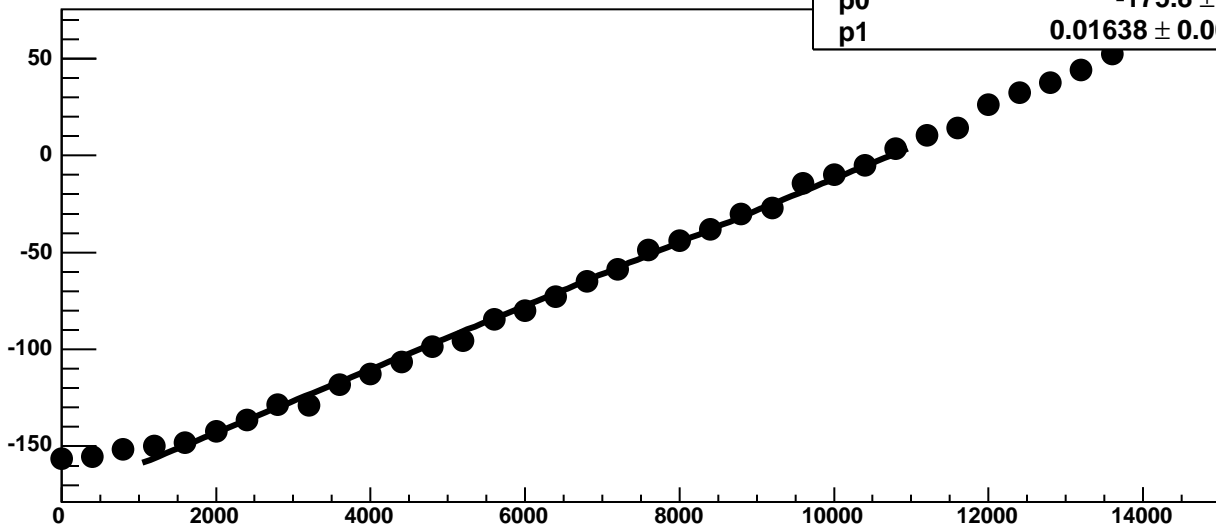
Chip 6, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

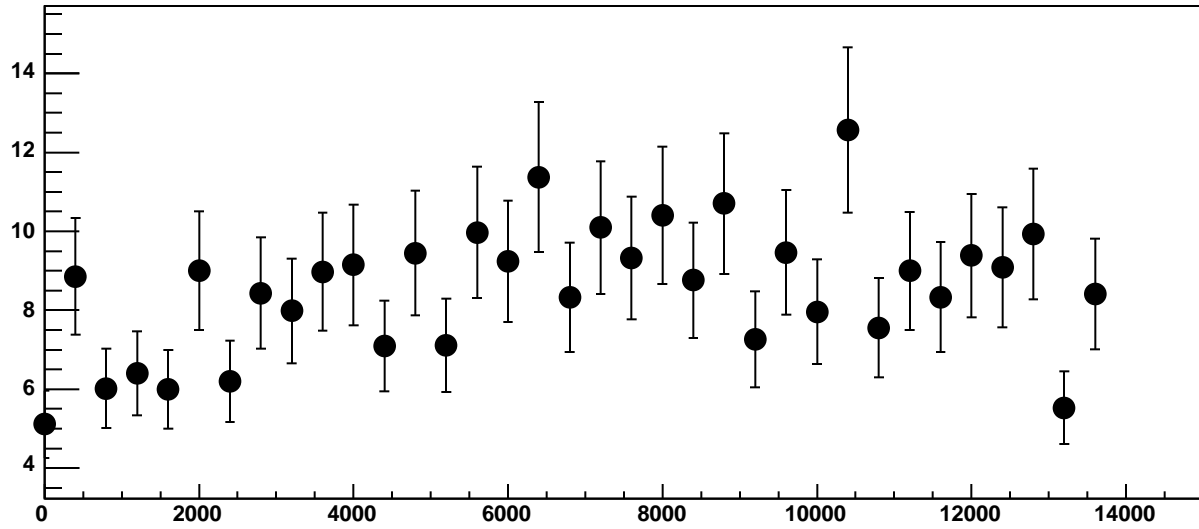


Chip 6, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

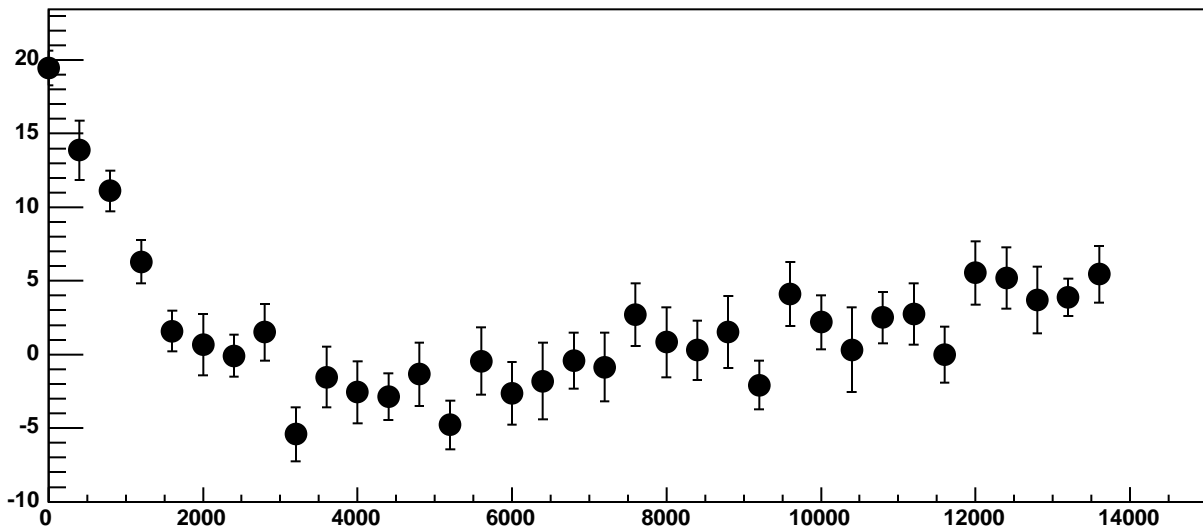


χ^2 / ndf 56.43 / 23
p0 -175.8 ± 0.7829
p1 0.01638 ± 0.0001253

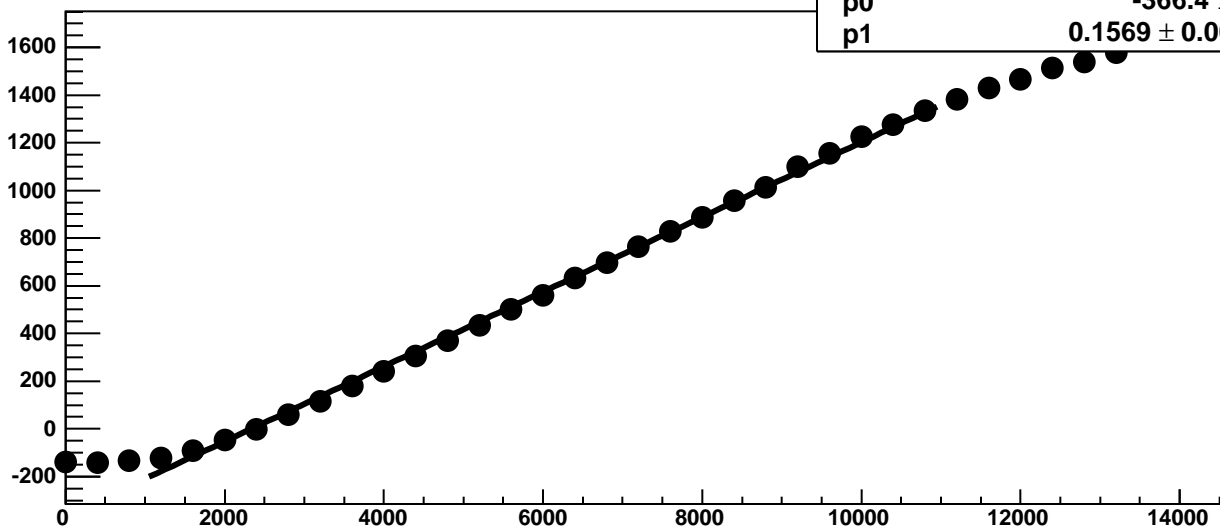
Chip 6, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



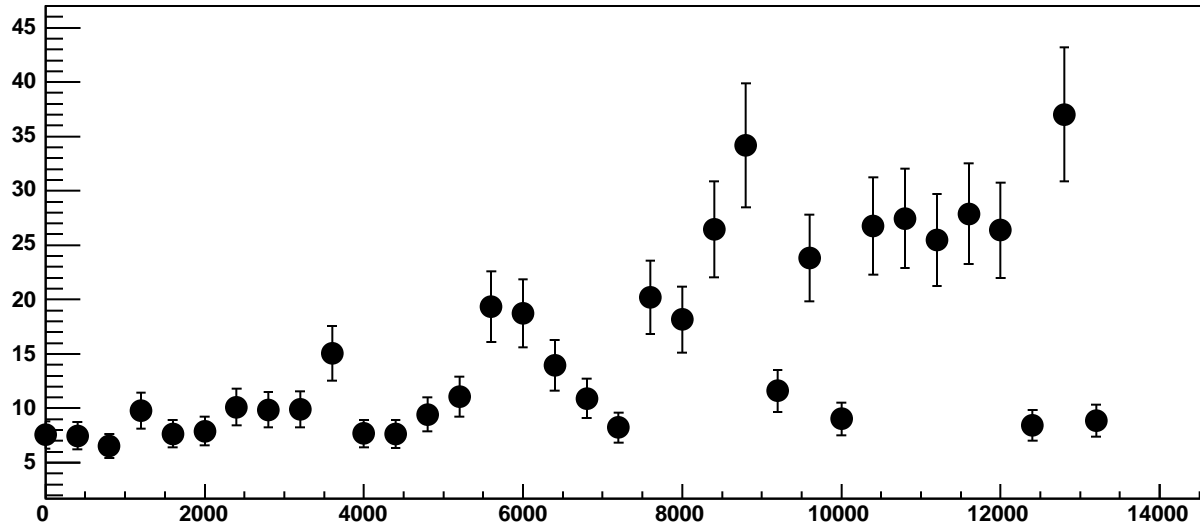
Chip 6, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC



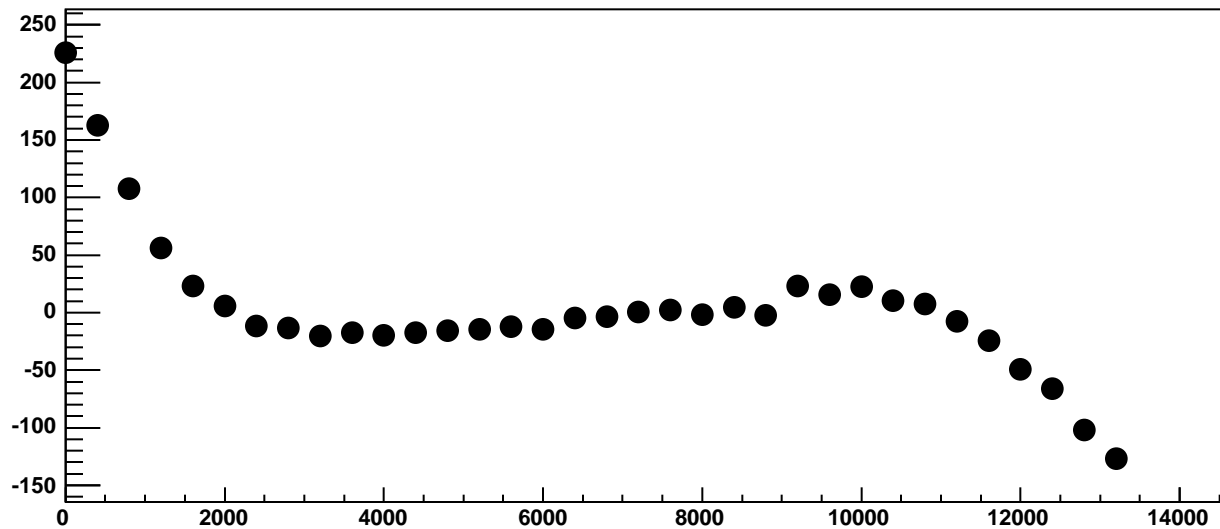
Chip 6, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



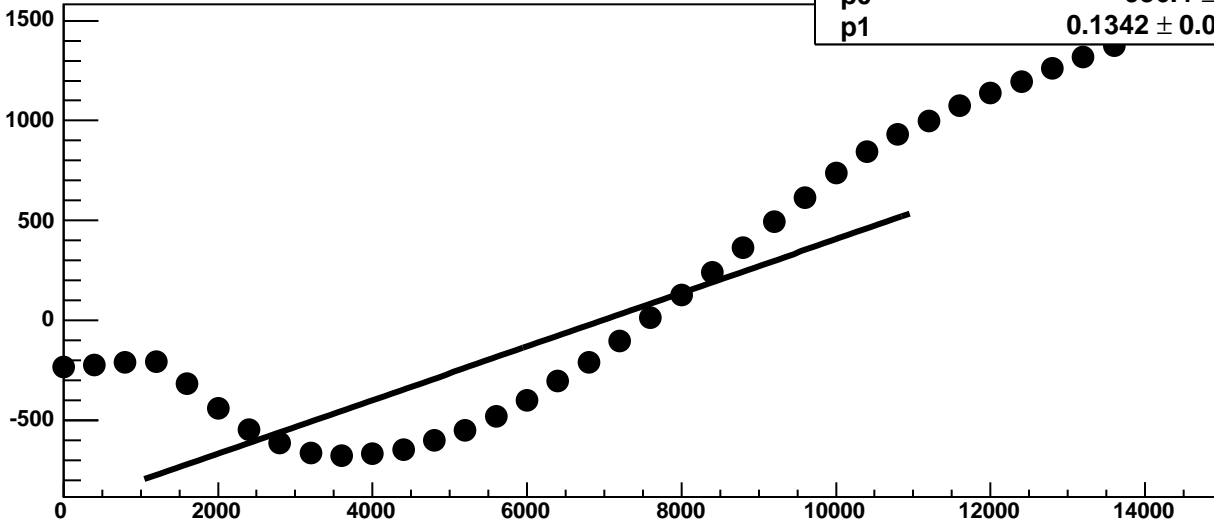
Chip 6, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

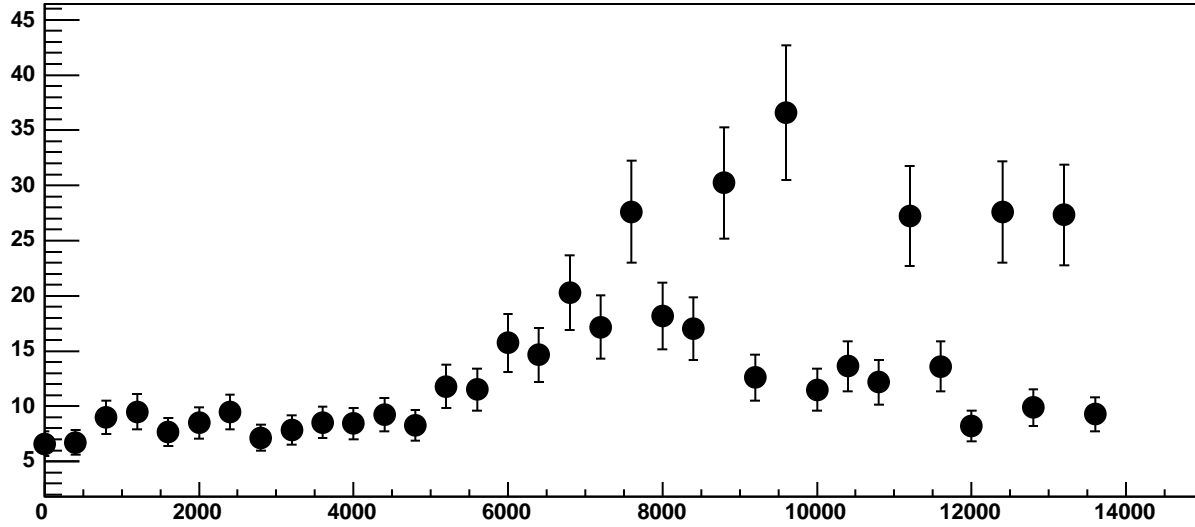


Chip 6, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

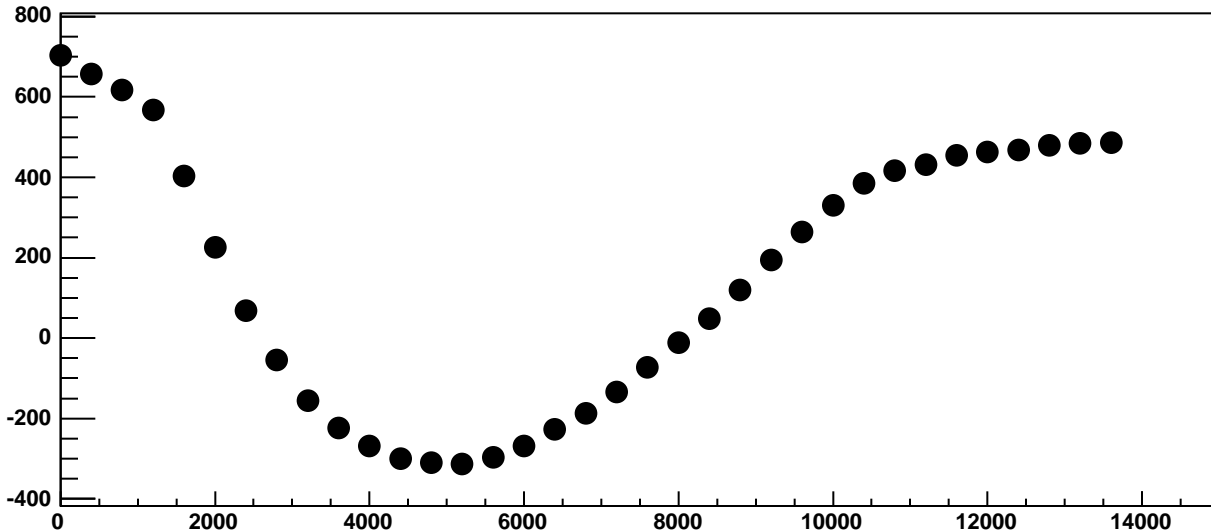


χ^2 / ndf 3.209e+05 / 23
p0 -936.4 ± 0.9803
p1 0.1342 ± 0.0001849

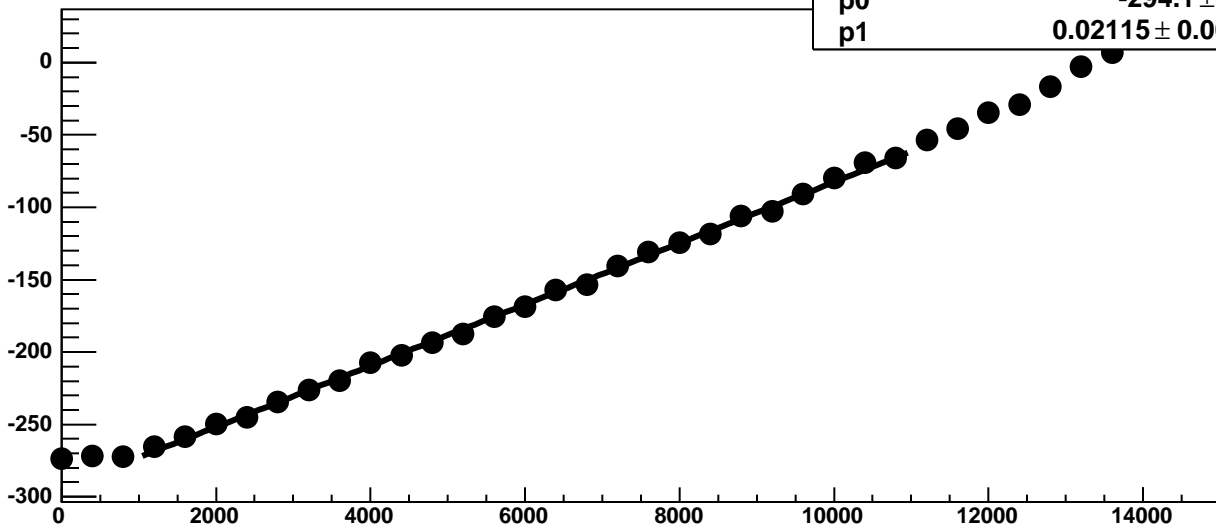
Chip 6, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



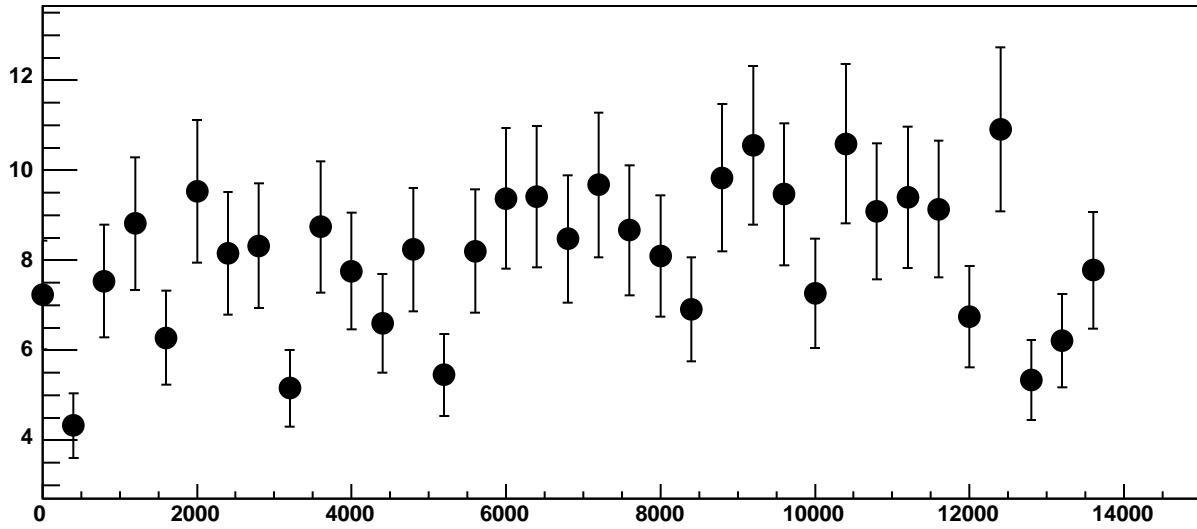
Chip 6, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



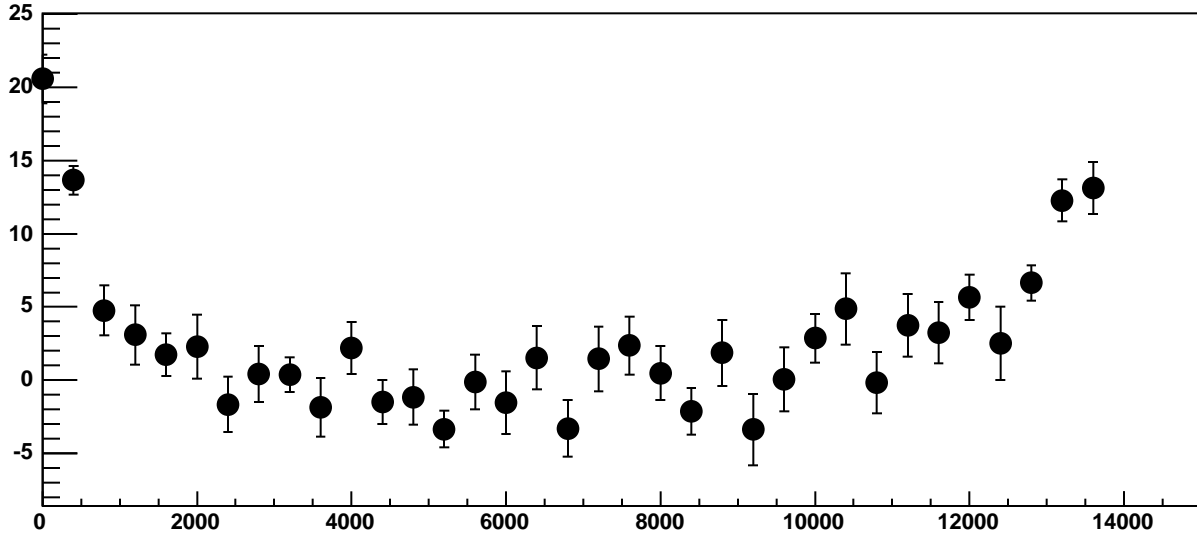
Chip 6, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



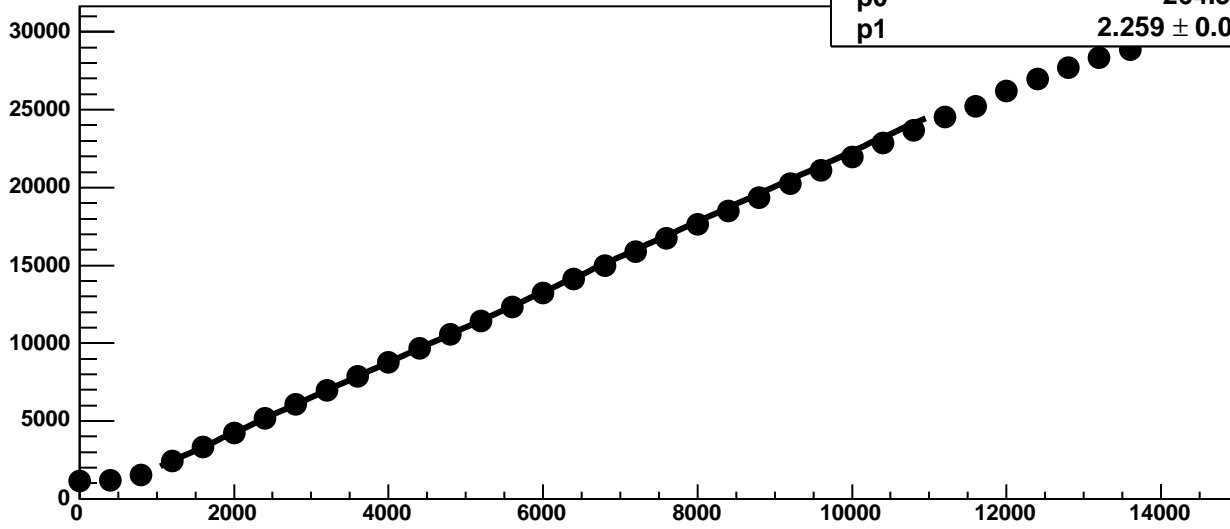
Chip 6, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



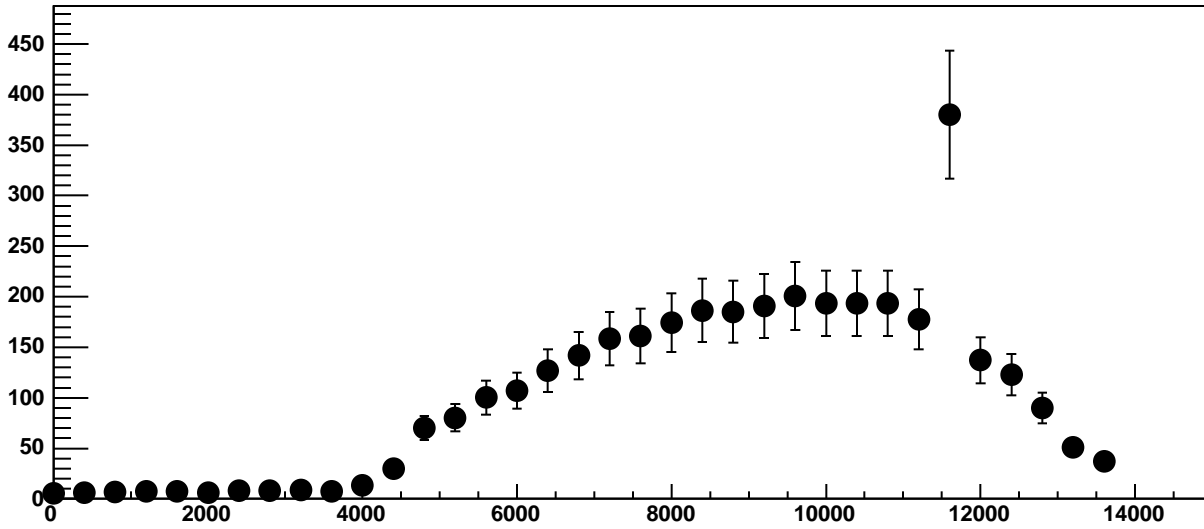
Chip 6, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



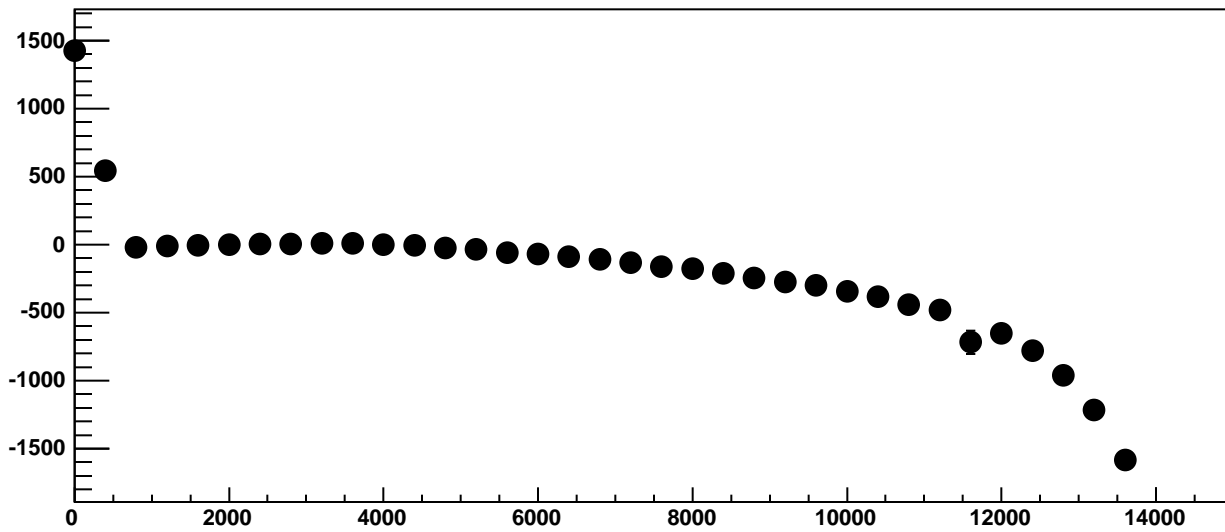
Chip 6, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC



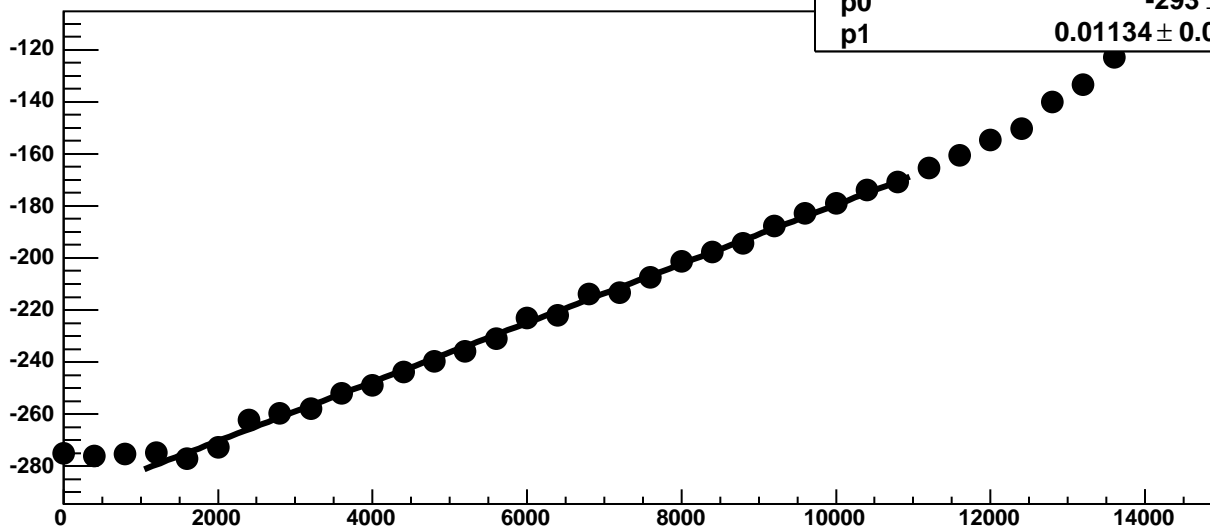
Chip 6, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



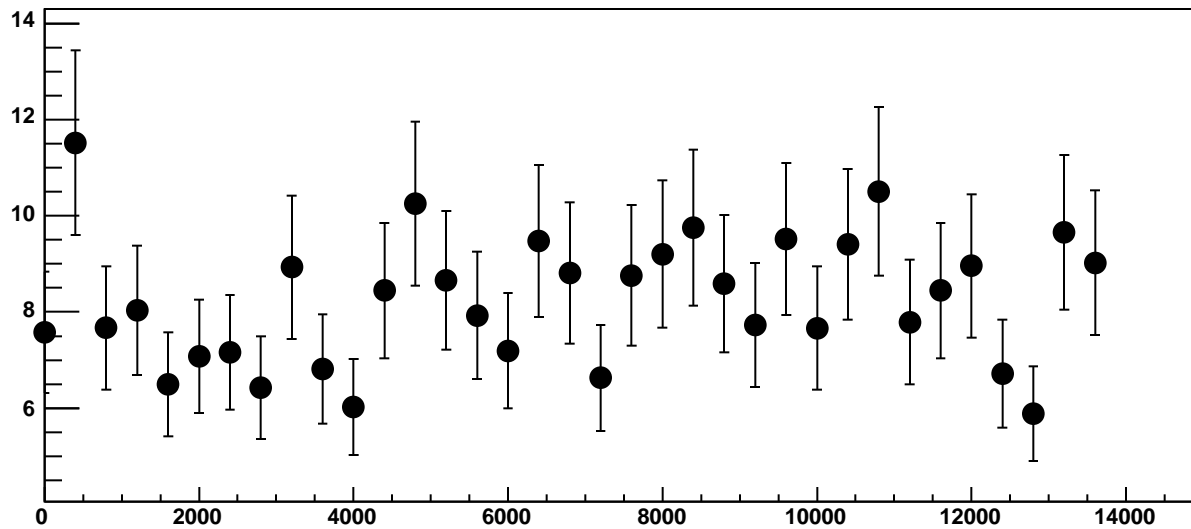
Chip 6, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC



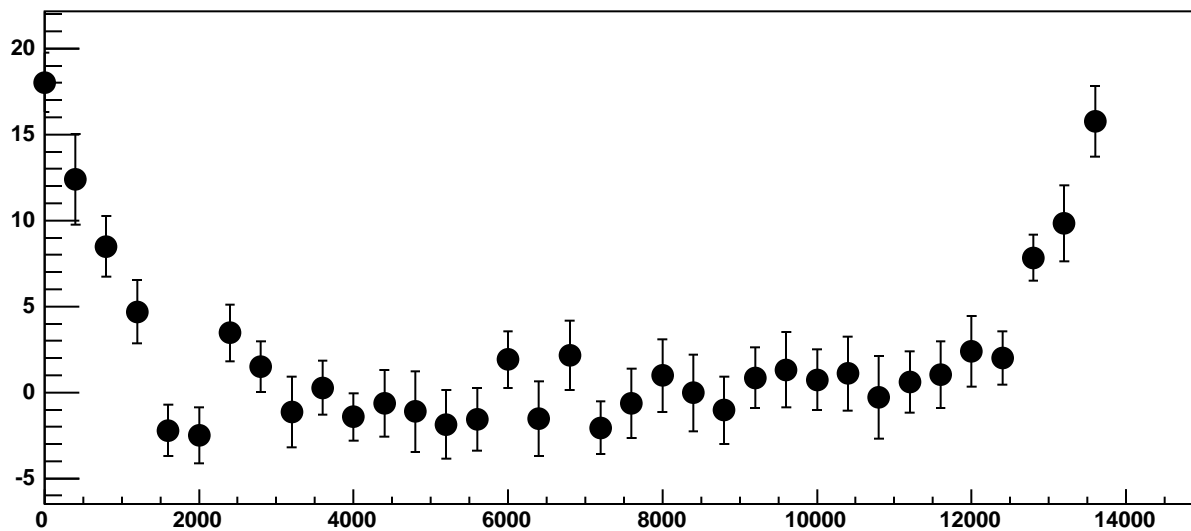
Chip 6, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



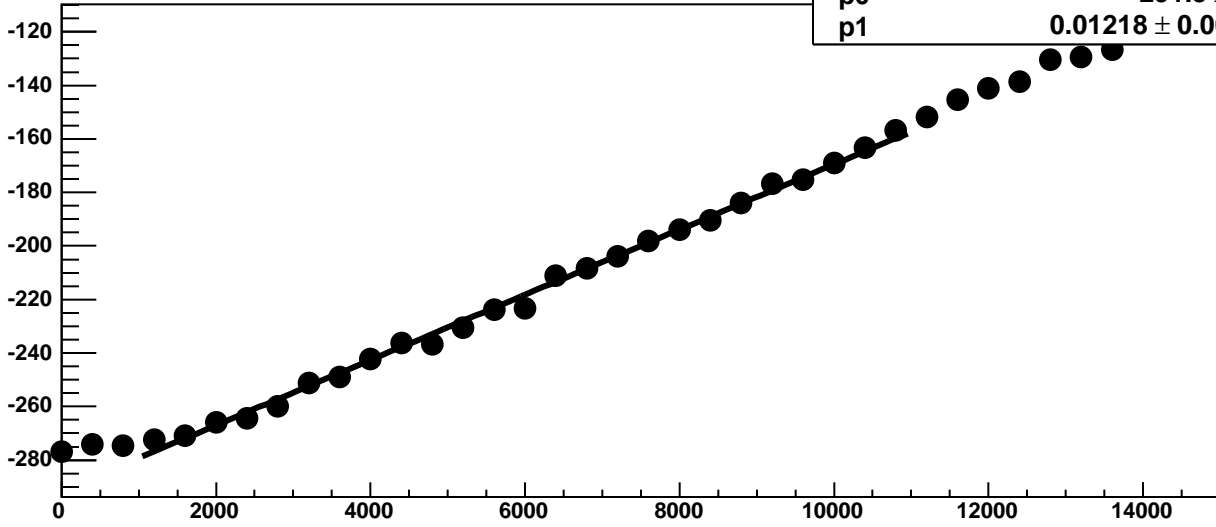
Chip 6, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

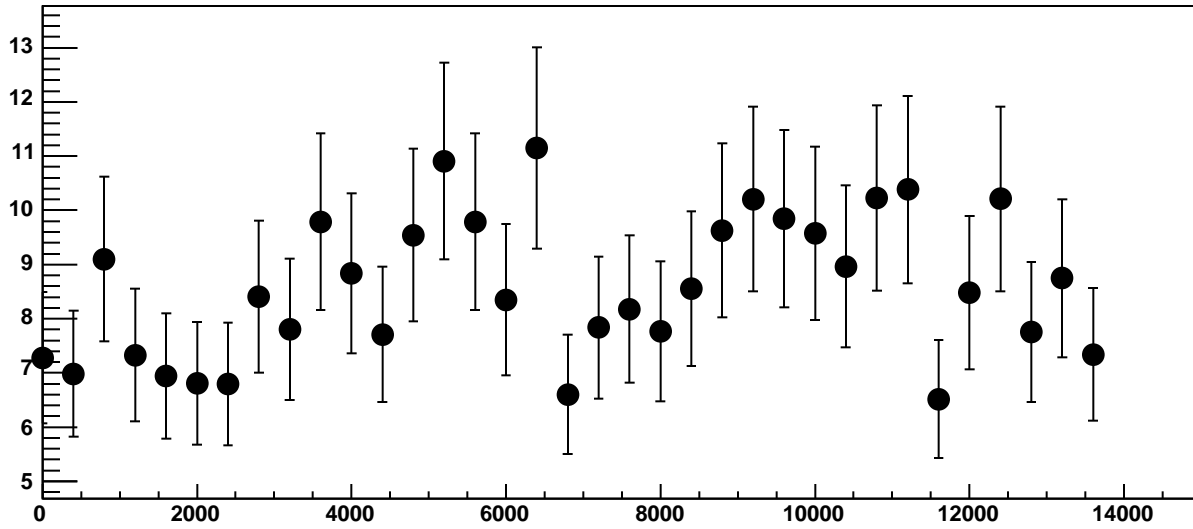


Chip 6, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

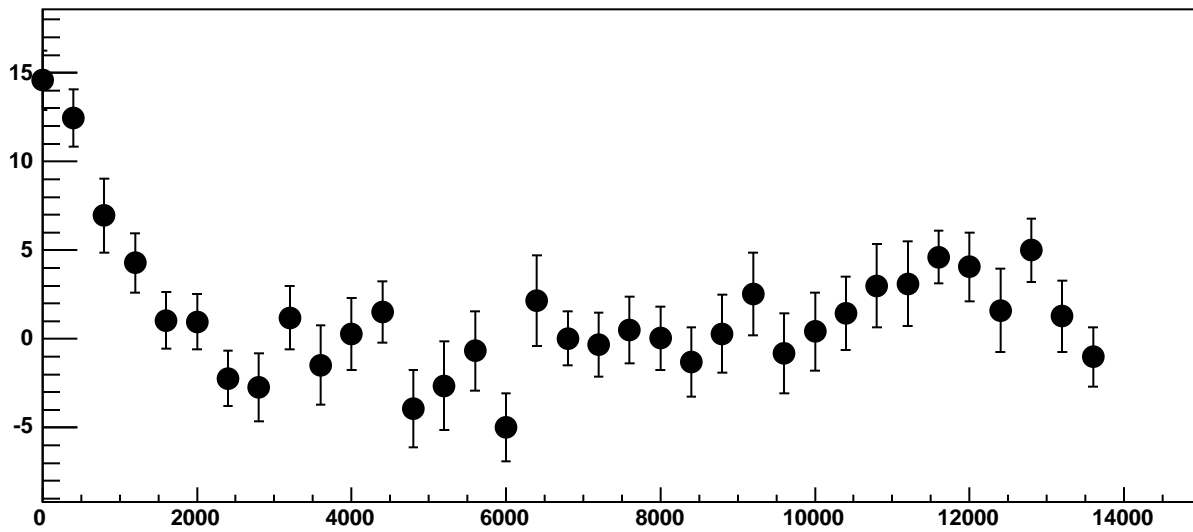


χ^2 / ndf 28.91 / 23
p0 -291.3 ± 0.827
p1 0.01218 ± 0.0001323

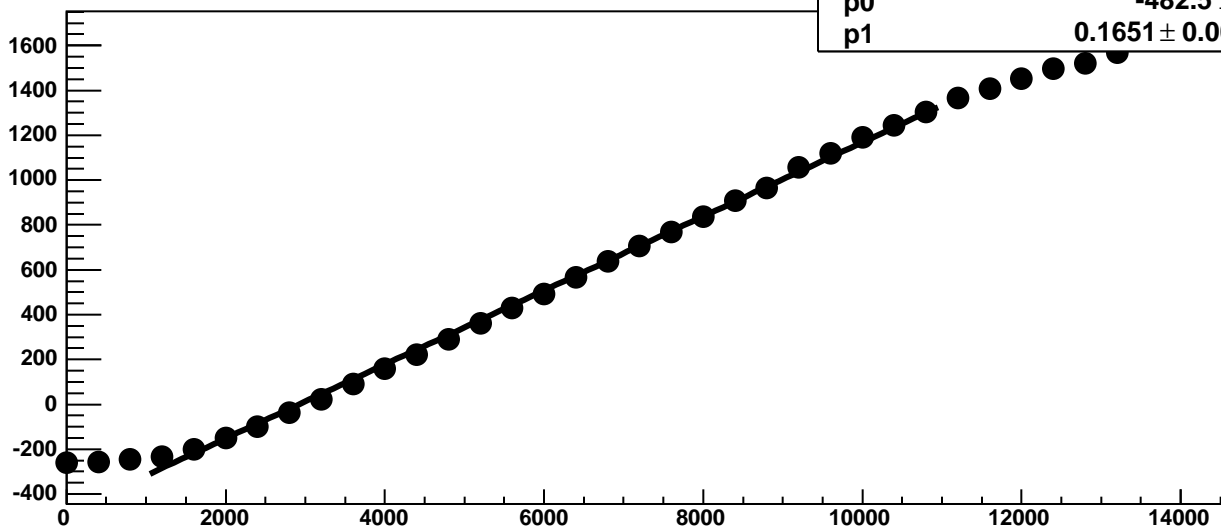
Chip 6, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



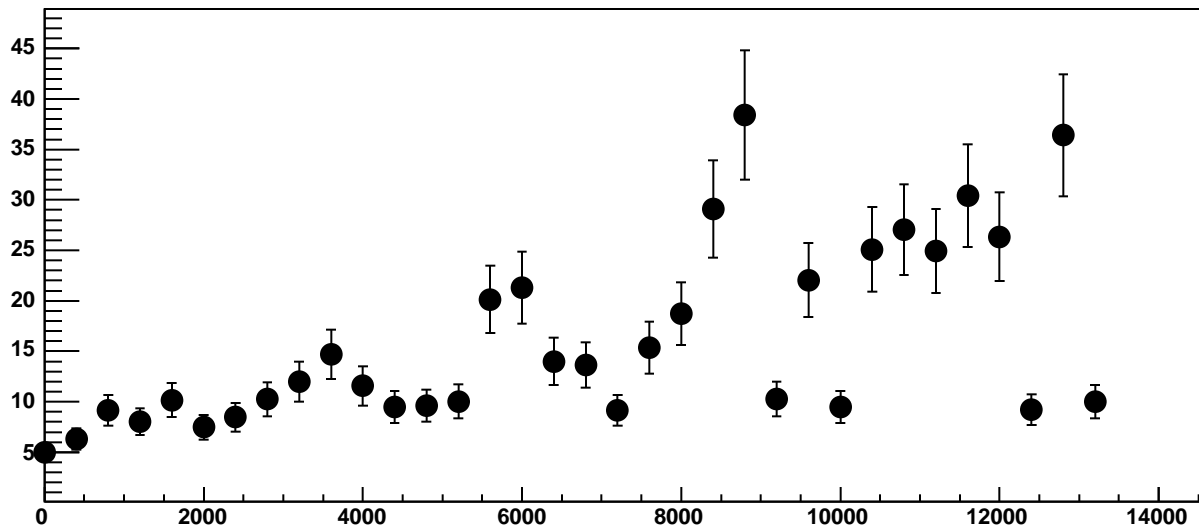
Chip 6, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



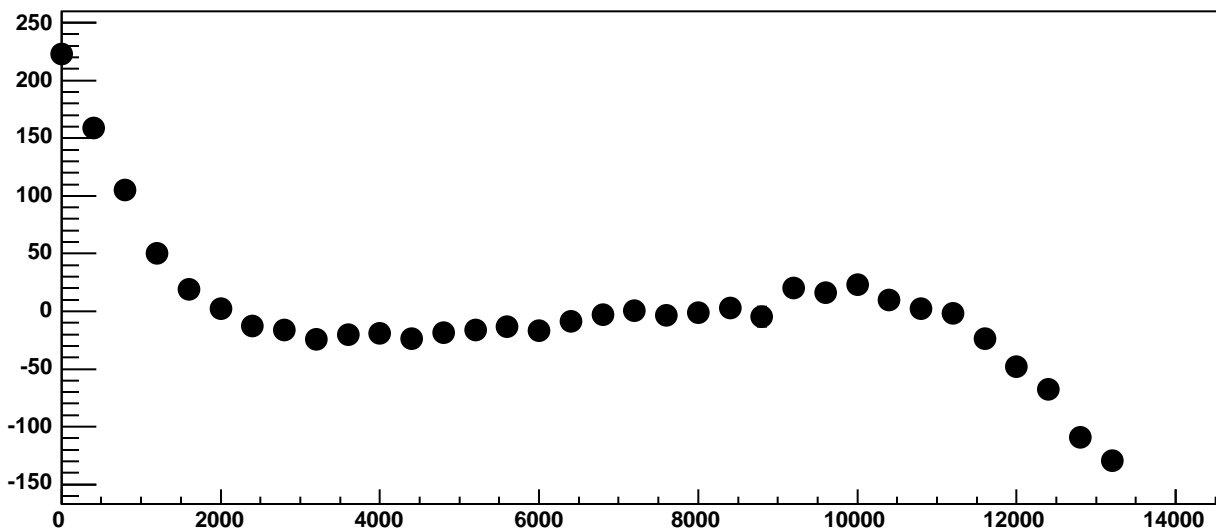
Chip 6, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC



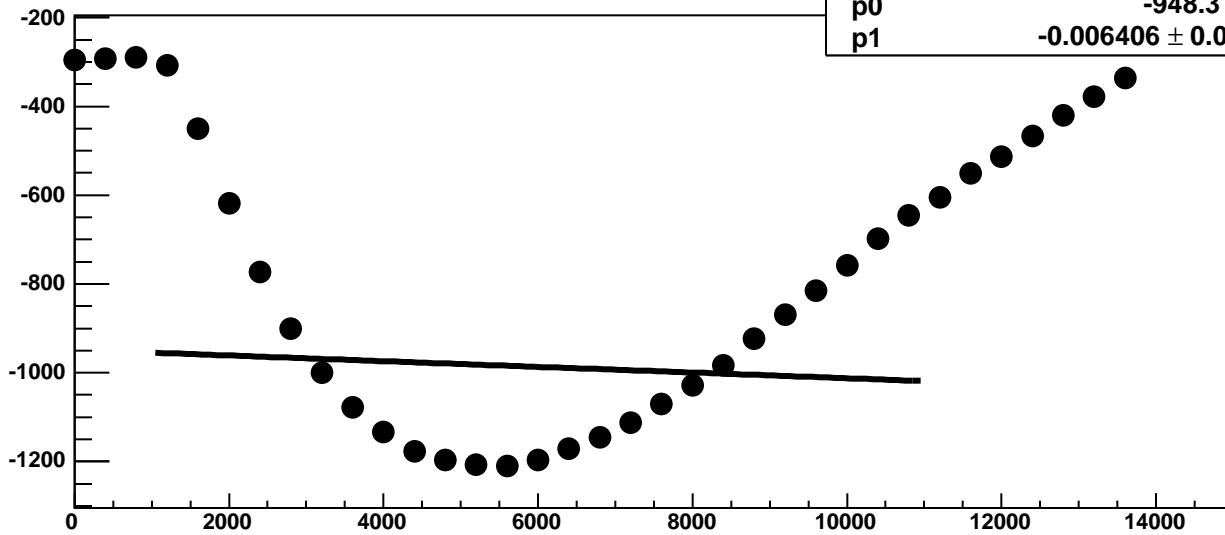
Chip 6, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC

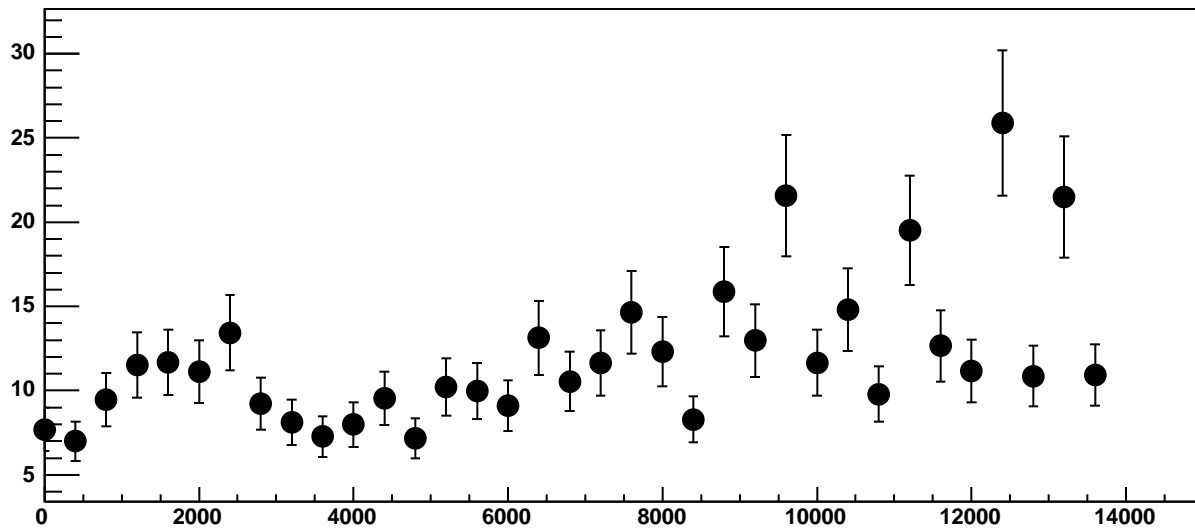


Chip 6, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

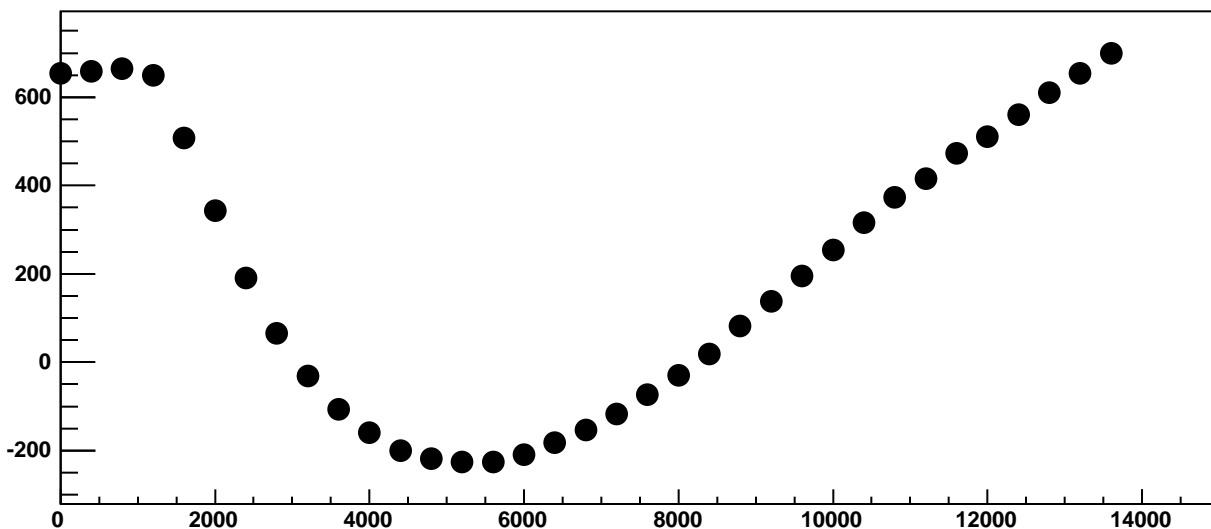


χ^2 / ndf 2.455e+05 / 23
p0 -948.3 ± 1.093
p1 -0.006406 ± 0.0001794

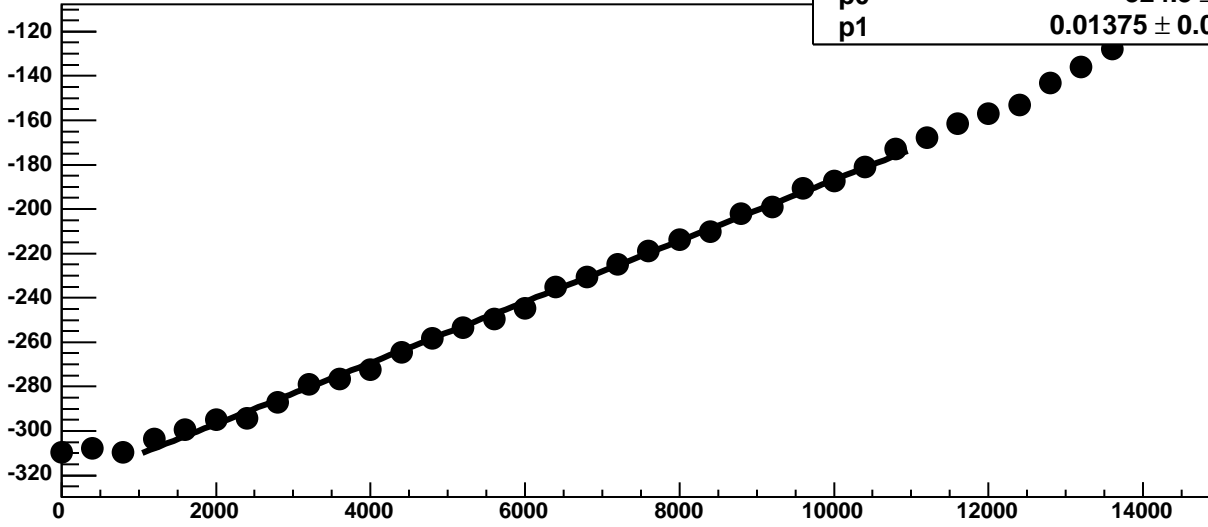
Chip 6, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



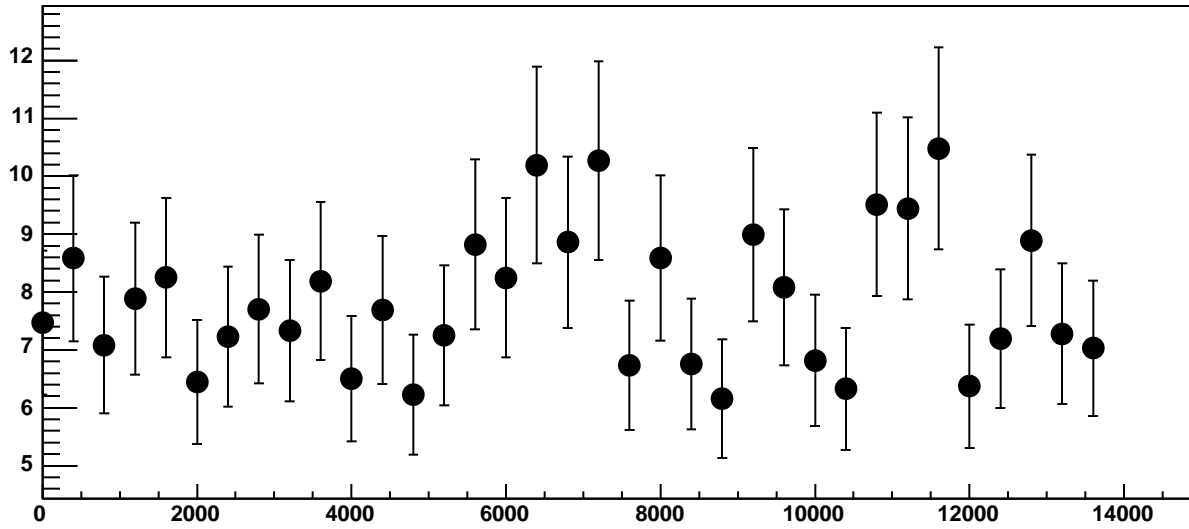
Chip 6, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



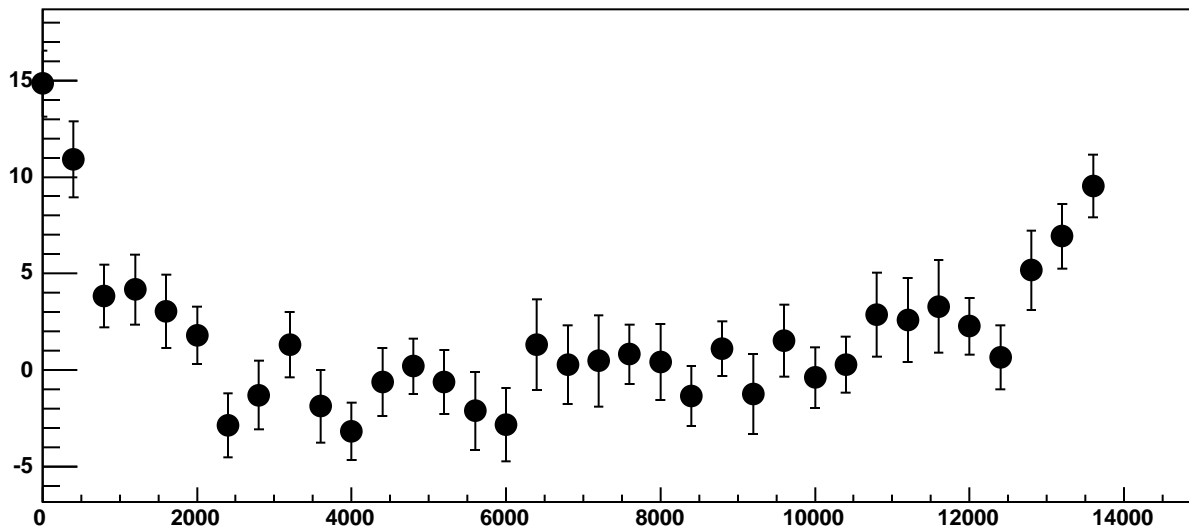
Chip 6, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



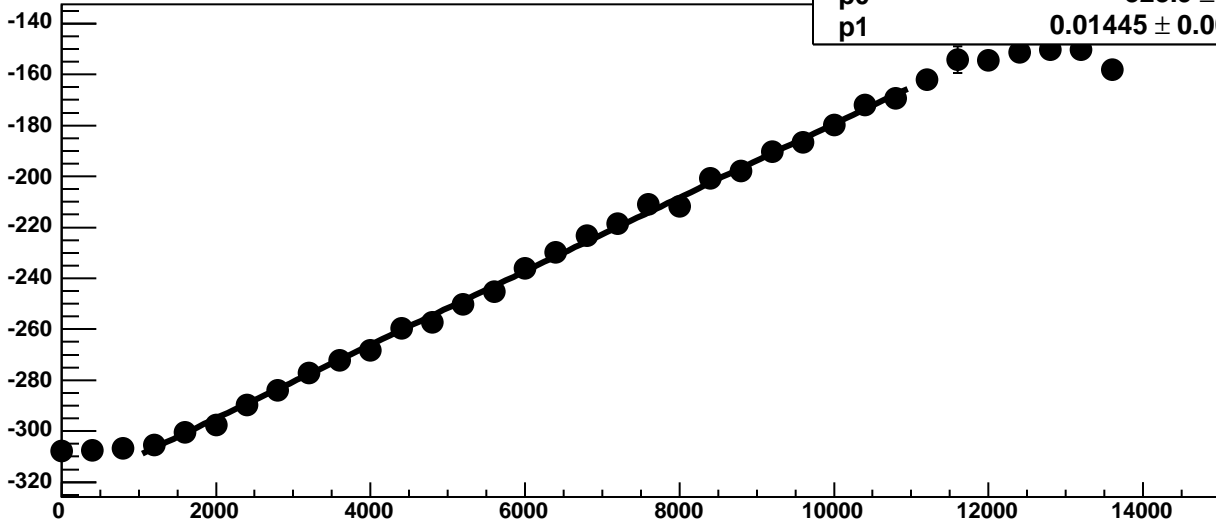
Chip 6, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



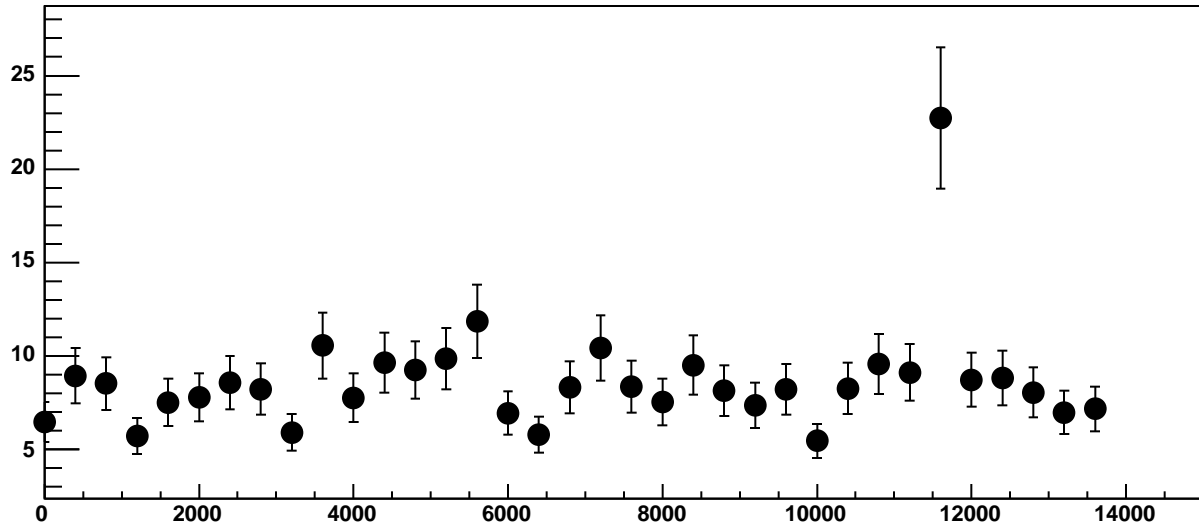
Chip 6, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



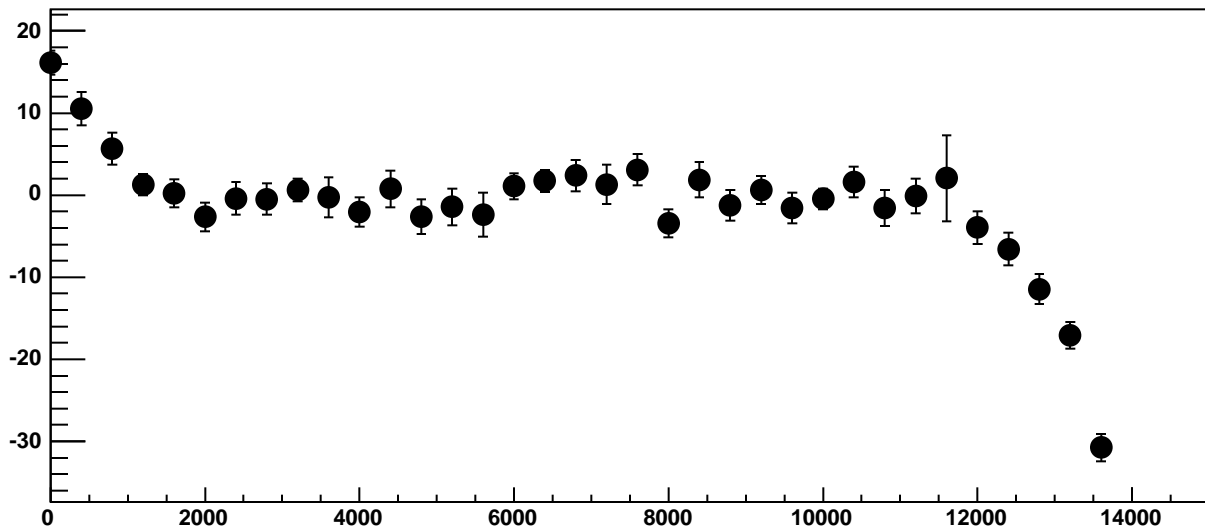
Chip 6, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC



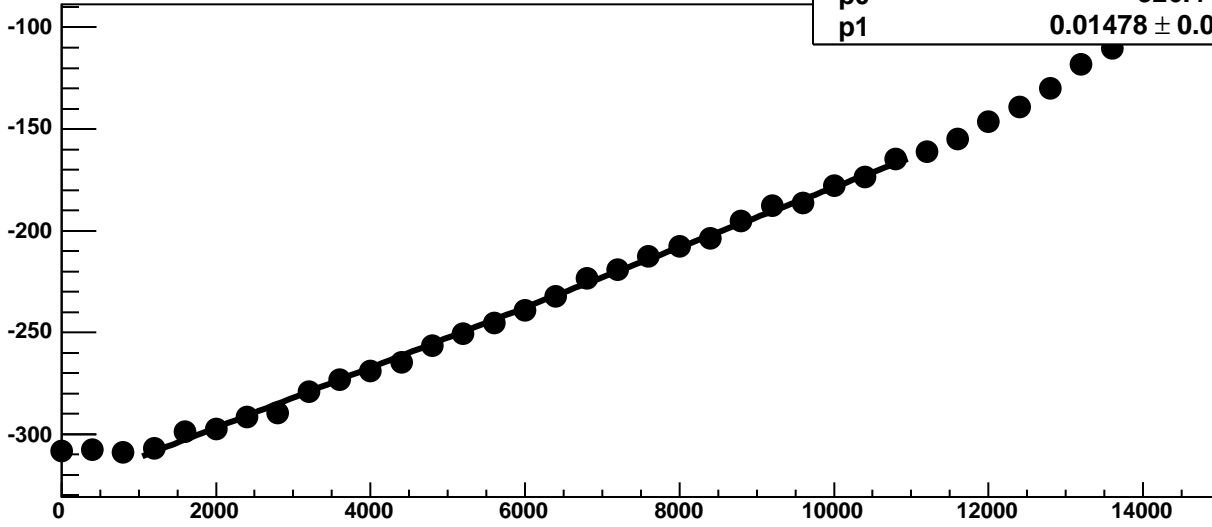
Chip 6, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

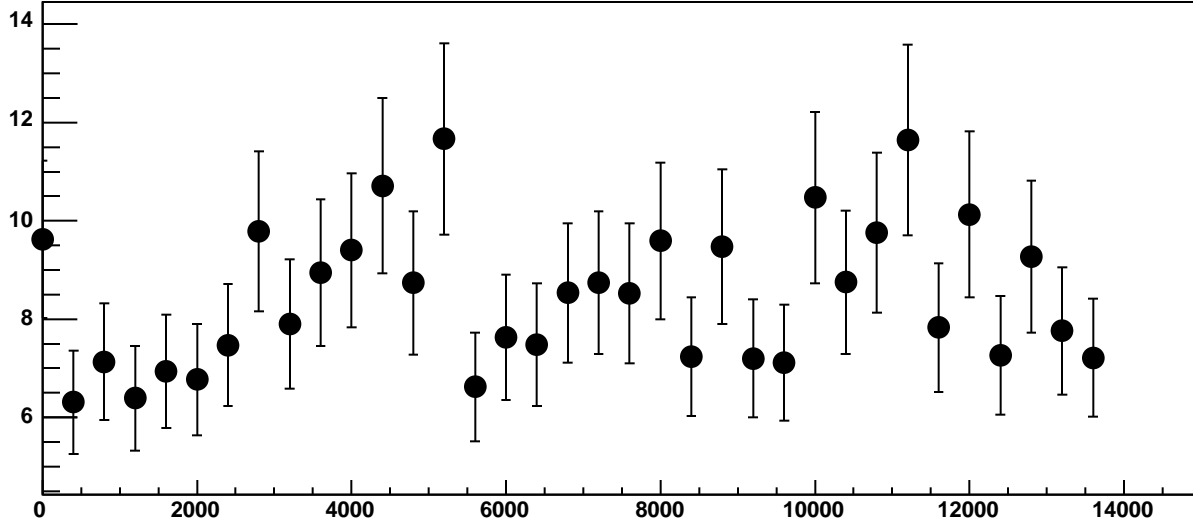


Chip 6, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

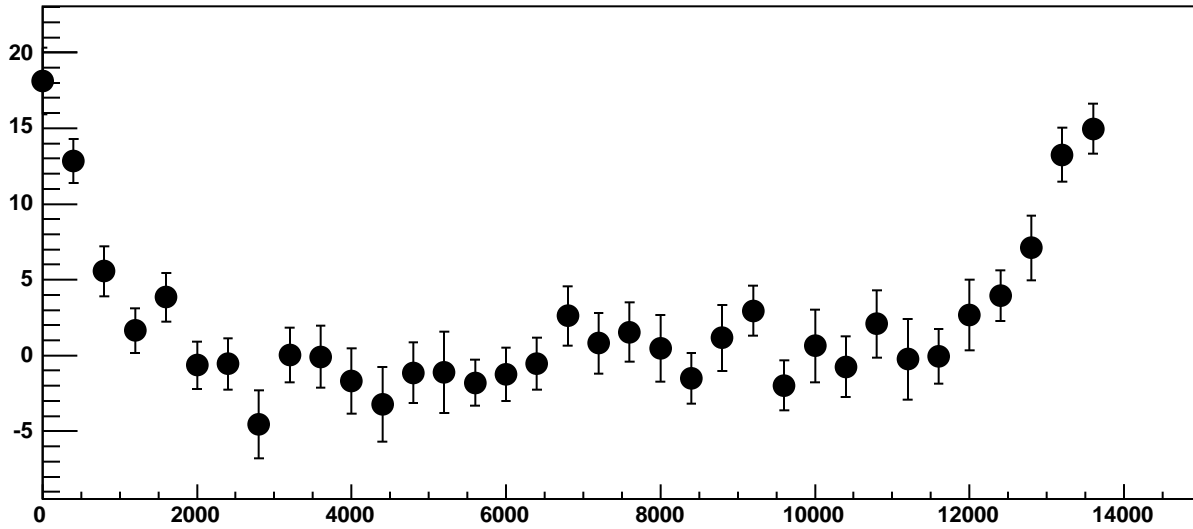


χ^2 / ndf 25.68 / 23
p0 -326.4 ± 0.812
p1 0.01478 ± 0.0001259

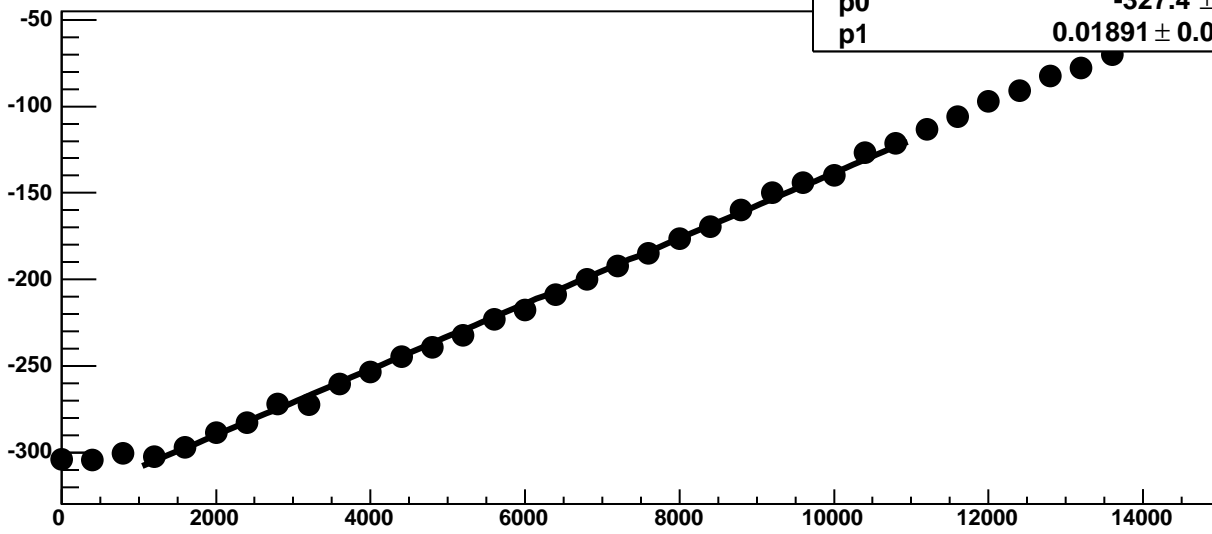
Chip 6, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

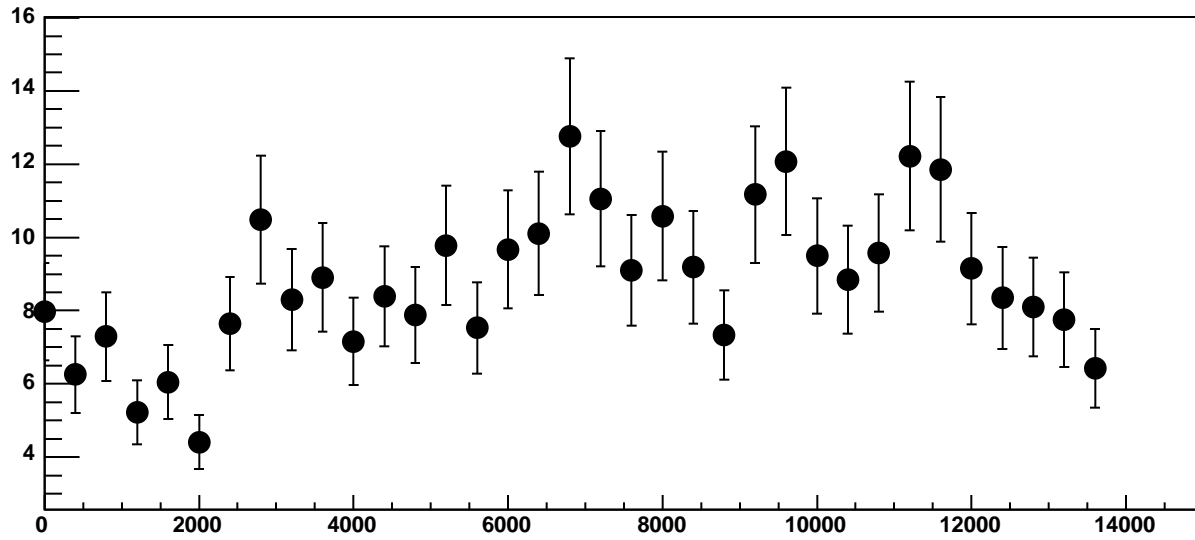


Chip 6, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

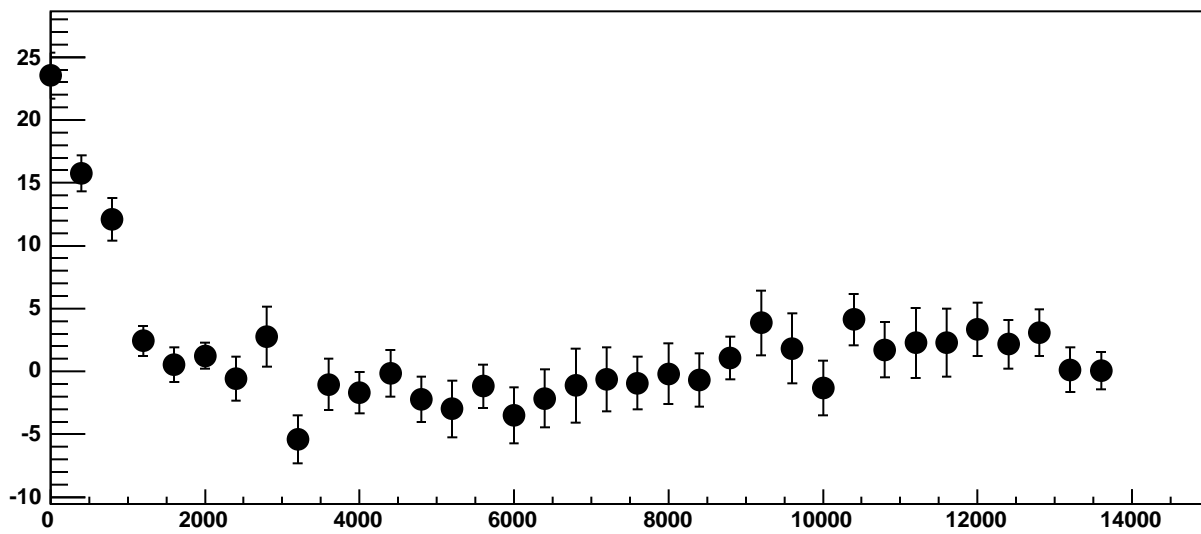


χ^2 / ndf 32.28 / 23
p0 -327.4 ± 0.7042
p1 0.01891 ± 0.0001222

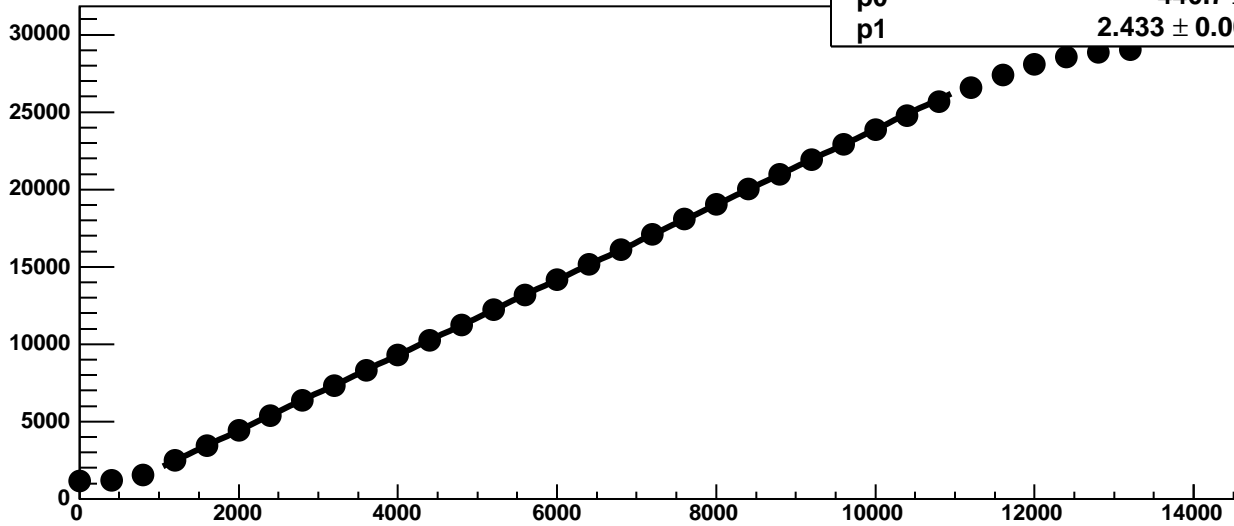
Chip 6, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



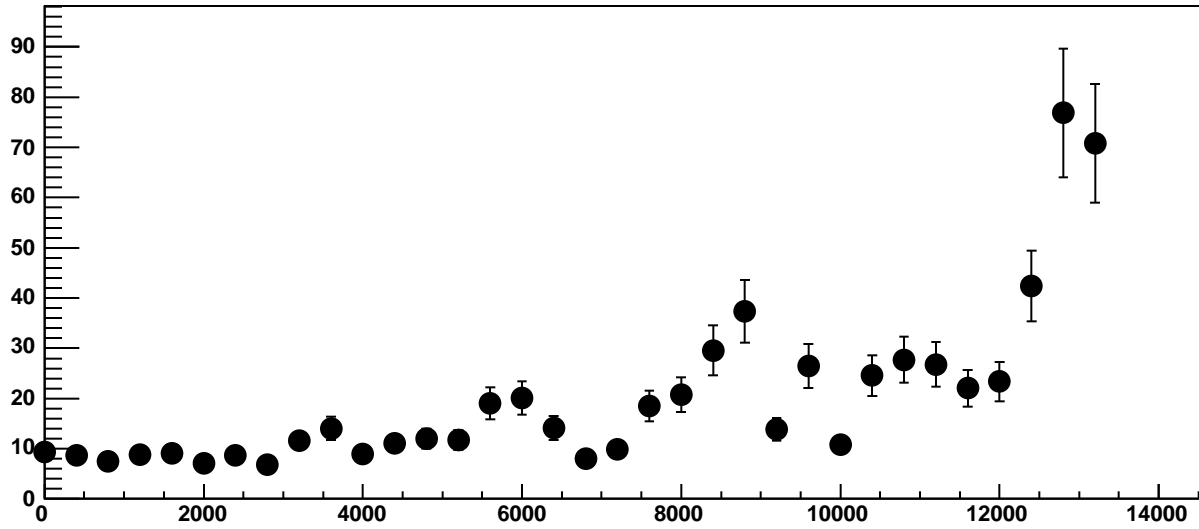
Chip 6, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



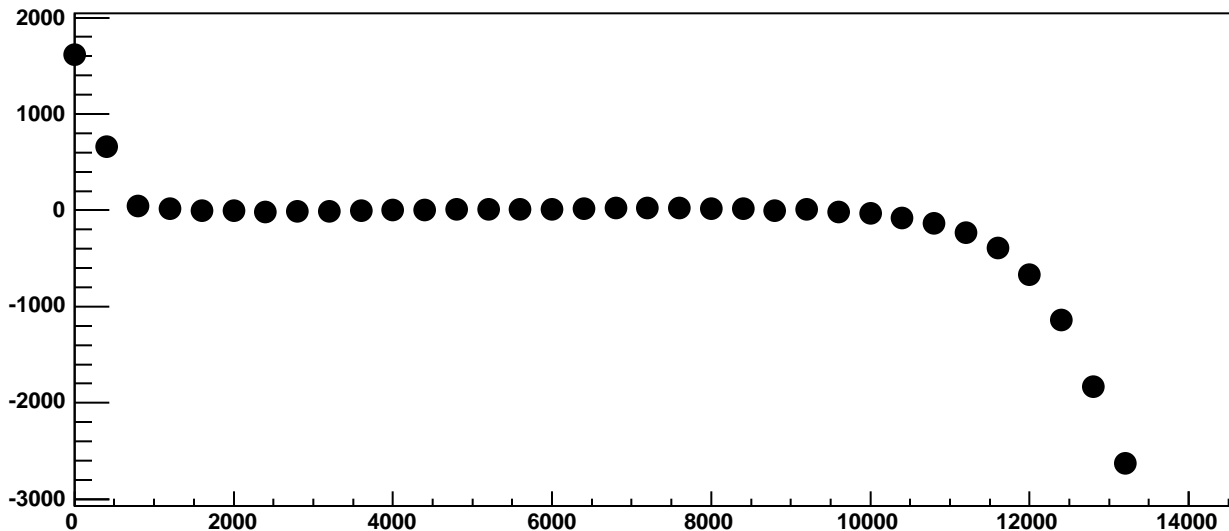
Chip 6, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC



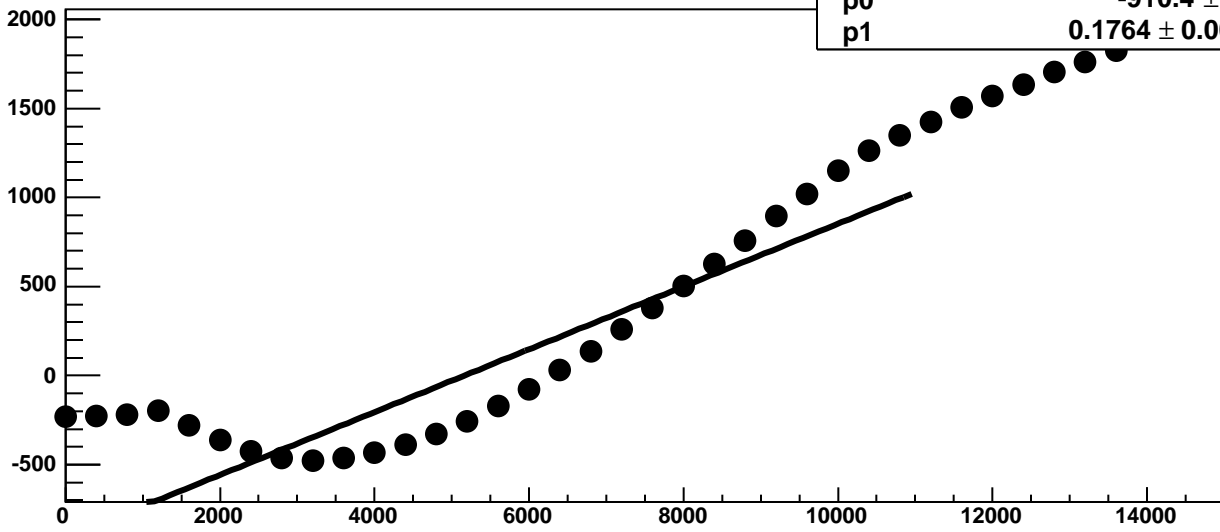
Chip 6, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC

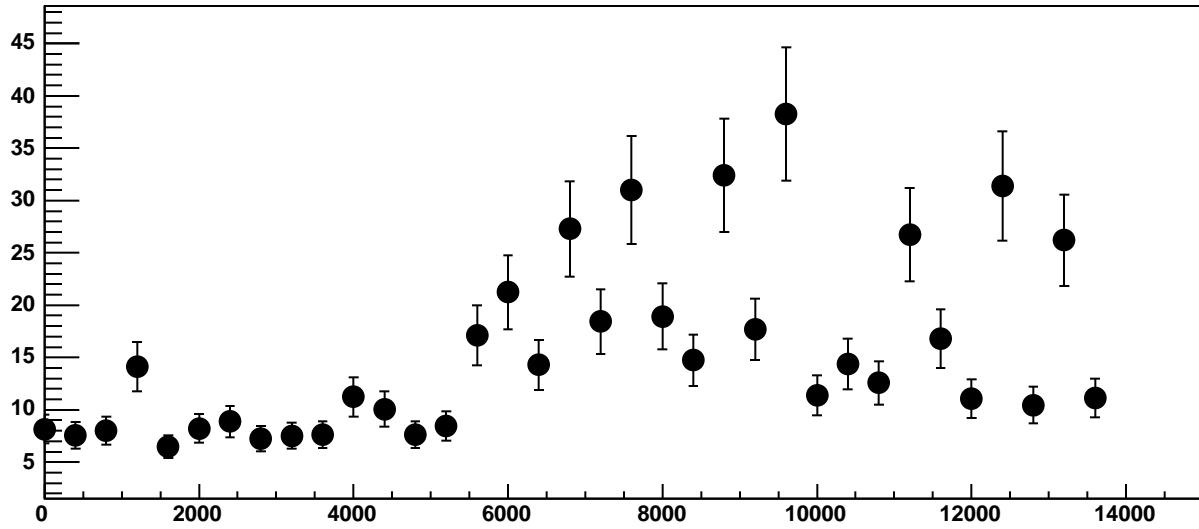


Chip 6, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

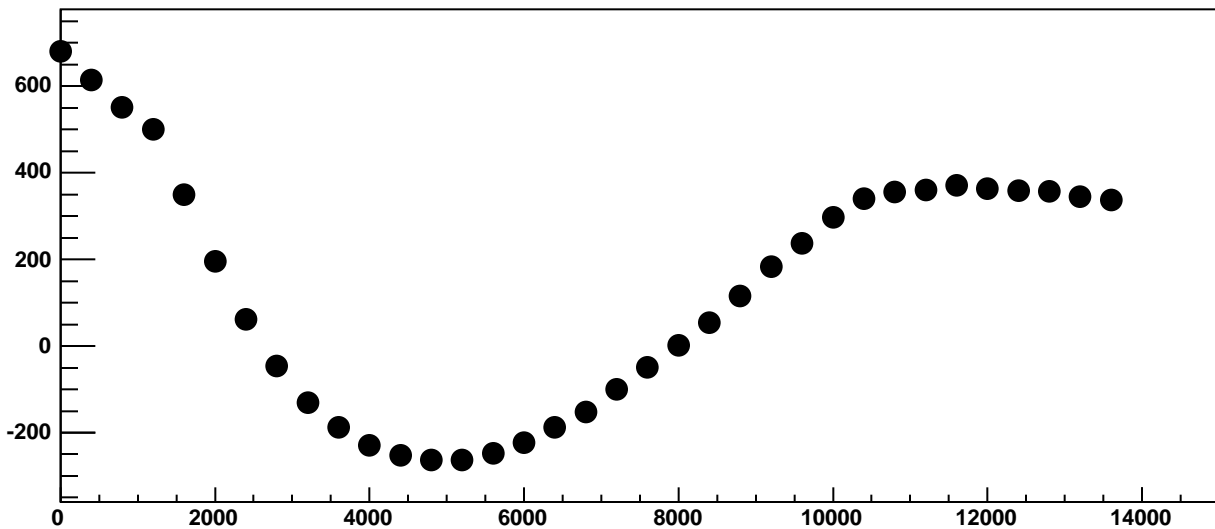


χ^2 / ndf 2.229e+05 / 23
p0 -910.4 ± 0.9836
p1 0.1764 ± 0.0001913

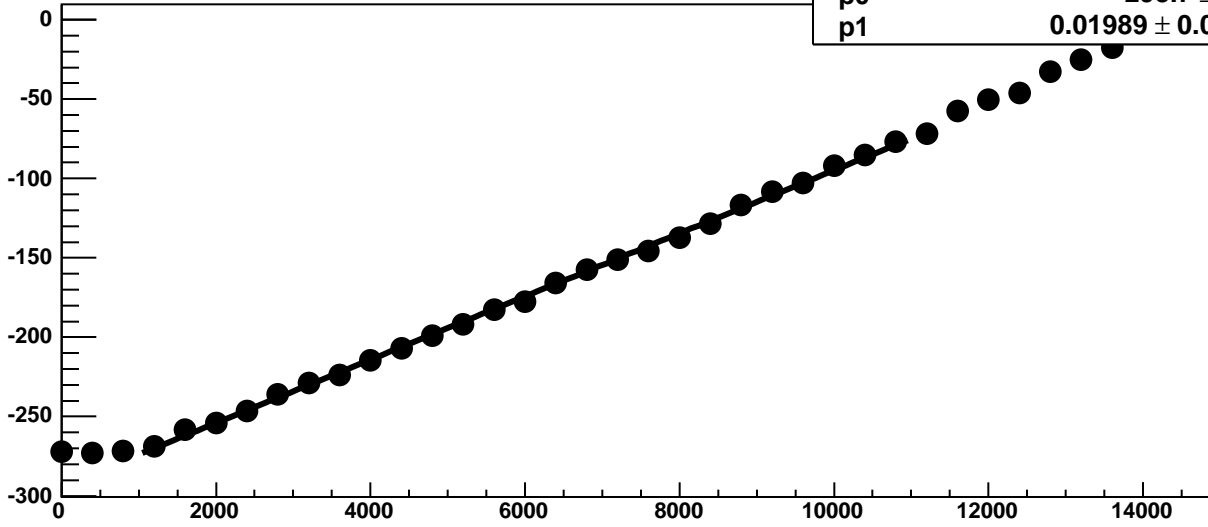
Chip 6, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



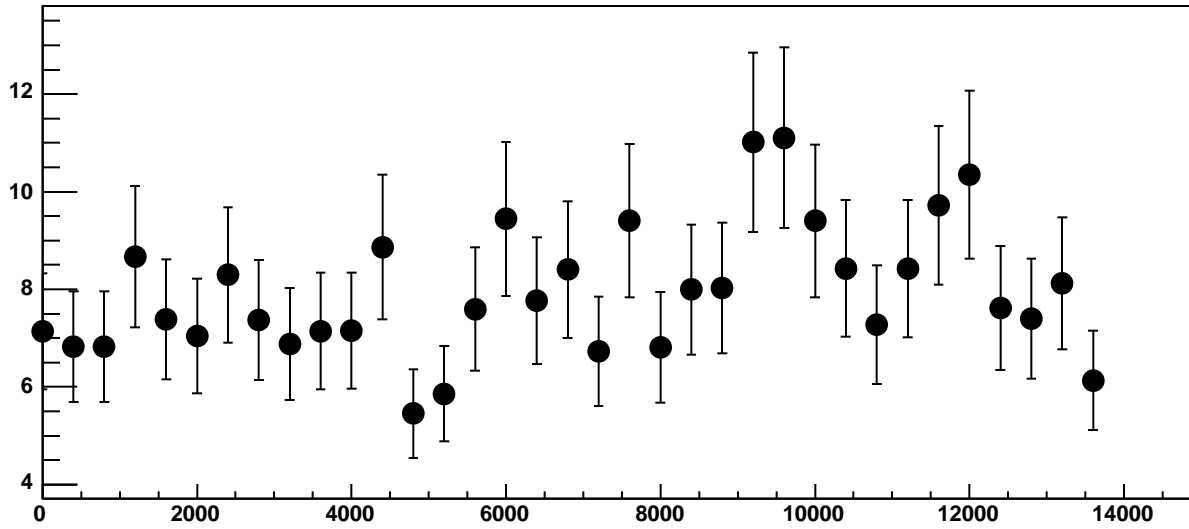
Chip 6, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



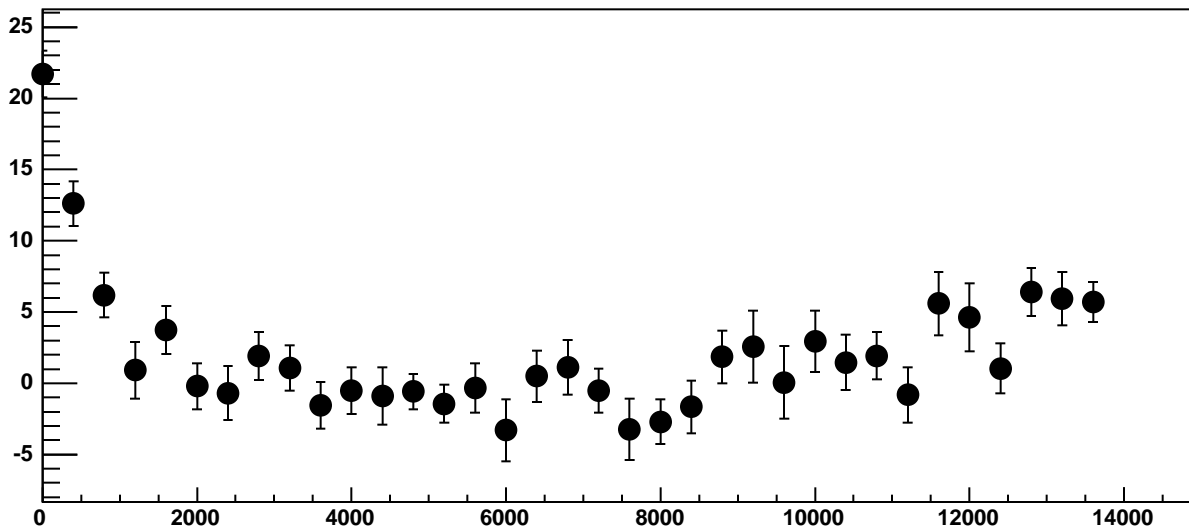
Chip 6, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



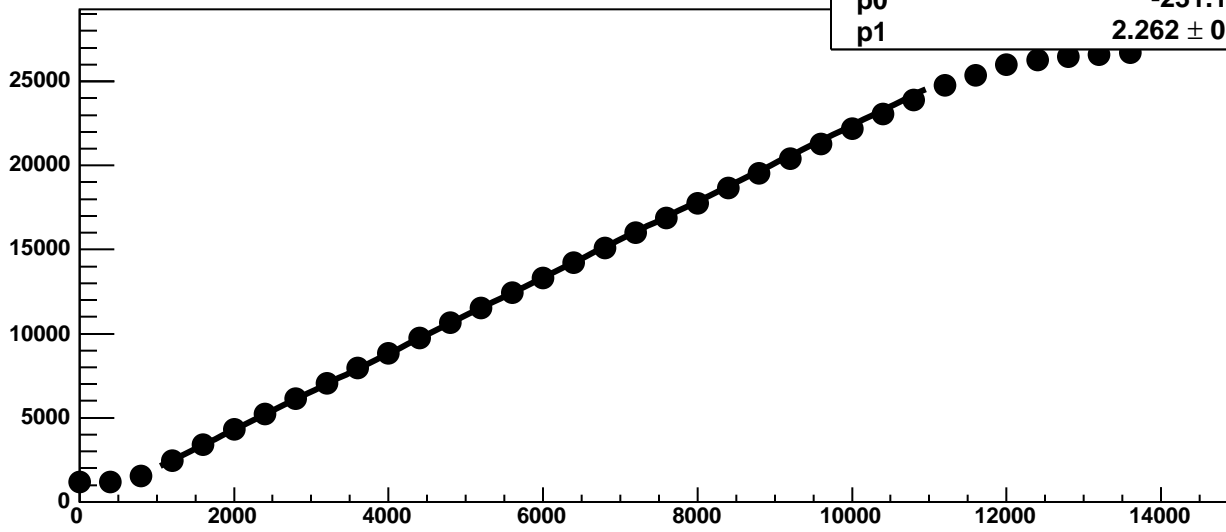
Chip 6, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



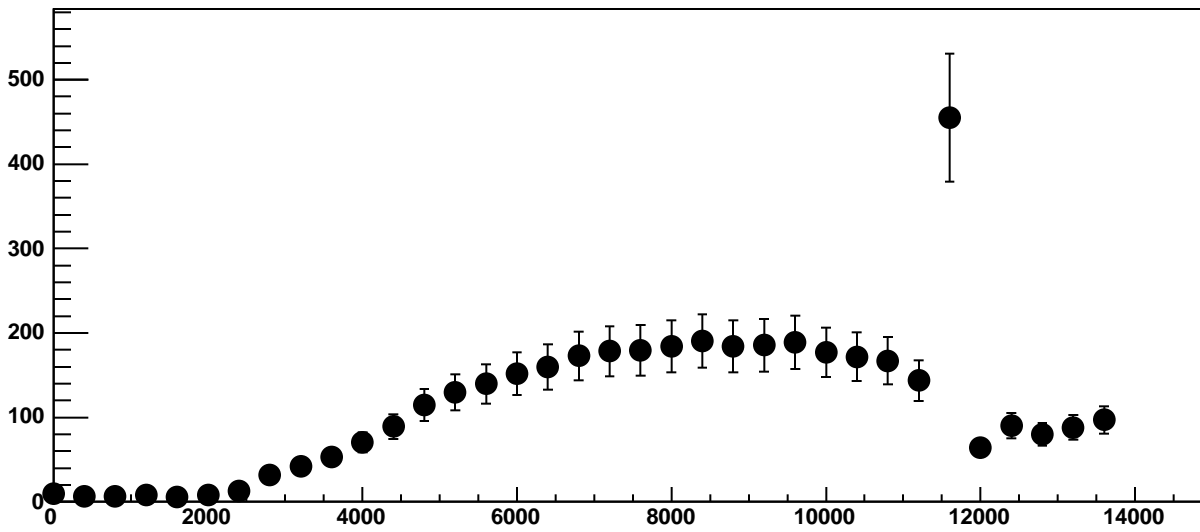
Chip 6, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



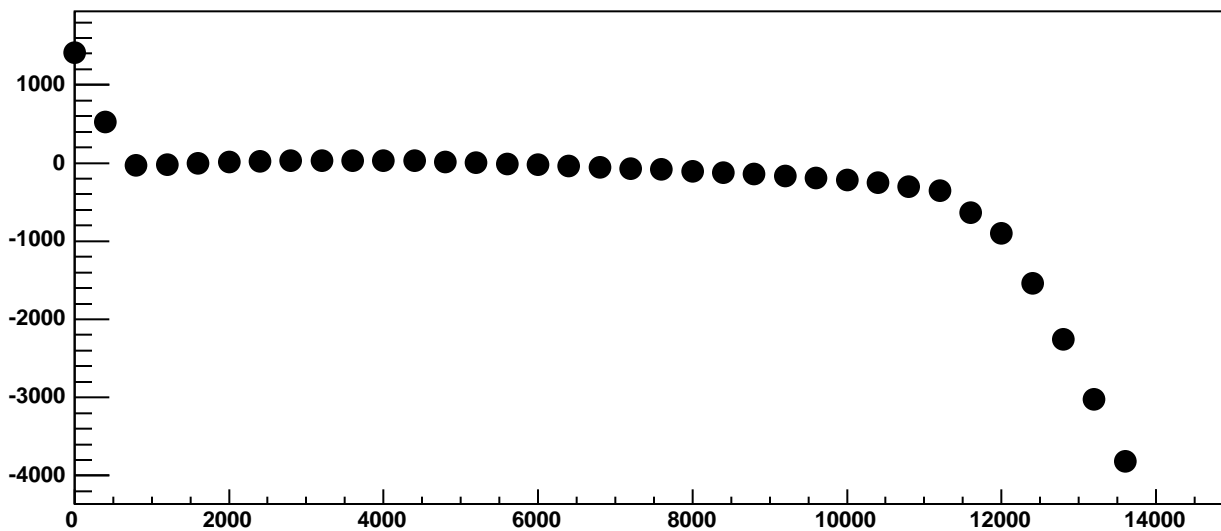
Chip 6, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC



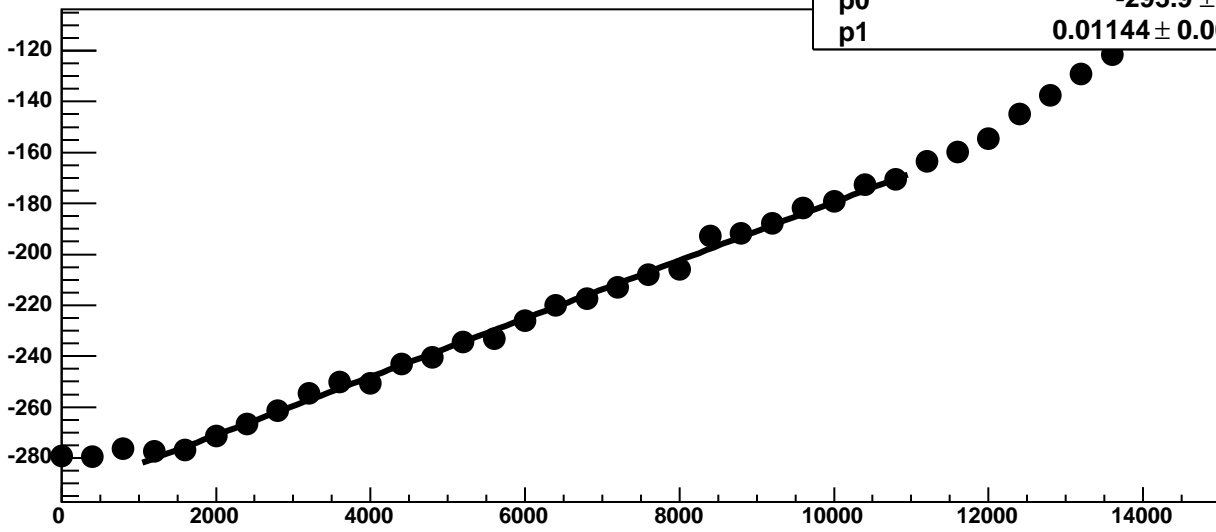
Chip 6, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC

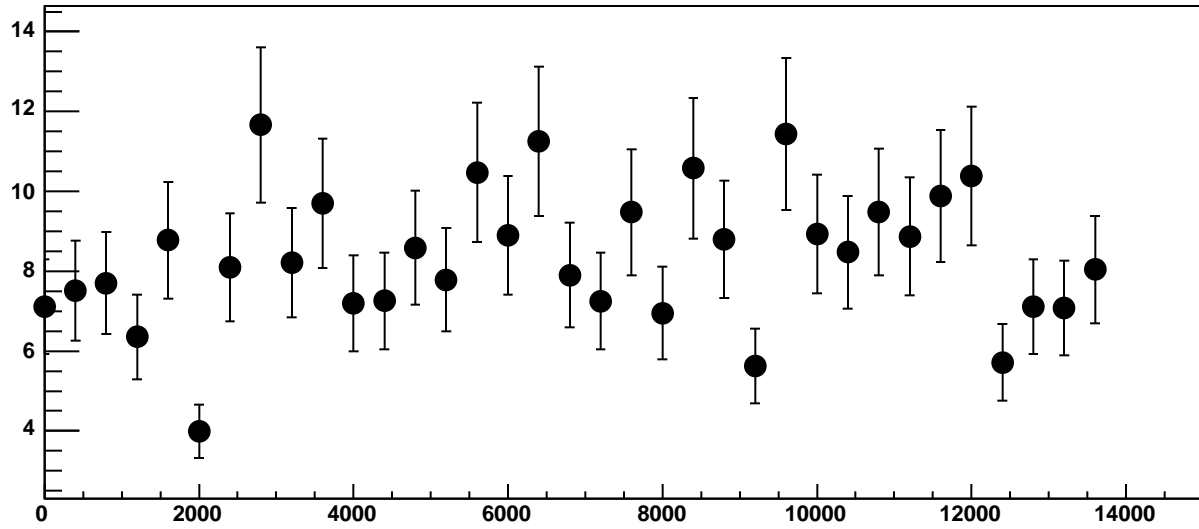


Chip 6, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

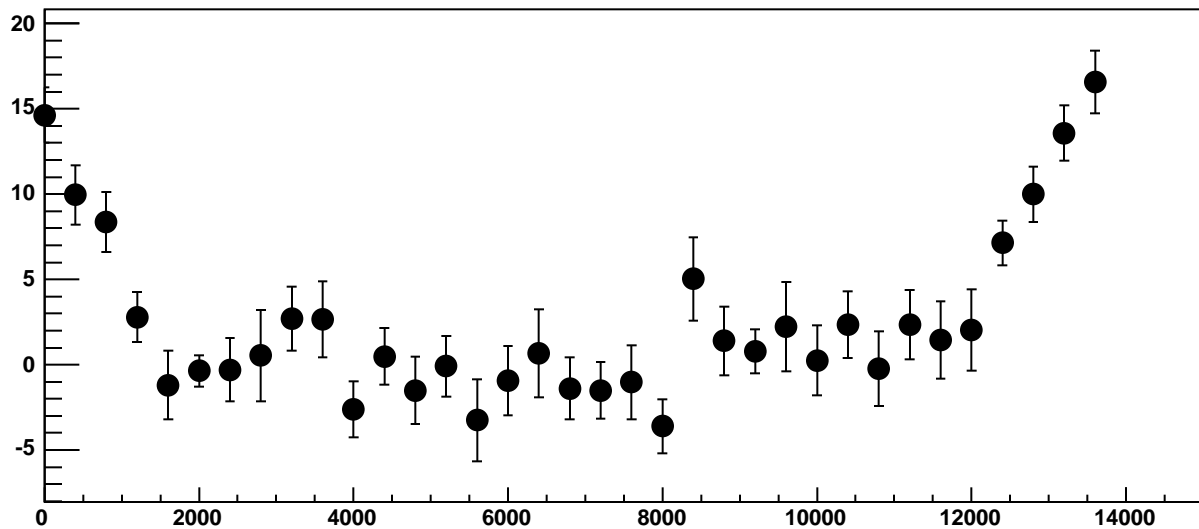


χ^2 / ndf 27.08 / 23
p0 -293.9 ± 0.7372
p1 0.01144 ± 0.0001183

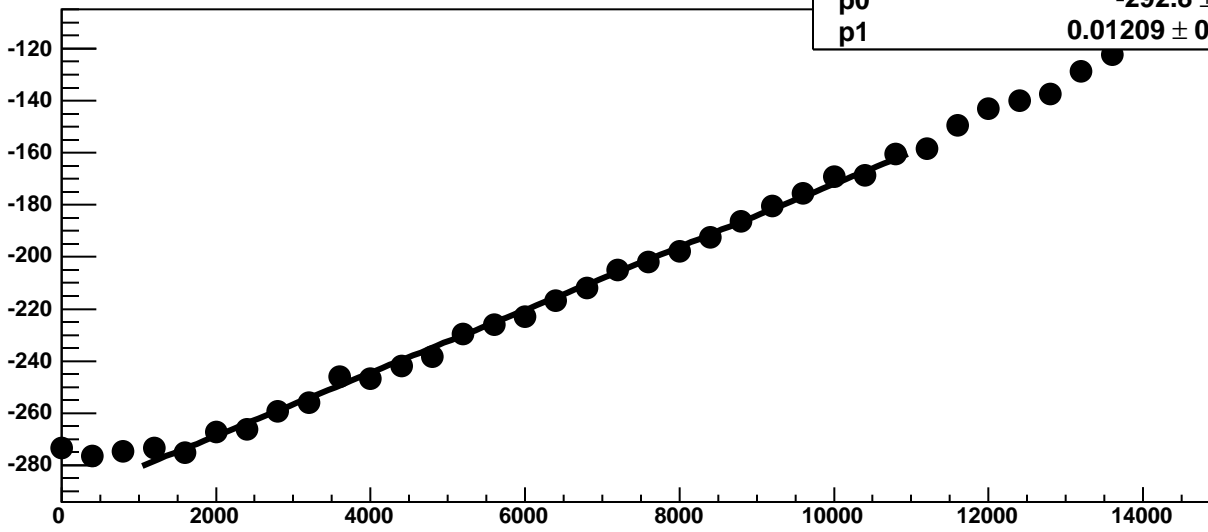
Chip 6, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



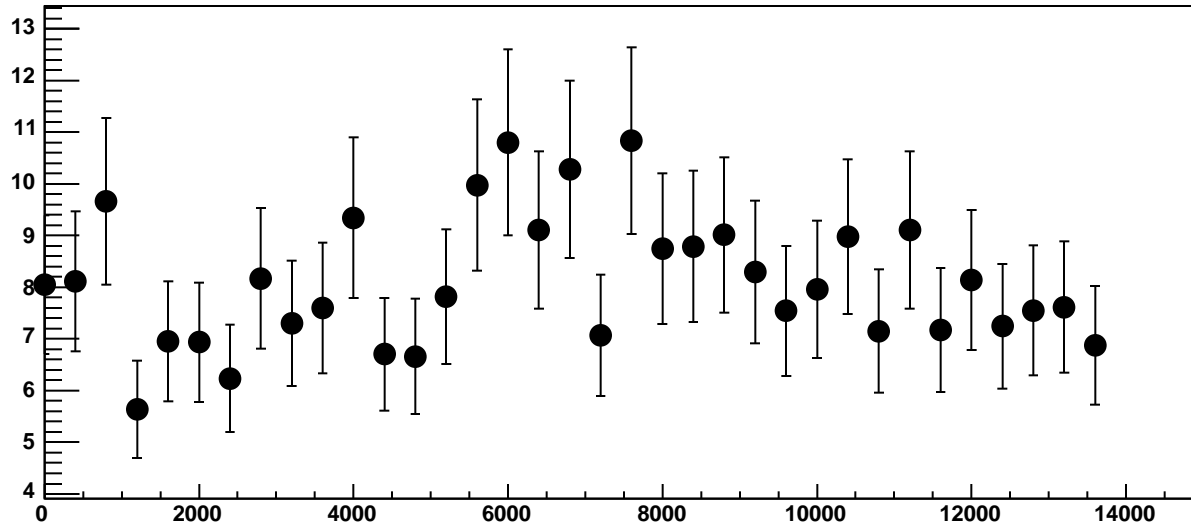
Chip 6, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



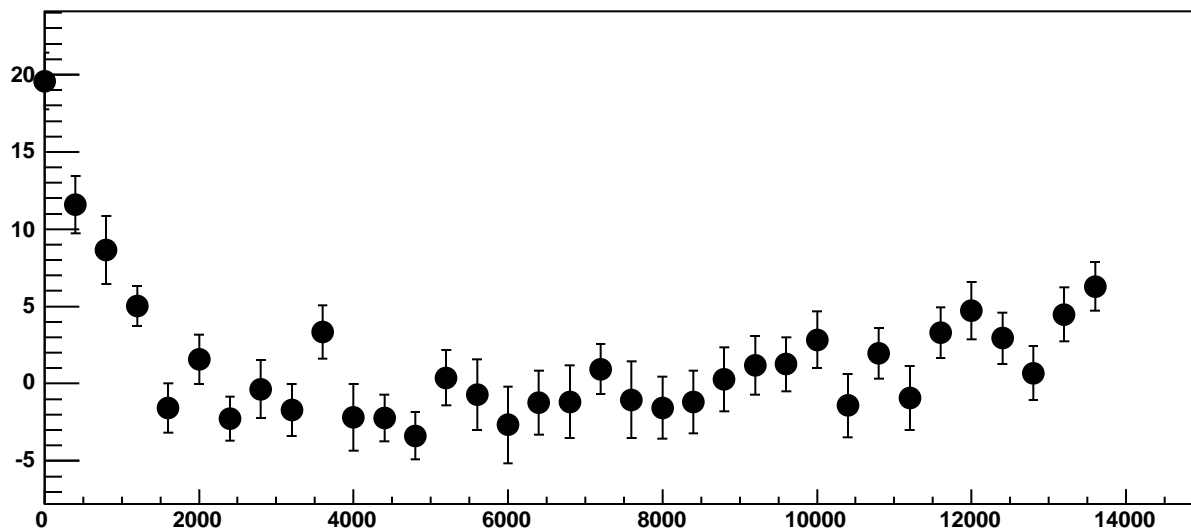
Chip 6, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



Chip 6, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

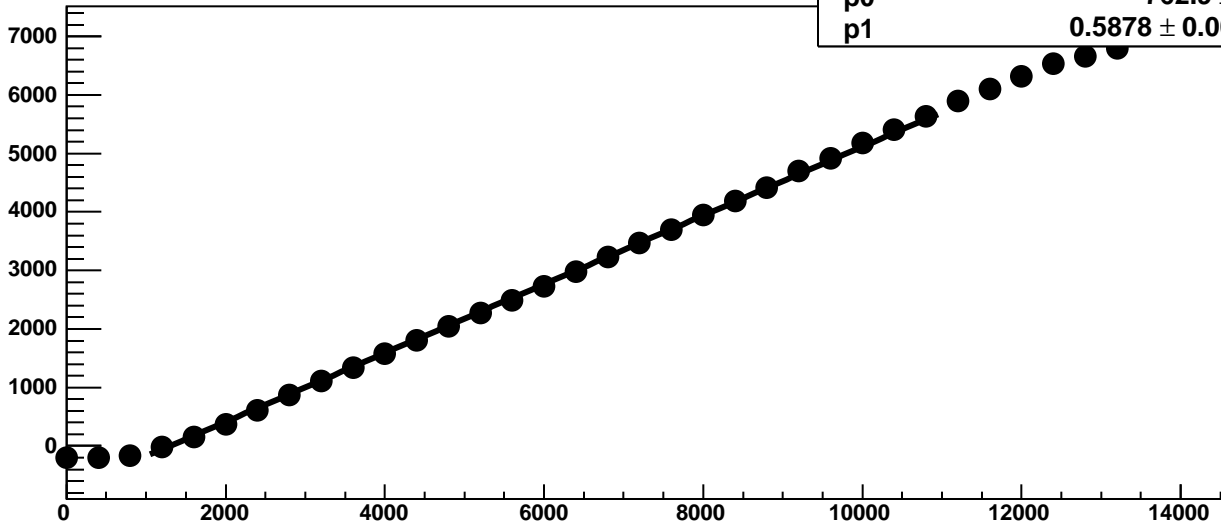


Chip 6, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

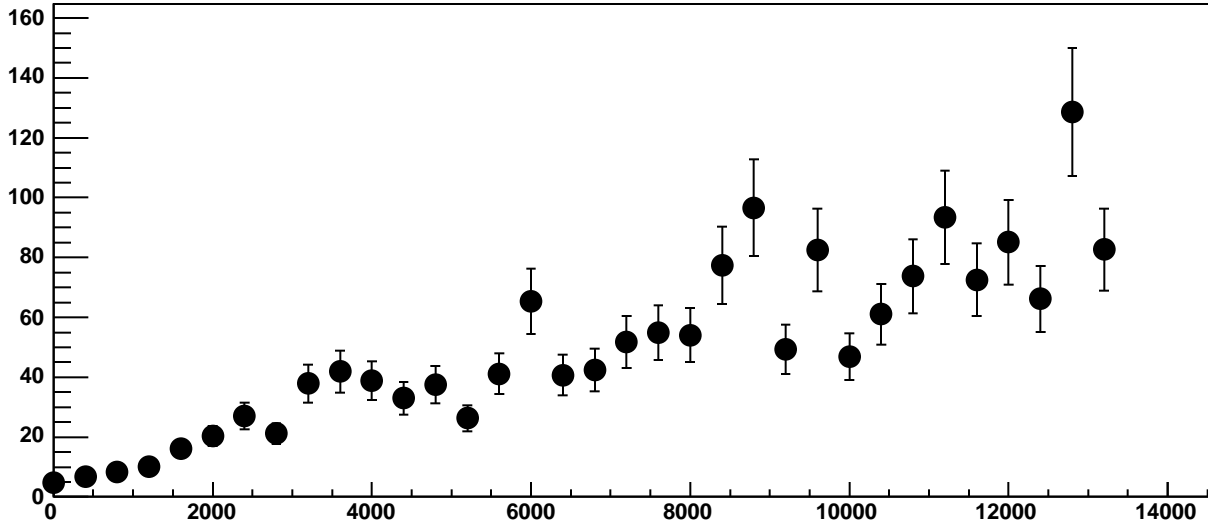


Chip 6, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC

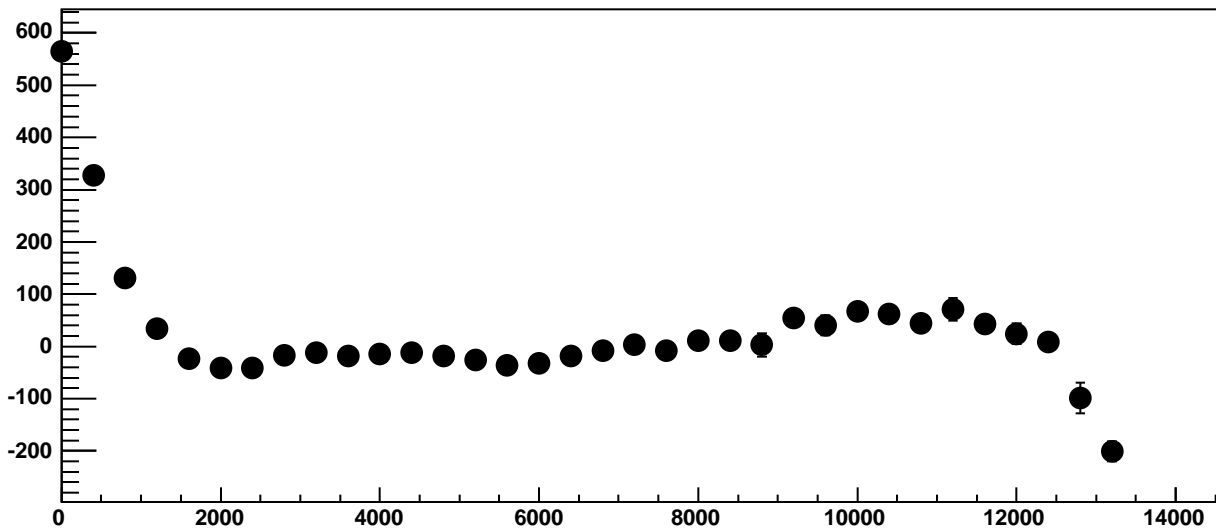
χ^2 / ndf 544.8 / 23
p0 -762.9 ± 2.168
p1 0.5878 ± 0.0005477



Chip 6, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC

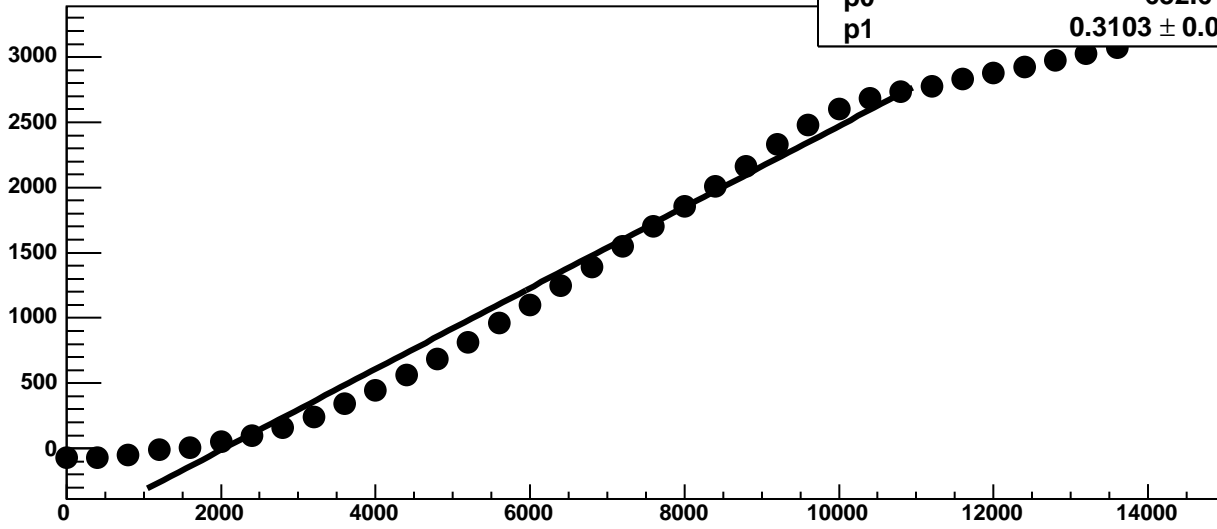


Chip 6, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC

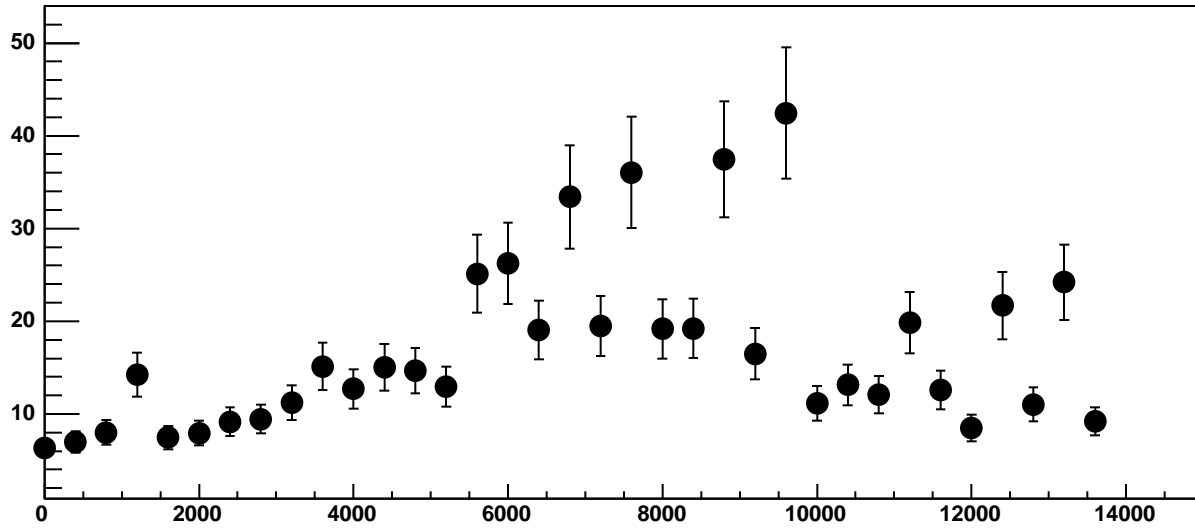


Chip 7, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

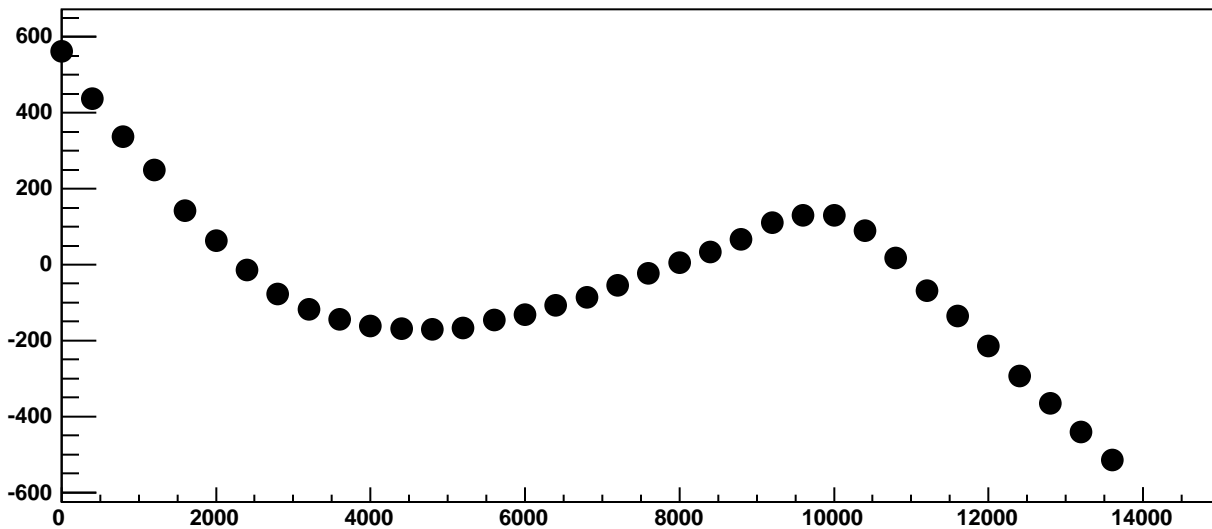
χ^2 / ndf 3.687e+04 / 23
p0 -632.6 ± 1.108
p1 0.3103 ± 0.0001972



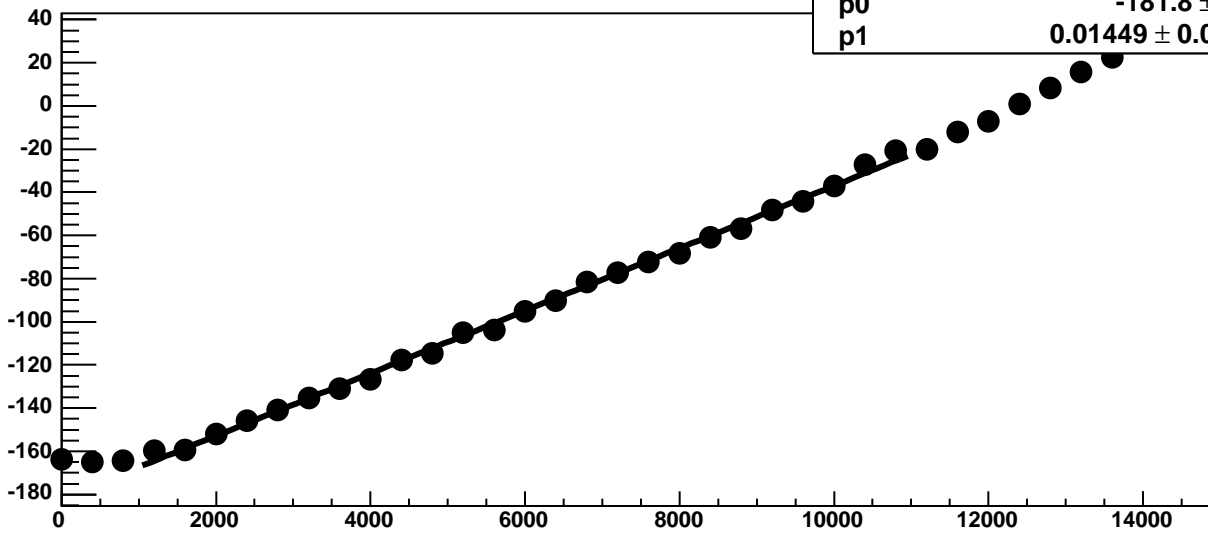
Chip 7, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



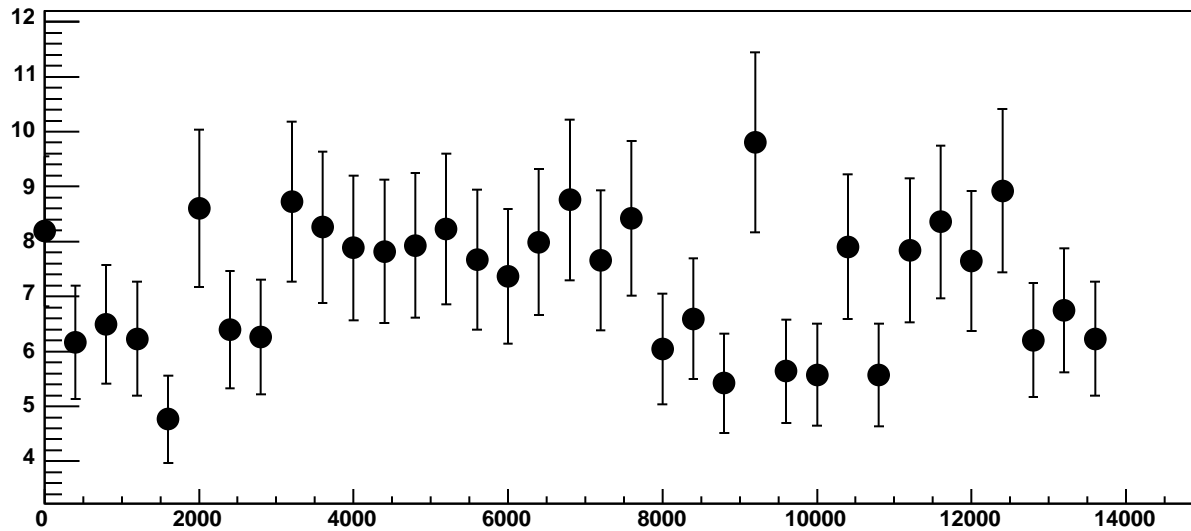
Chip 7, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC



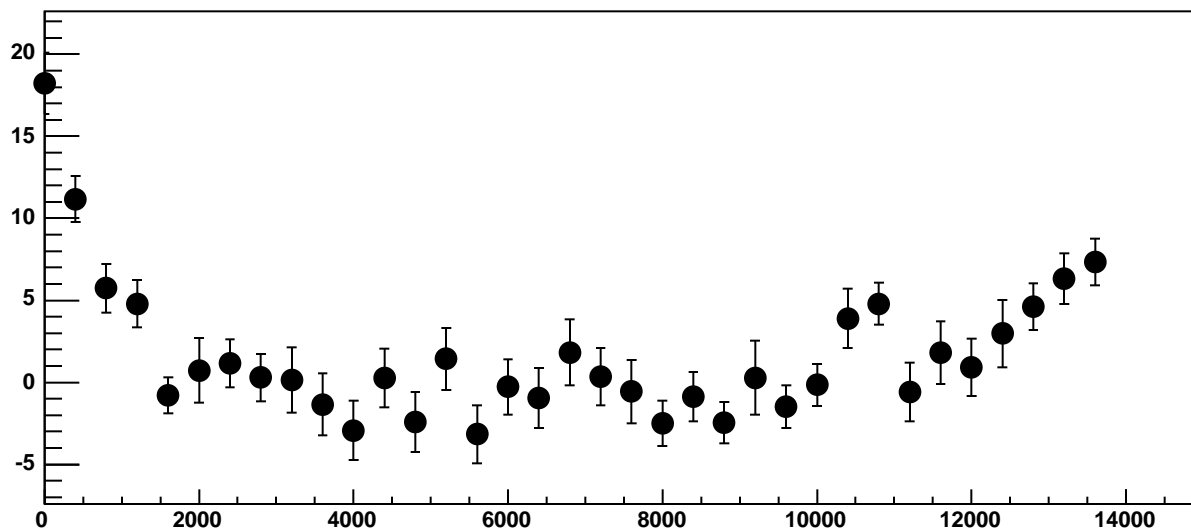
Chip 7, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC



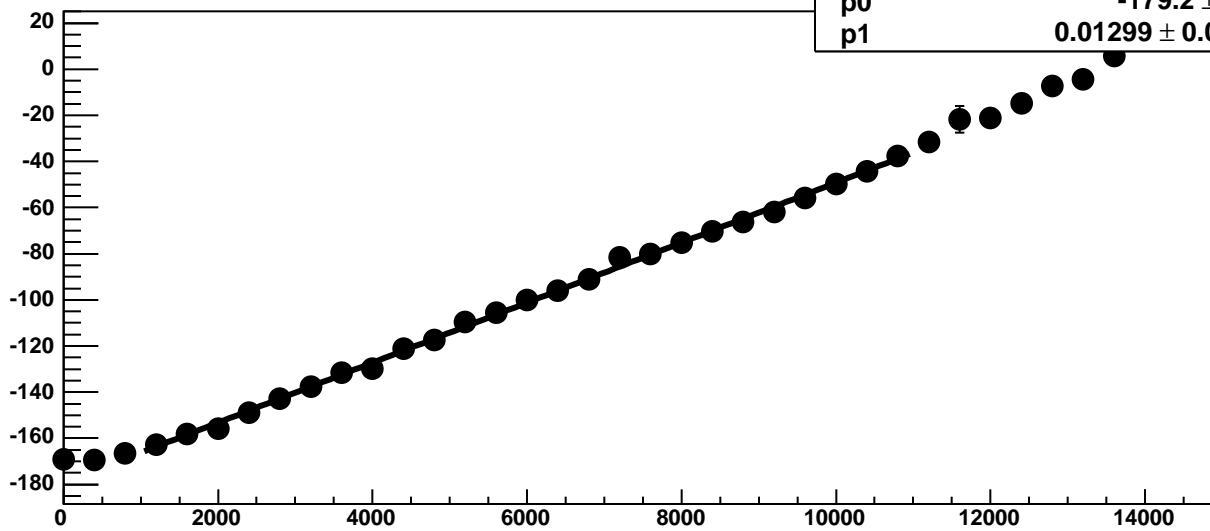
Chip 7, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



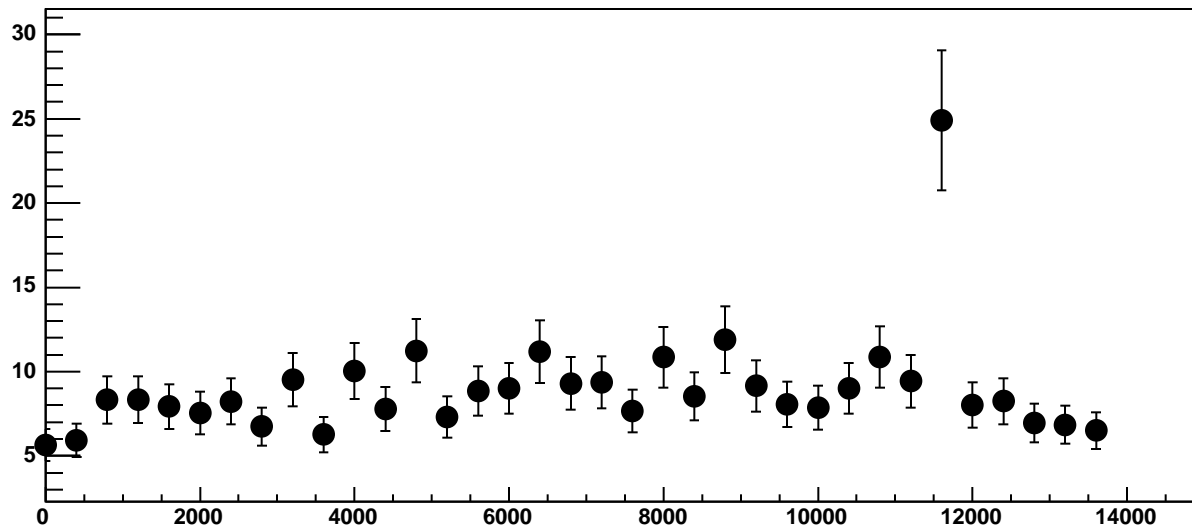
Chip 7, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



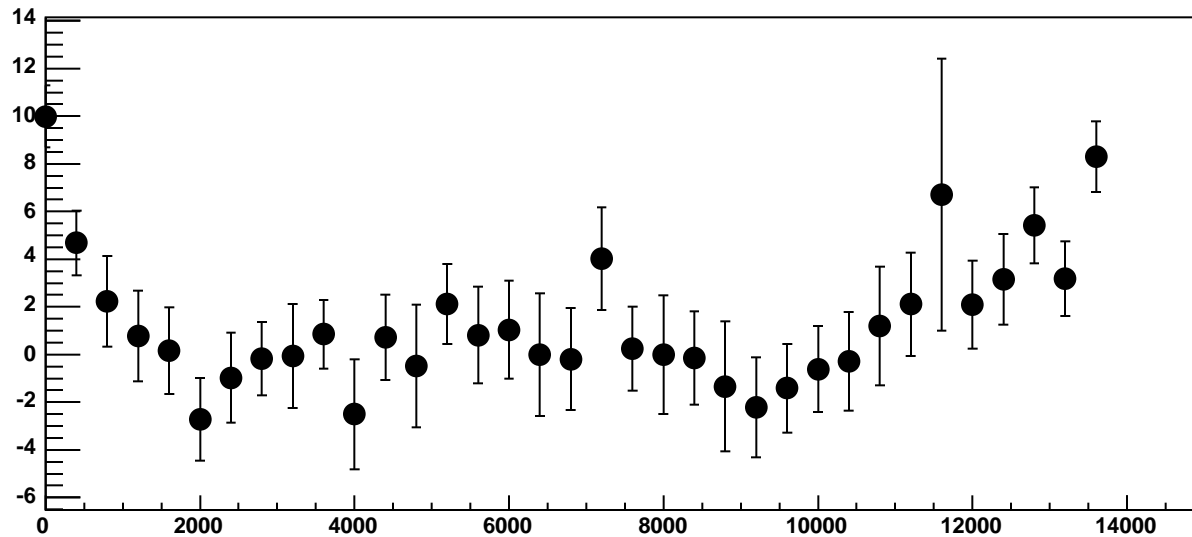
Chip 7, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



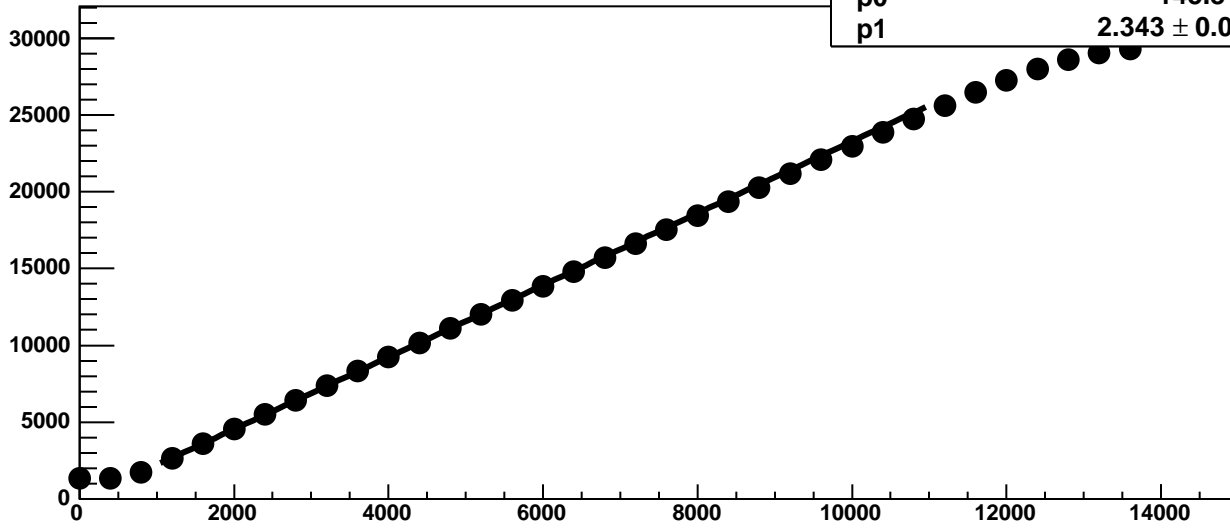
Chip 7, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



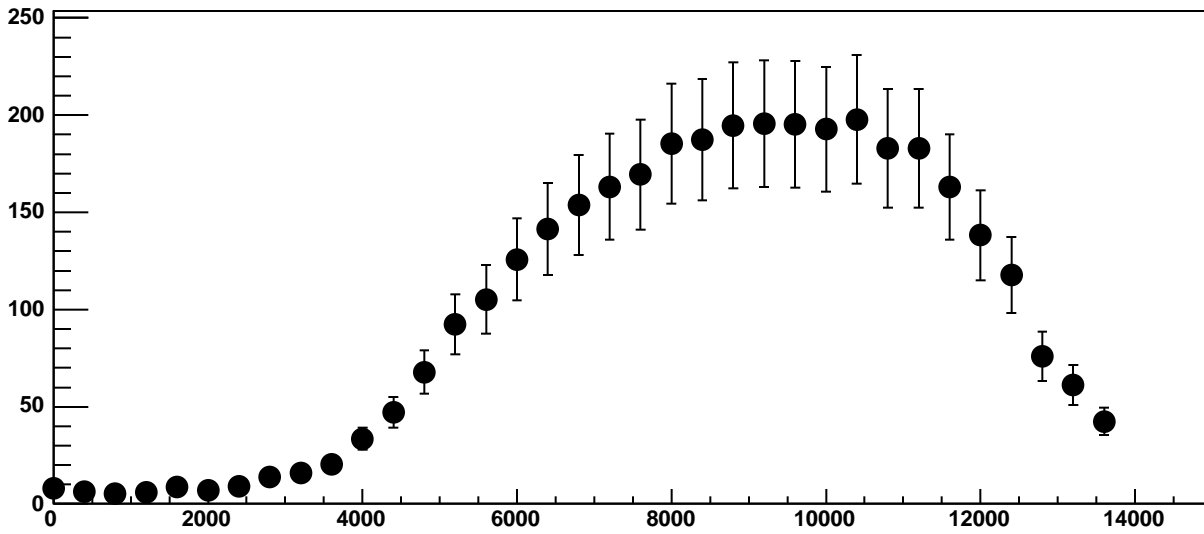
Chip 7, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



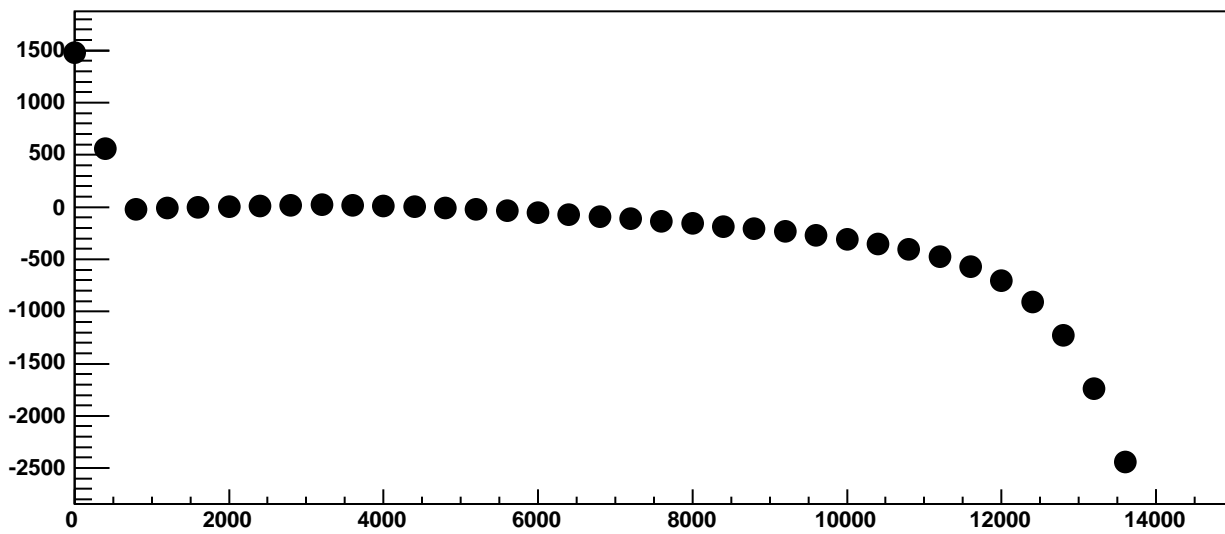
Chip 7, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC



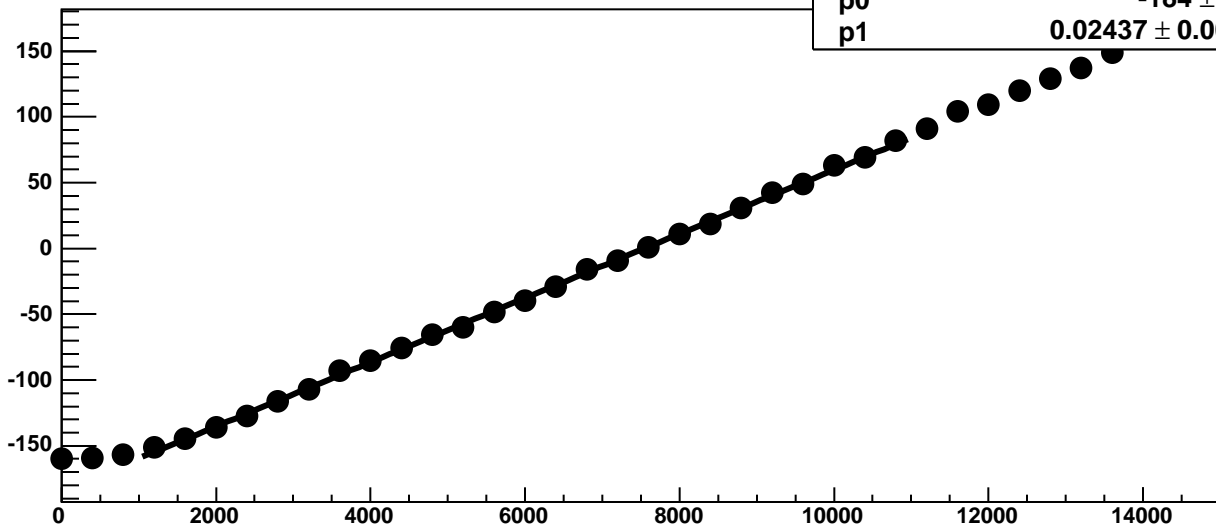
Chip 7, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC

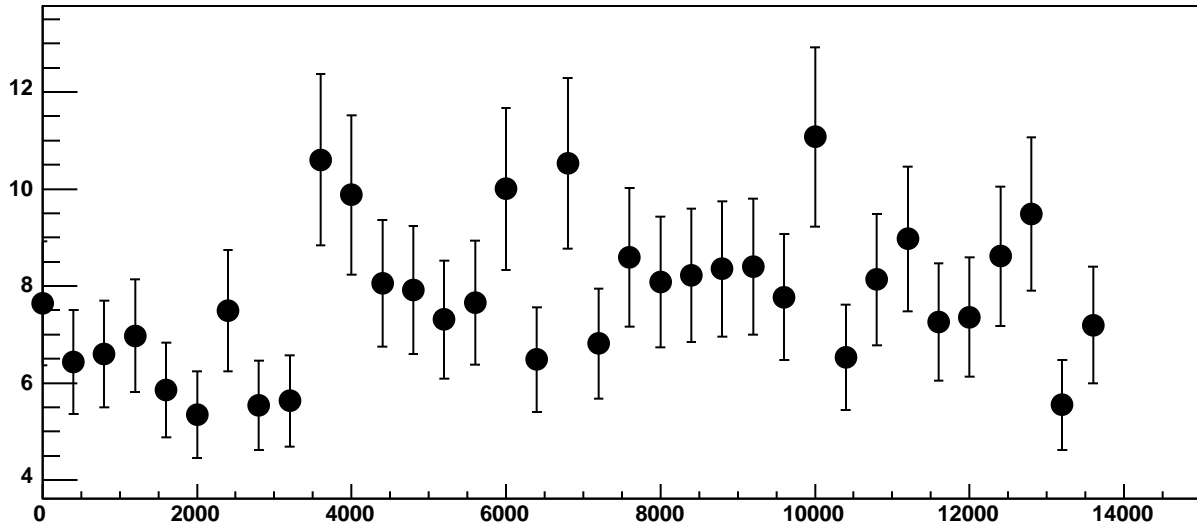


Chip 7, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

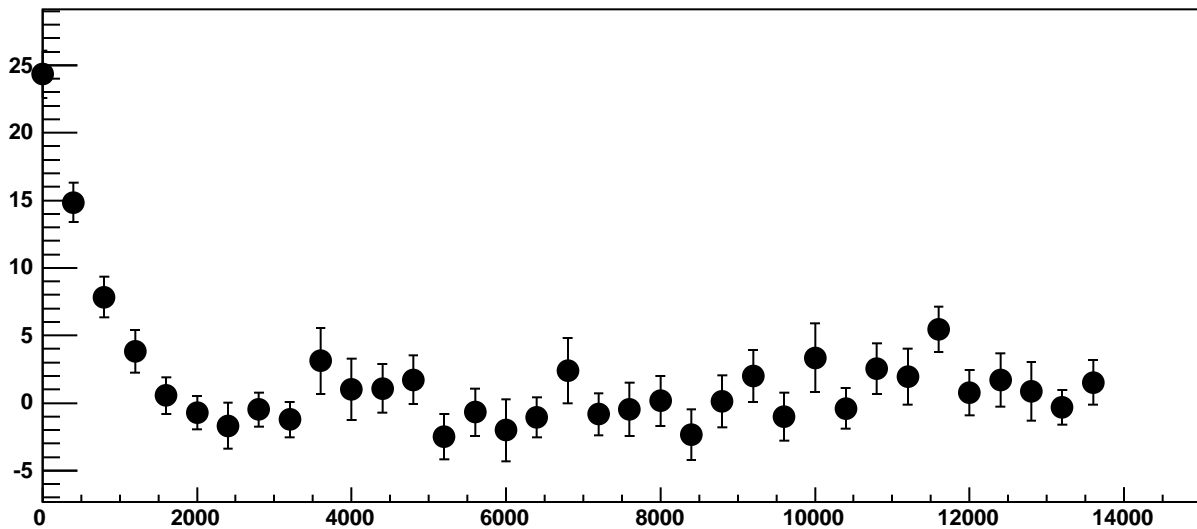


χ^2 / ndf 22.93 / 23
p0 -184 ± 0.7074
p1 0.02437 ± 0.0001137

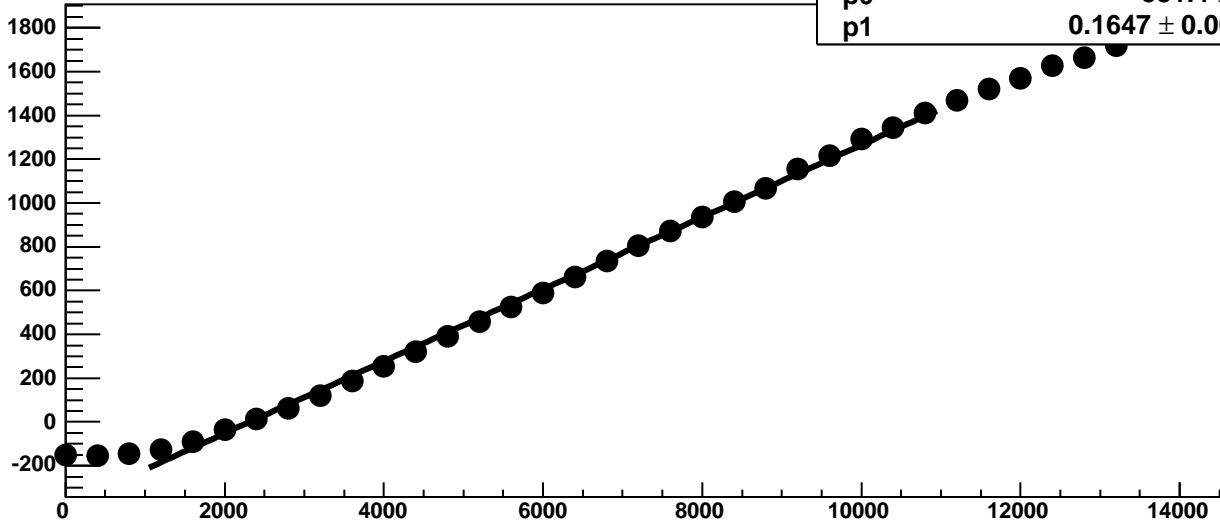
Chip 7, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



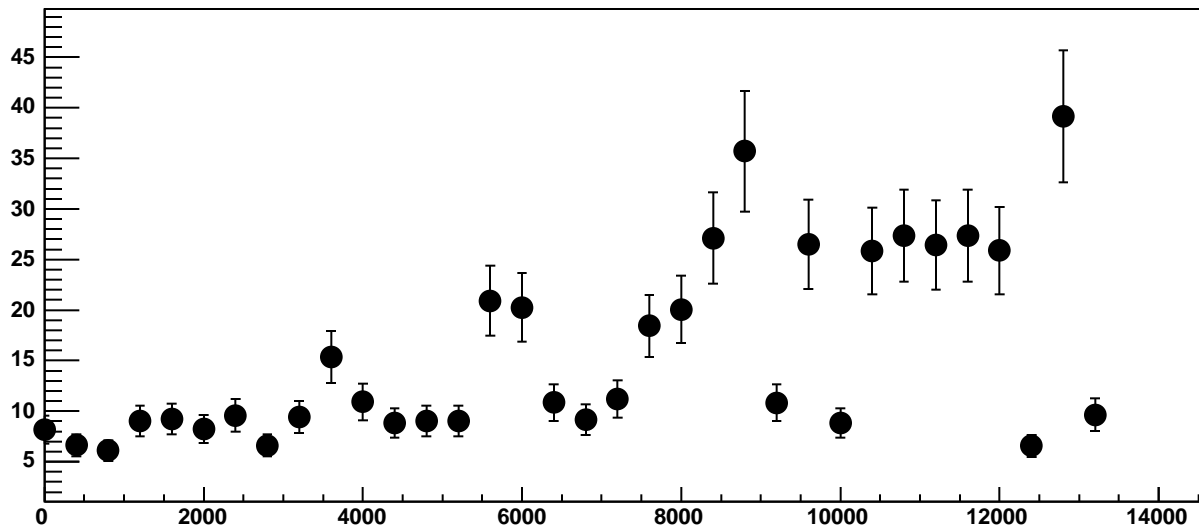
Chip 7, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



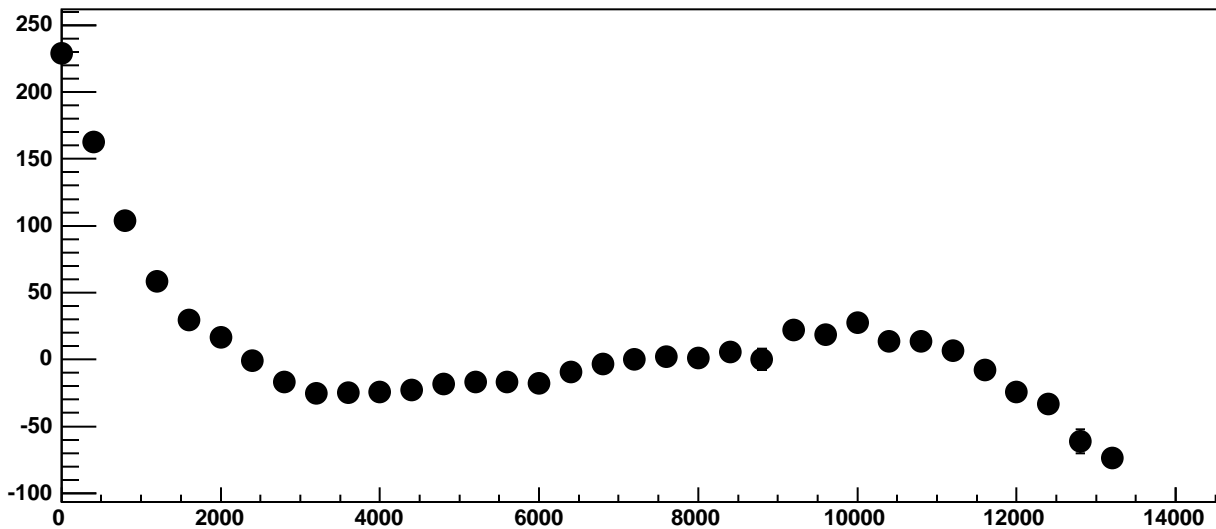
Chip 7, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



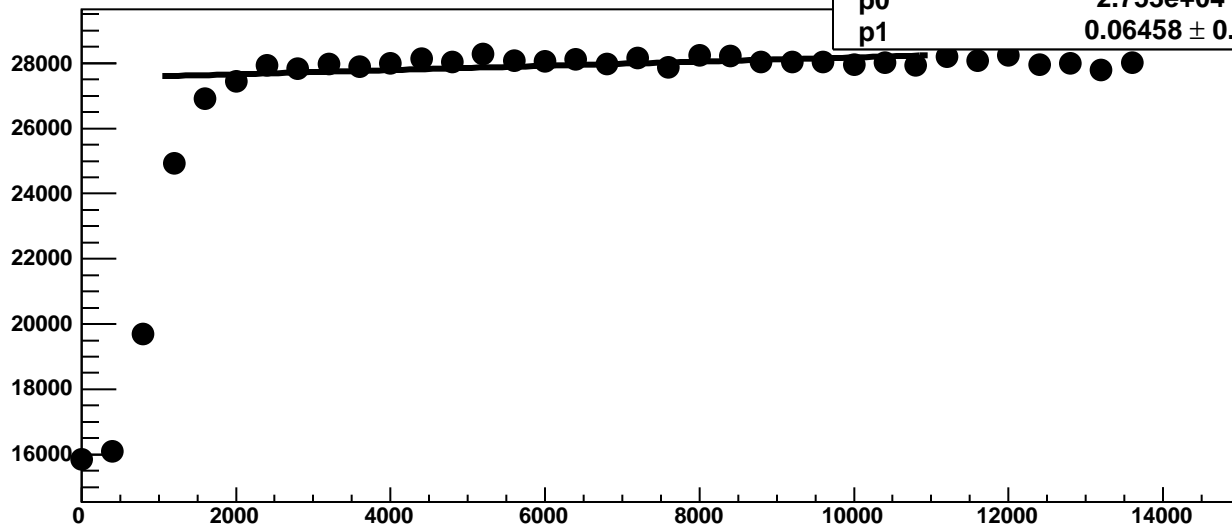
Chip 7, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



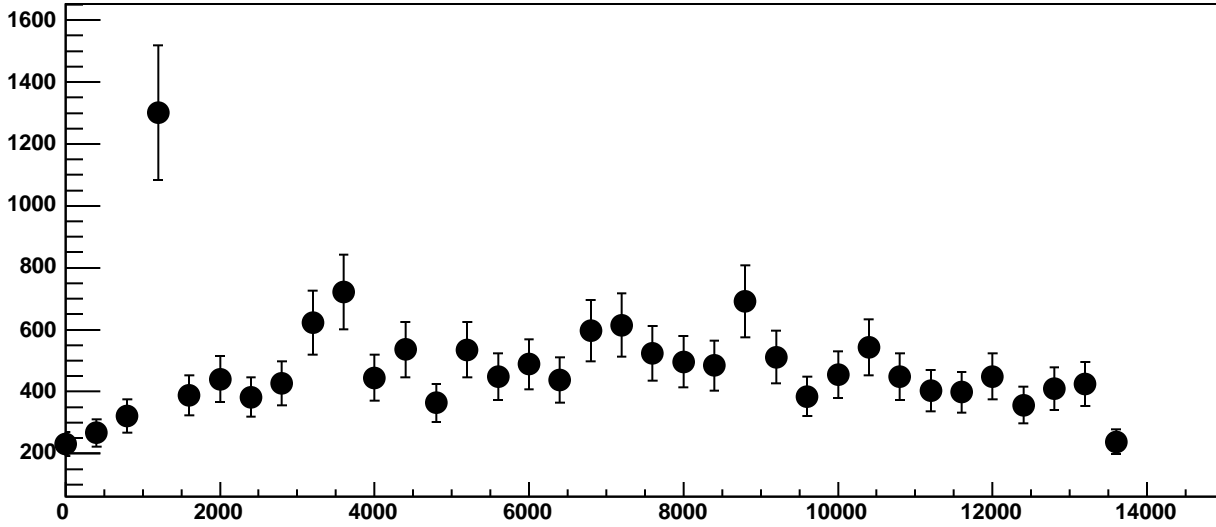
Chip 7, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



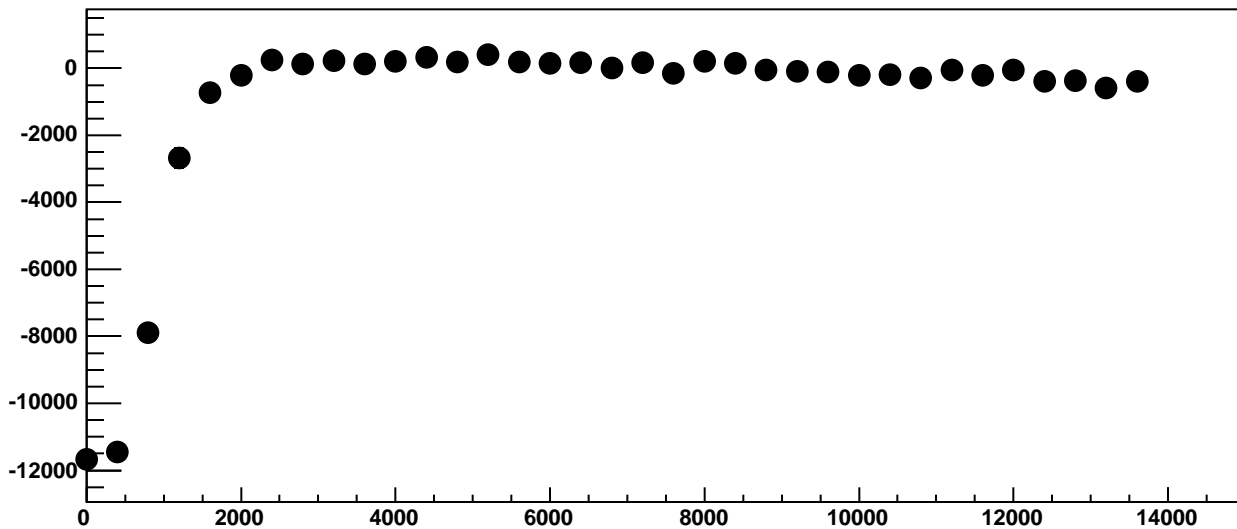
Chip 7, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



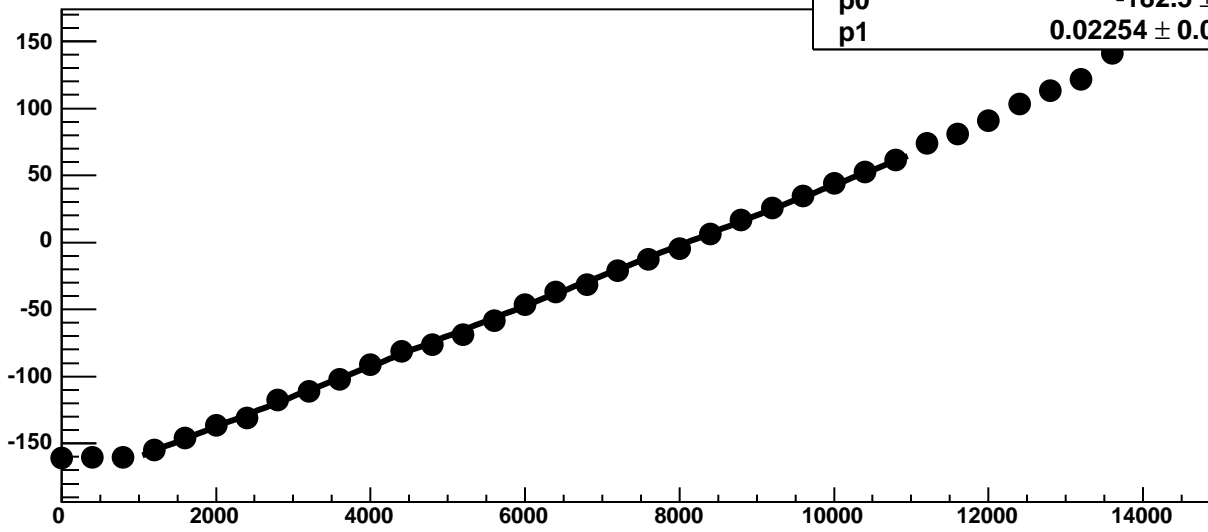
Chip 7, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC

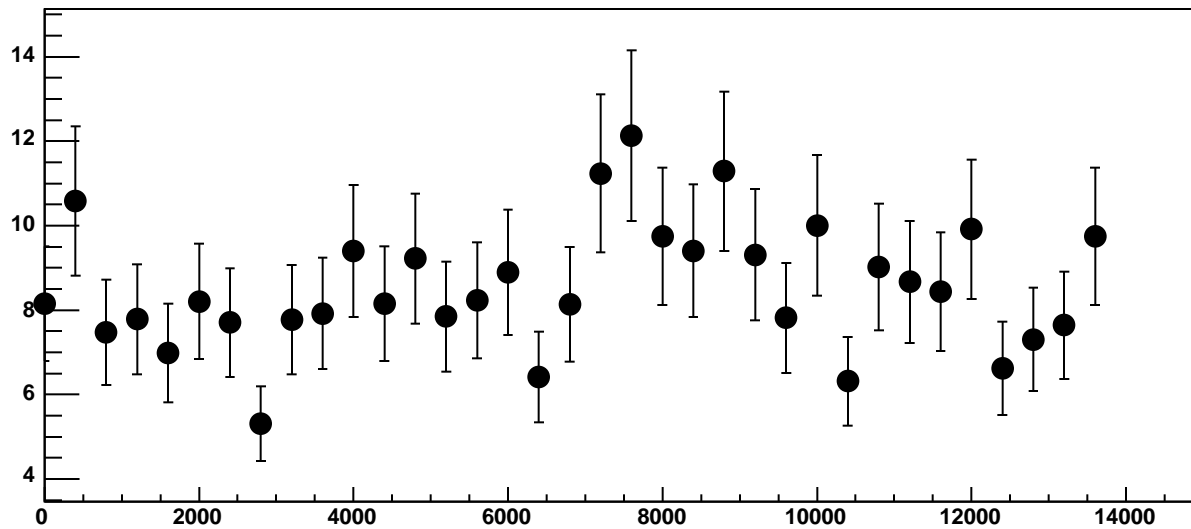


Chip 7, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

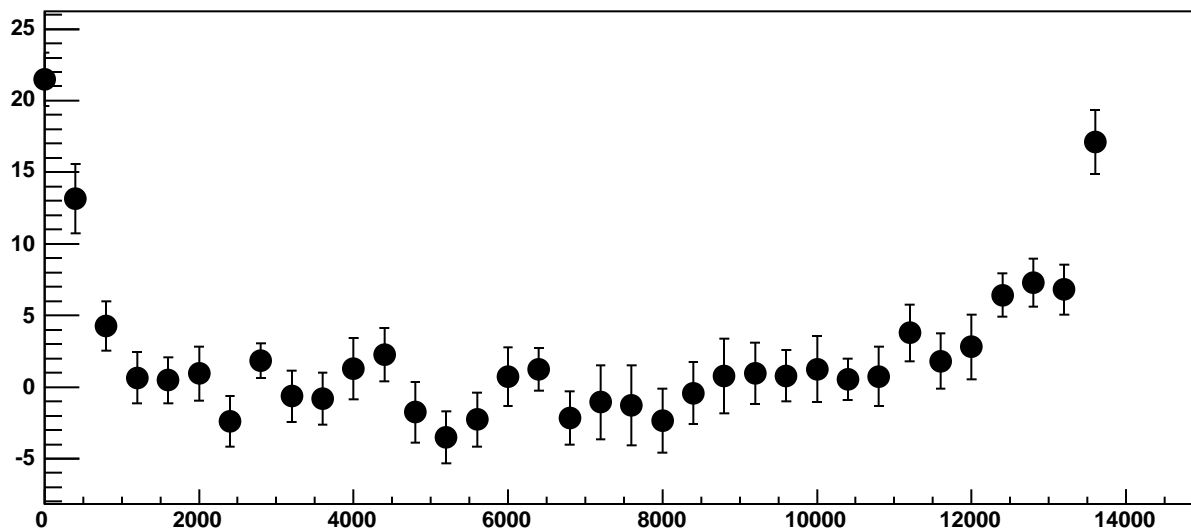


χ^2 / ndf 17.41 / 23
p0 -182.5 ± 0.7964
p1 0.02254 ± 0.0001255

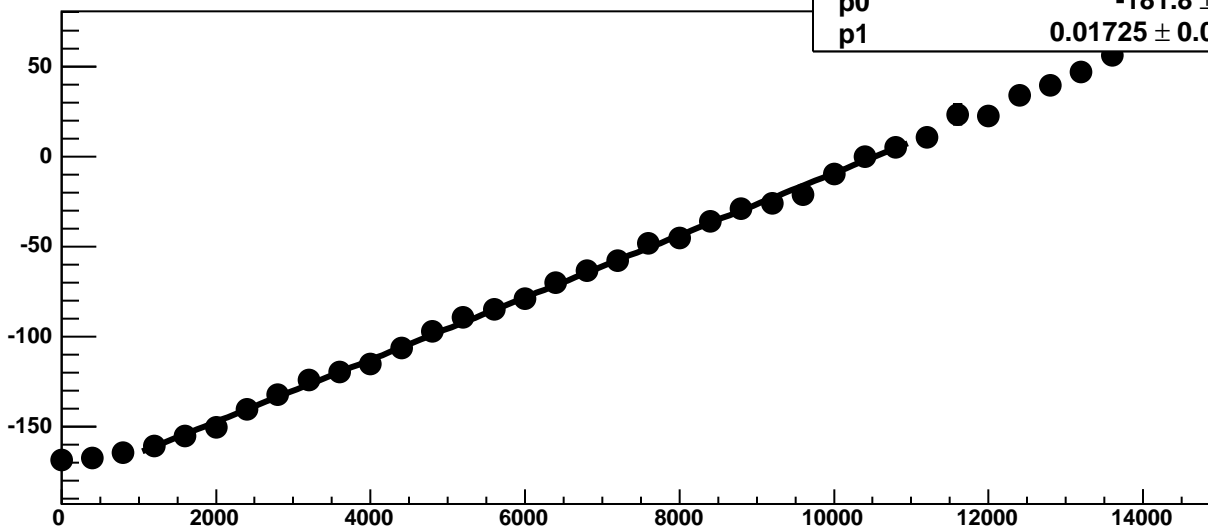
Chip 7, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

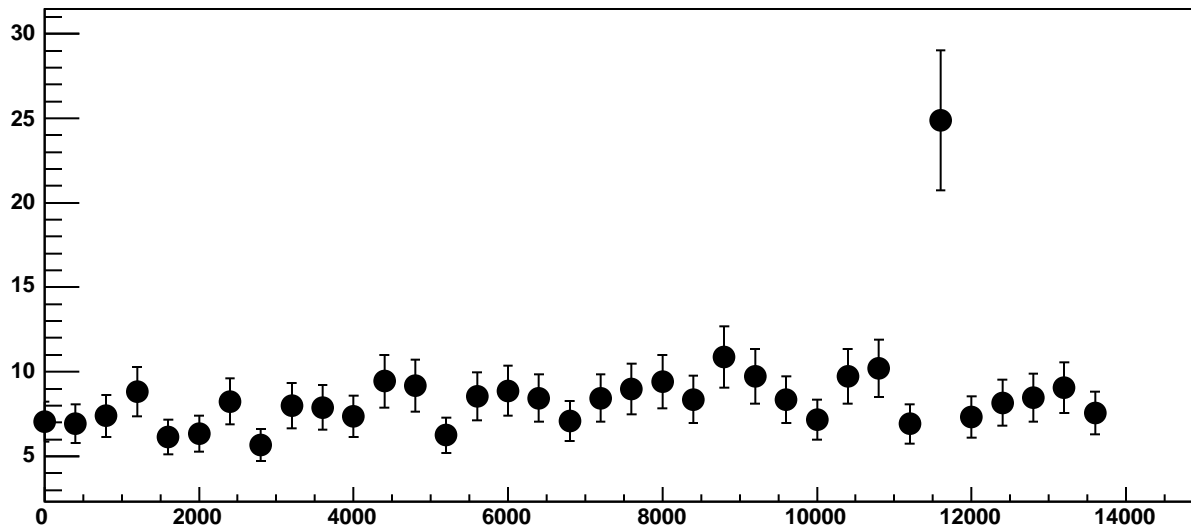


Chip 7, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

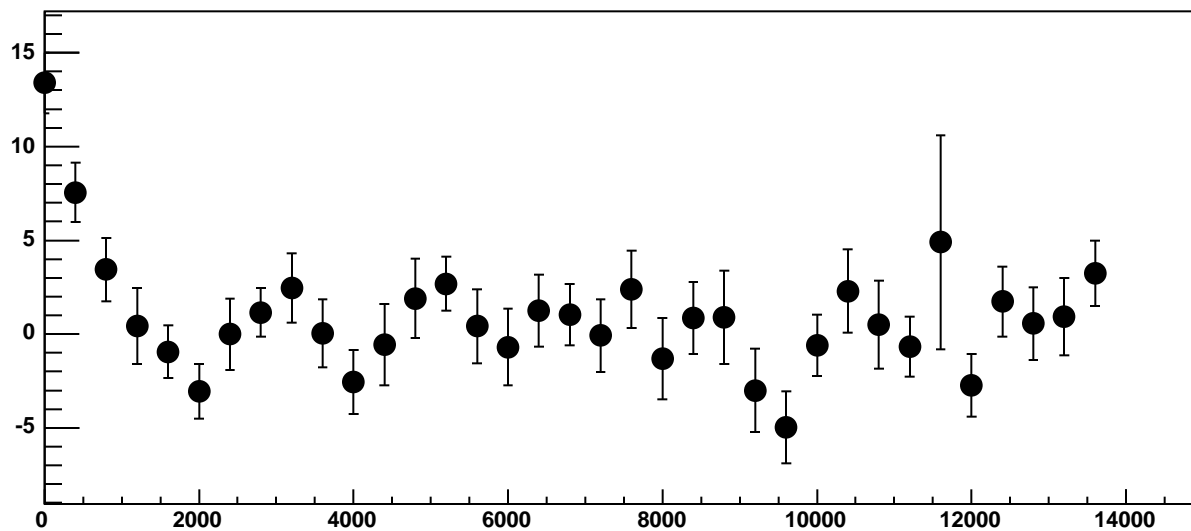


χ^2 / ndf 26.93 / 23
p0 -181.8 ± 0.7848
p1 0.01725 ± 0.0001272

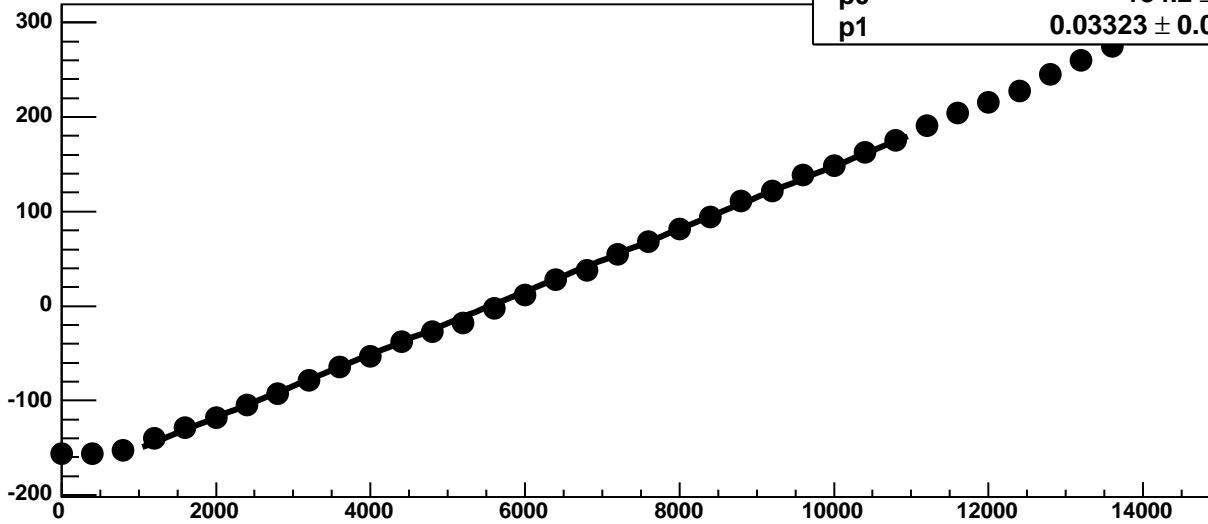
Chip 7, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

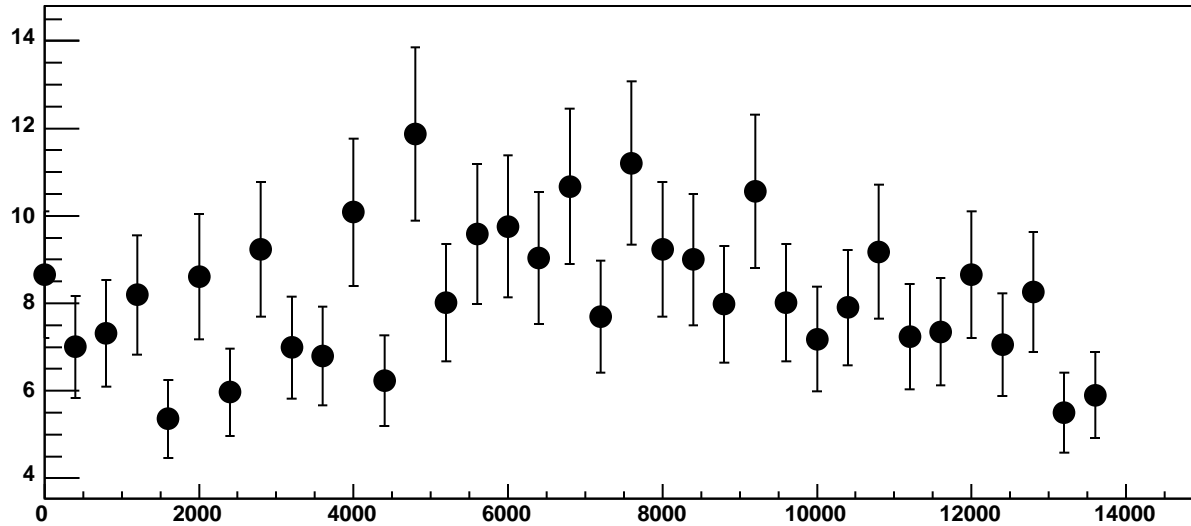


Chip 7, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC

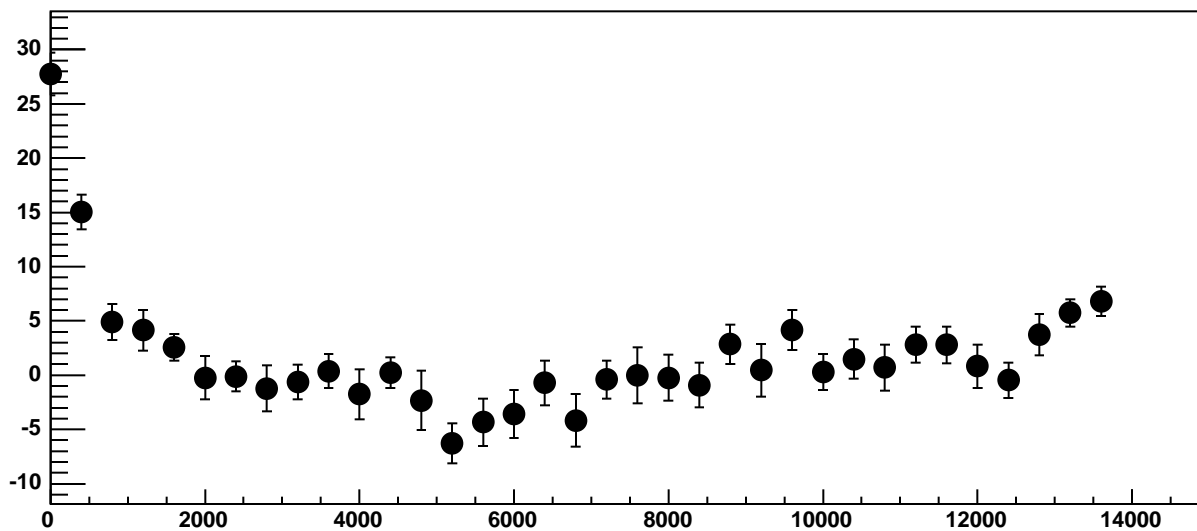


χ^2 / ndf 40.85 / 23
p0 -184.2 ± 0.7704
p1 0.03323 ± 0.0001222

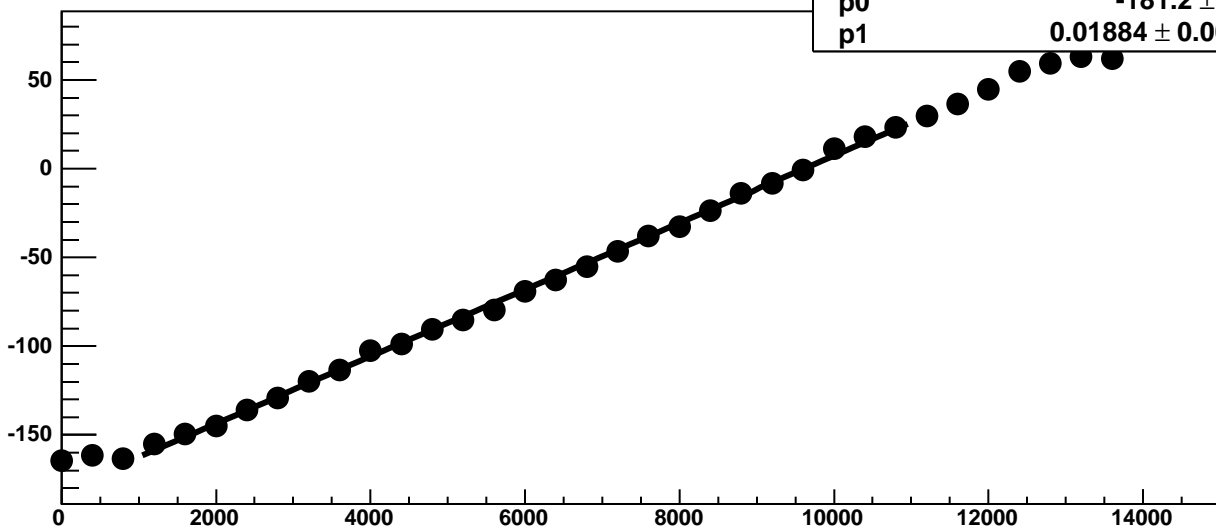
Chip 7, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC

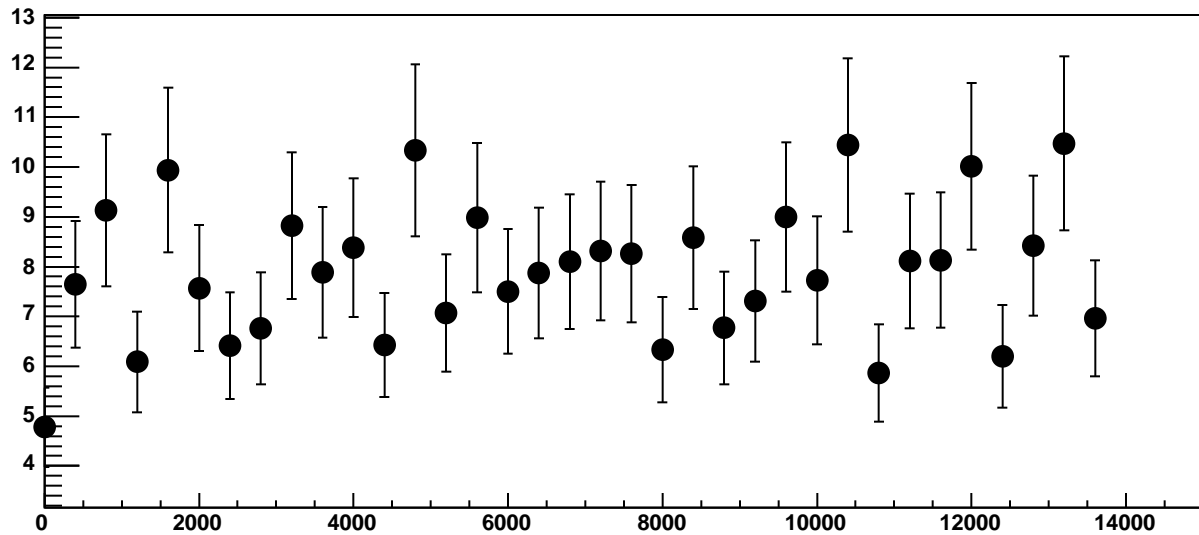


Chip 7, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

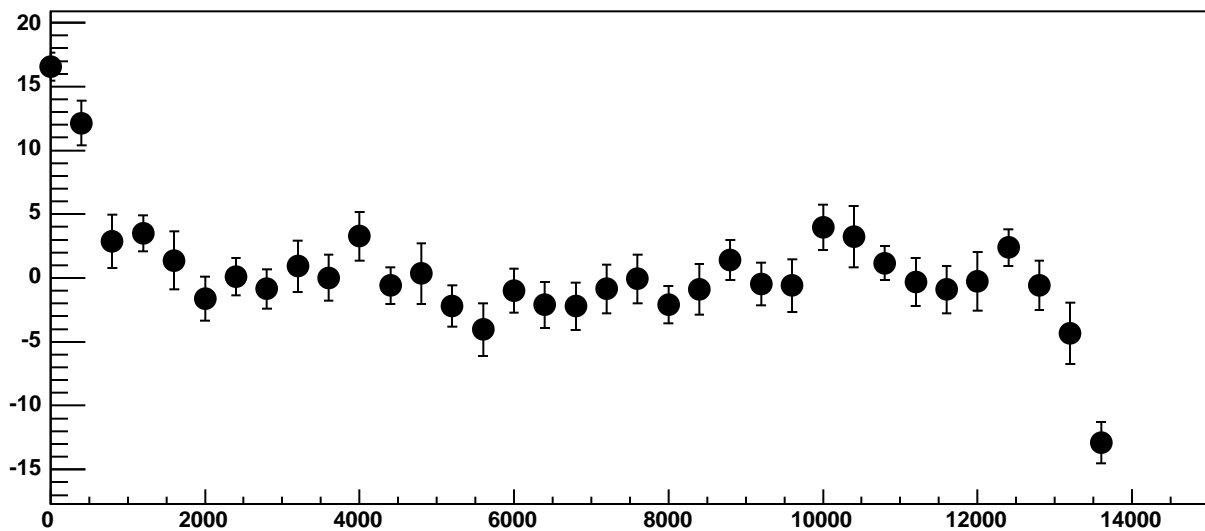


χ^2 / ndf 30.89 / 23
p0 -181.2 ± 0.7762
p1 0.01884 ± 0.0001167

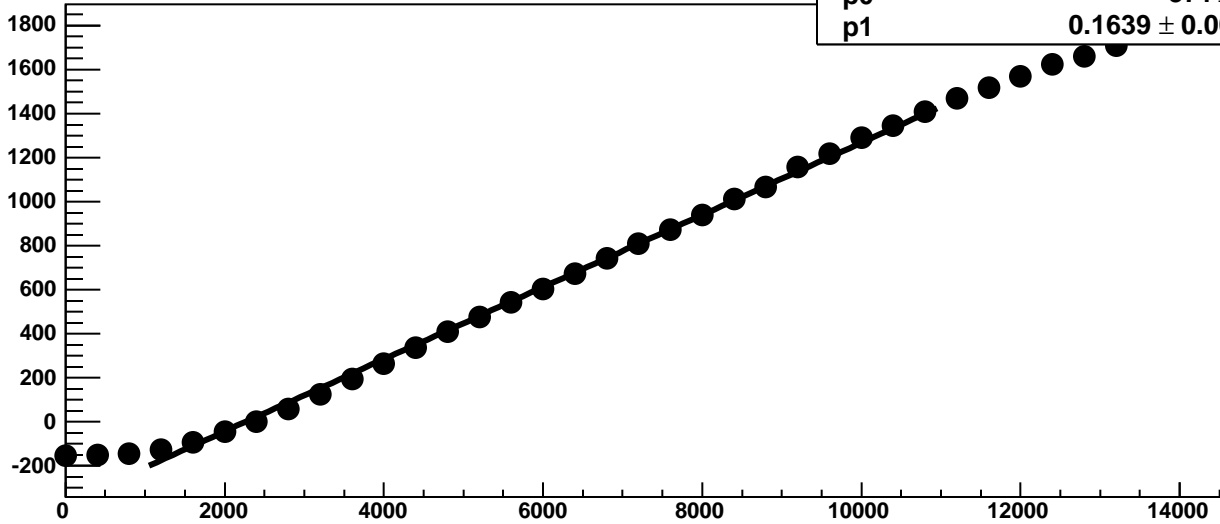
Chip 7, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



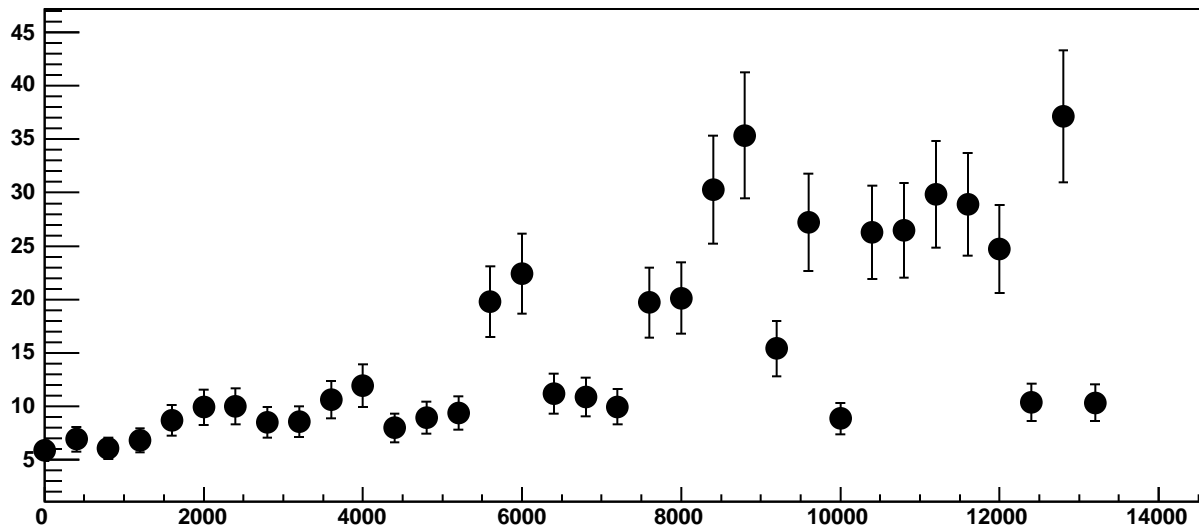
Chip 7, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



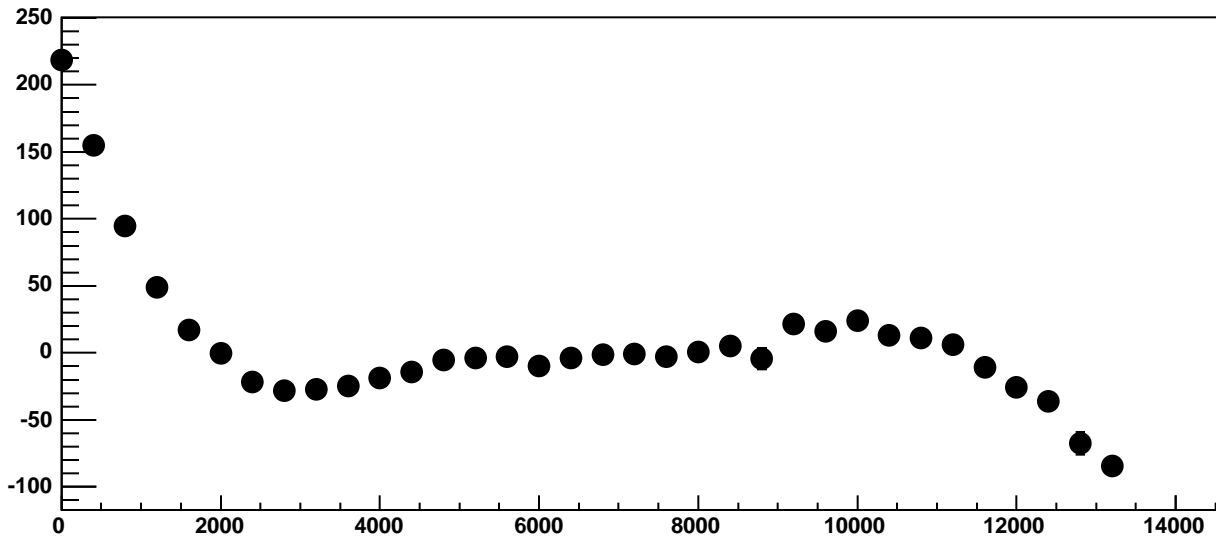
Chip 7, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



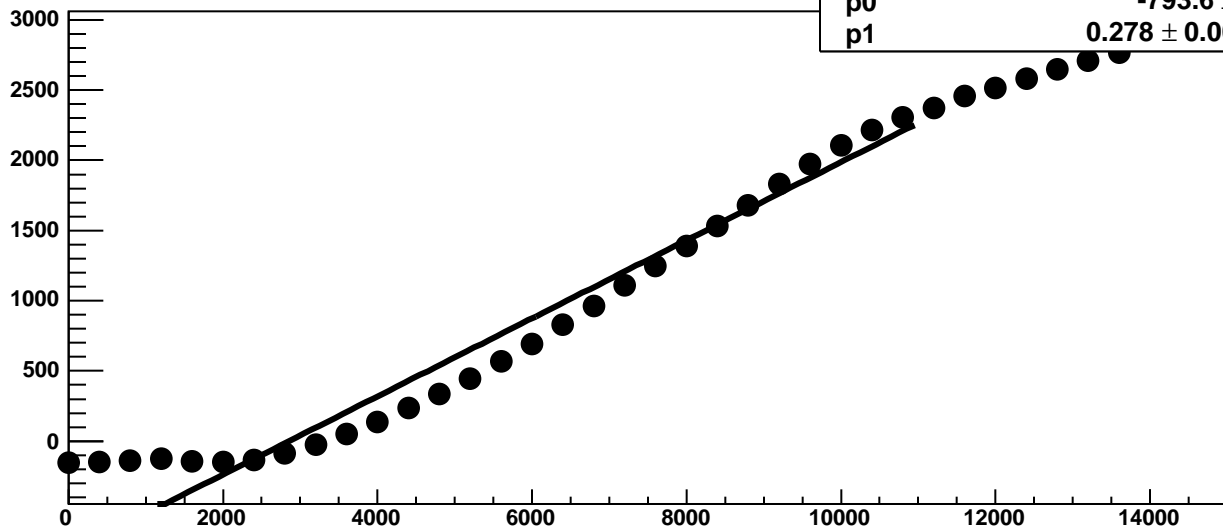
Chip 7, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



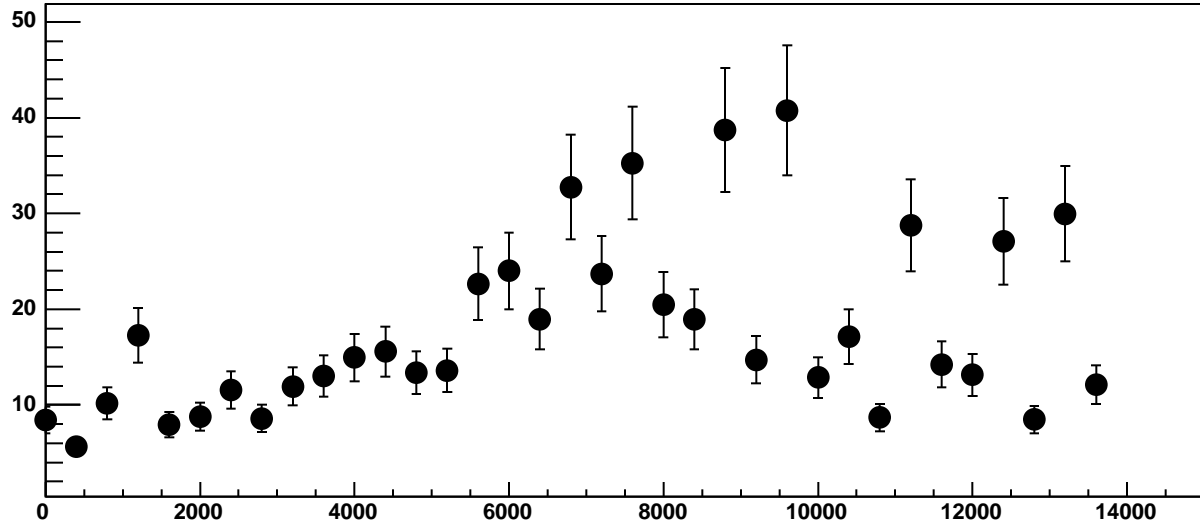
Chip 7, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



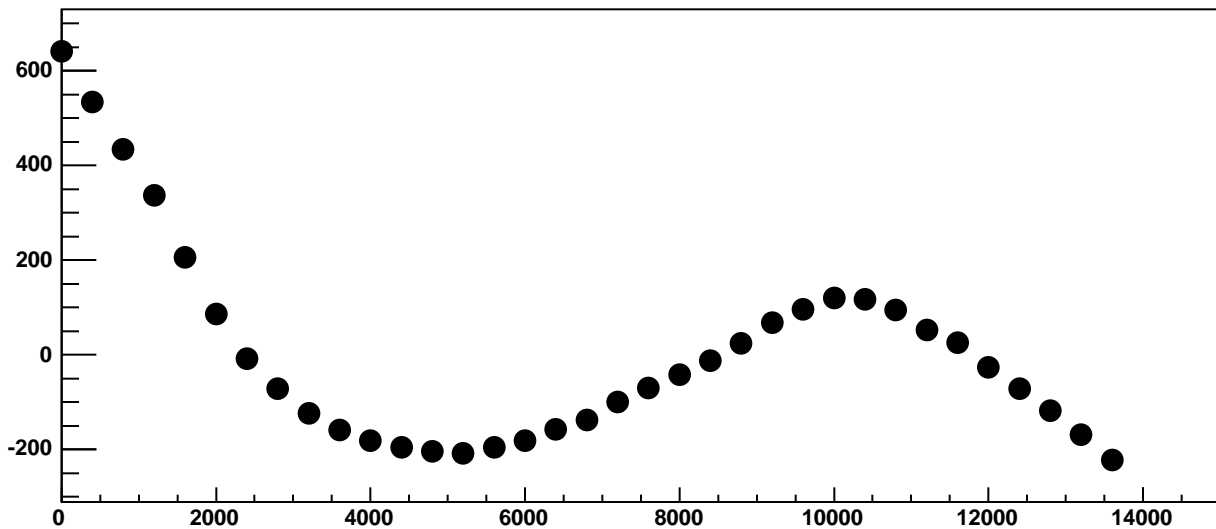
Chip 7, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC



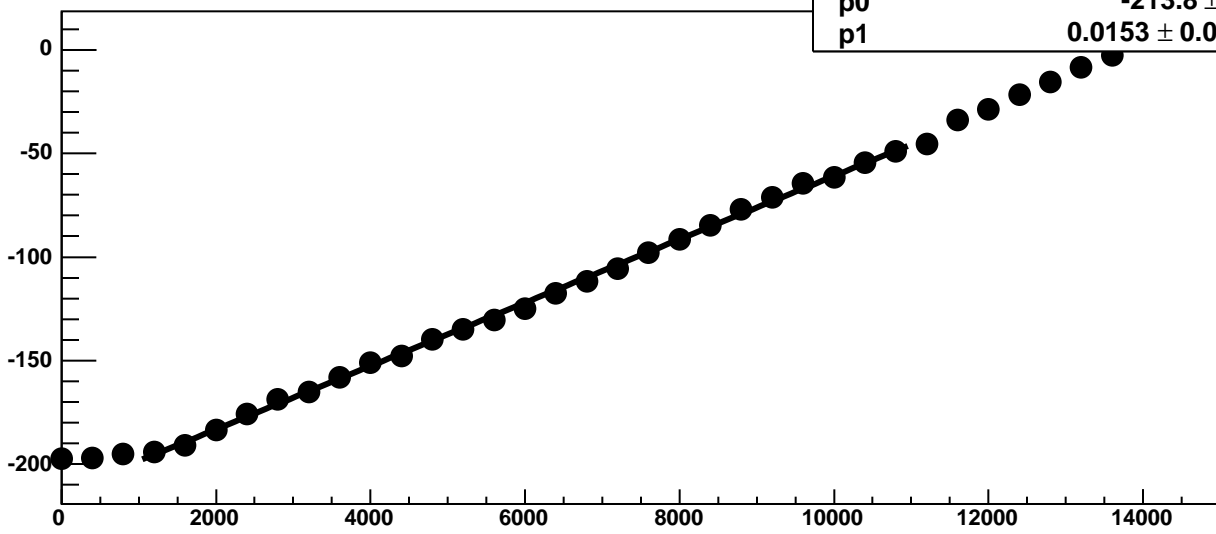
Chip 7, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

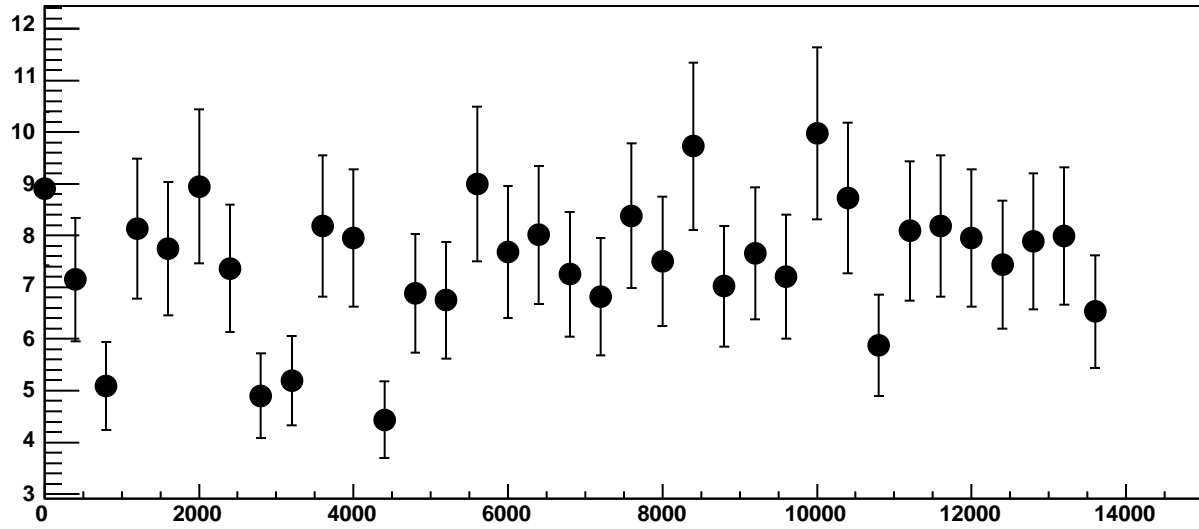


Chip 7, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

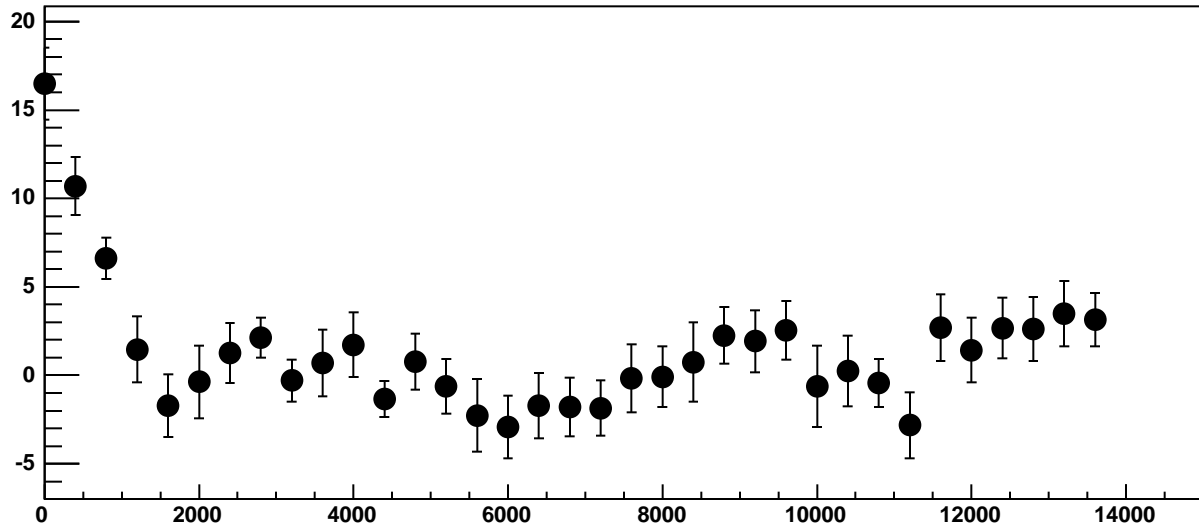


χ^2 / ndf 22.17 / 23
p0 -213.8 ± 0.7311
p1 0.0153 ± 0.0001149

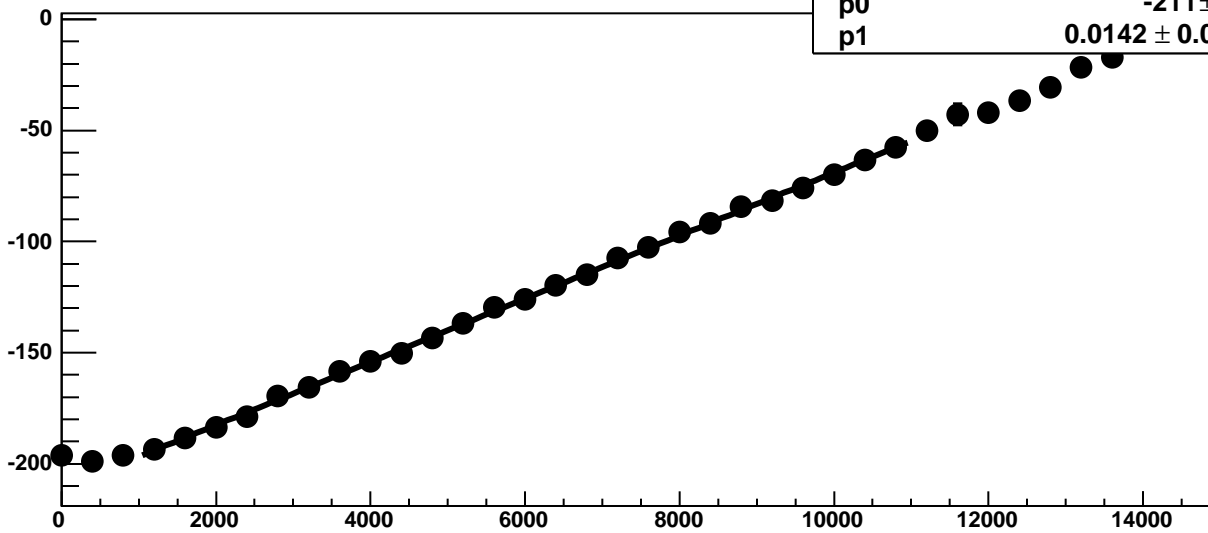
Chip 7, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



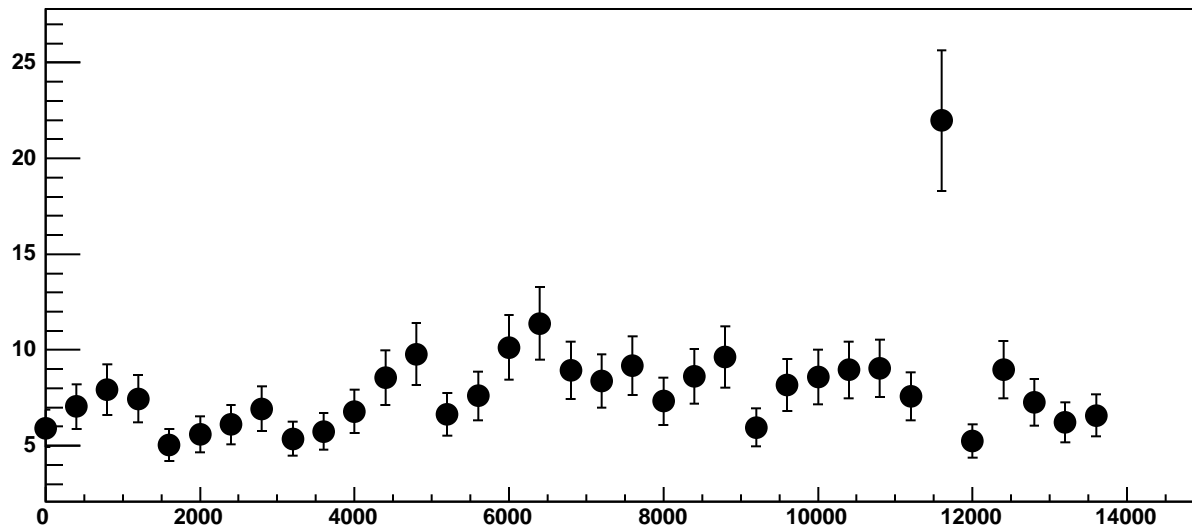
Chip 7, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



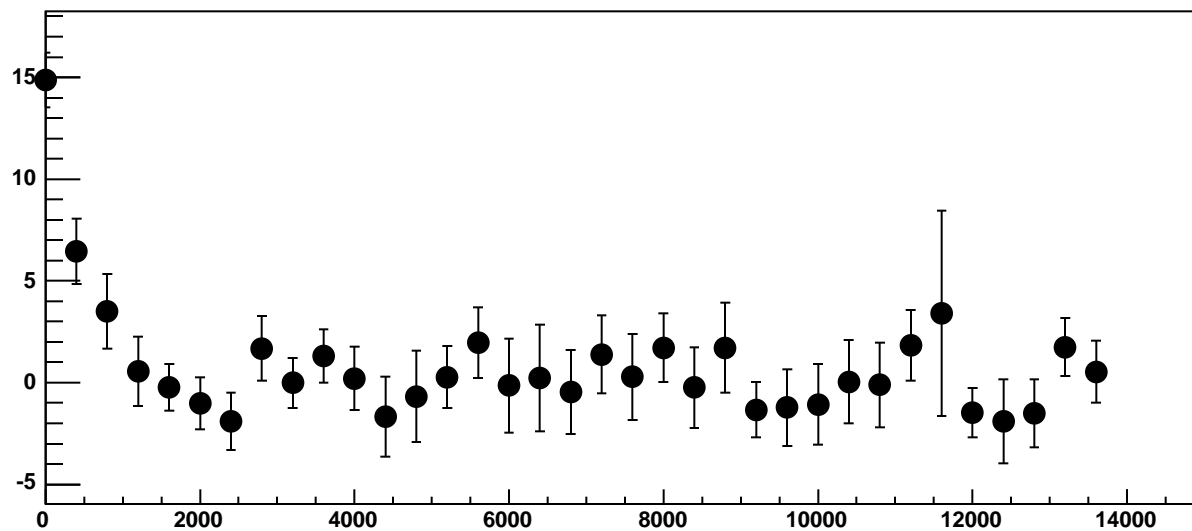
Chip 7, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



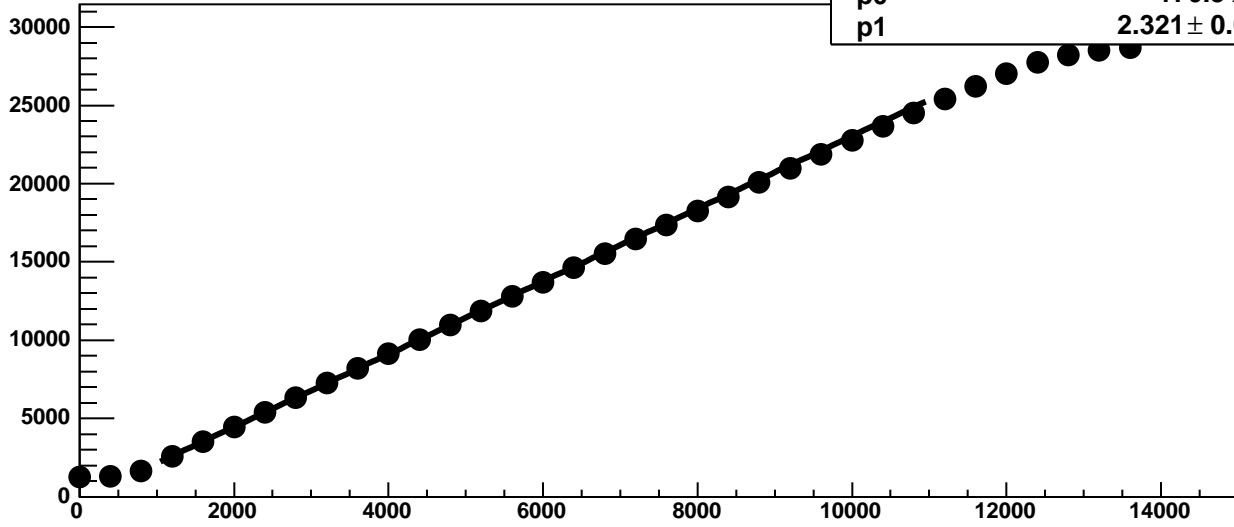
Chip 7, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



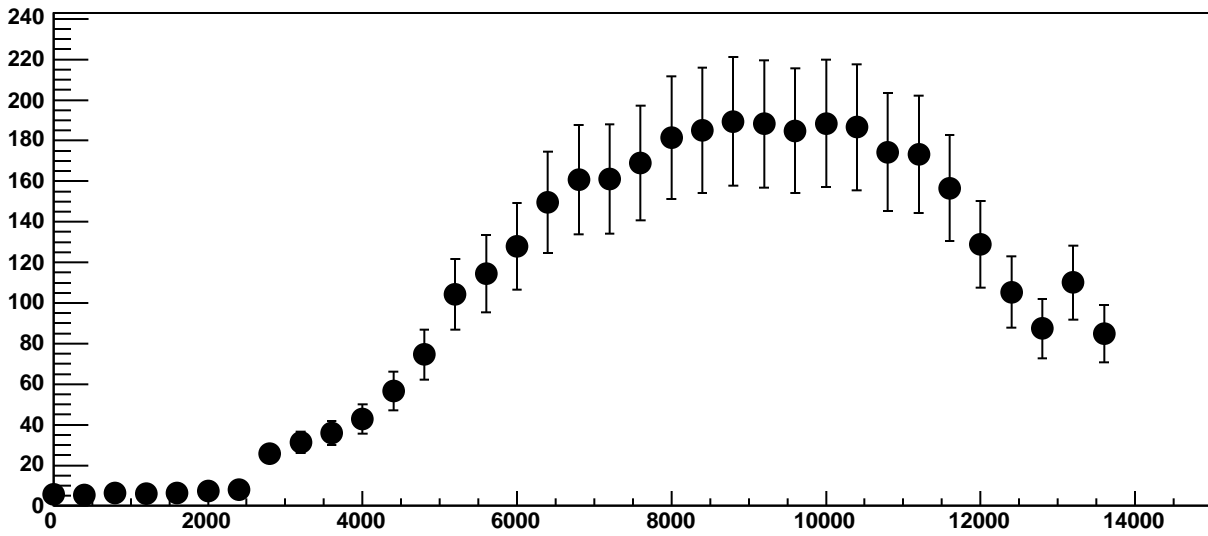
Chip 7, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



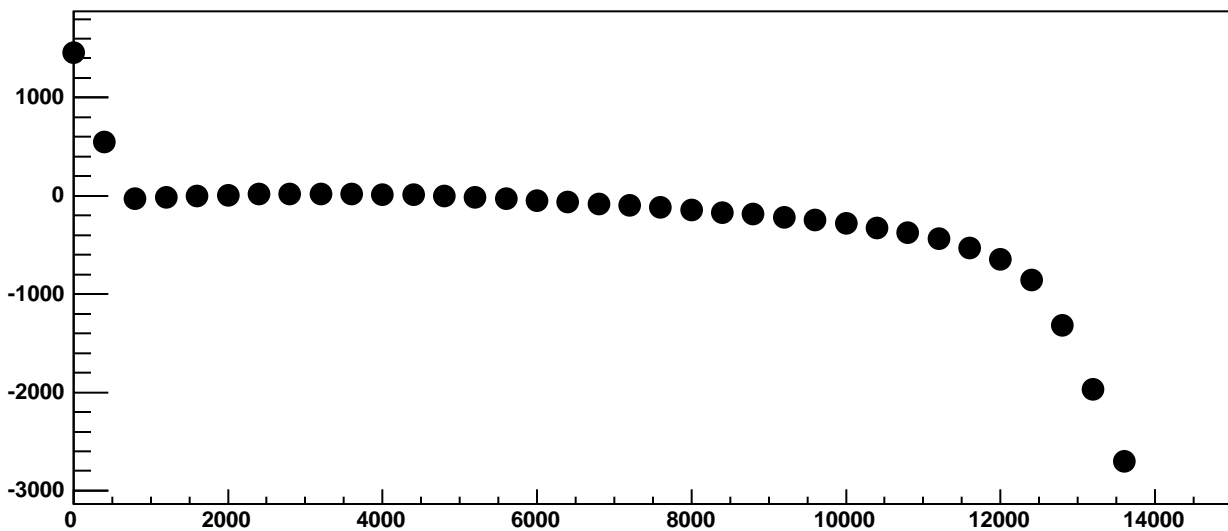
Chip 7, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC



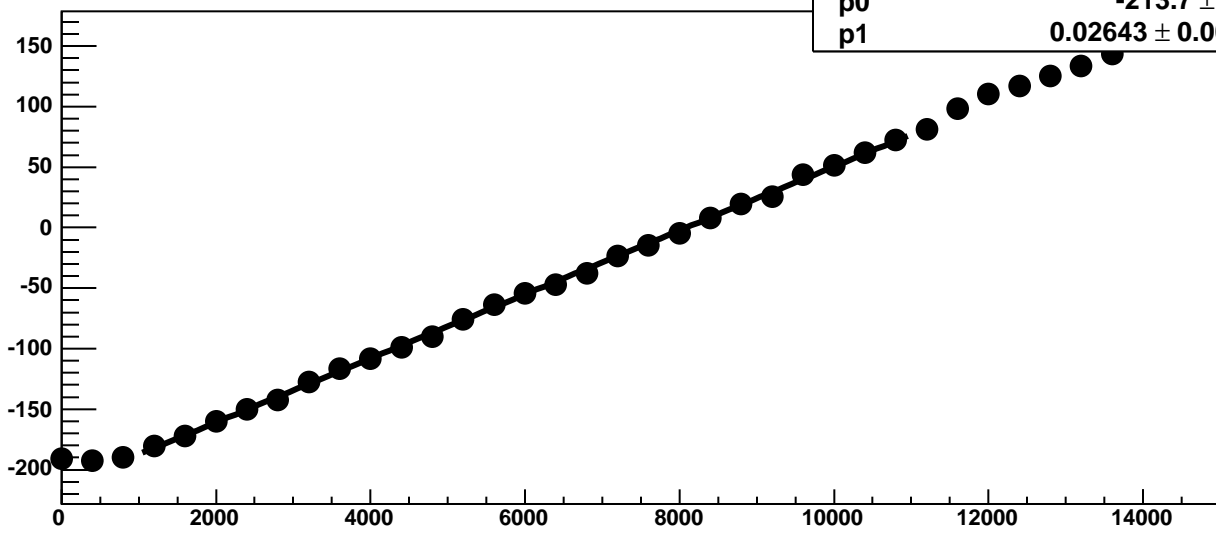
Chip 7, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC

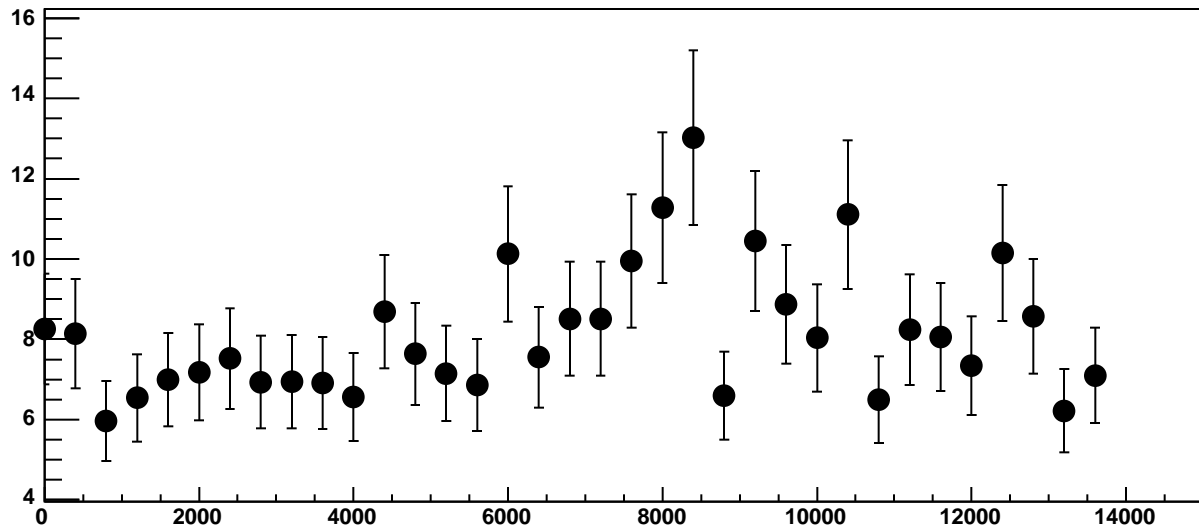


Chip 7, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

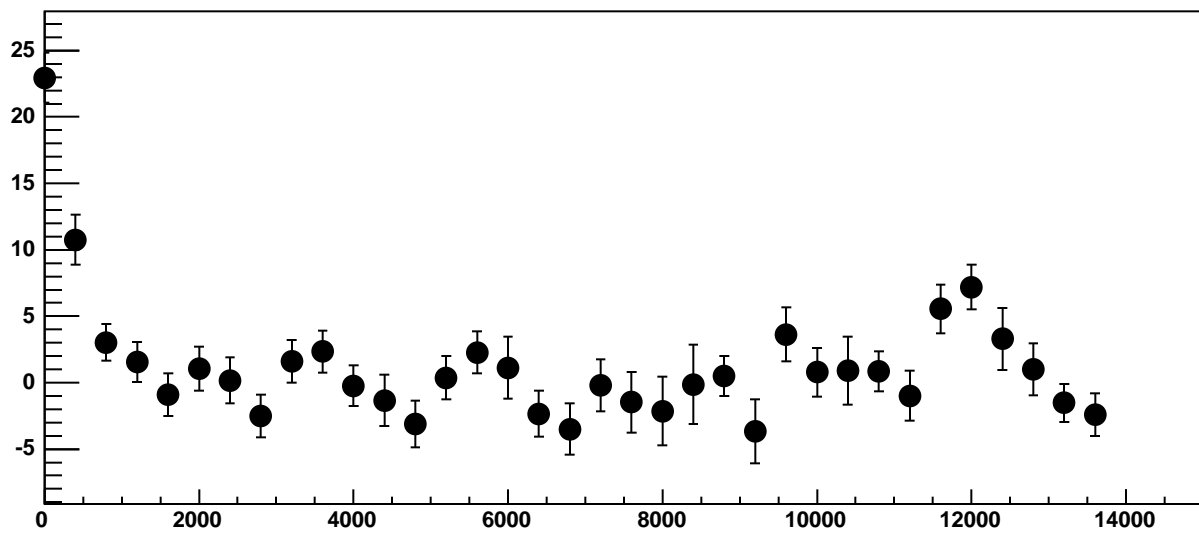


χ^2 / ndf 25.88 / 23
p0 -213.7 ± 0.7607
p1 0.02643 ± 0.0001217

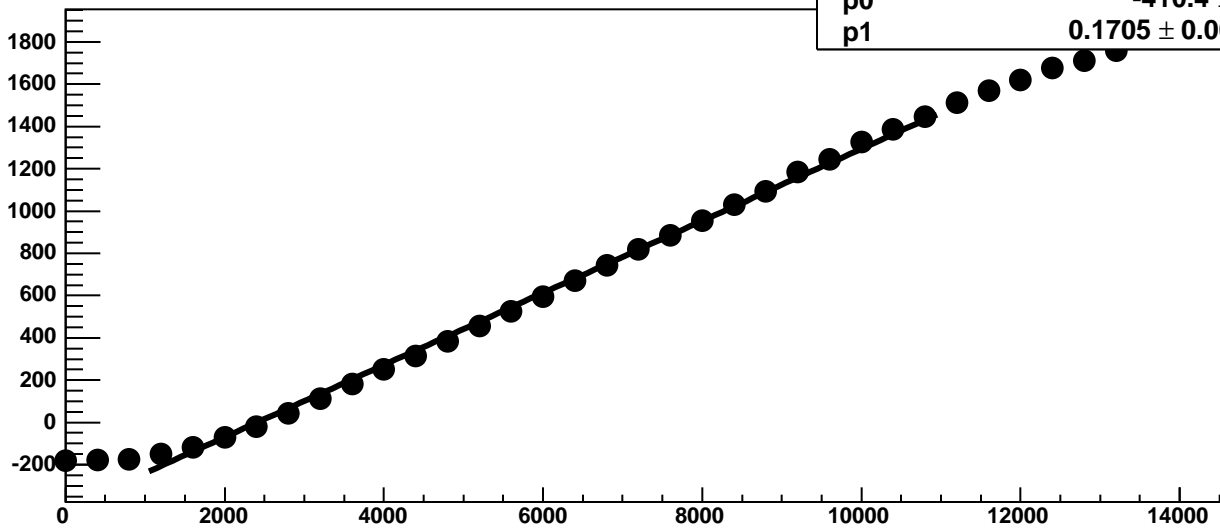
Chip 7, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



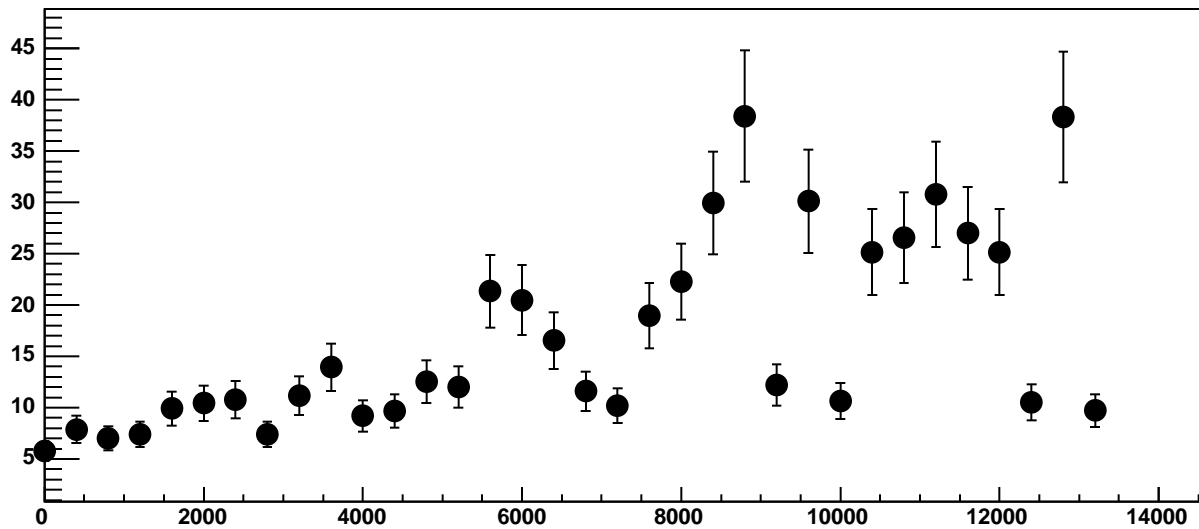
Chip 7, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



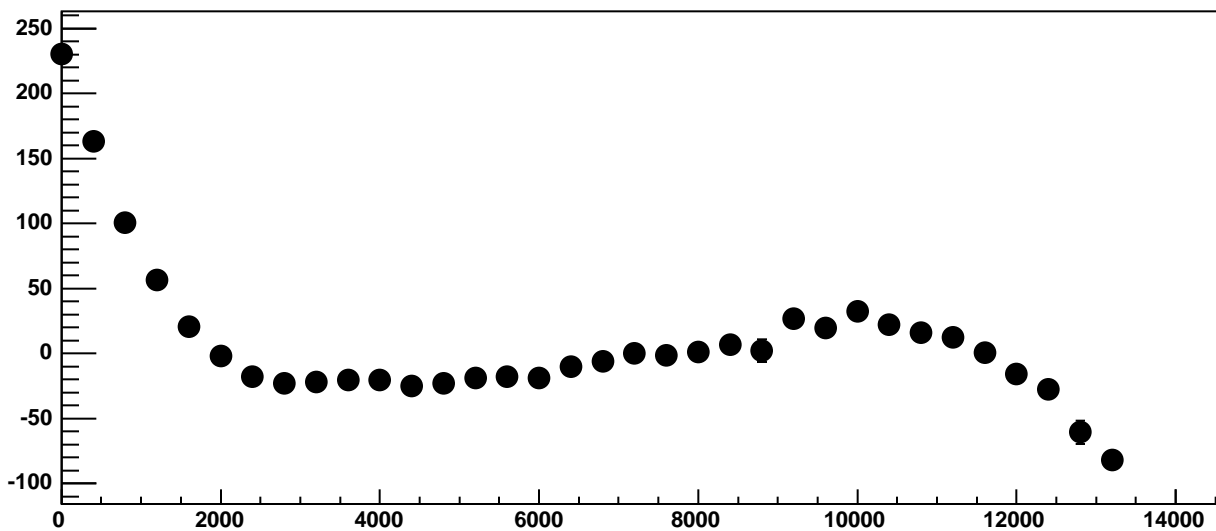
Chip 7, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



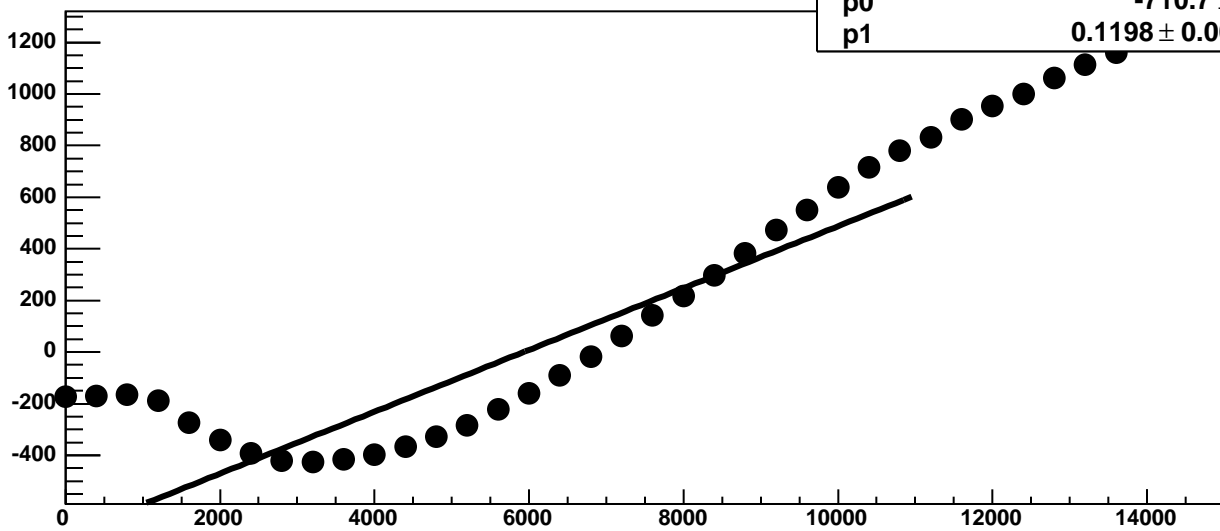
Chip 7, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 7, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

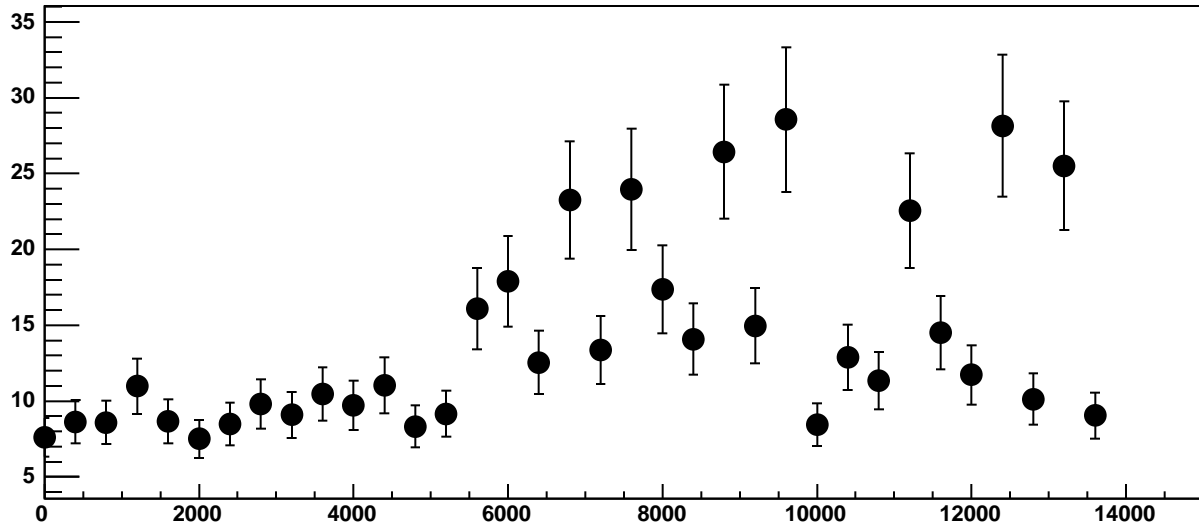


Chip 7, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

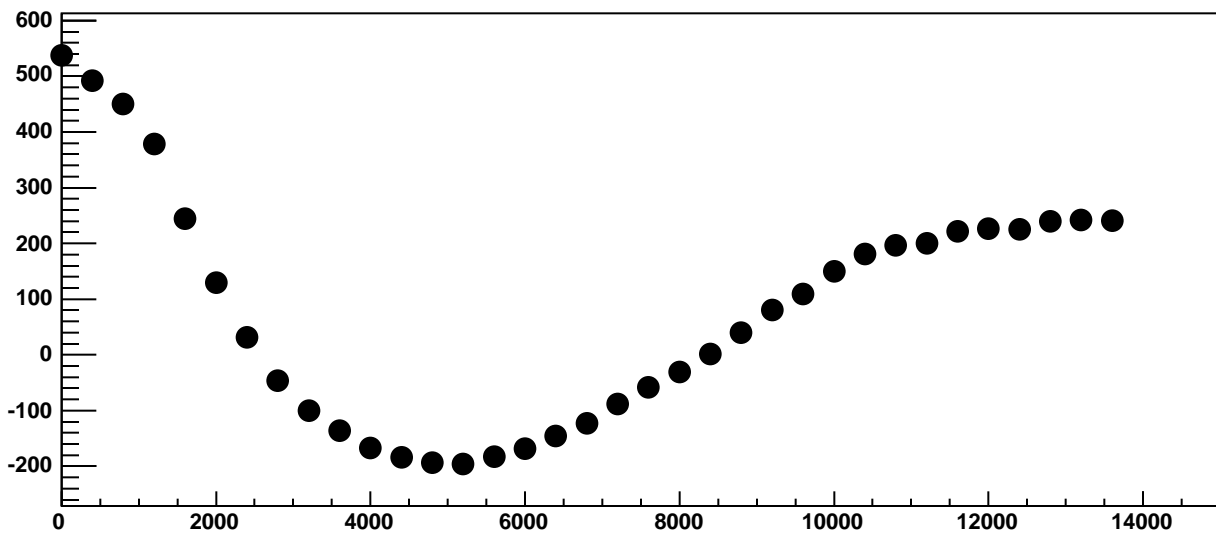


χ^2 / ndf 1.036e+05 / 23
p0 -710.7 ± 1.019
p1 0.1198 ± 0.0001755

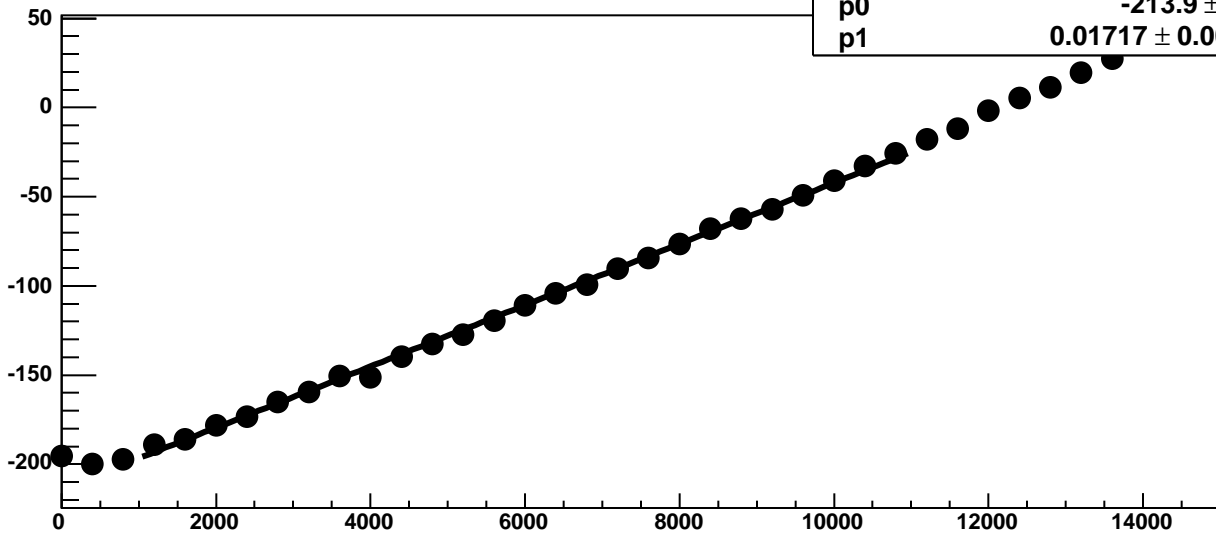
Chip 7, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



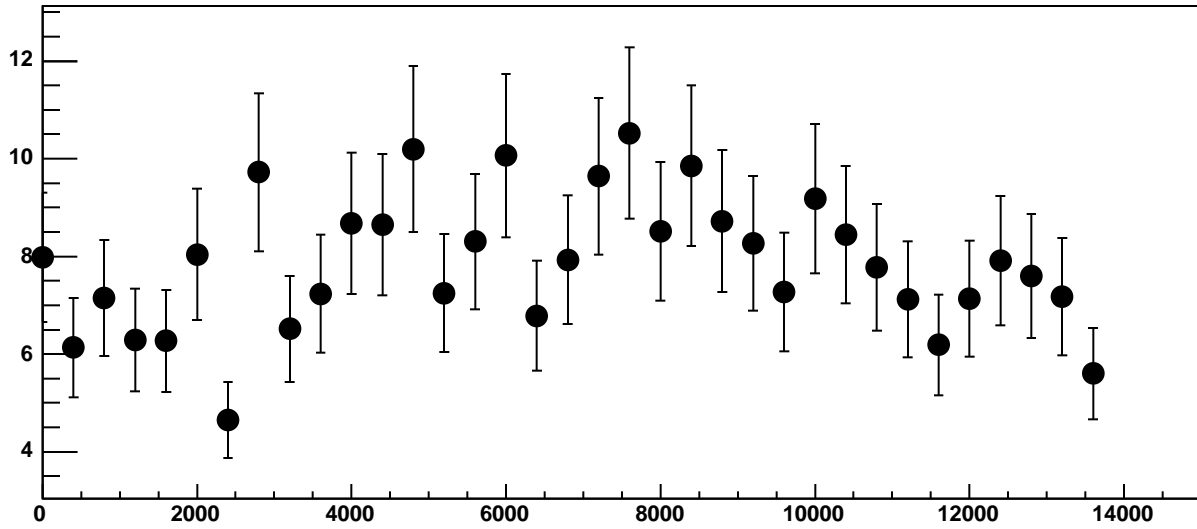
Chip 7, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



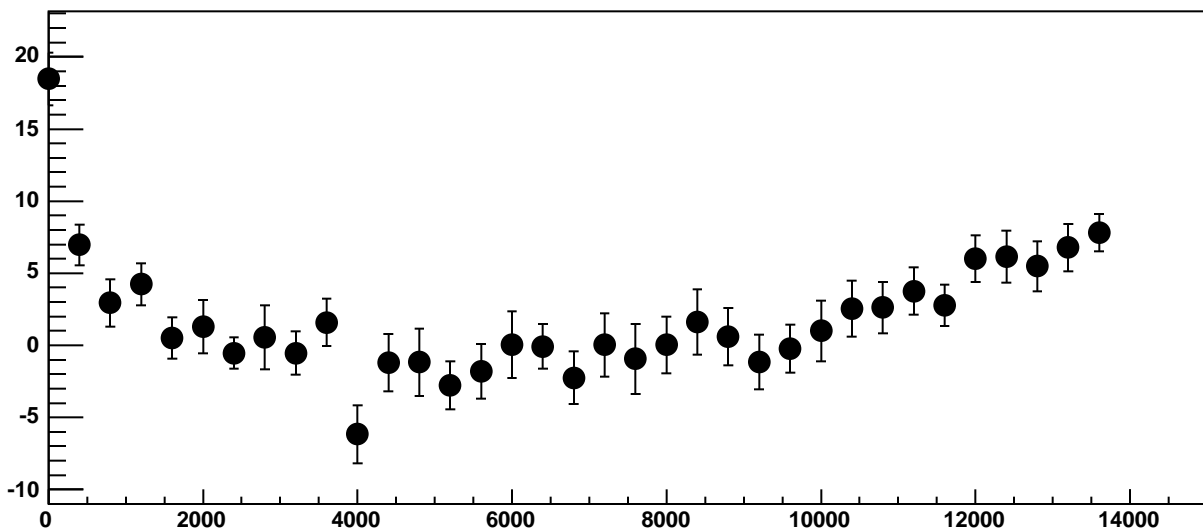
Chip 7, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC



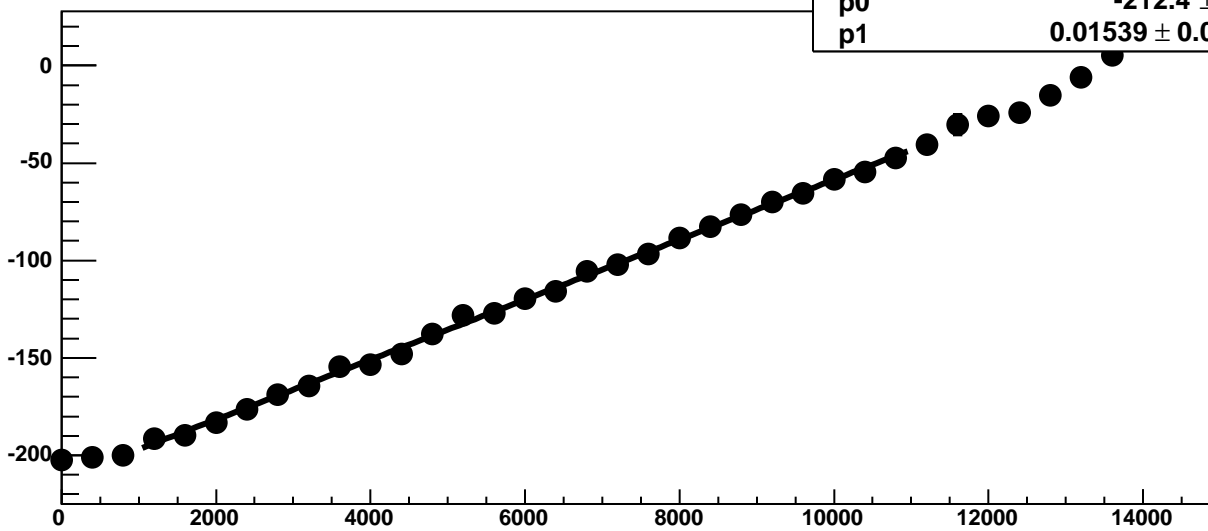
Chip 7, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



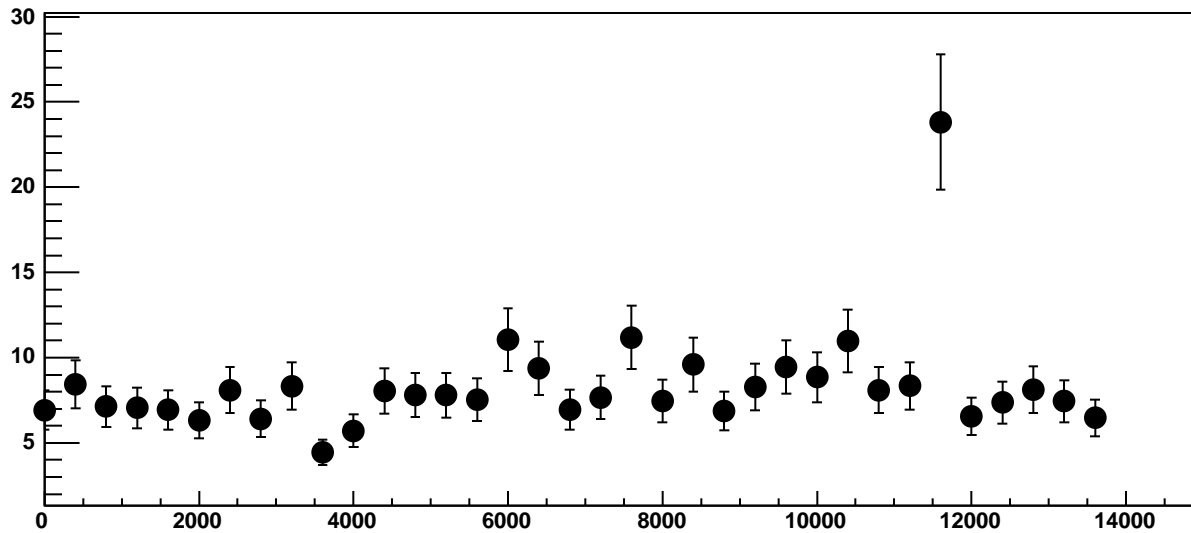
Chip 7, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC



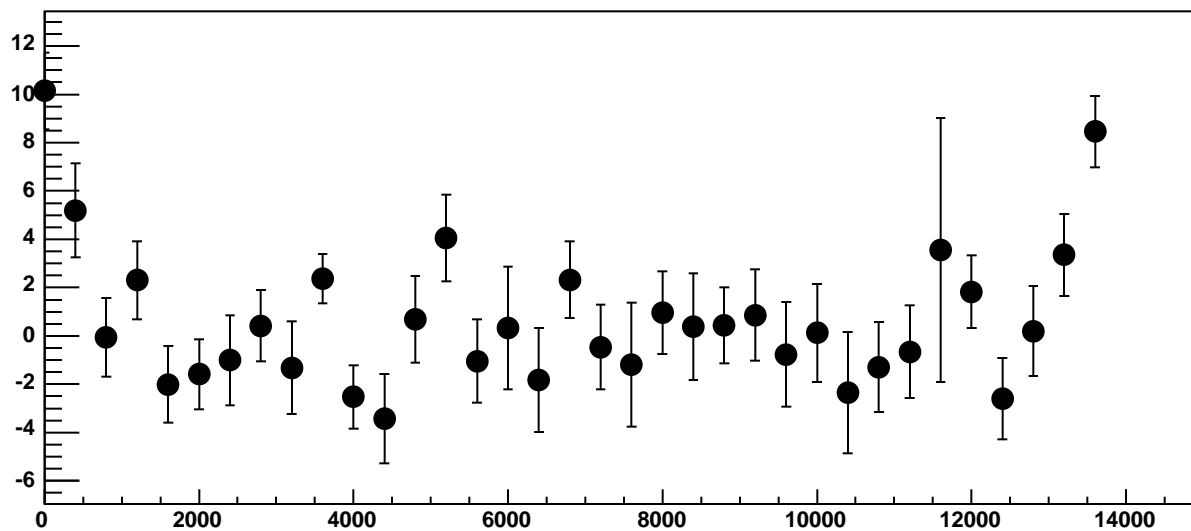
Chip 7, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



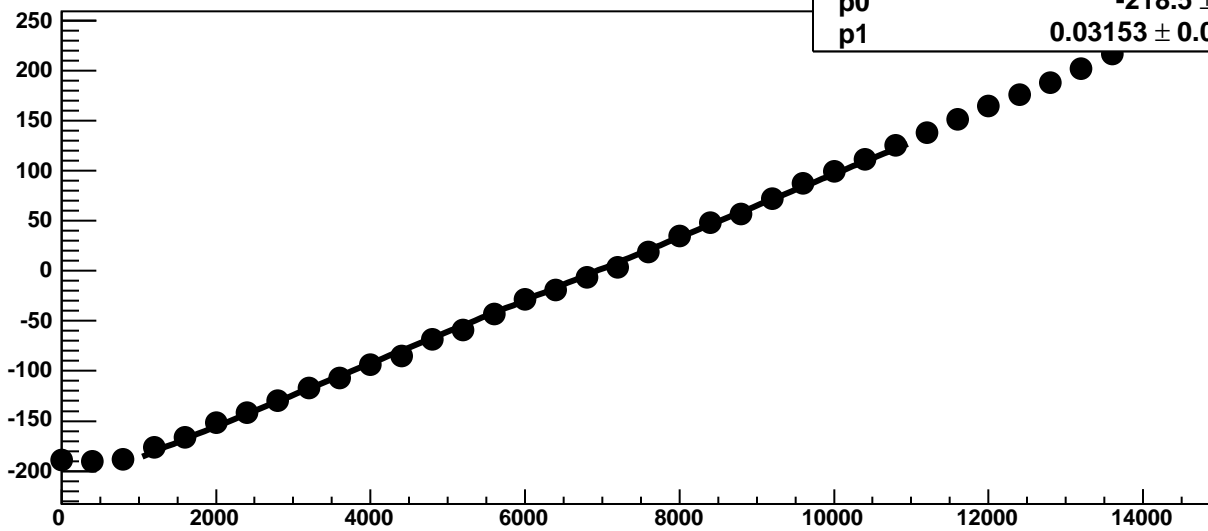
Chip 7, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

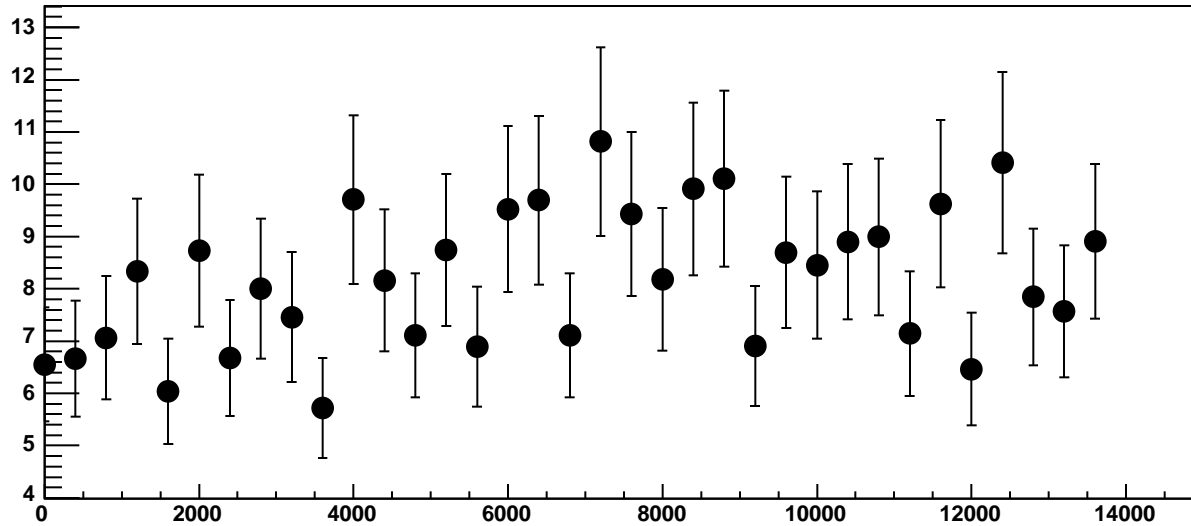


Chip 7, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

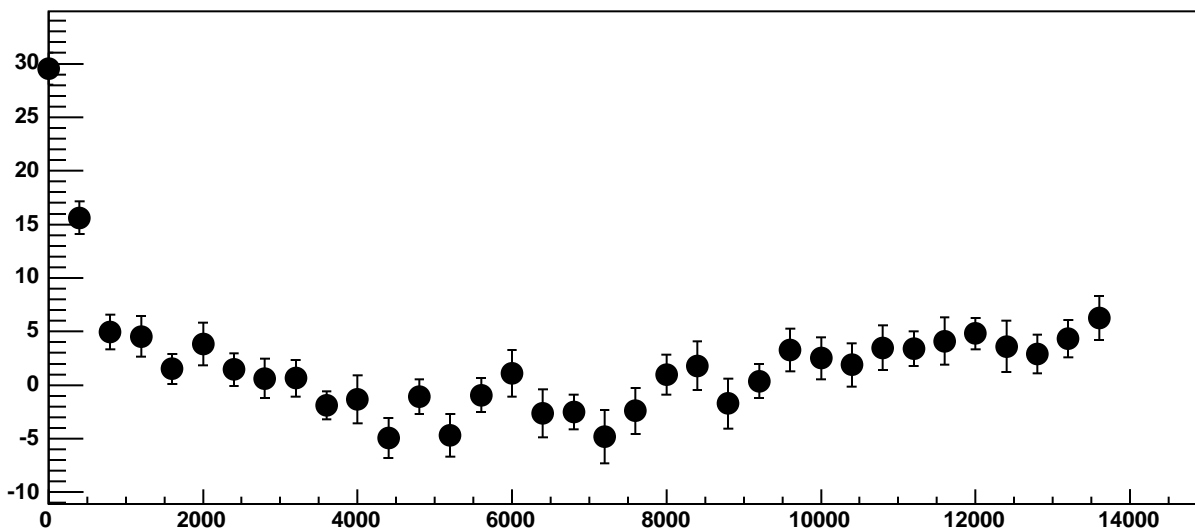


χ^2 / ndf 45.71 / 23
p0 -218.5 ± 0.7953
p1 0.03153 ± 0.0001267

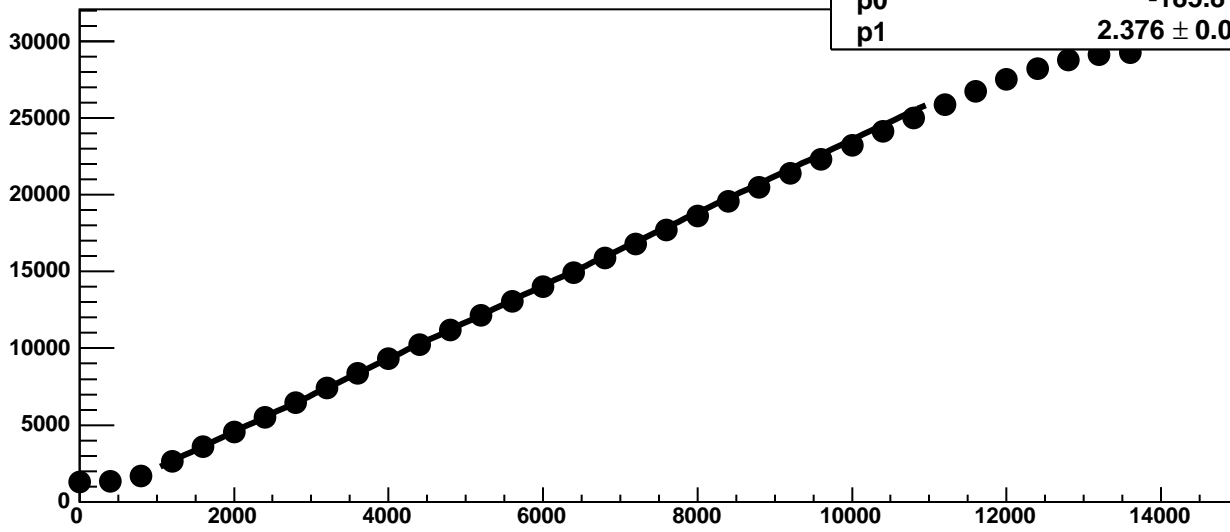
Chip 7, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



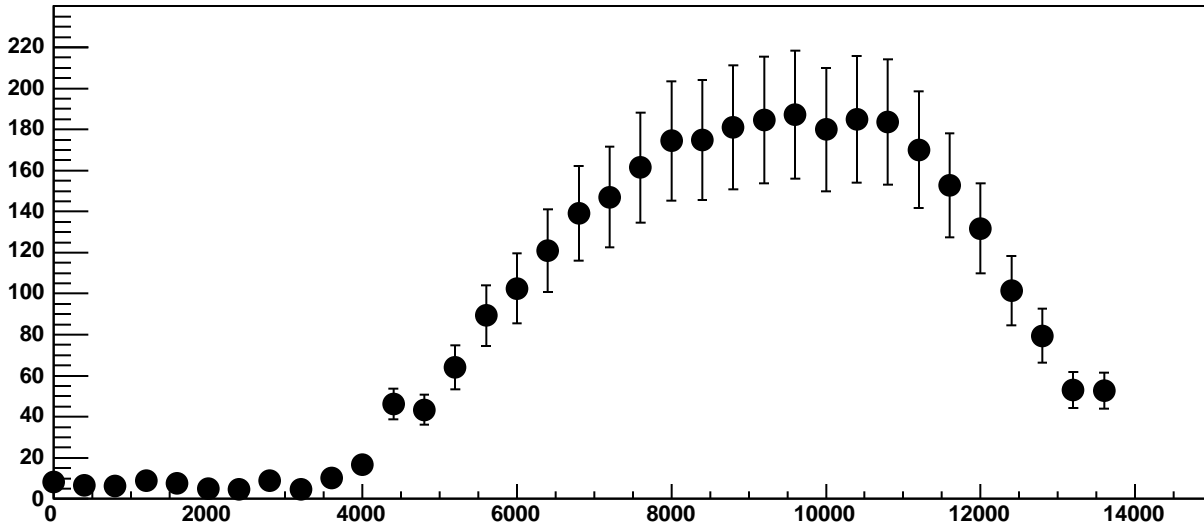
Chip 7, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



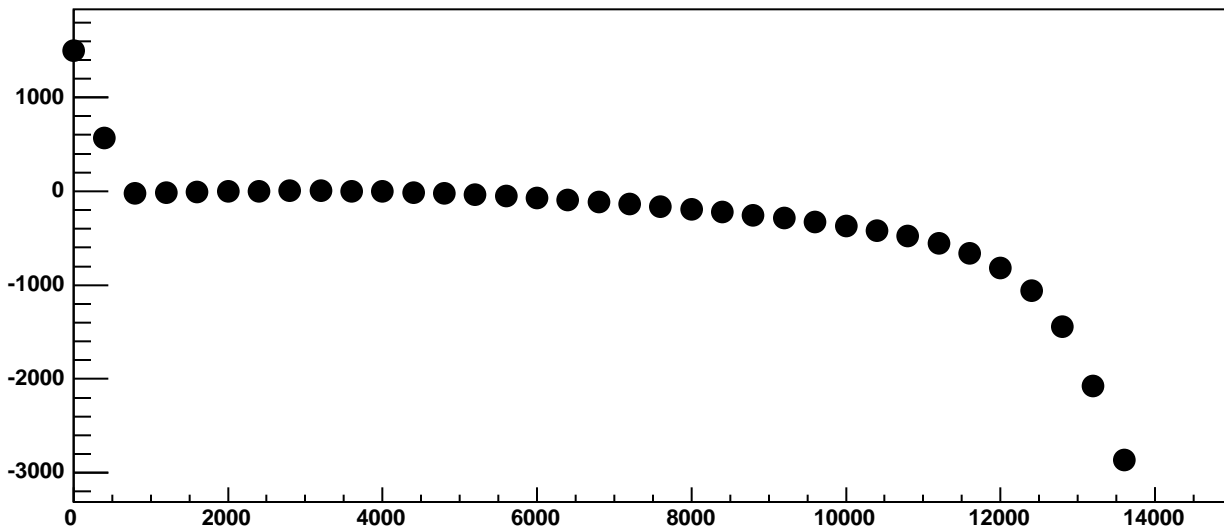
Chip 7, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC



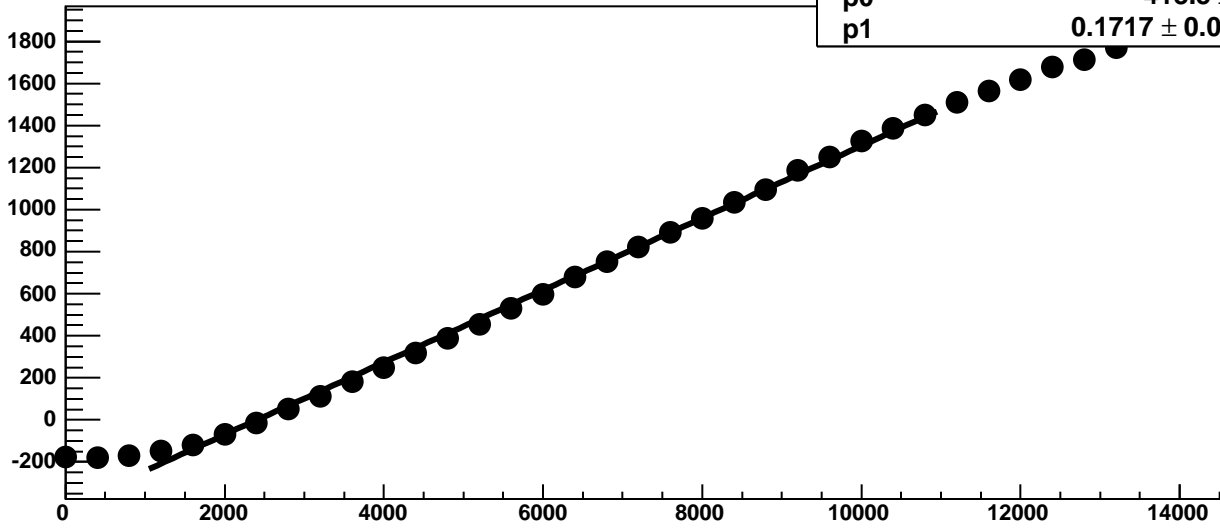
Chip 7, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

2044 / 23

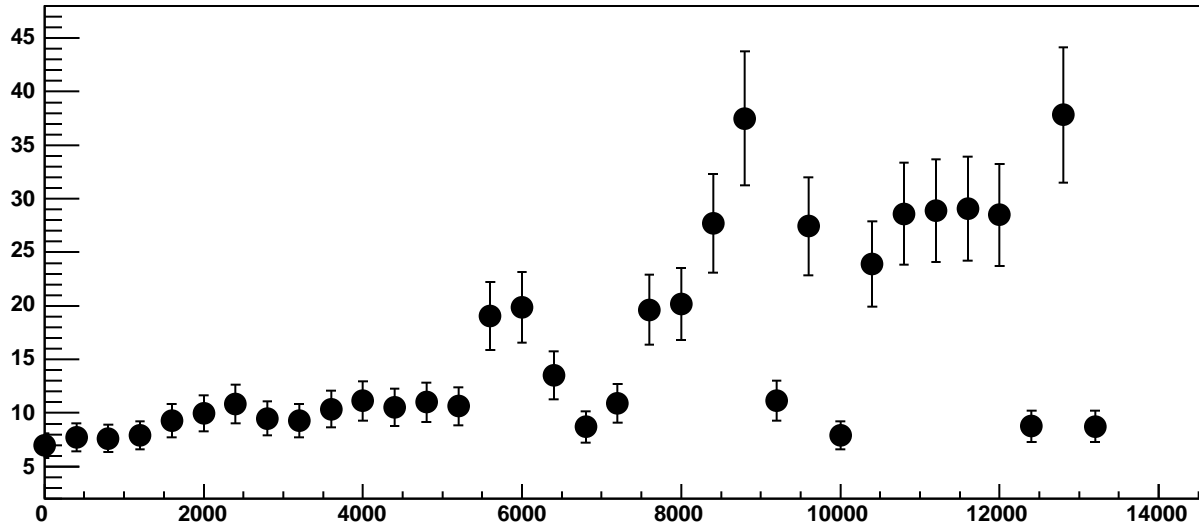
p0

-413.5 ± 1.082

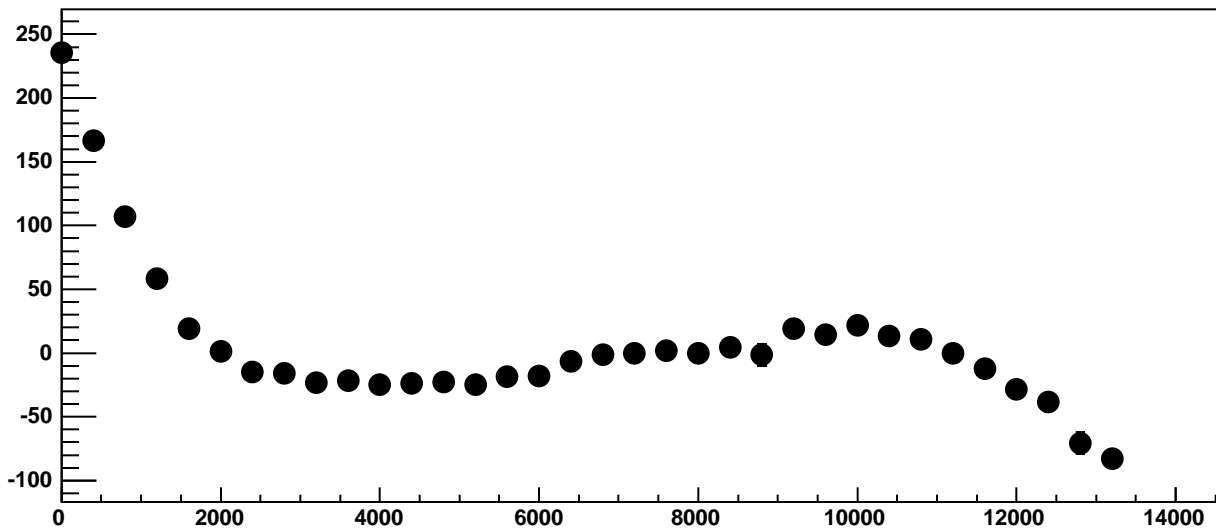
p1

0.1717 ± 0.0001901

Chip 7, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC

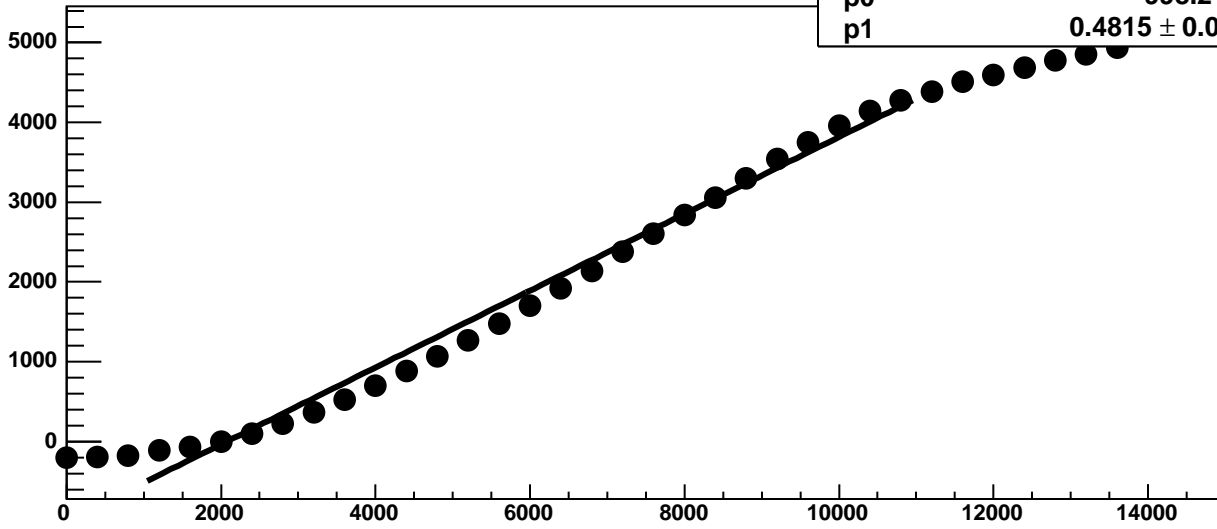


Chip 7, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

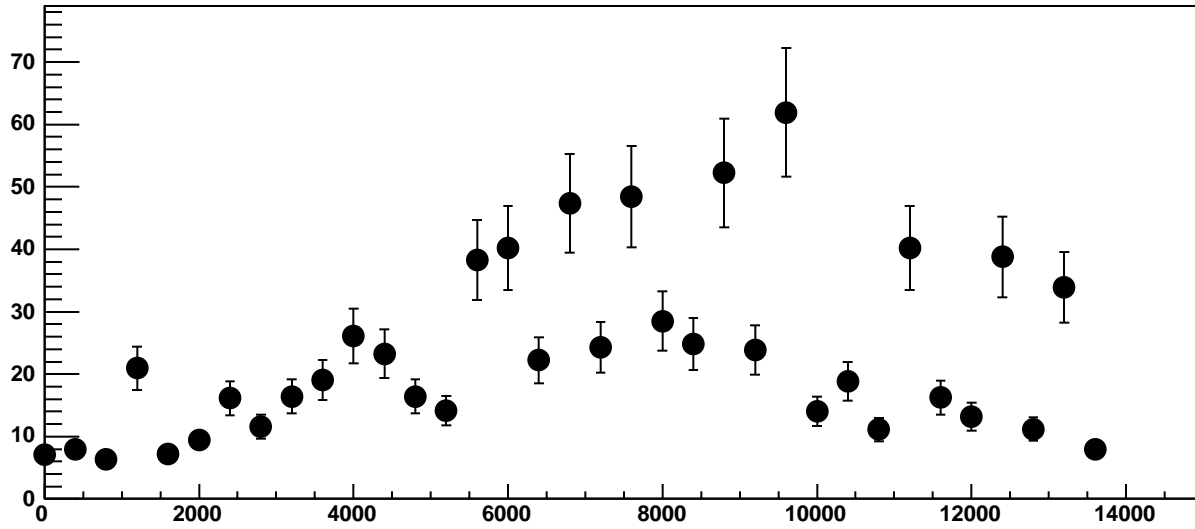


Chip 7, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

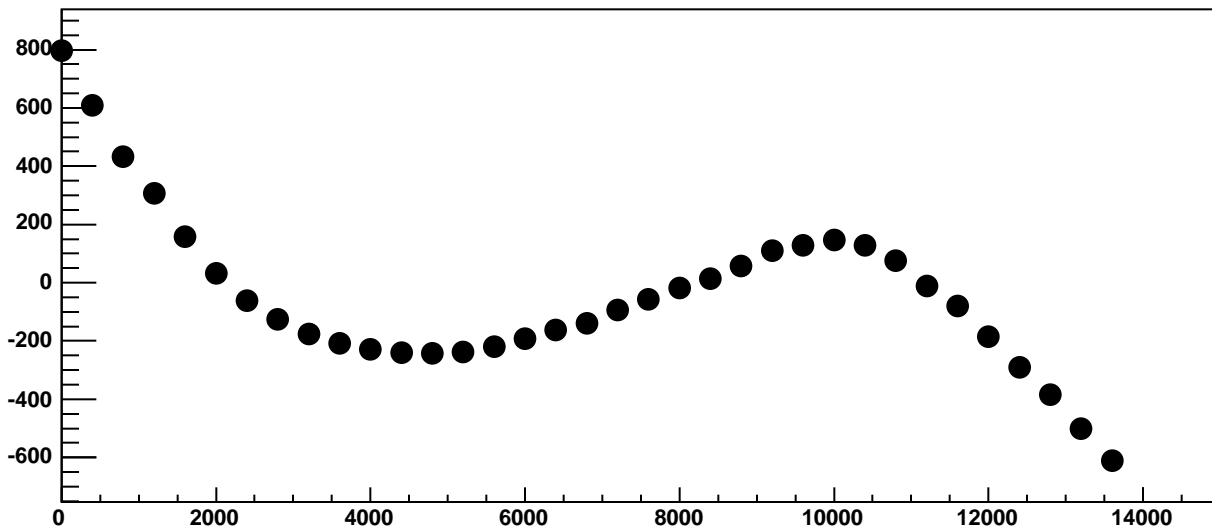
χ^2 / ndf 4.047e+04 / 23
p0 -998.2 ± 1.313
p1 0.4815 ± 0.0002294



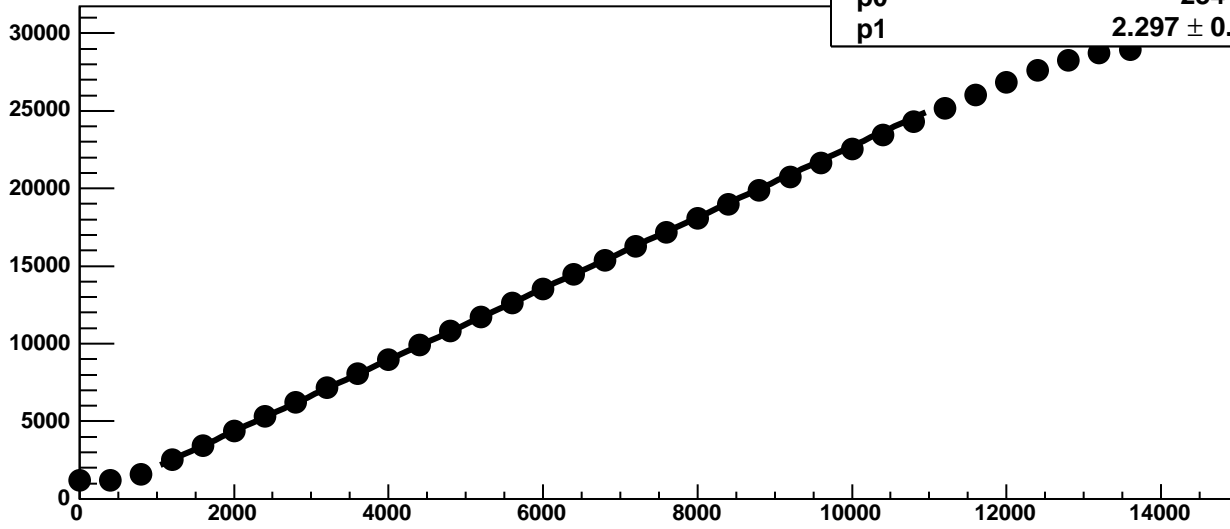
Chip 7, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

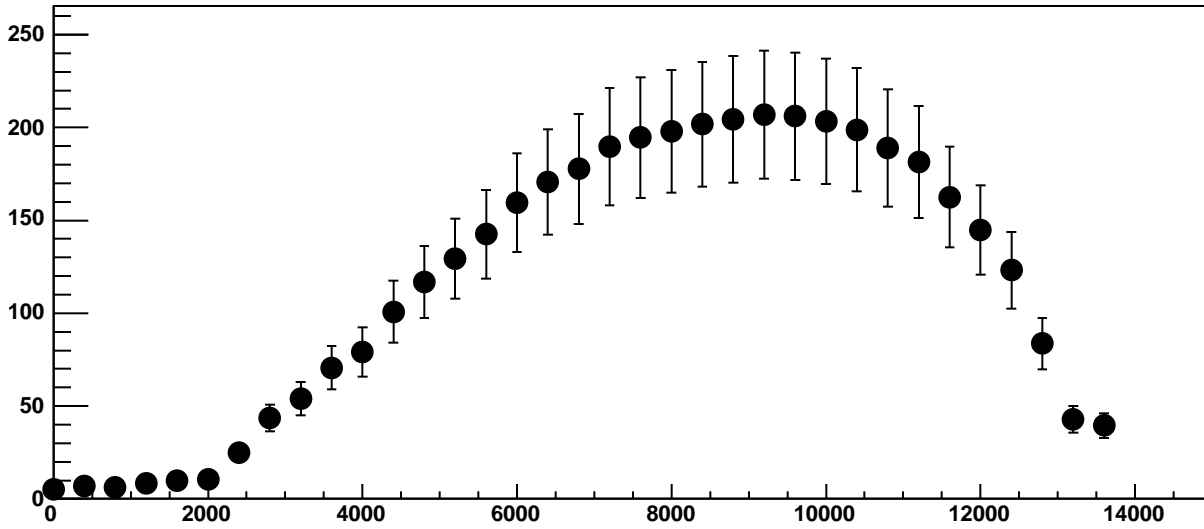


Chip 7, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC

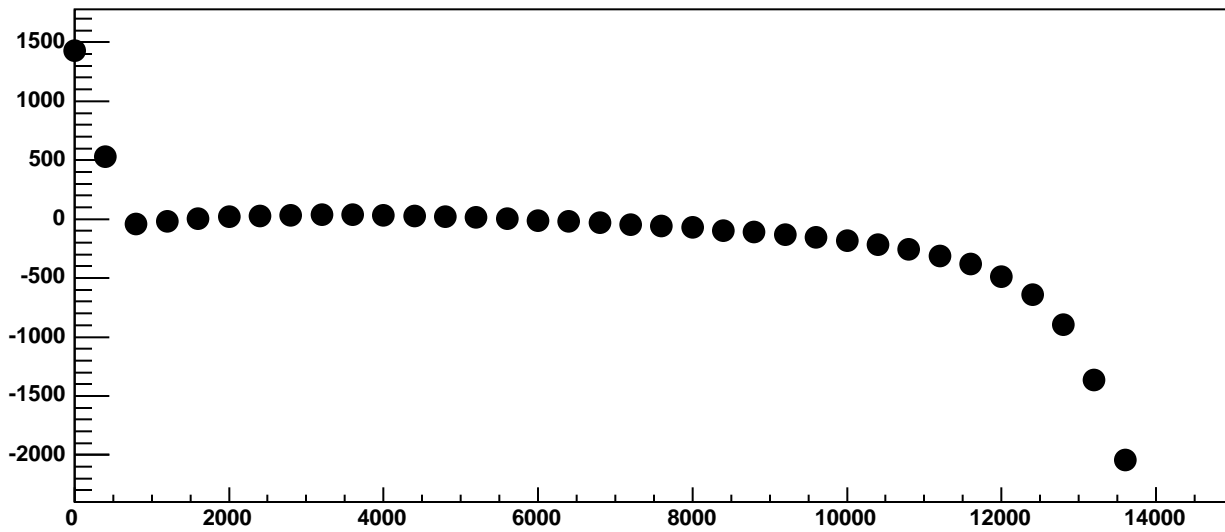


χ^2 / ndf 294 / 23
p0 -234 ± 2.662
p1 2.297 ± 0.001389

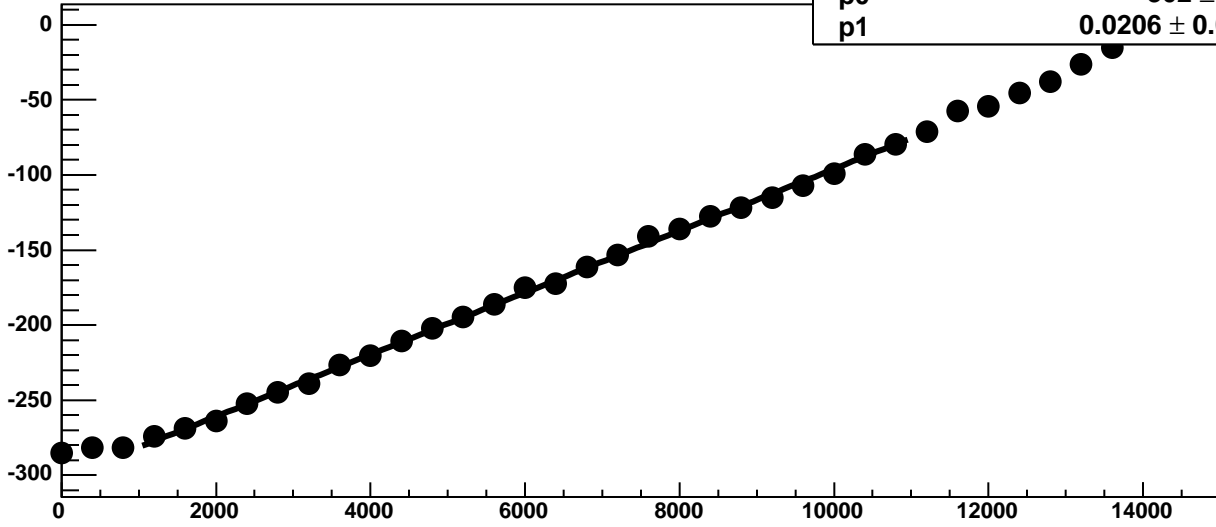
Chip 7, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



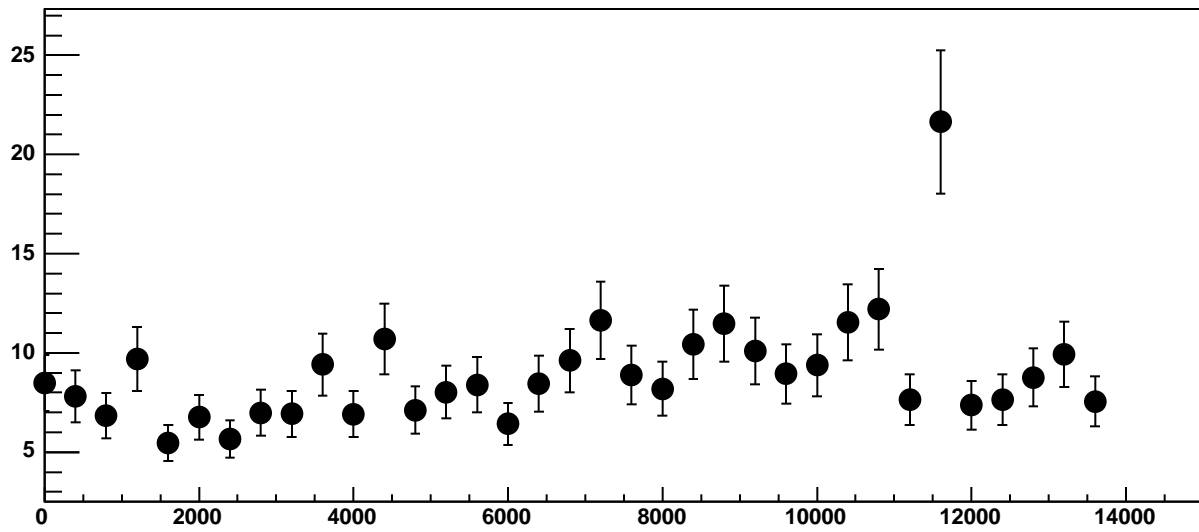
Chip 7, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC



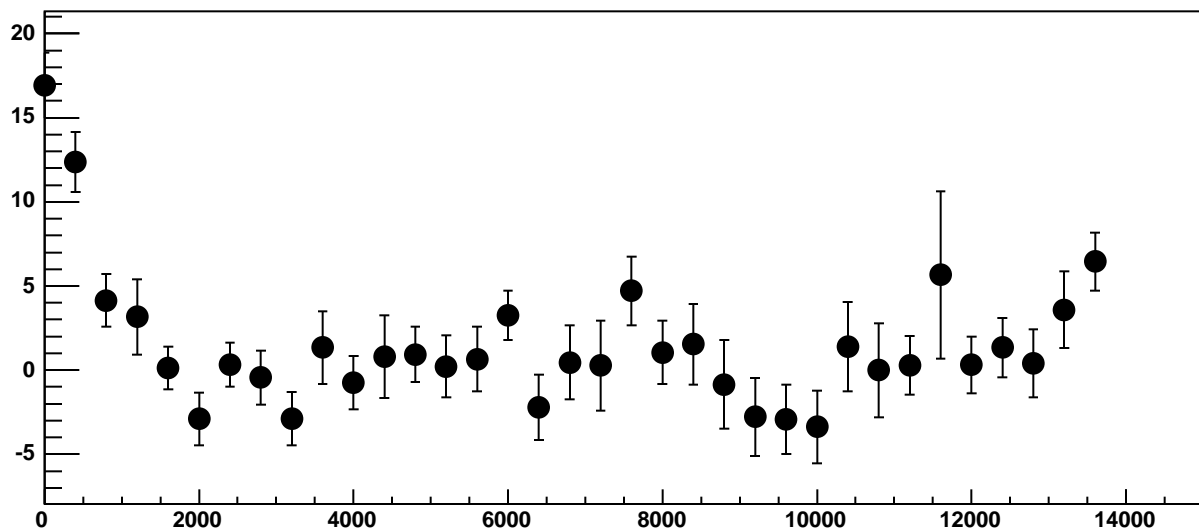
Chip 7, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



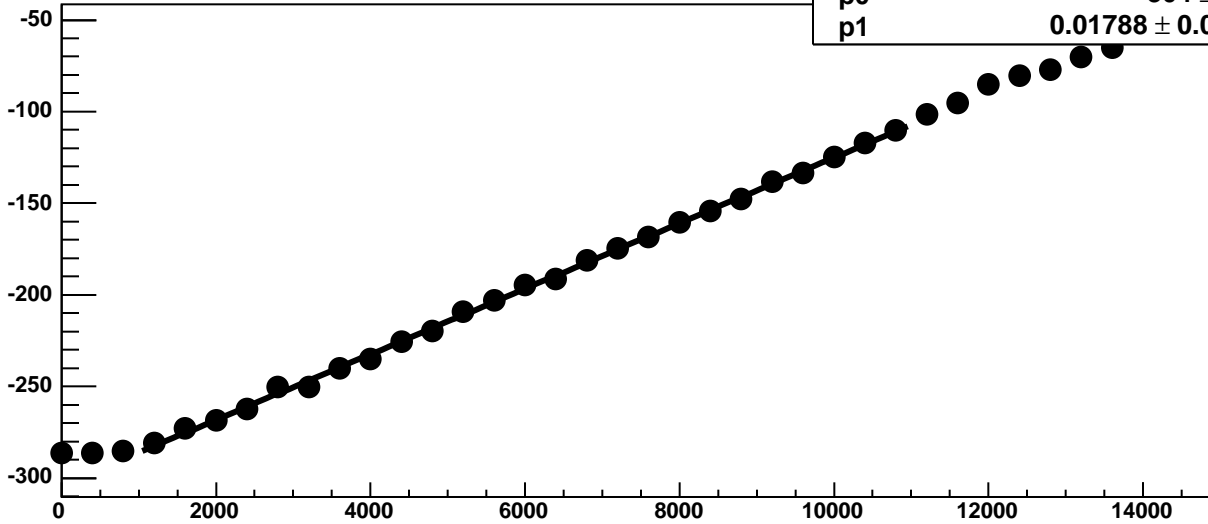
Chip 7, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

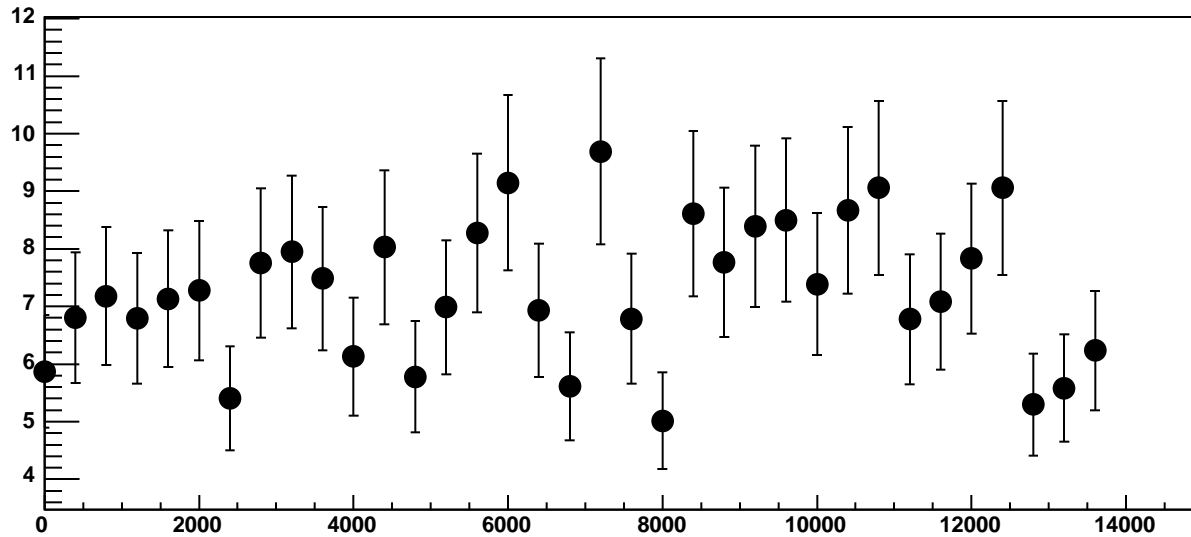


Chip 7, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

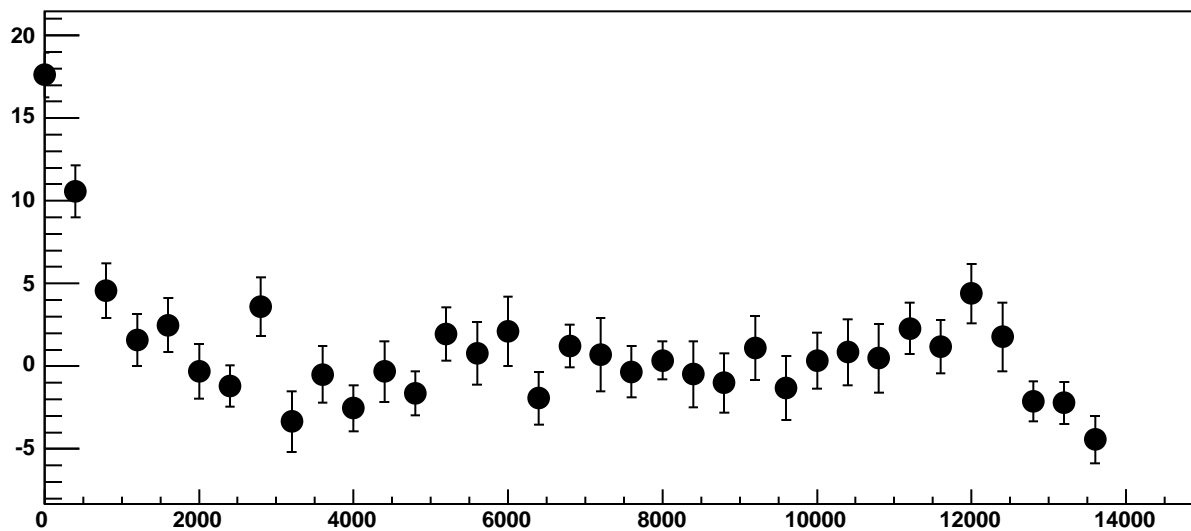


χ^2 / ndf 23.38 / 23
p0 -304 ± 0.7475
p1 0.01788 ± 0.0001176

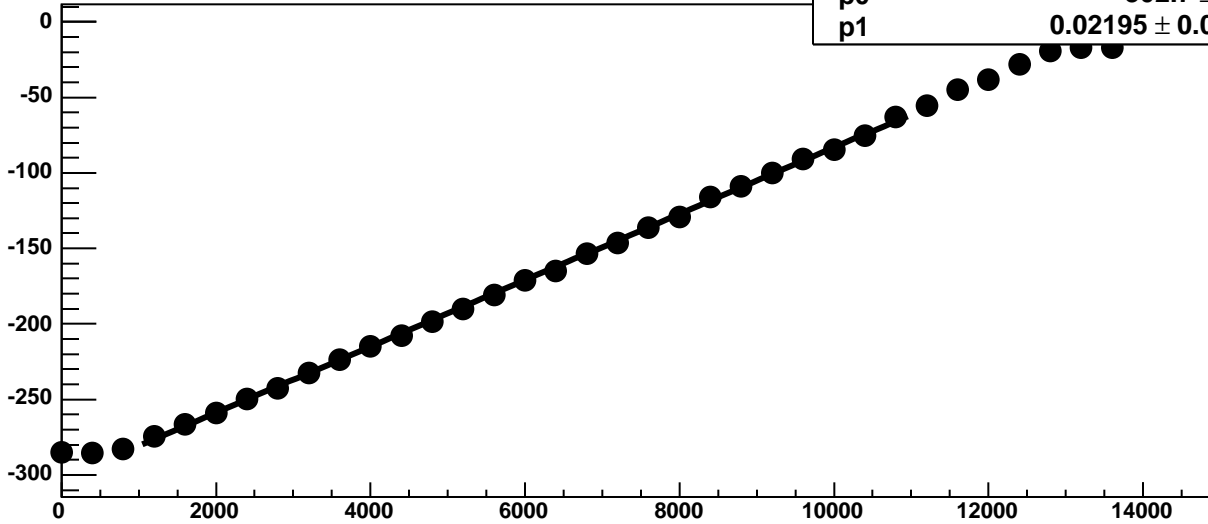
Chip 7, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

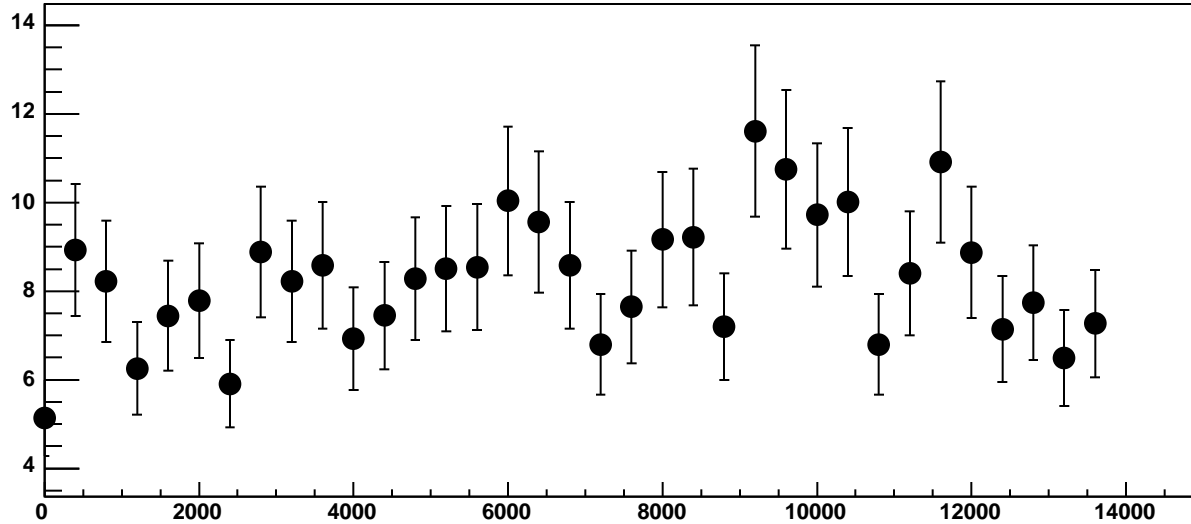


Chip 7, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

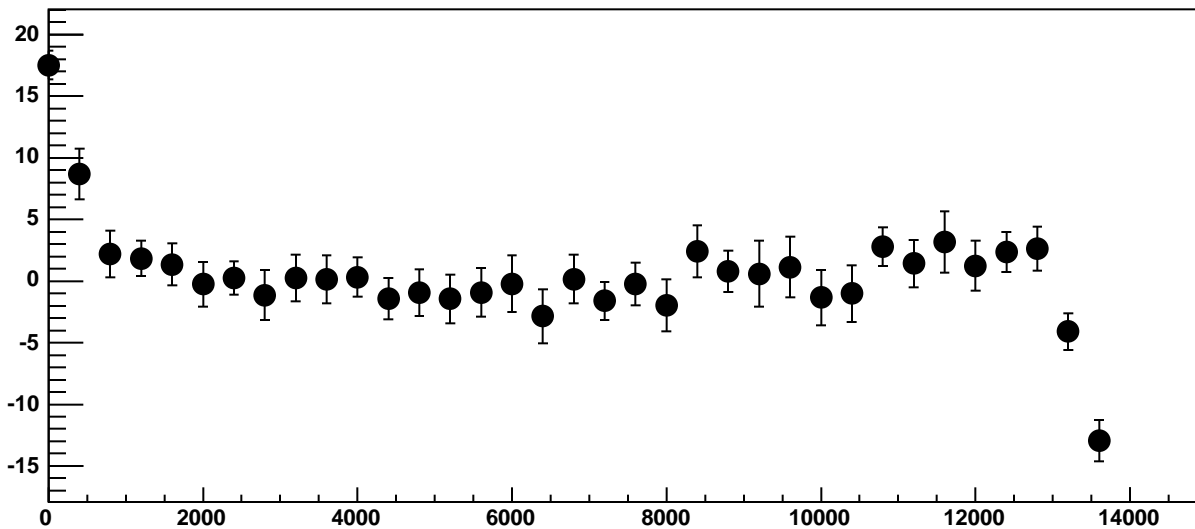


χ^2 / ndf 13.63 / 23
p0 -302.7 ± 0.7849
p1 0.02195 ± 0.0001247

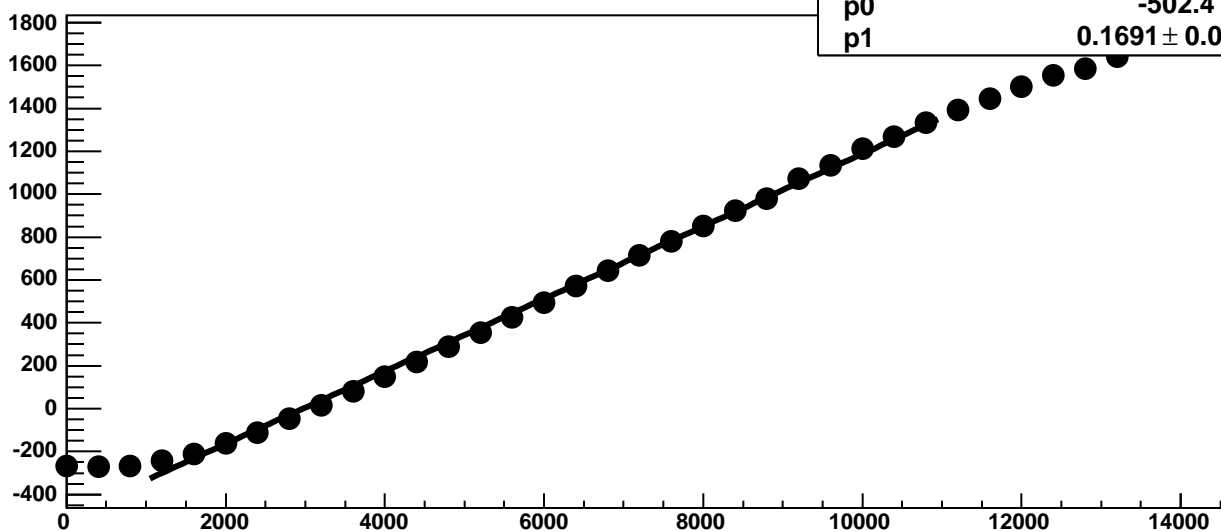
Chip 7, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



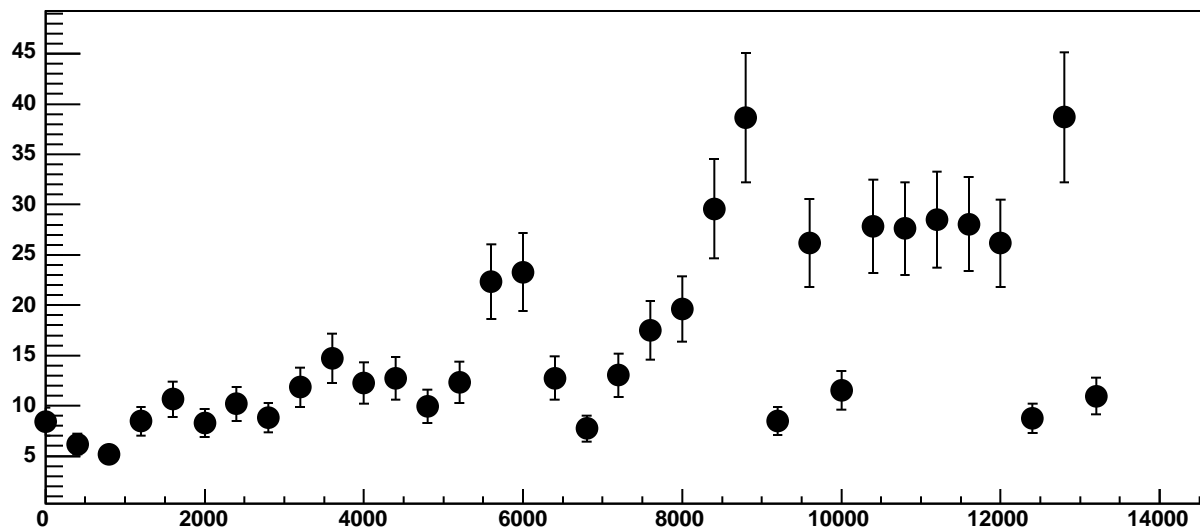
Chip 7, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC



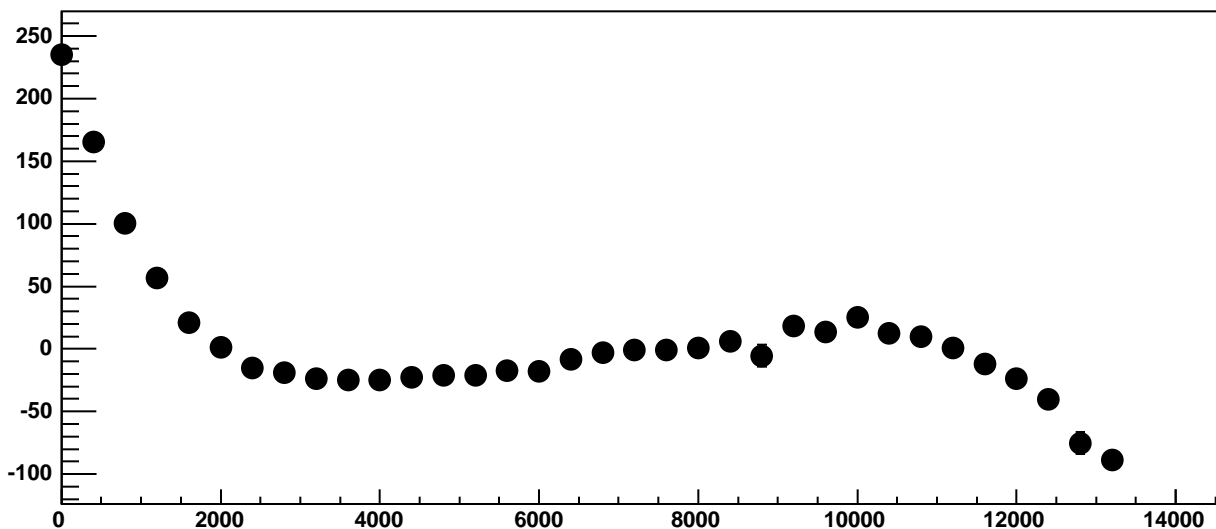
Chip 7, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



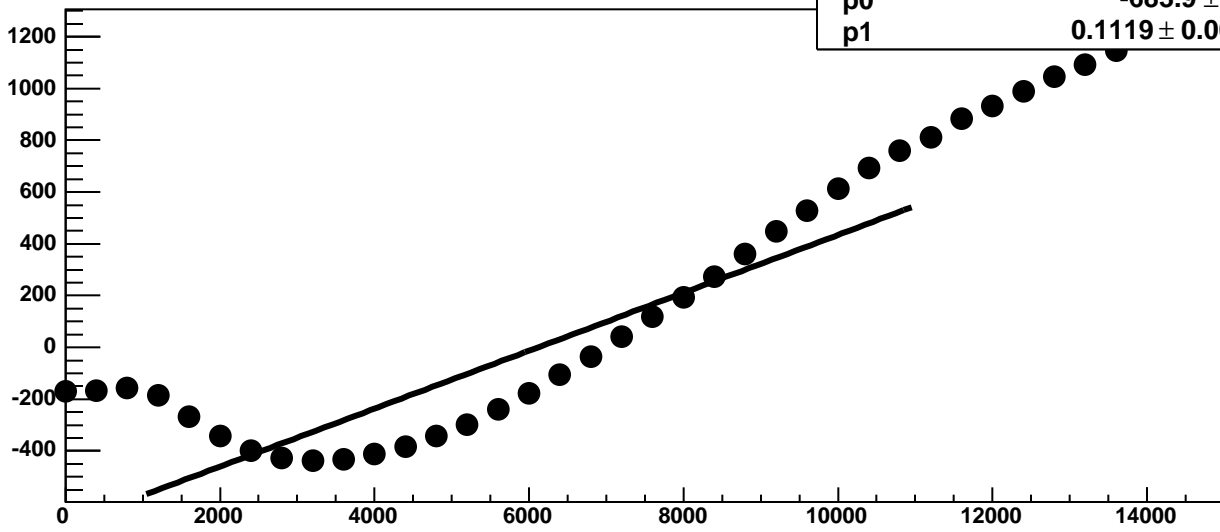
Chip 7, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 7, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

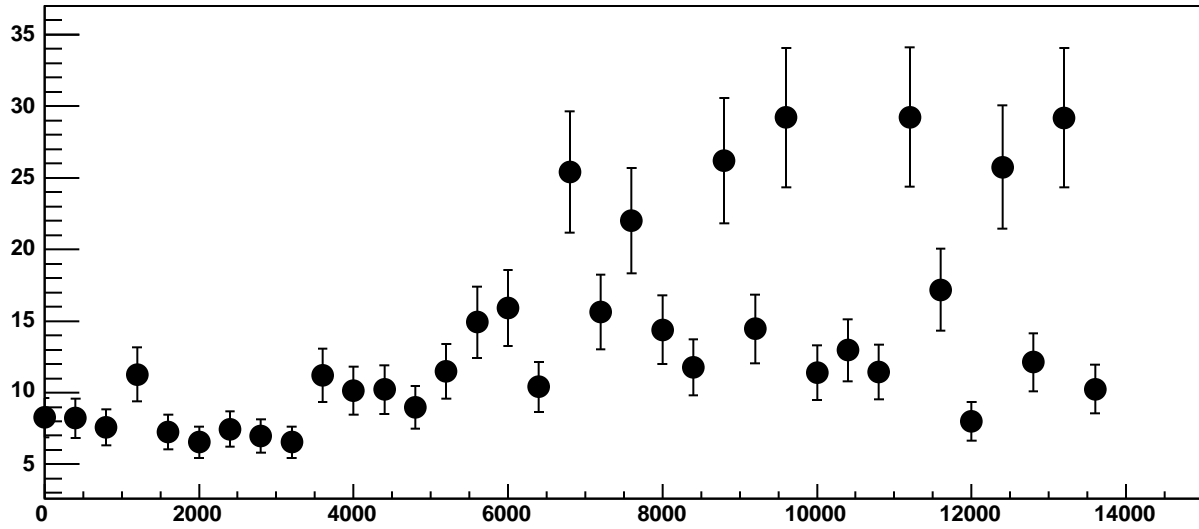


Chip 7, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

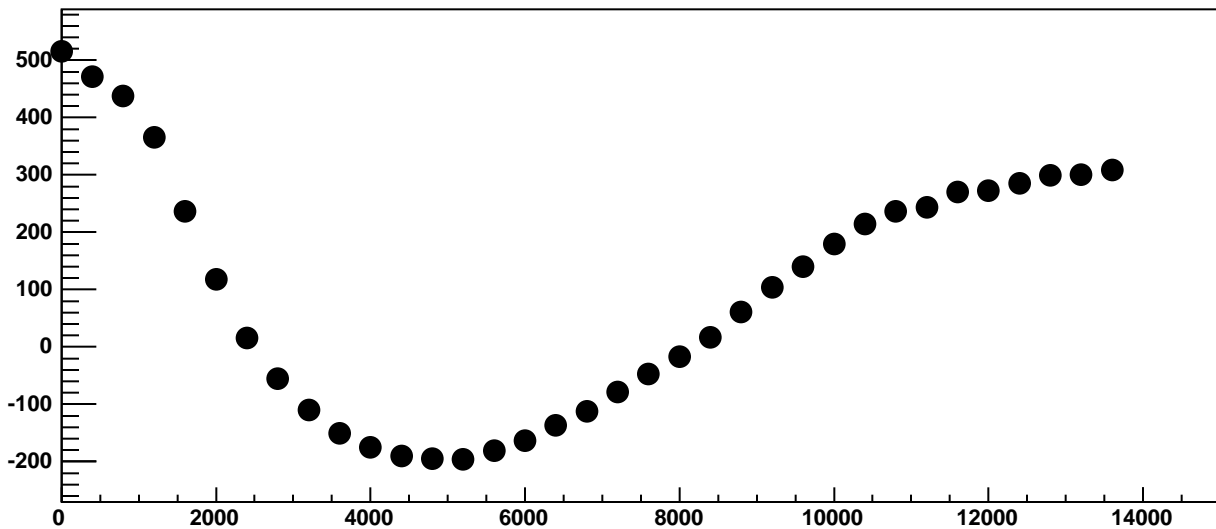


χ^2 / ndf 1.12e+05 / 23
p0 -683.9 ± 0.9203
p1 0.1119 ± 0.0001727

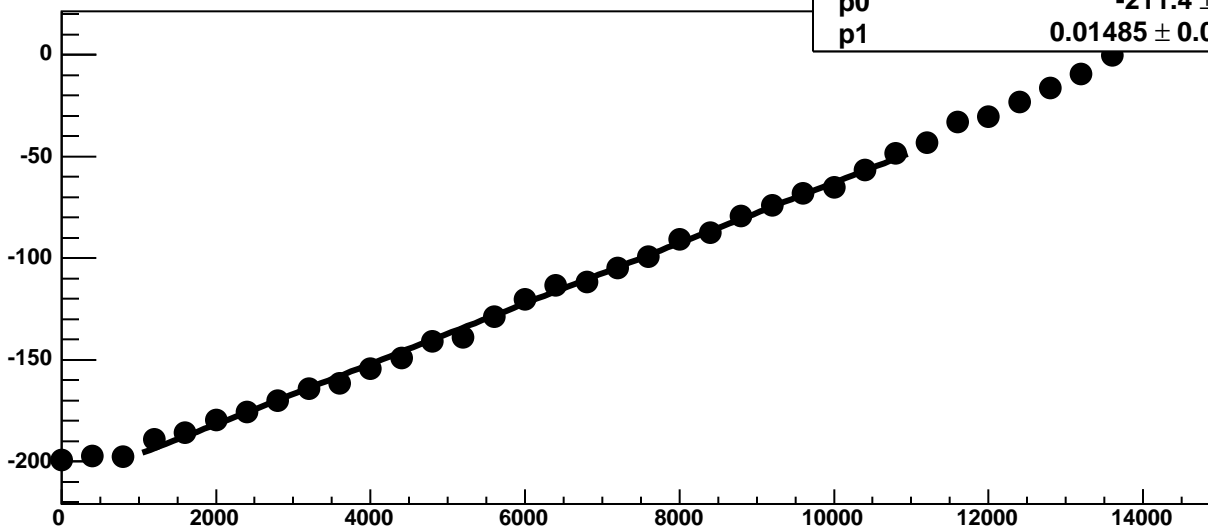
Chip 7, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

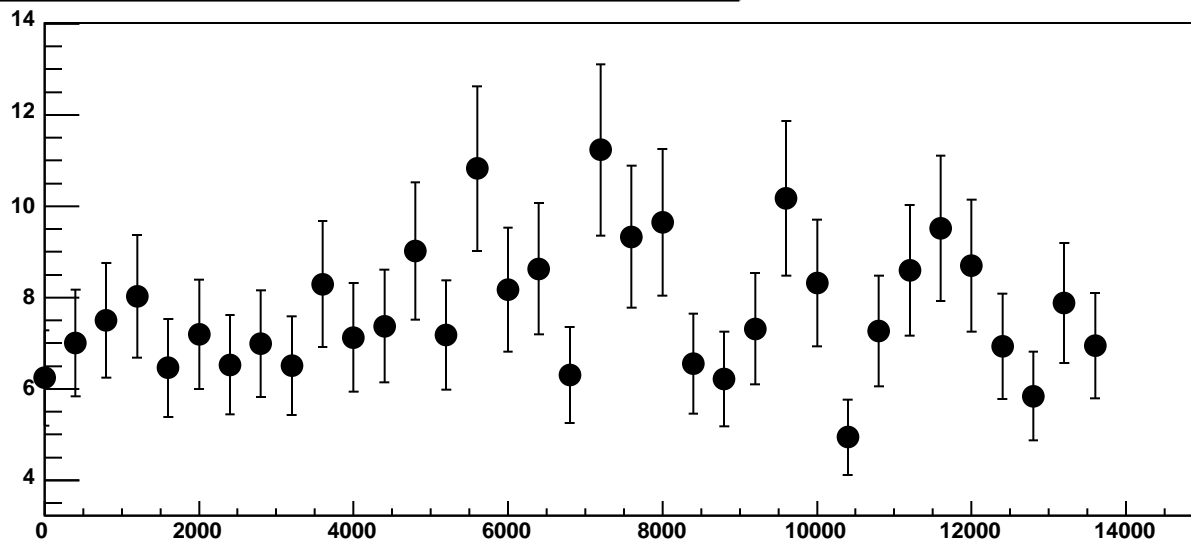


Chip 7, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

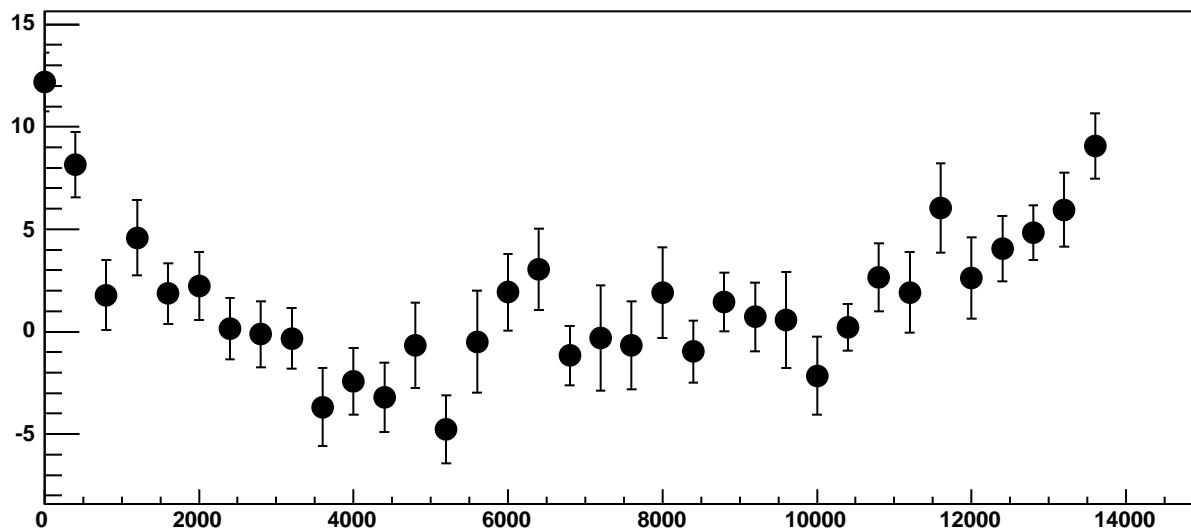


χ^2 / ndf 38.16 / 23
p0 -211.4 ± 0.7503
p1 0.01485 ± 0.0001108

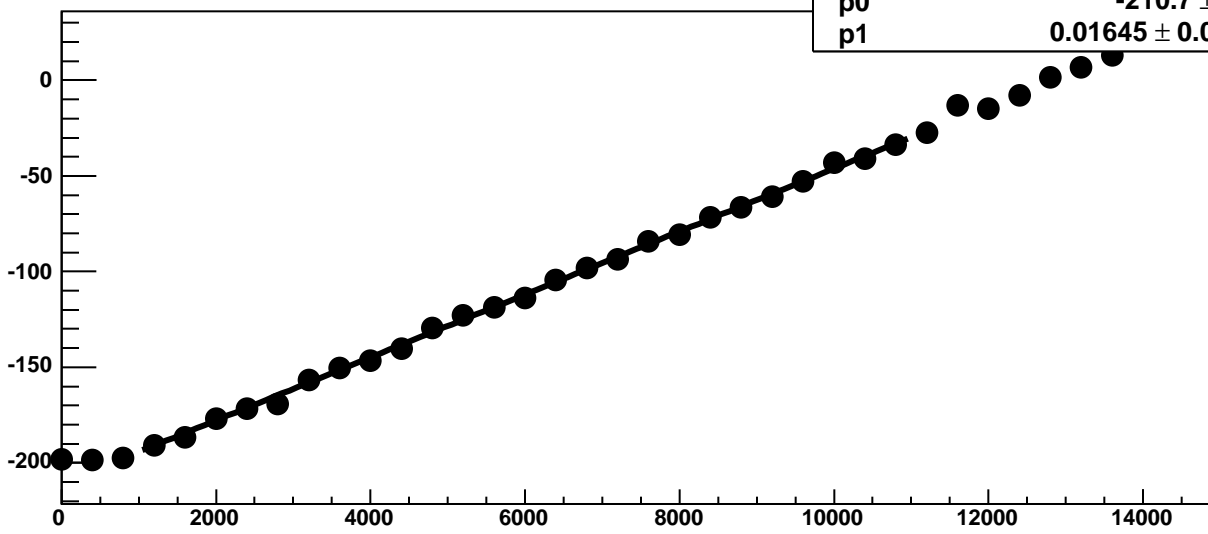
Chip 7, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



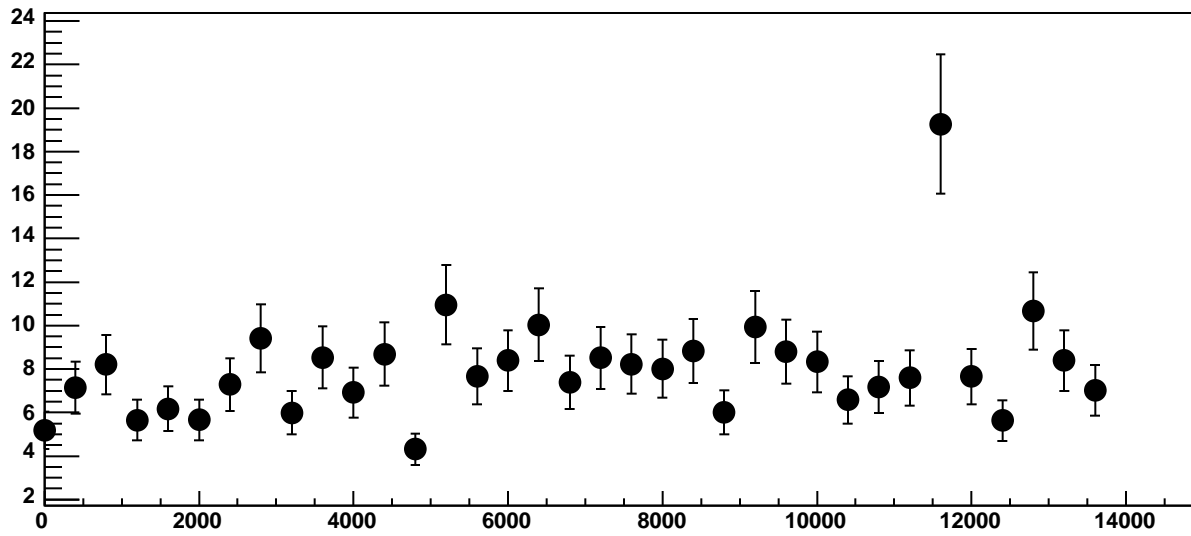
Chip 7, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC



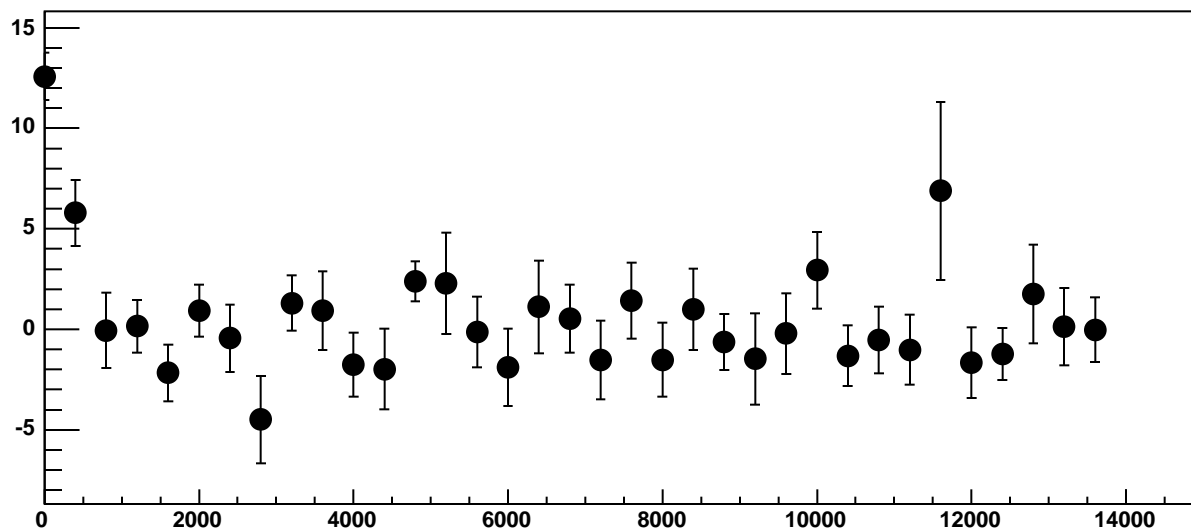
Chip 7, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



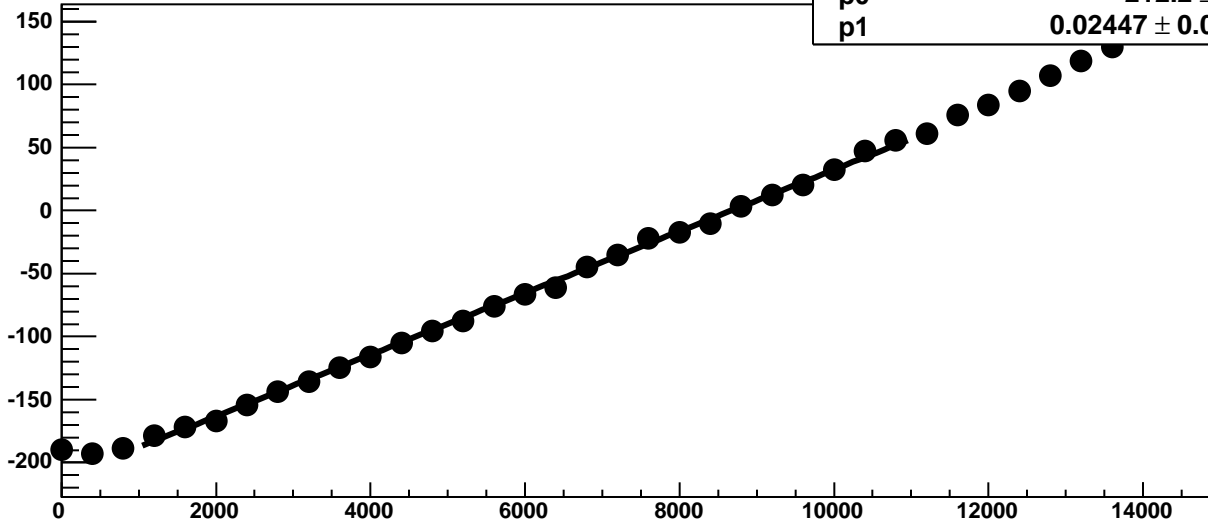
Chip 7, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

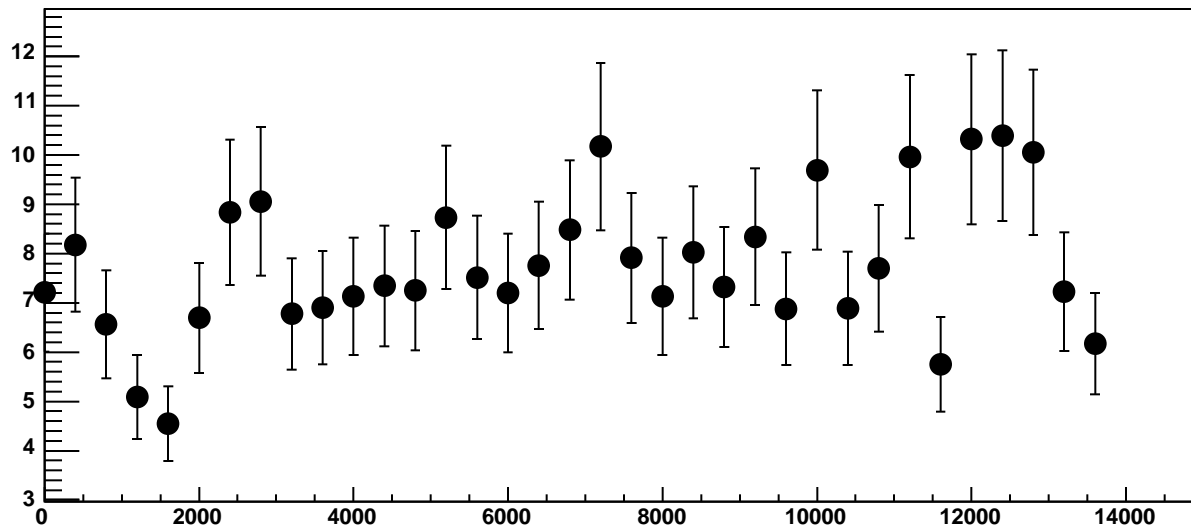


Chip 7, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

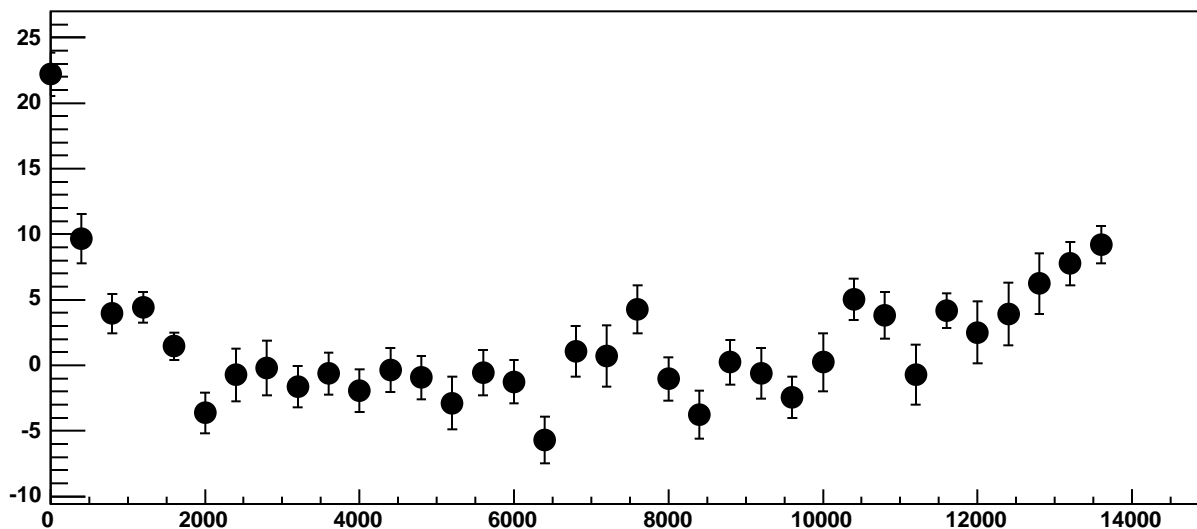


χ^2 / ndf 65.84 / 23
p0 -212.2 ± 0.6723
p1 0.02447 ± 0.0001076

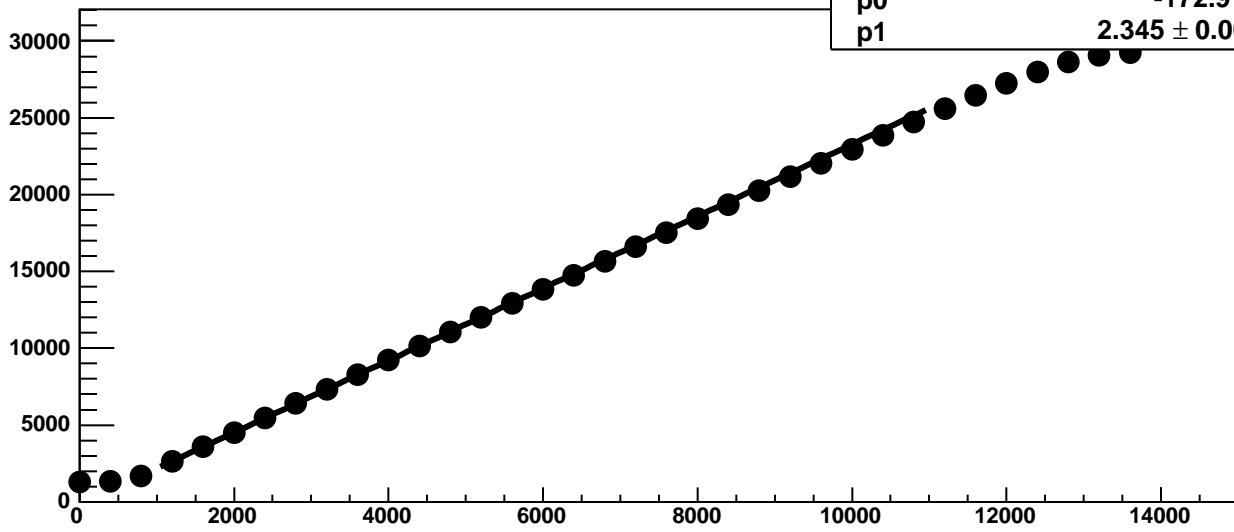
Chip 7, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

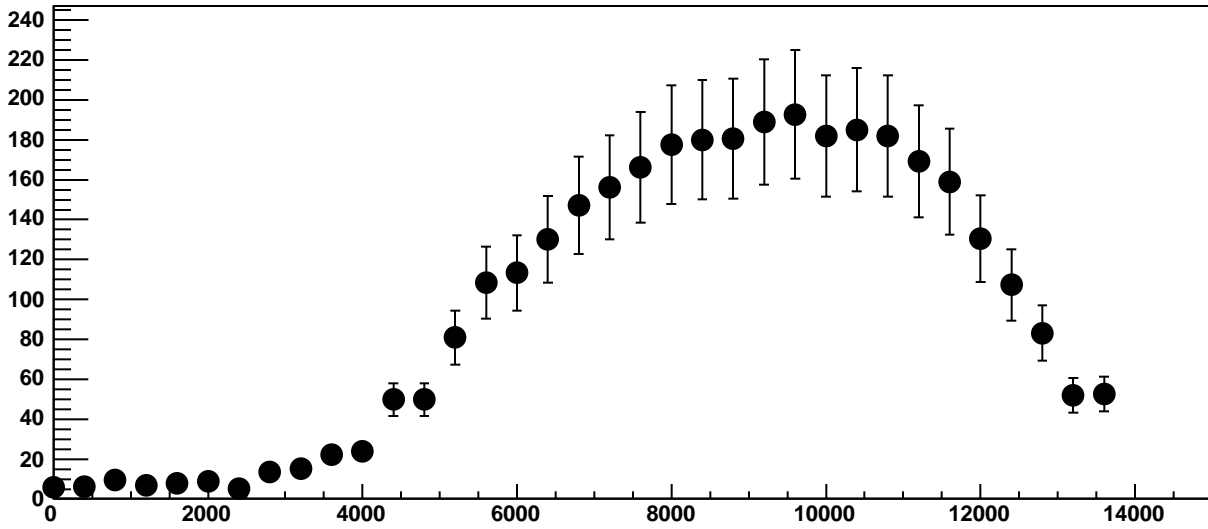


Chip 7, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC

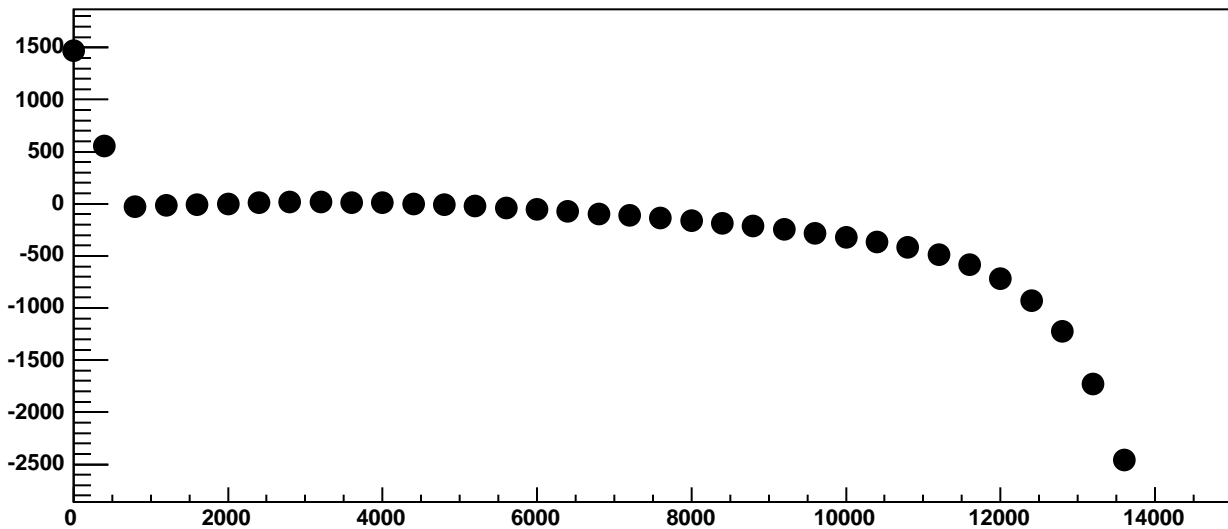


χ^2 / ndf 616.9 / 23
p0 -172.9 ± 1.981
p1 2.345 ± 0.0008682

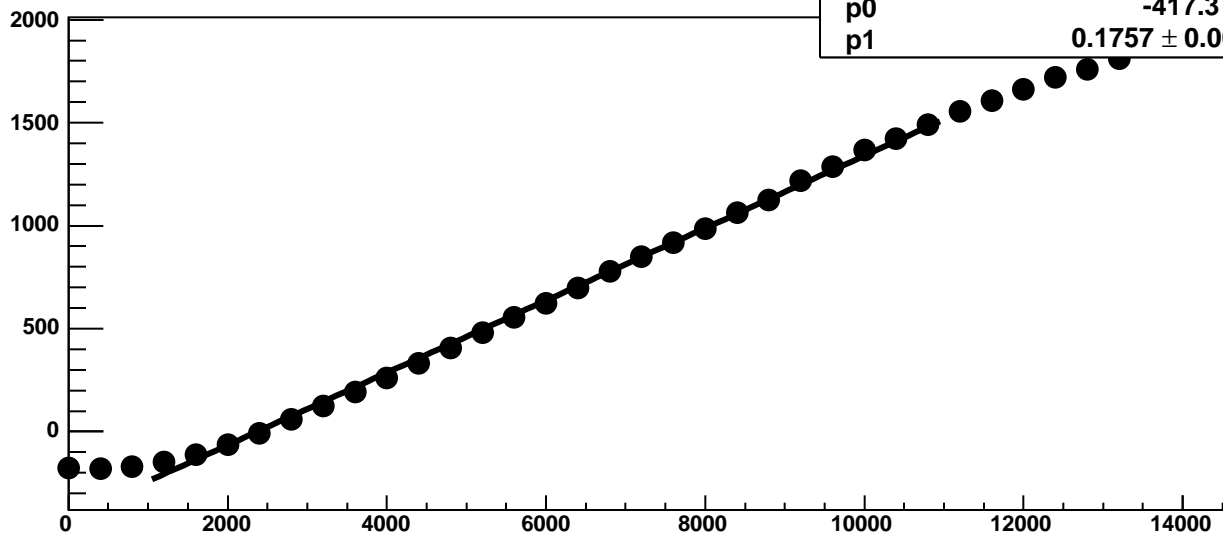
Chip 7, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC



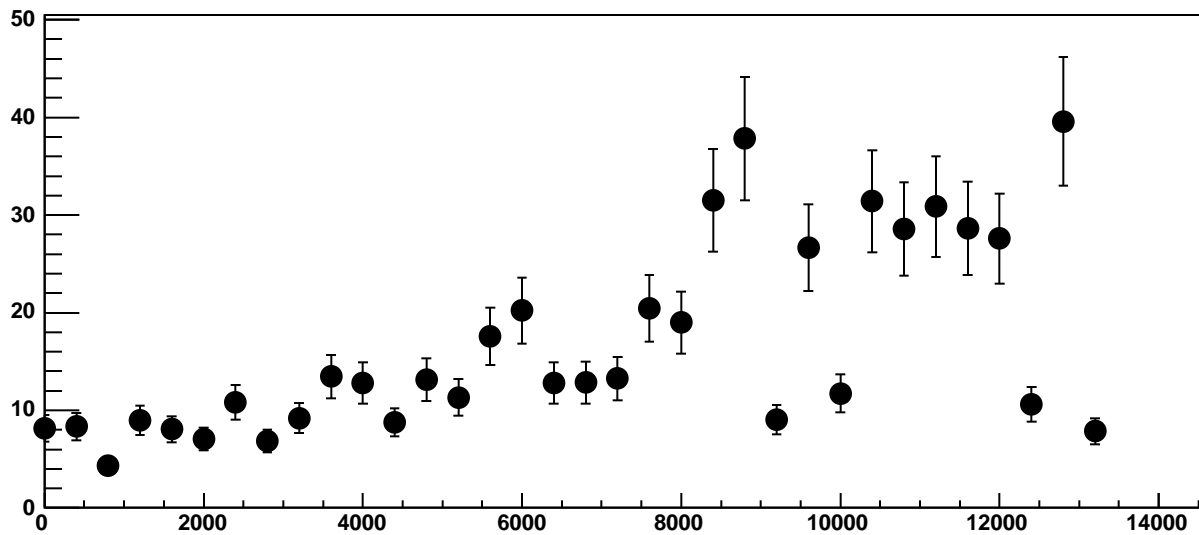
Chip 7, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC



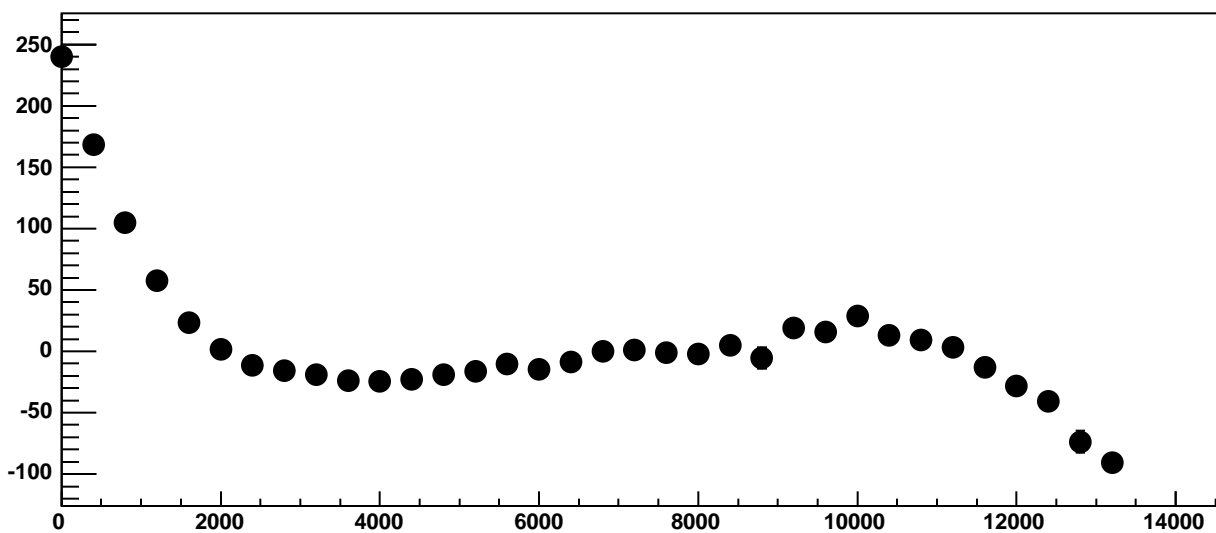
Chip 7, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



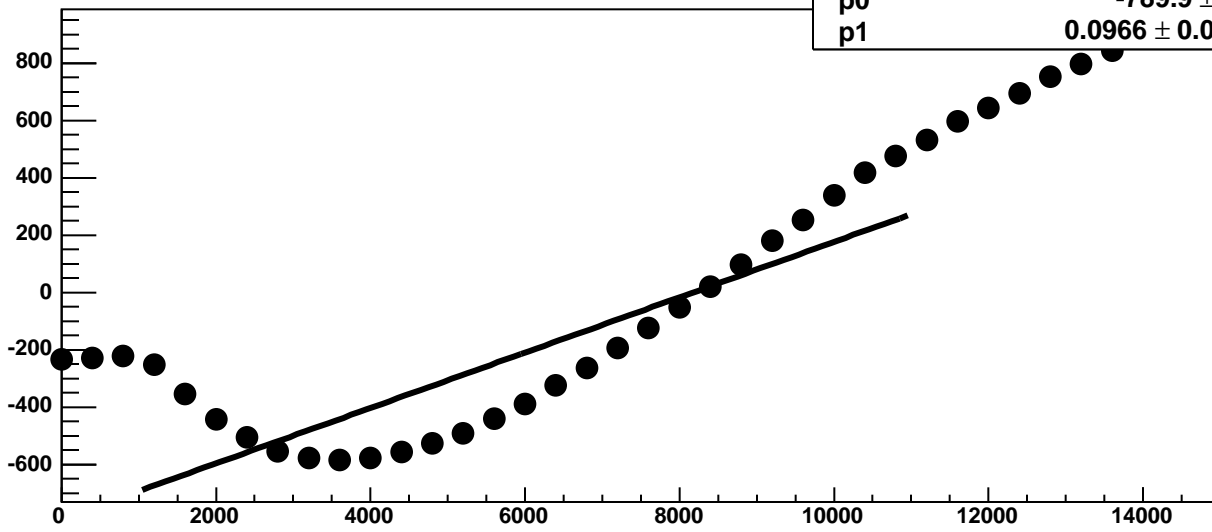
Chip 7, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



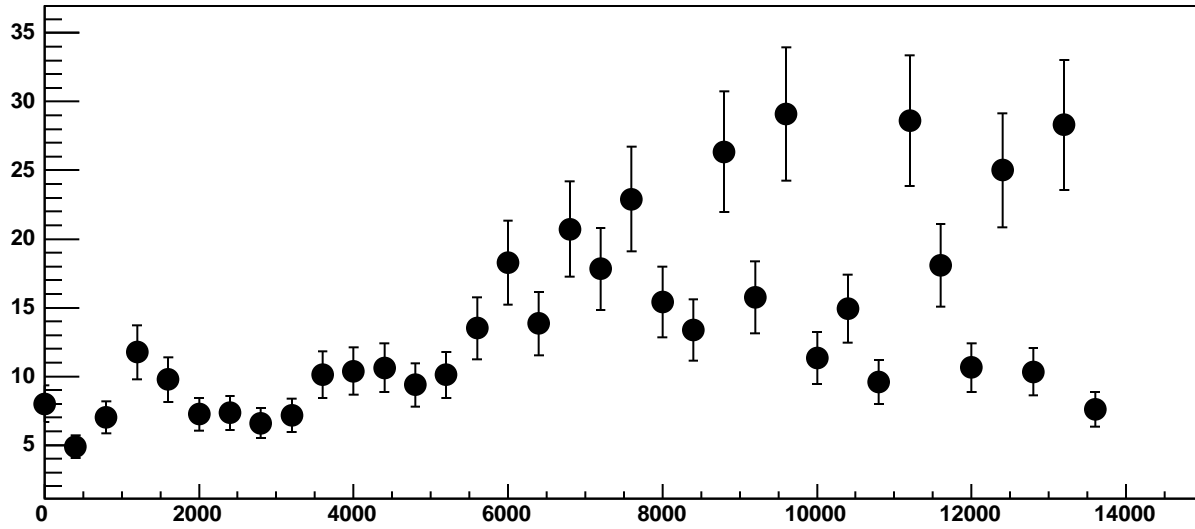
Chip 7, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



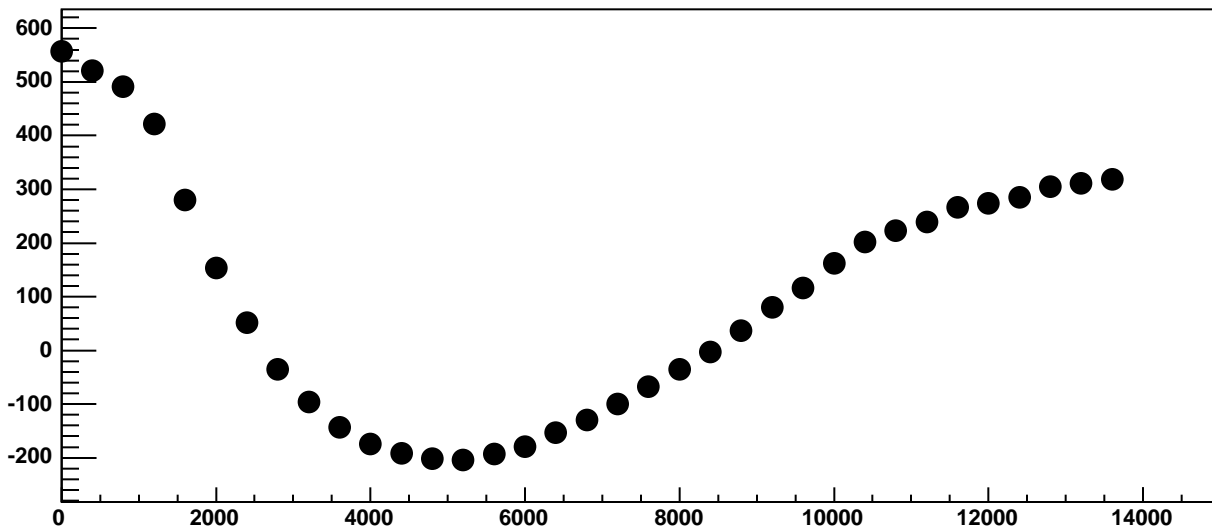
Chip 7, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



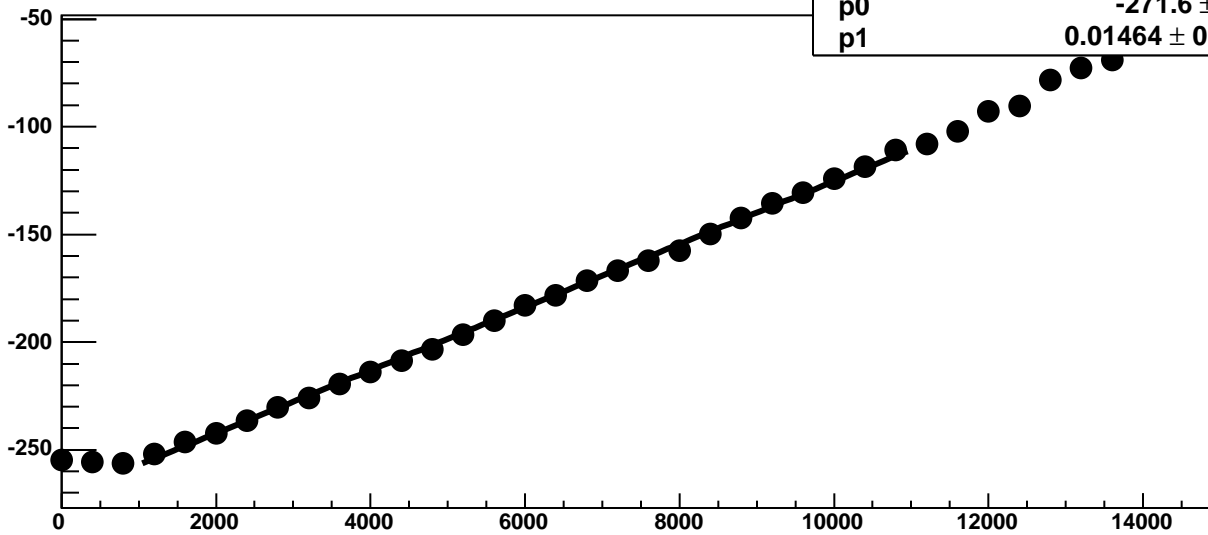
Chip 7, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

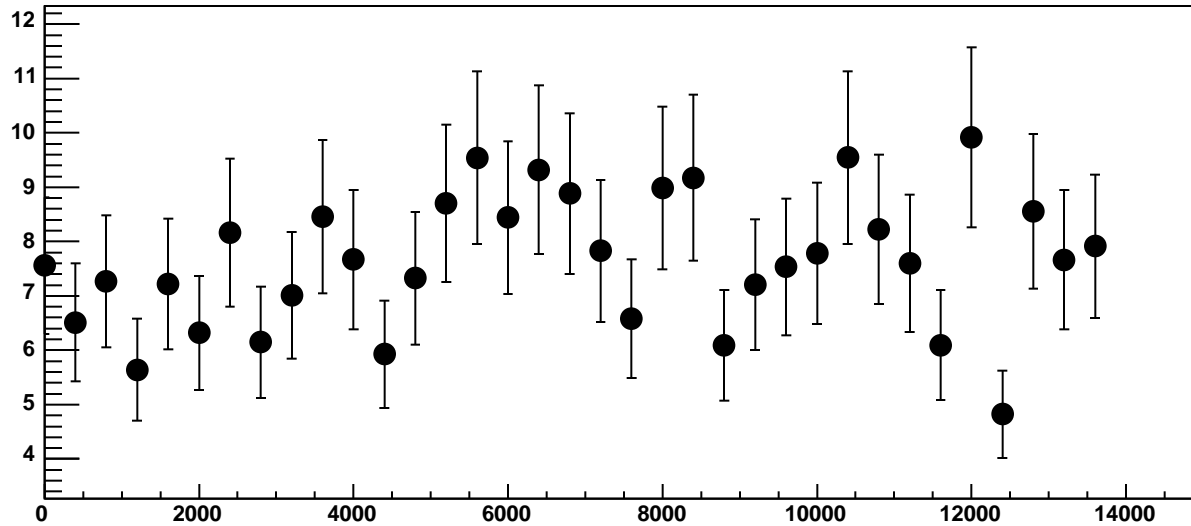


Chip 7, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

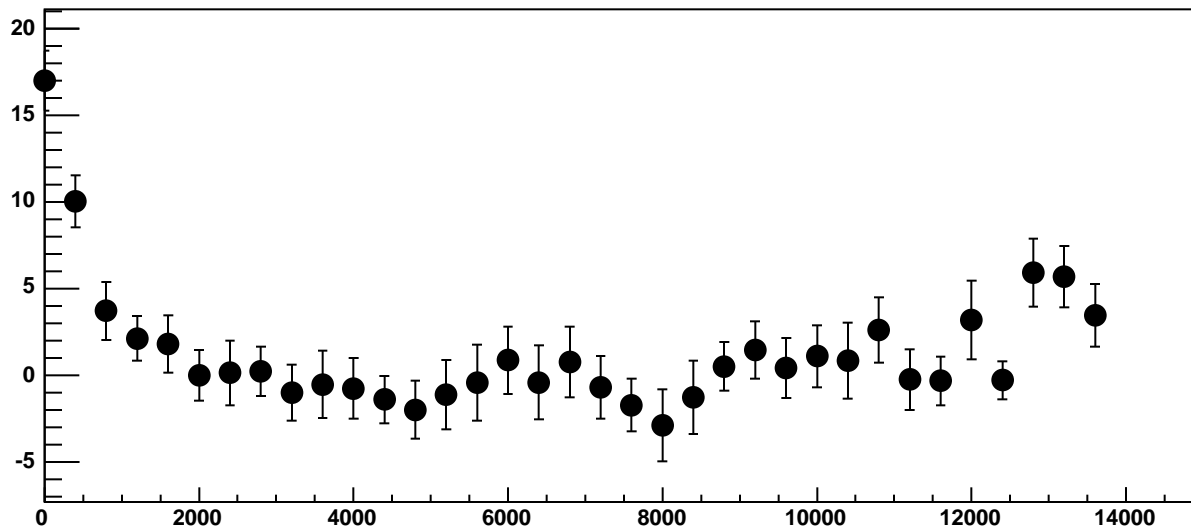


χ^2 / ndf 14.89 / 23
p0 -271.6 ± 0.7315
p1 0.01464 ± 0.000115

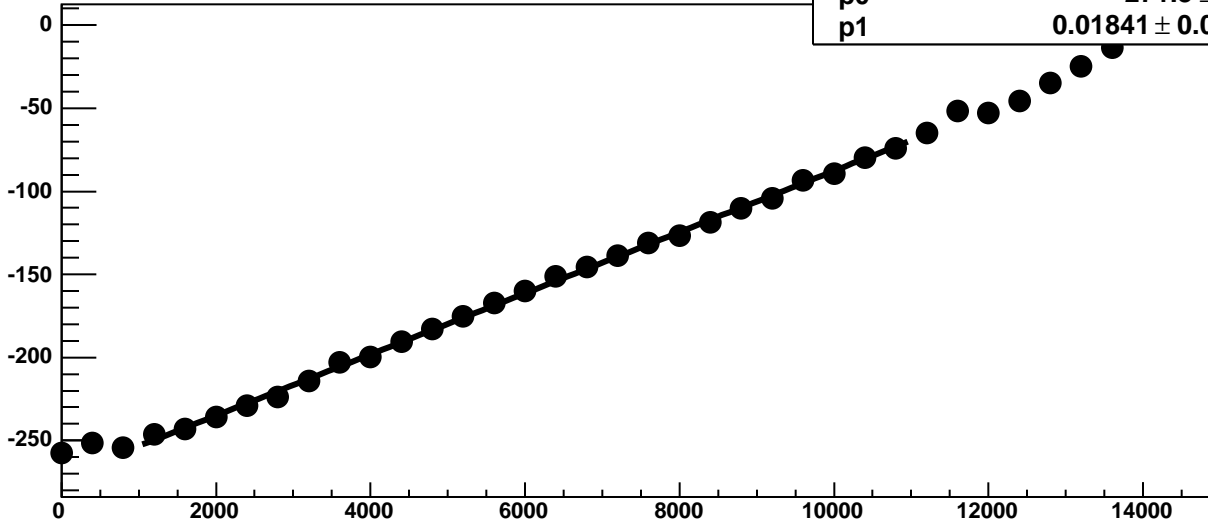
Chip 7, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

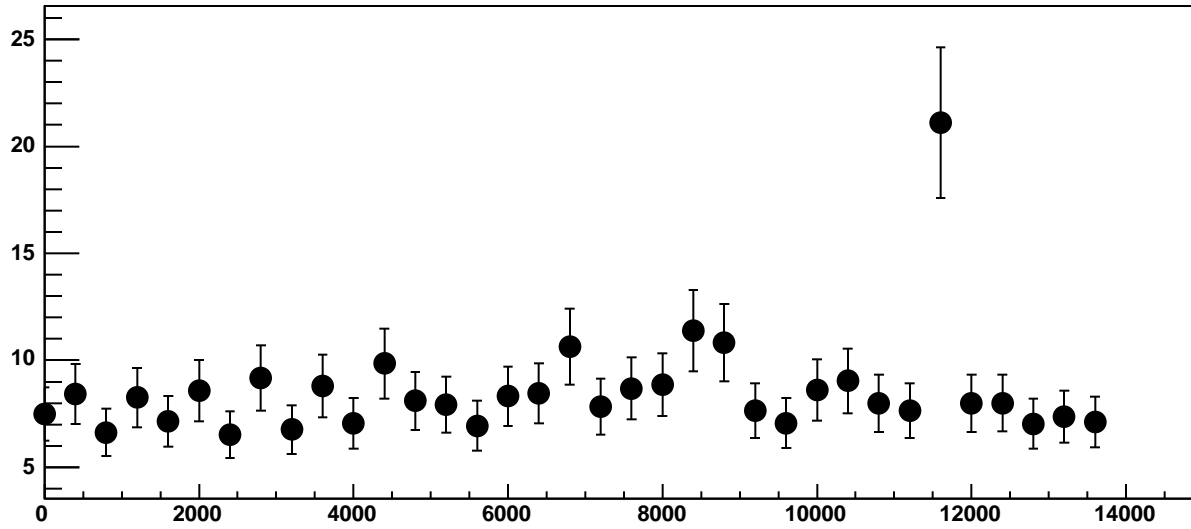


Chip 7, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC

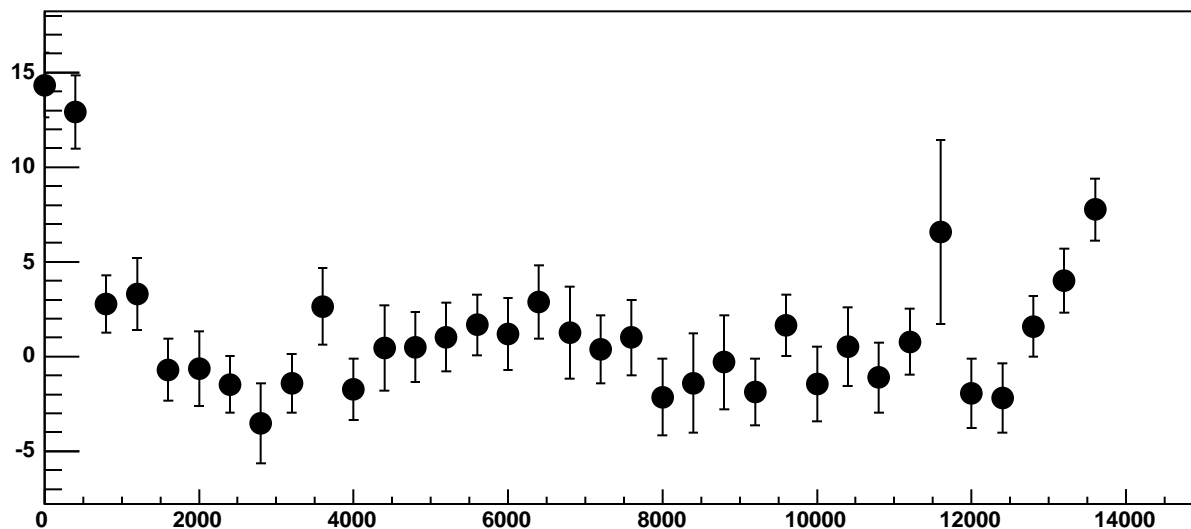


χ^2 / ndf 20.05 / 23
p0 -271.8 ± 0.8277
p1 0.01841 ± 0.0001282

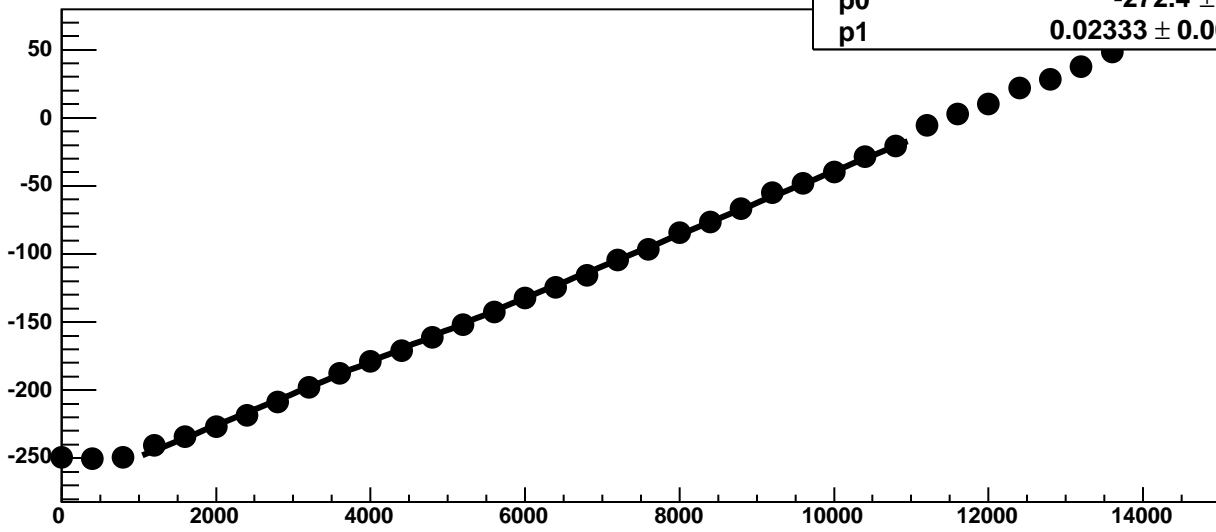
Chip 7, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



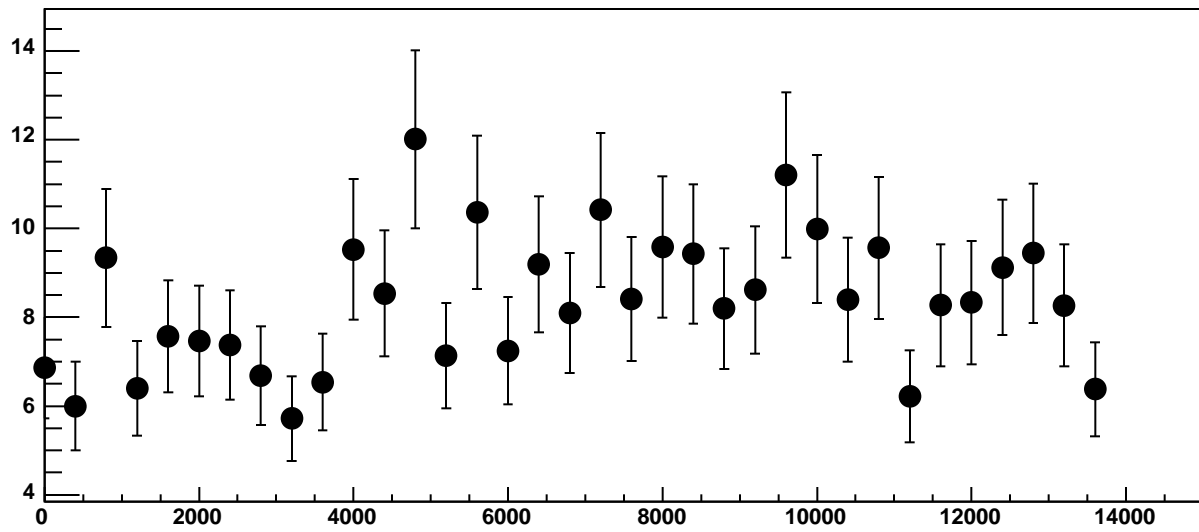
Chip 7, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC



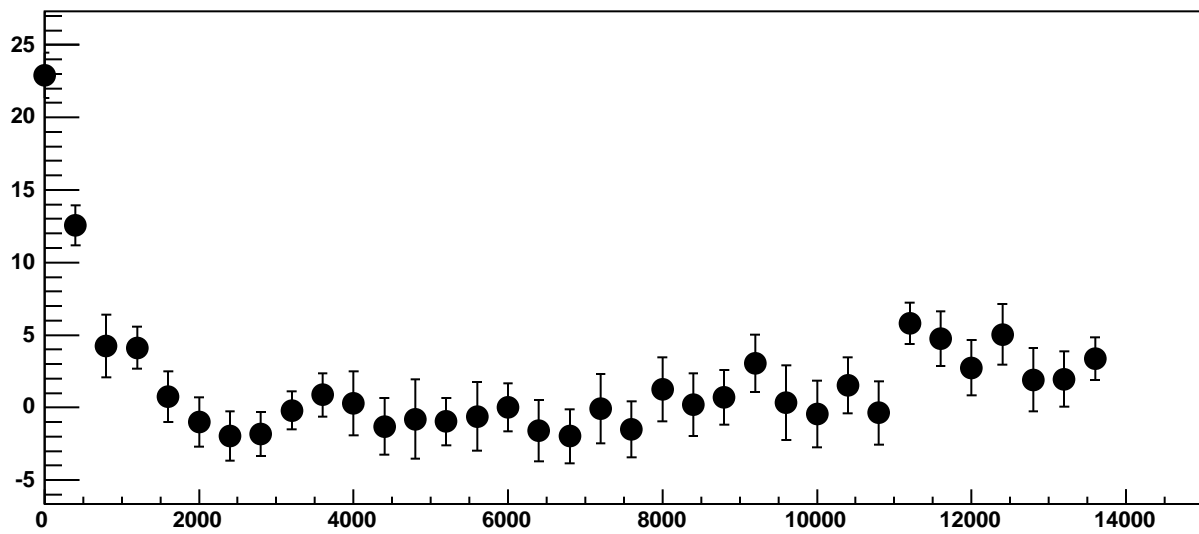
Chip 7, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



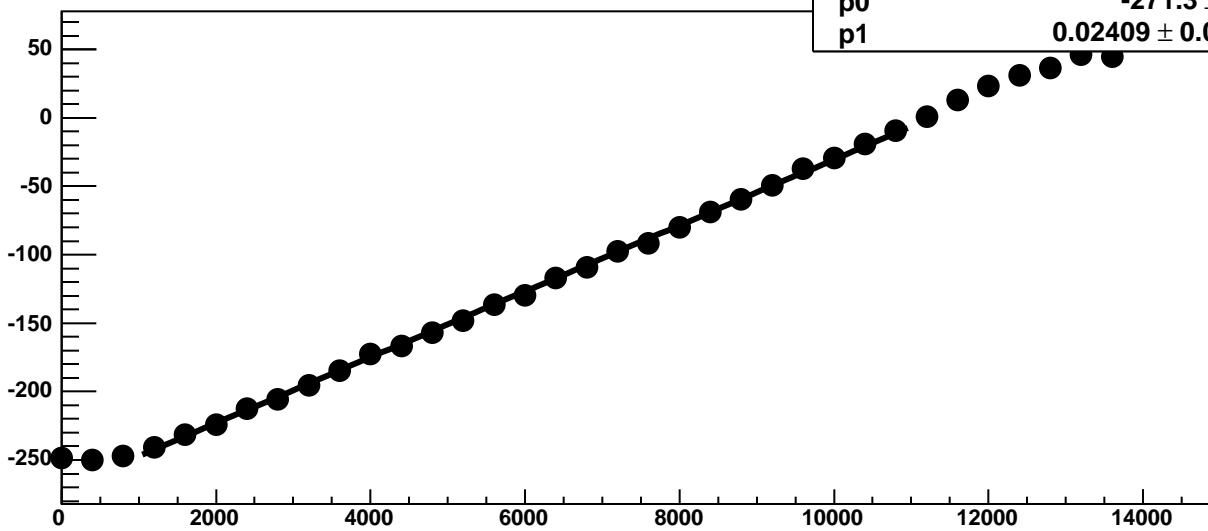
Chip 7, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

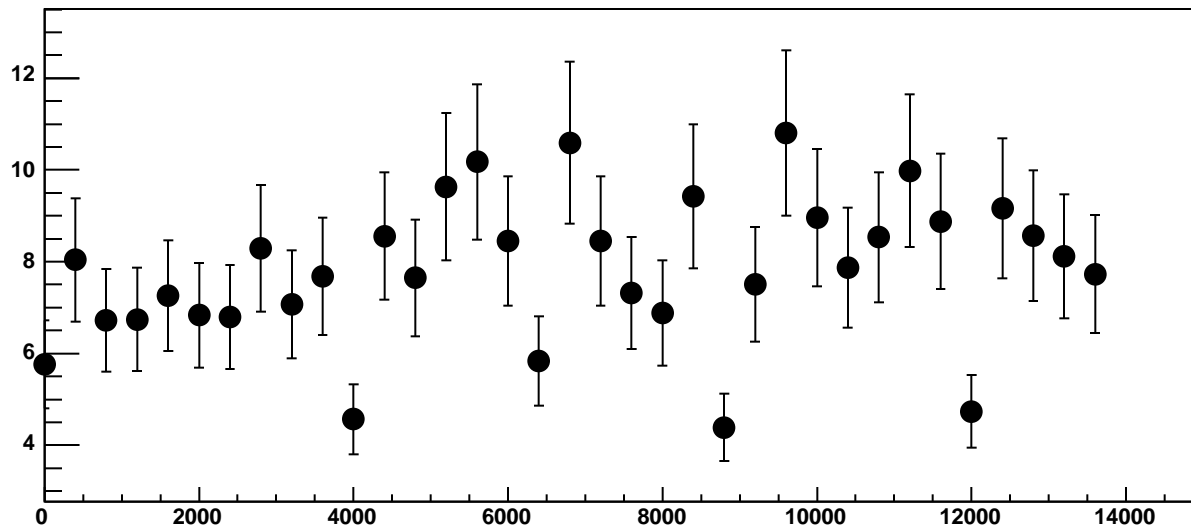


Chip 7, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

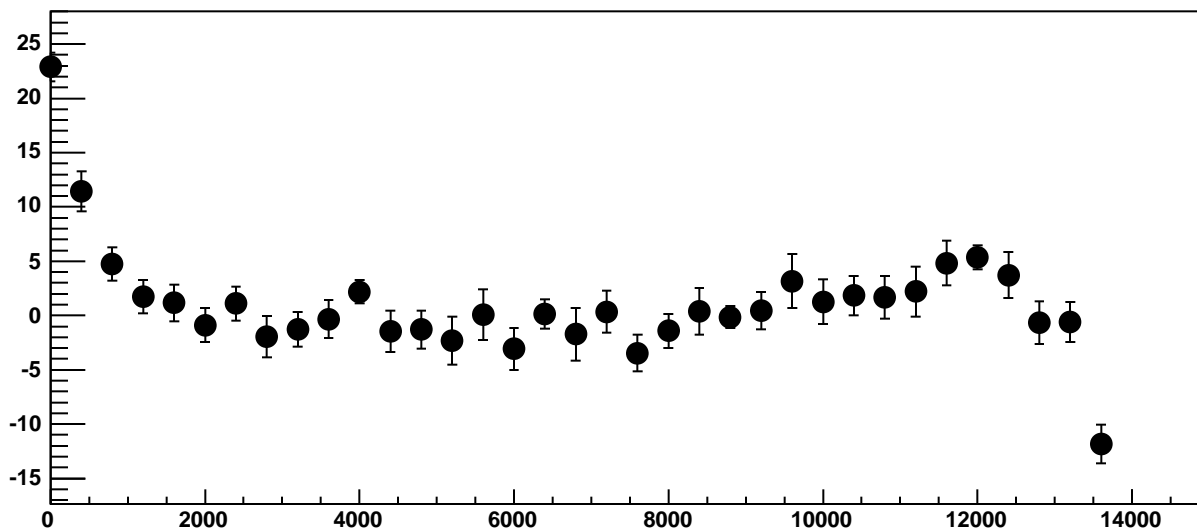


χ^2 / ndf 22.84 / 23
p0 -271.3 ± 0.7521
p1 0.02409 ± 0.0001162

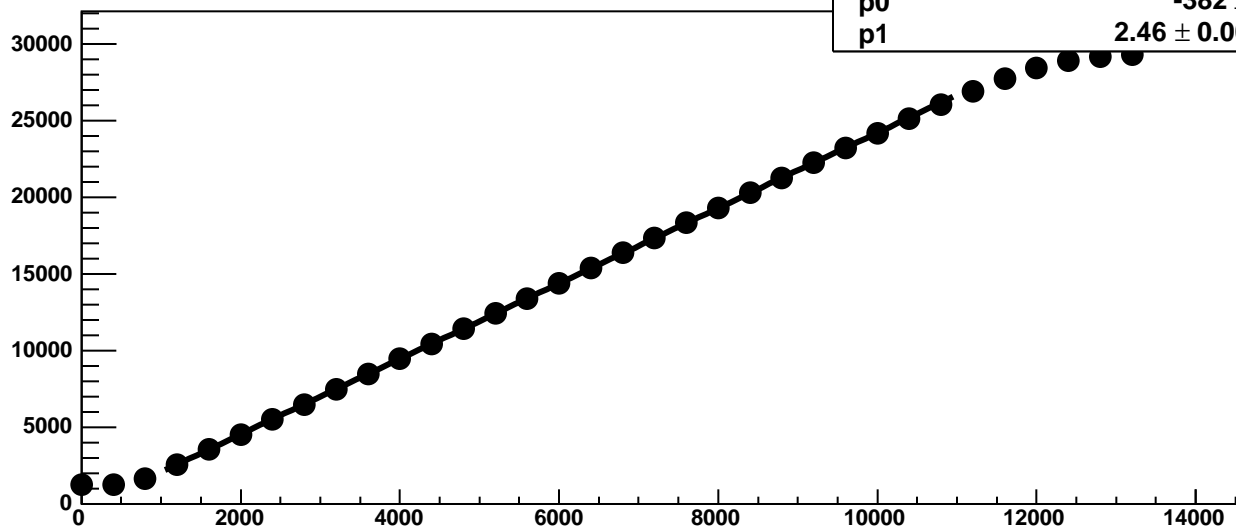
Chip 7, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



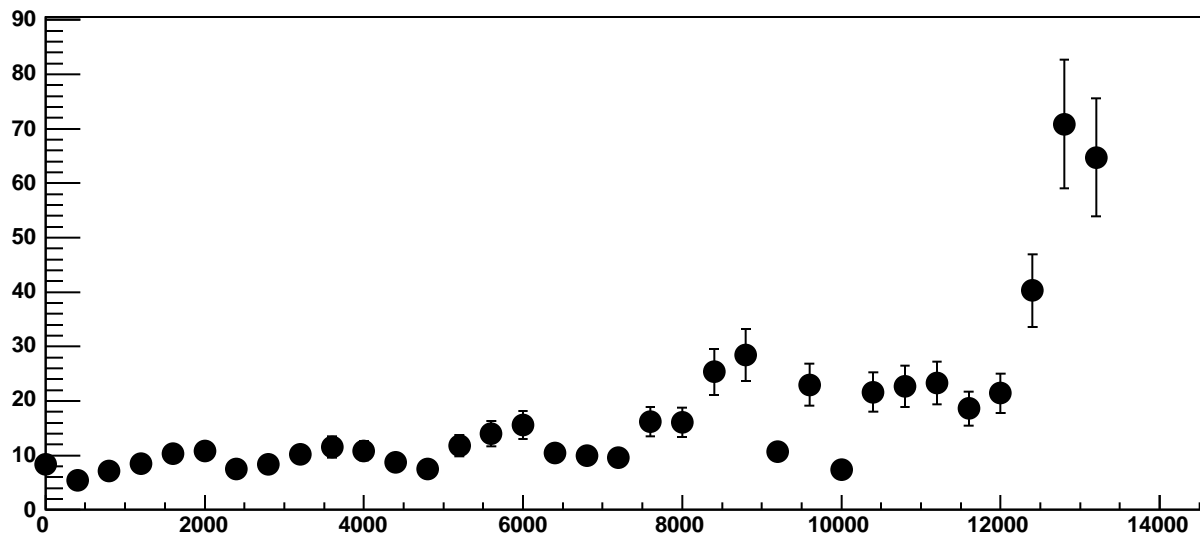
Chip 7, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



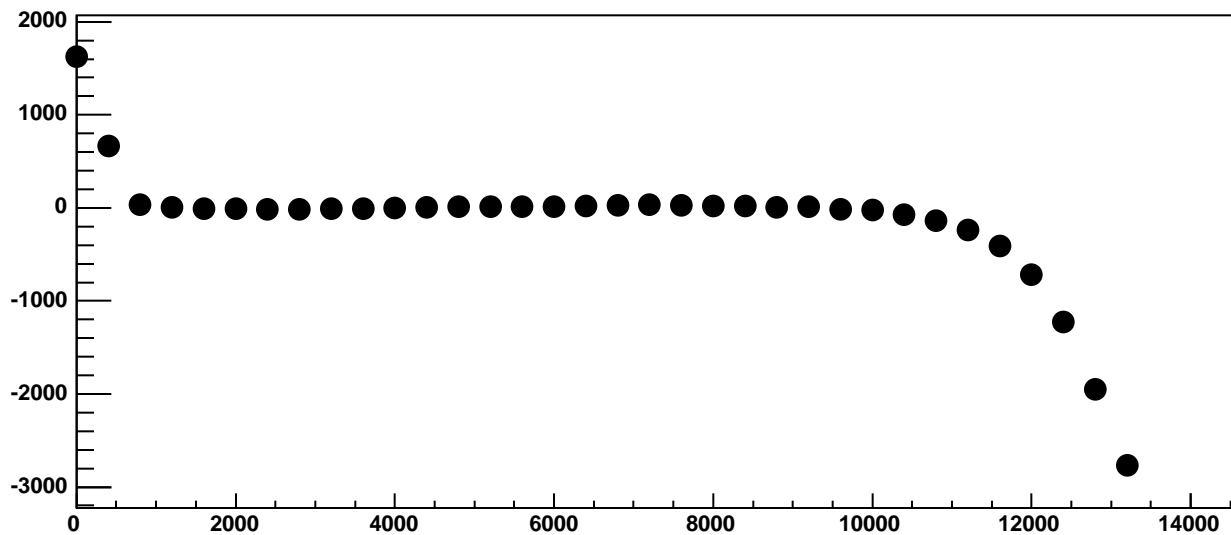
Chip 7, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC



Chip 7, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC

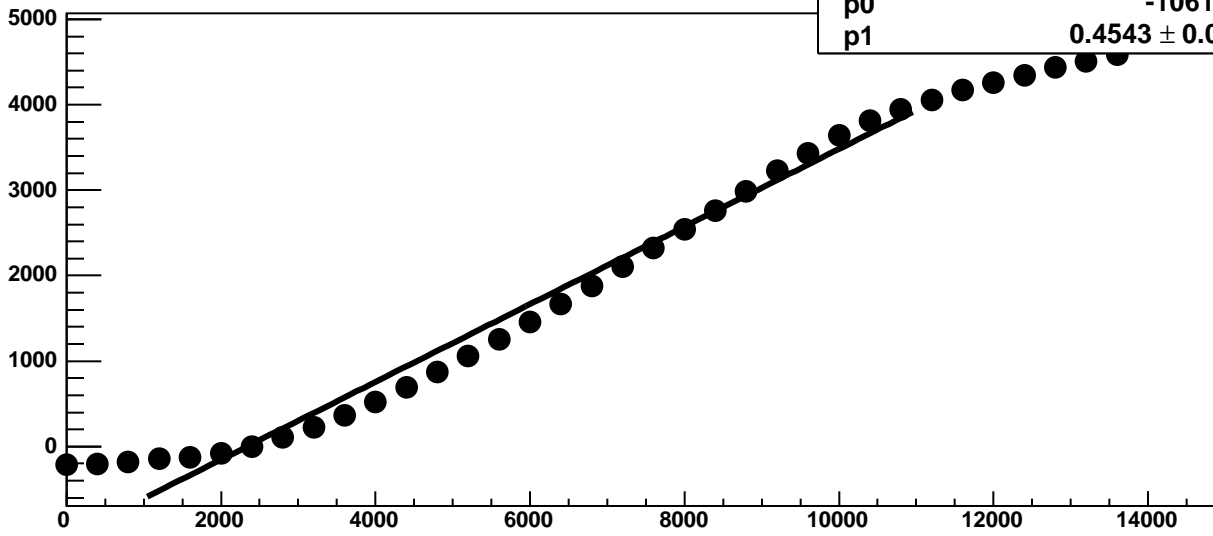


Chip 7, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC

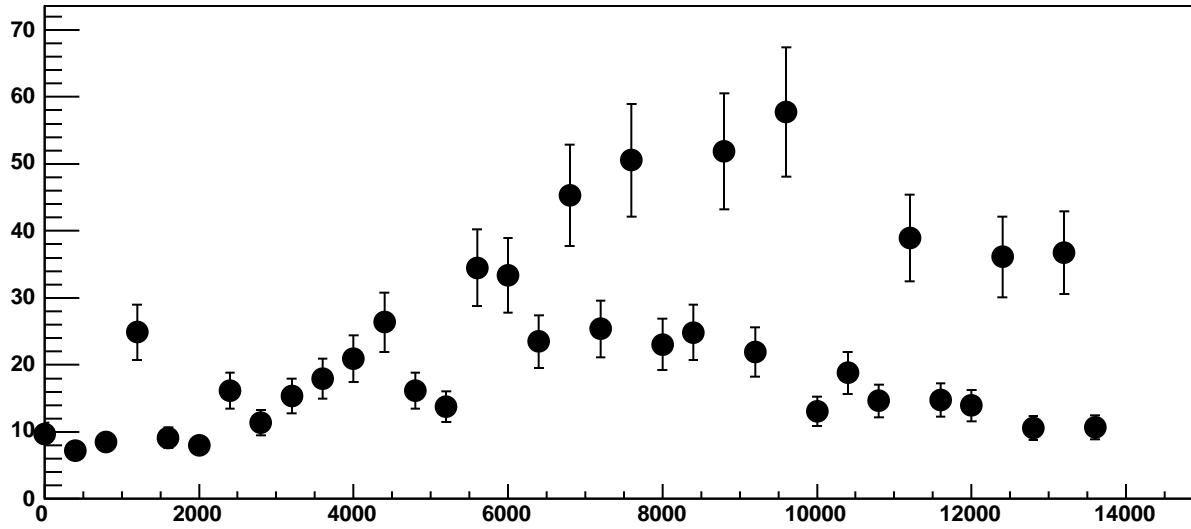


Chip 7, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

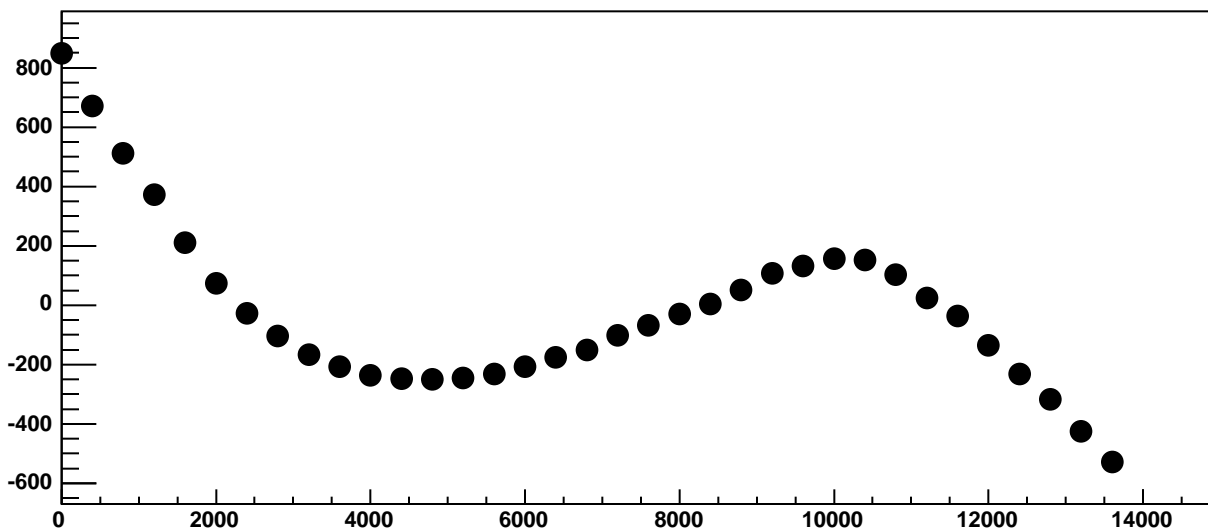
χ^2 / ndf 4.568e+04 / 23
p0 -1061 ± 1.383
p1 0.4543 ± 0.0002469



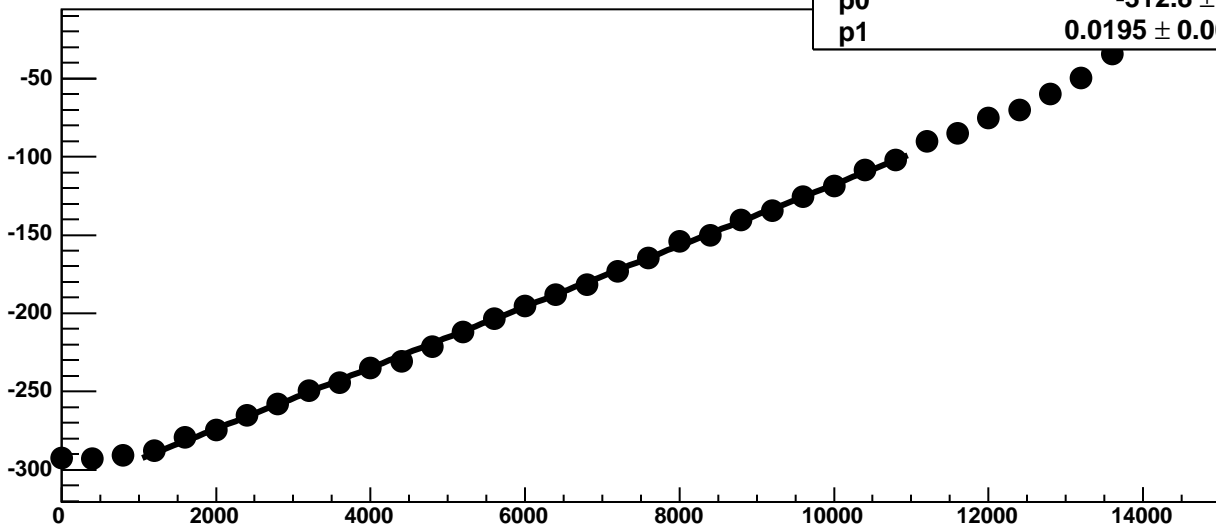
Chip 7, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

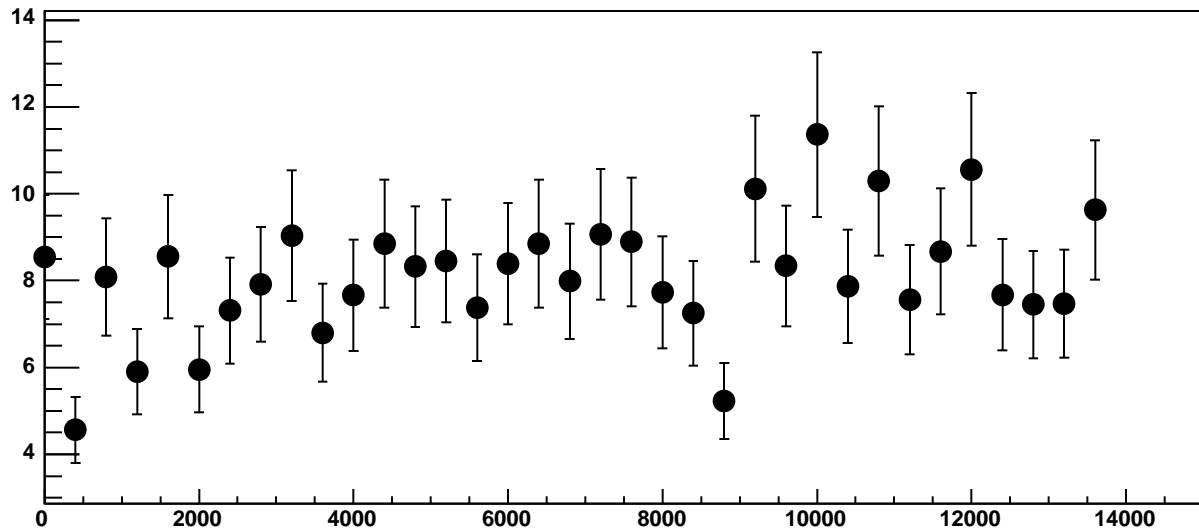


Chip 7, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

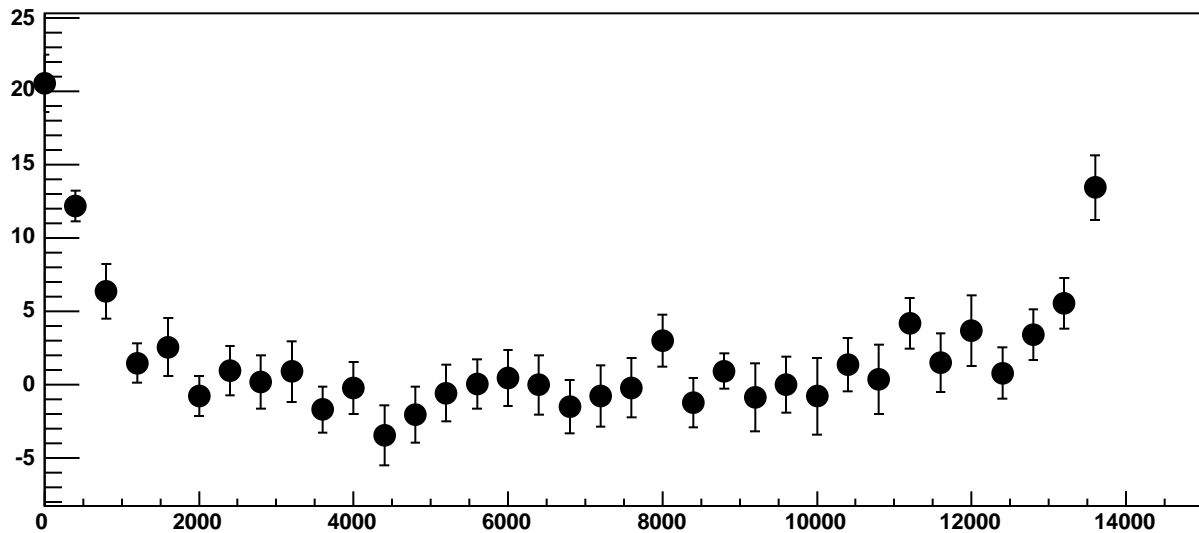


χ^2 / ndf 14.71 / 23
p0 -312.8 ± 0.7755
p1 0.0195 ± 0.0001217

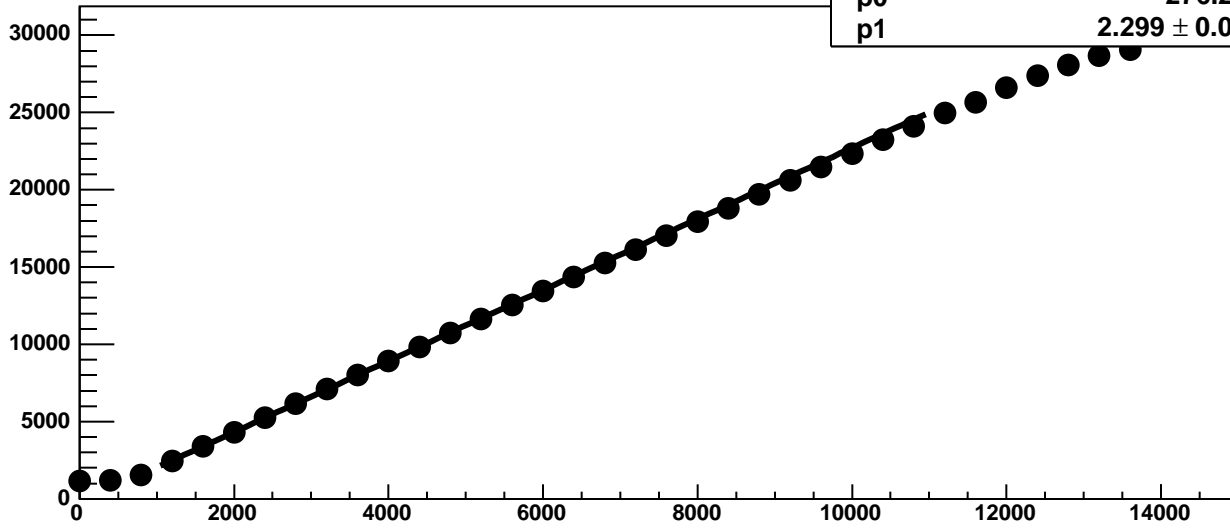
Chip 7, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



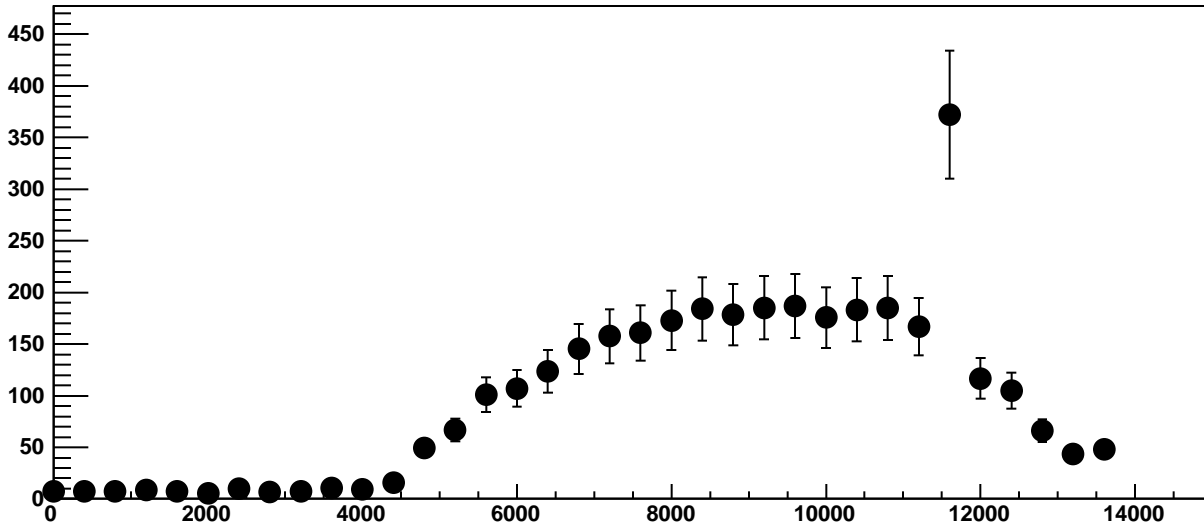
Chip 7, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



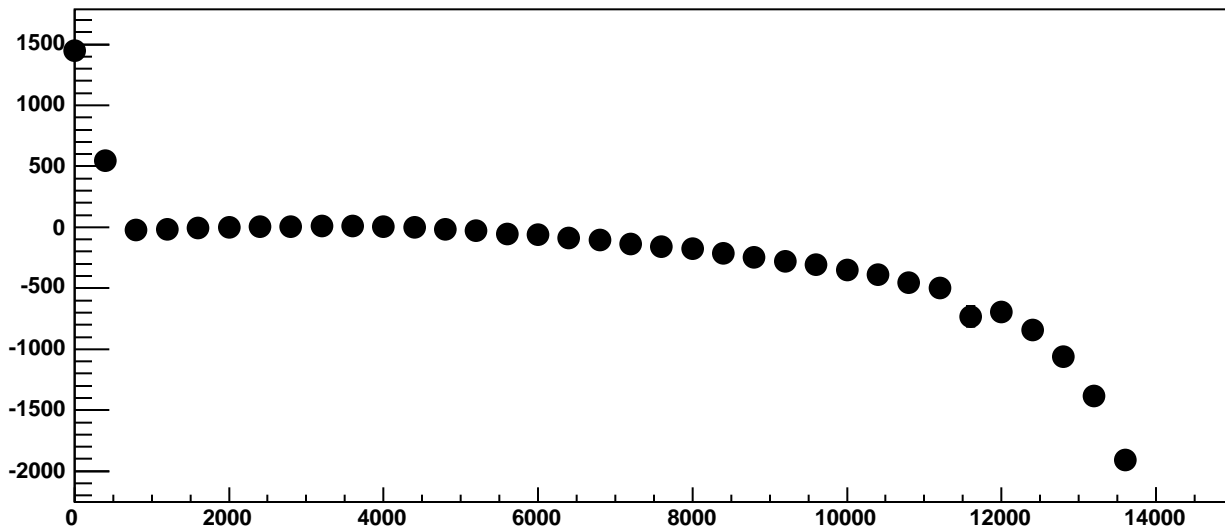
Chip 7, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC



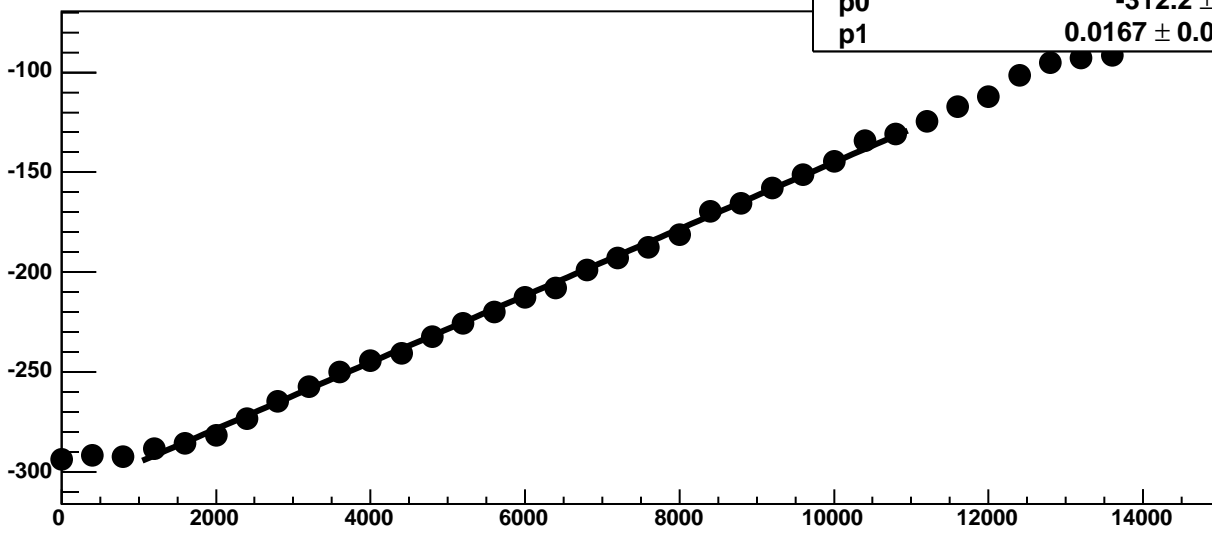
Chip 7, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC

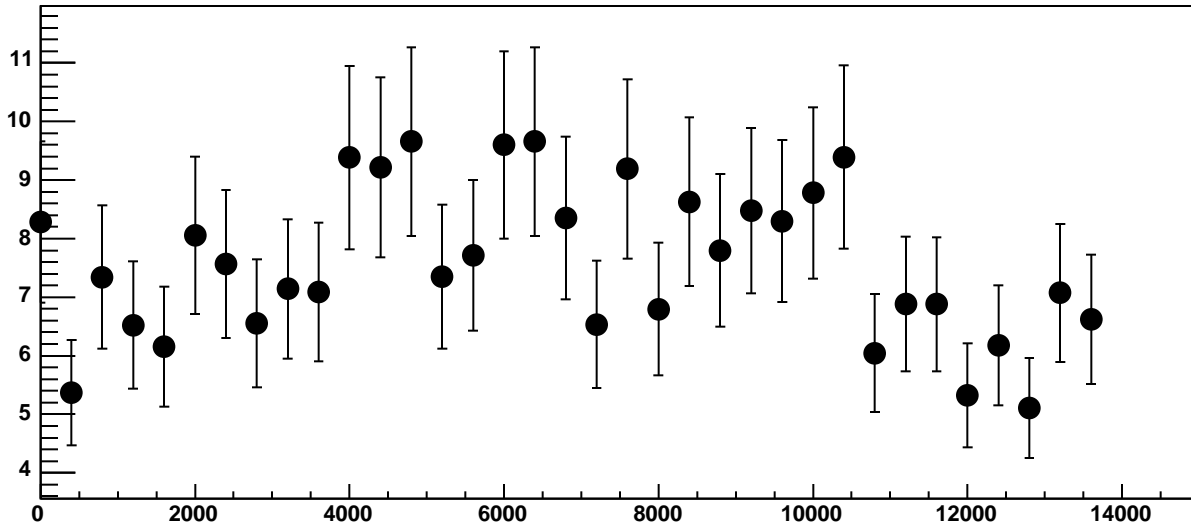


Chip 7, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

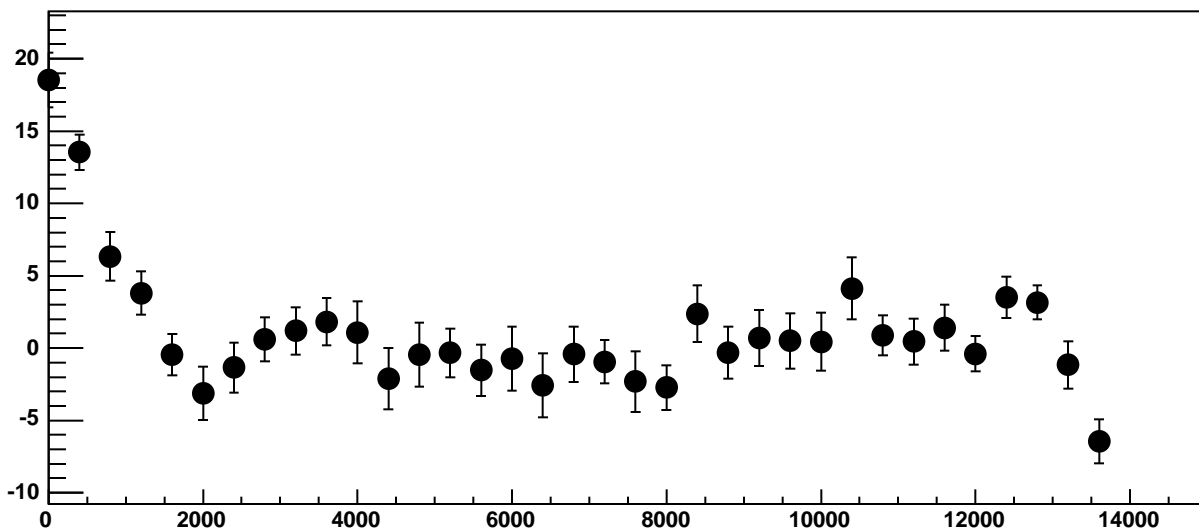


χ^2 / ndf 26.03 / 23
p0 -312.2 ± 0.7616
p1 0.0167 ± 0.0001166

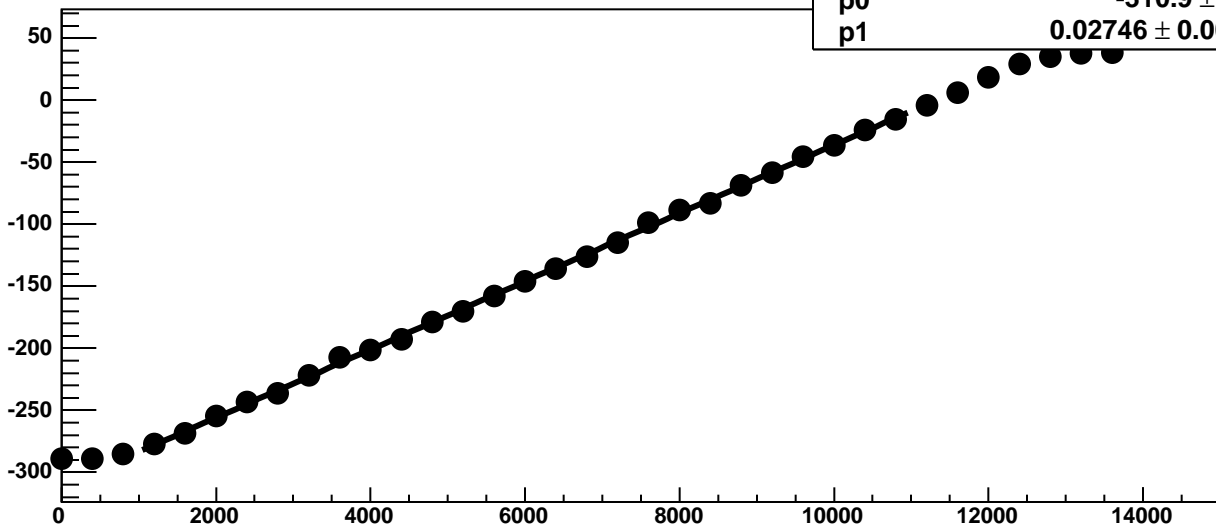
Chip 7, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

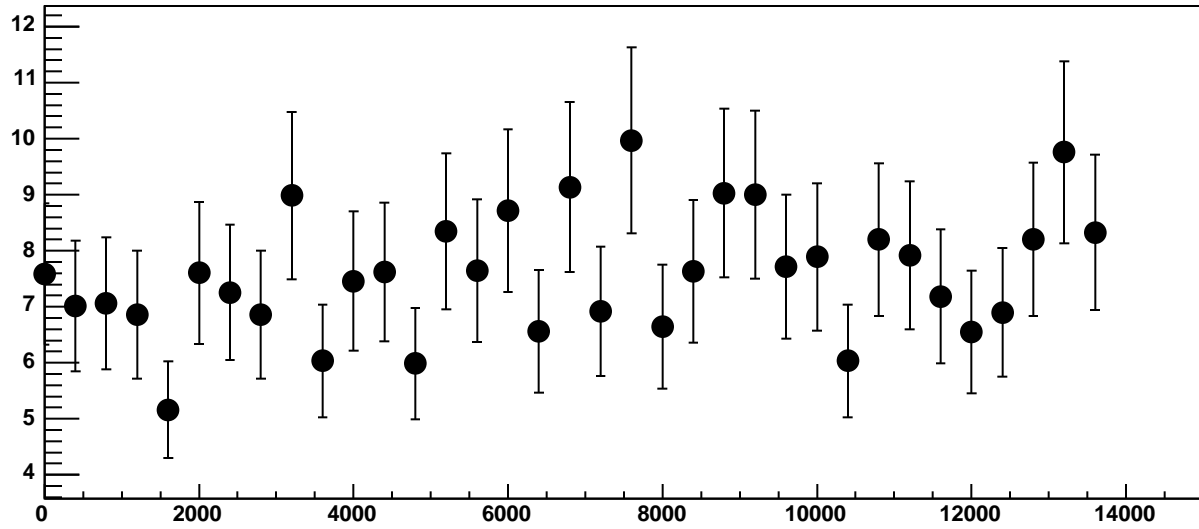


Chip 7, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

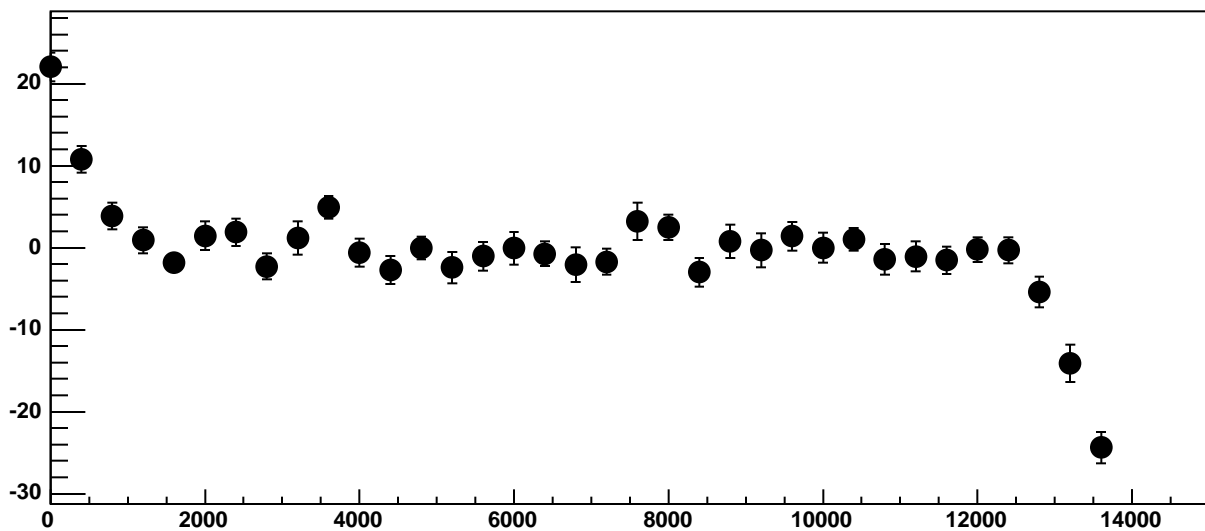


χ^2 / ndf 36.04 / 23
p0 -310.9 ± 0.7206
p1 0.02746 ± 0.0001125

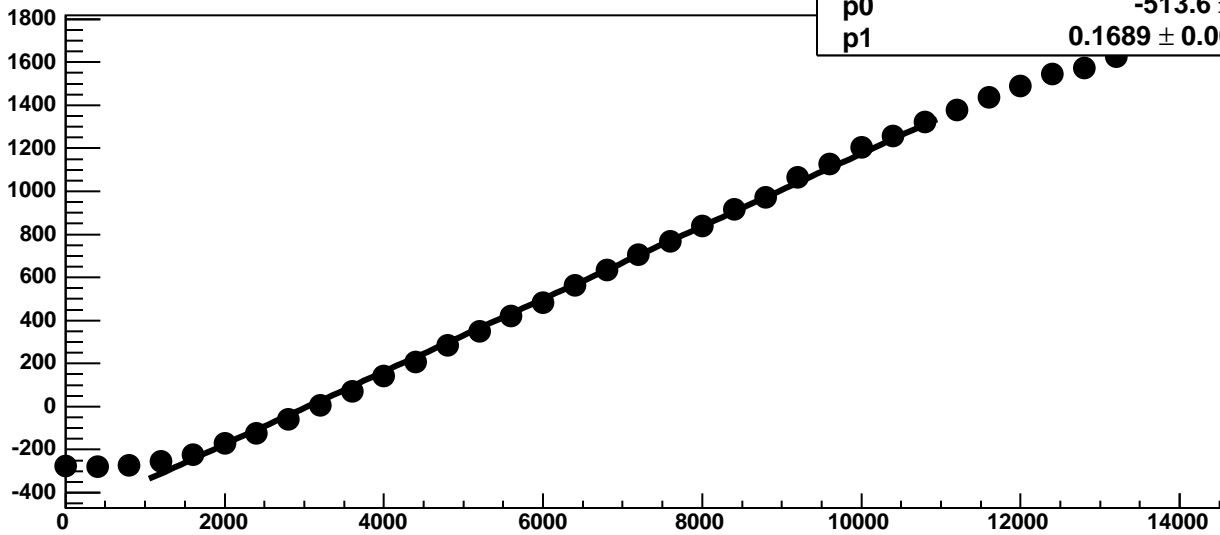
Chip 7, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



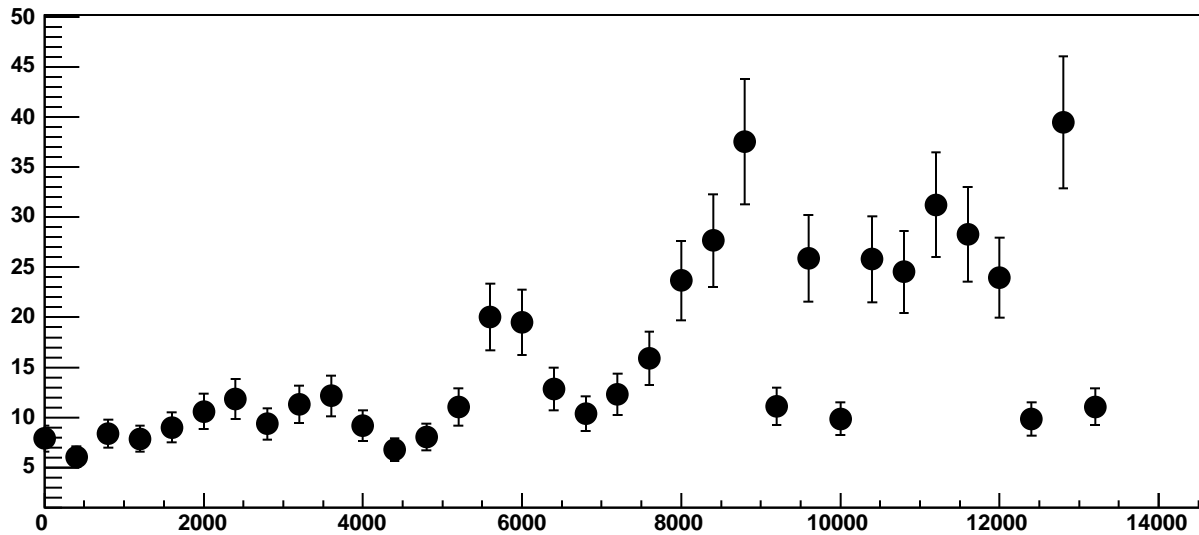
Chip 7, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



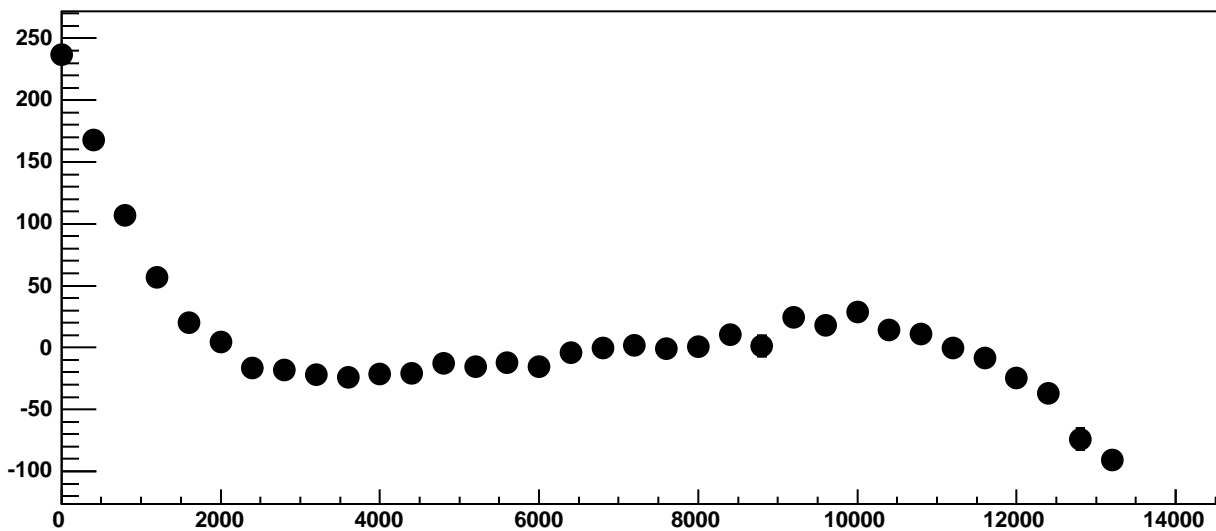
Chip 7, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC



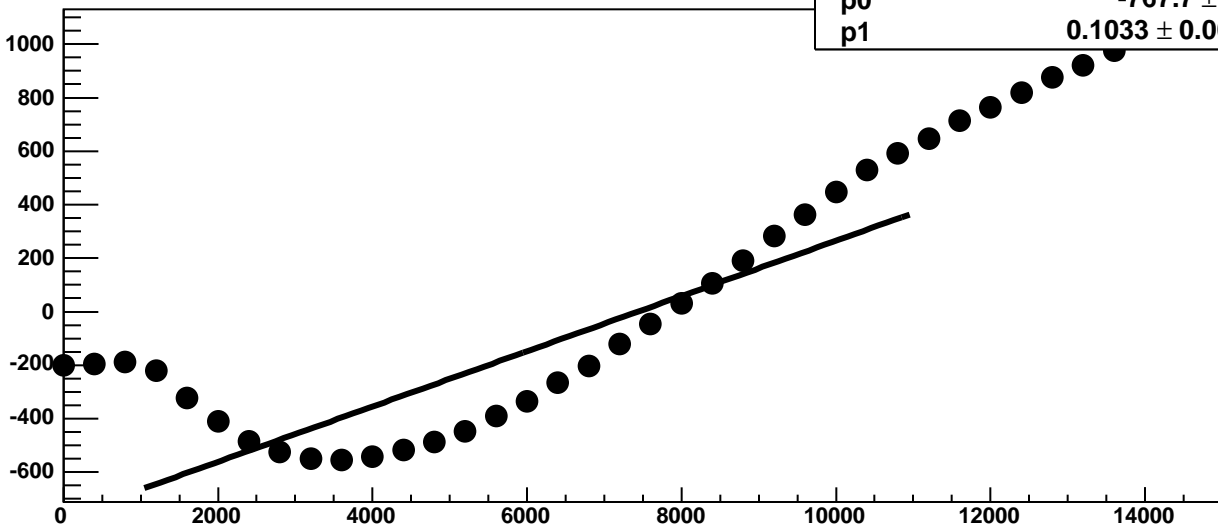
Chip 7, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



Chip 7, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC

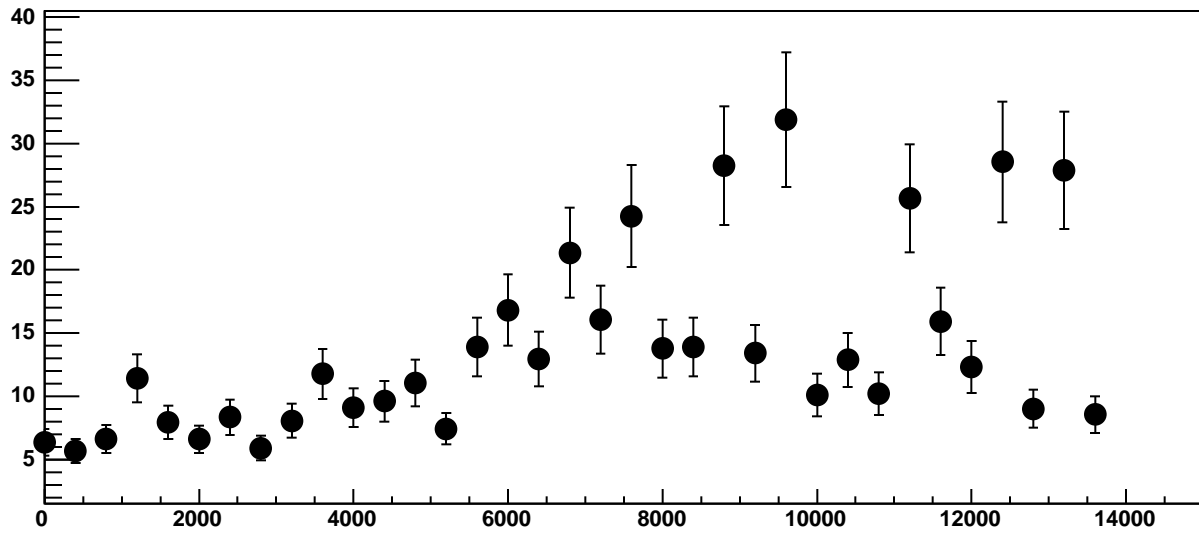


Chip 7, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

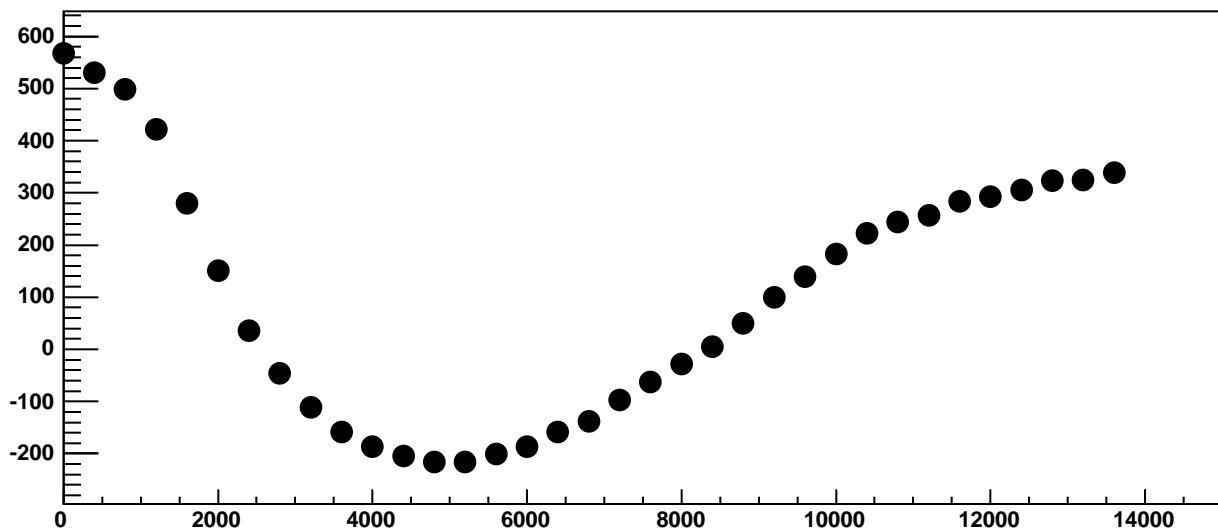


χ^2 / ndf 1.429e+05 / 23
p0 -767.7 ± 0.9278
p1 0.1033 ± 0.0001693

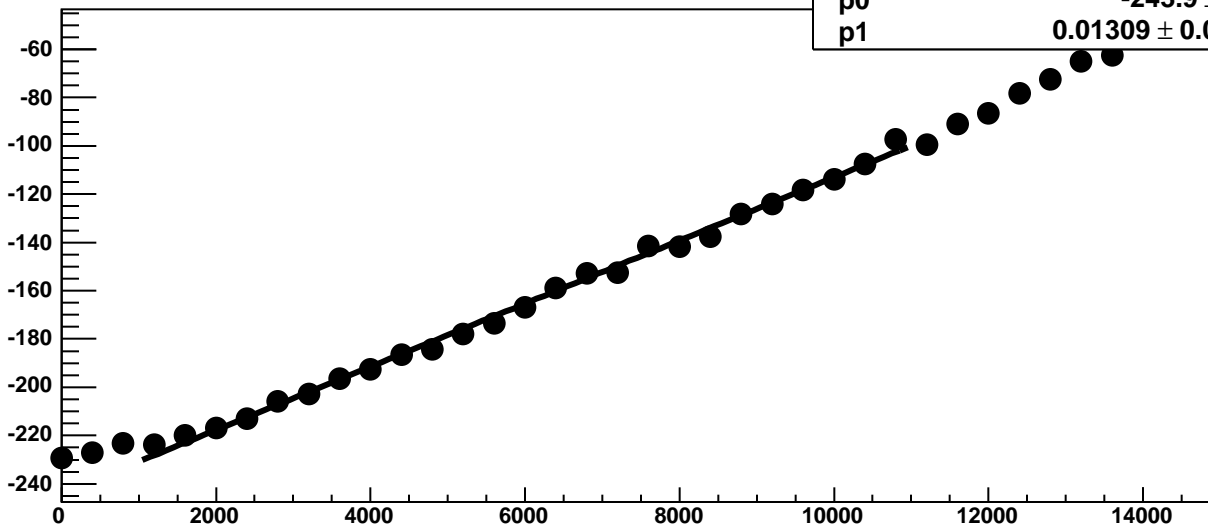
Chip 7, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

47.13 / 23

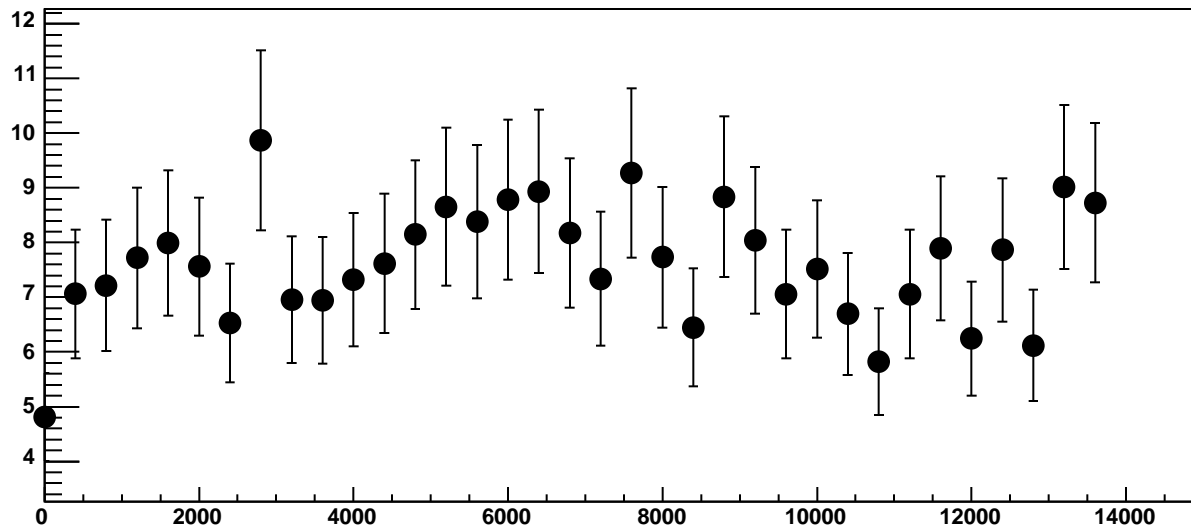
p0

-243.9 ± 0.7901

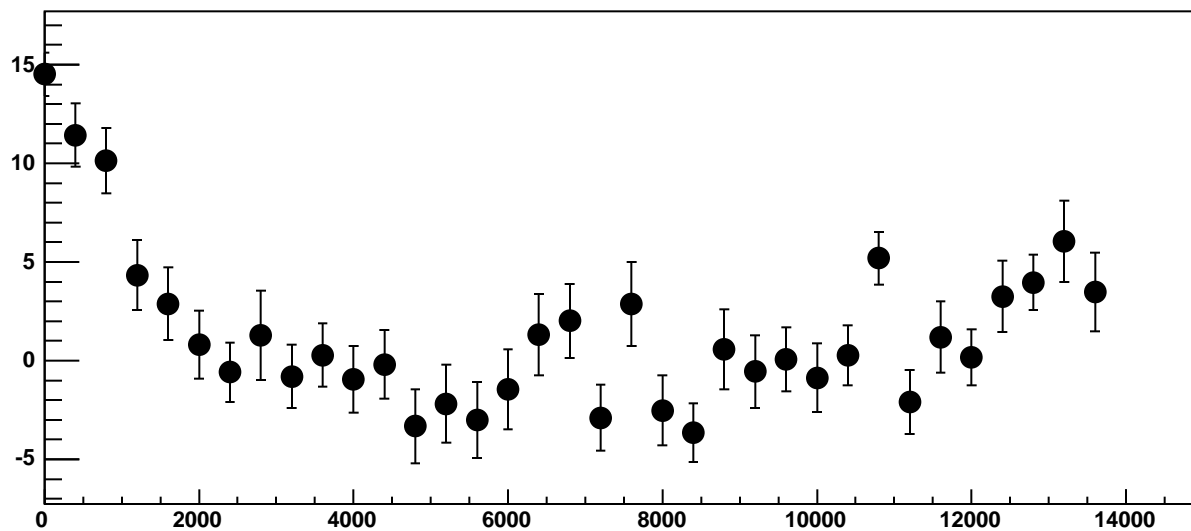
p1

0.01309 ± 0.0001149

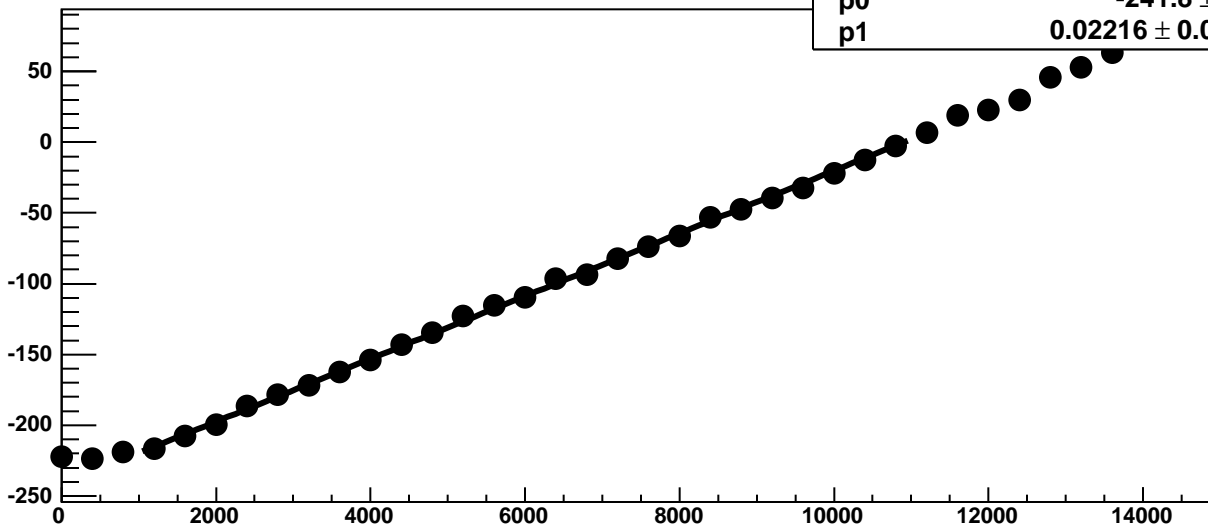
Chip 7, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

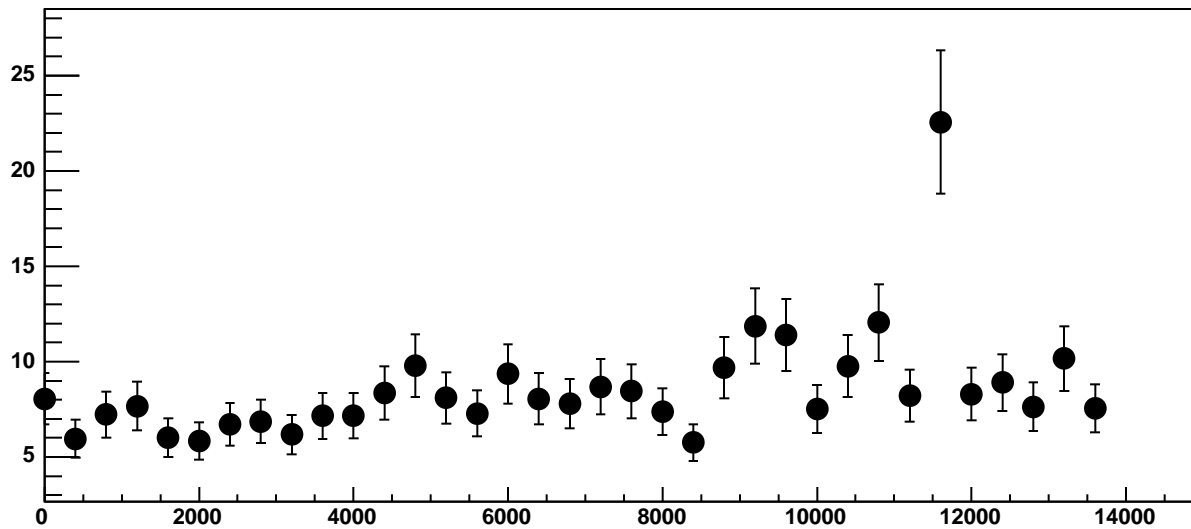


Chip 7, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC

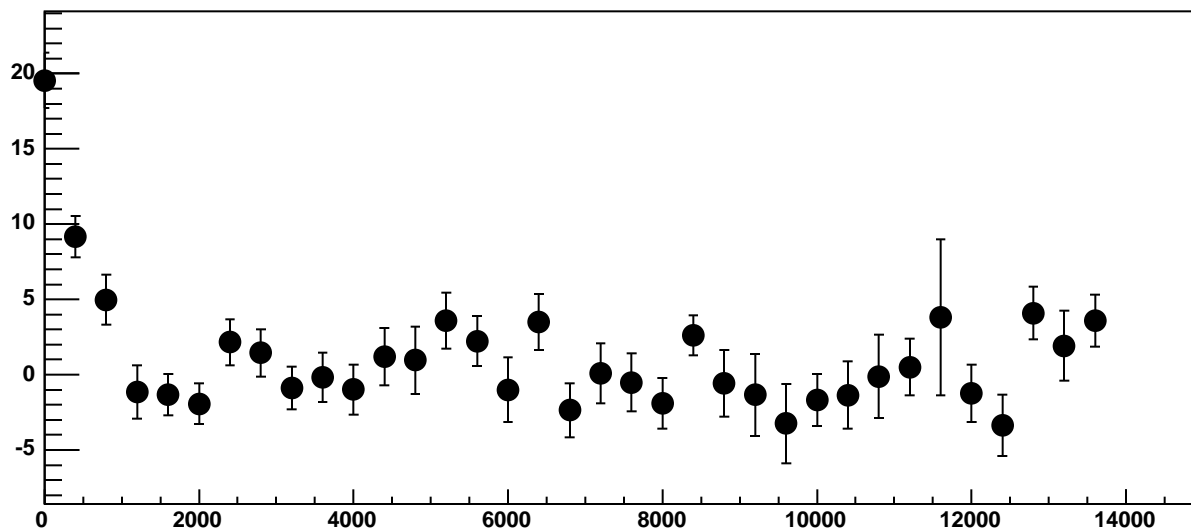


χ^2 / ndf 27.01 / 23
p0 -241.8 ± 0.7522
p1 0.02216 ± 0.0001247

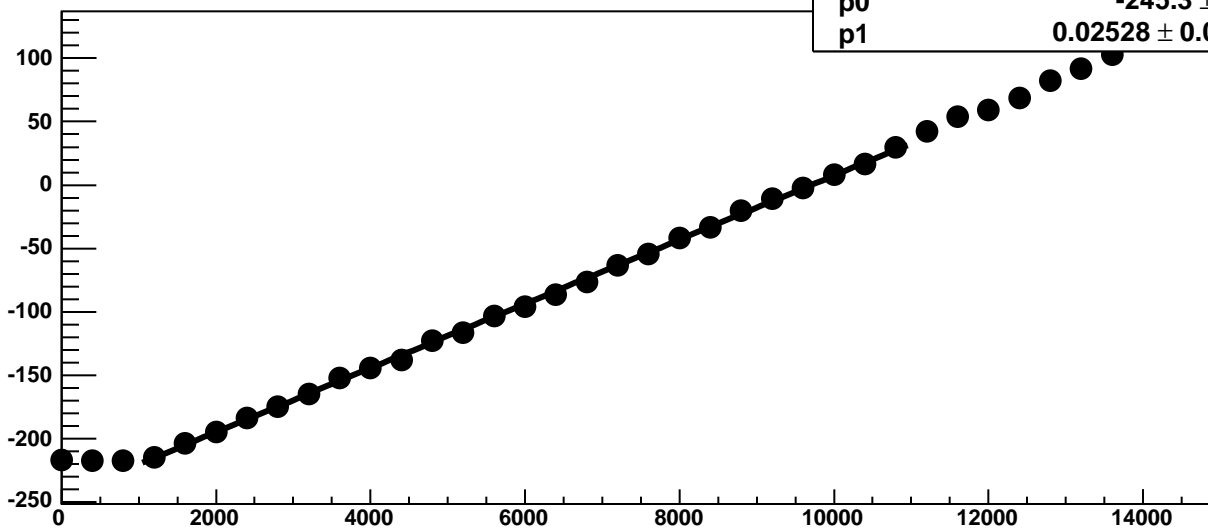
Chip 7, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC

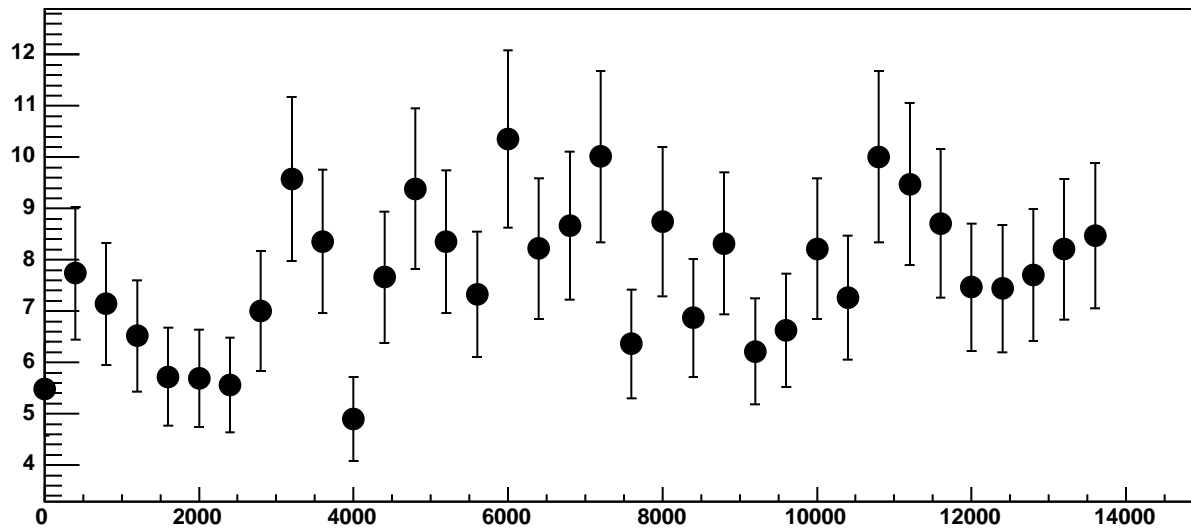


Chip 7, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

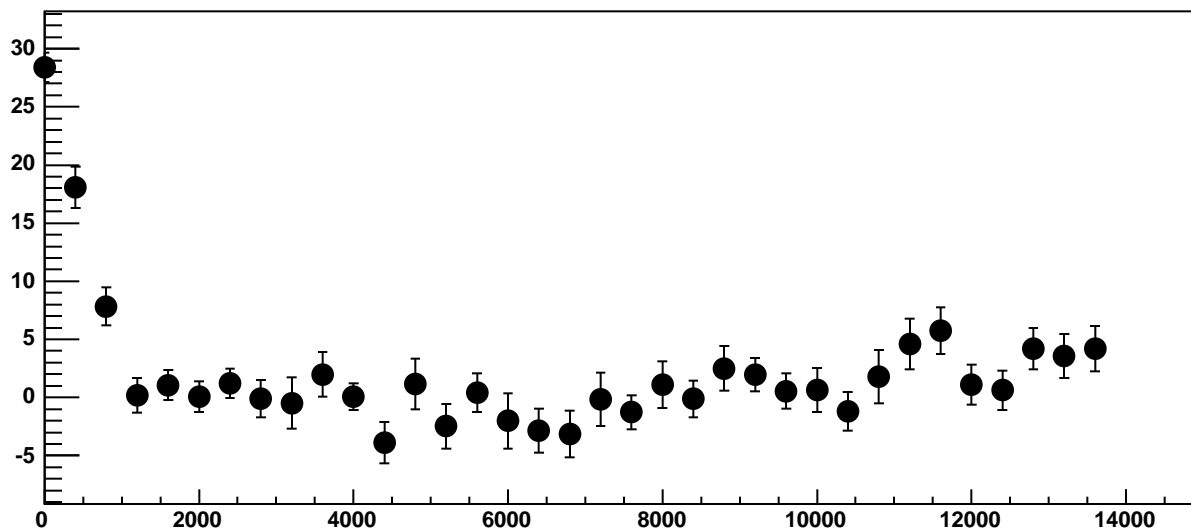


χ^2 / ndf 21.14 / 23
p0 -245.3 ± 0.6949
p1 0.02528 ± 0.0001102

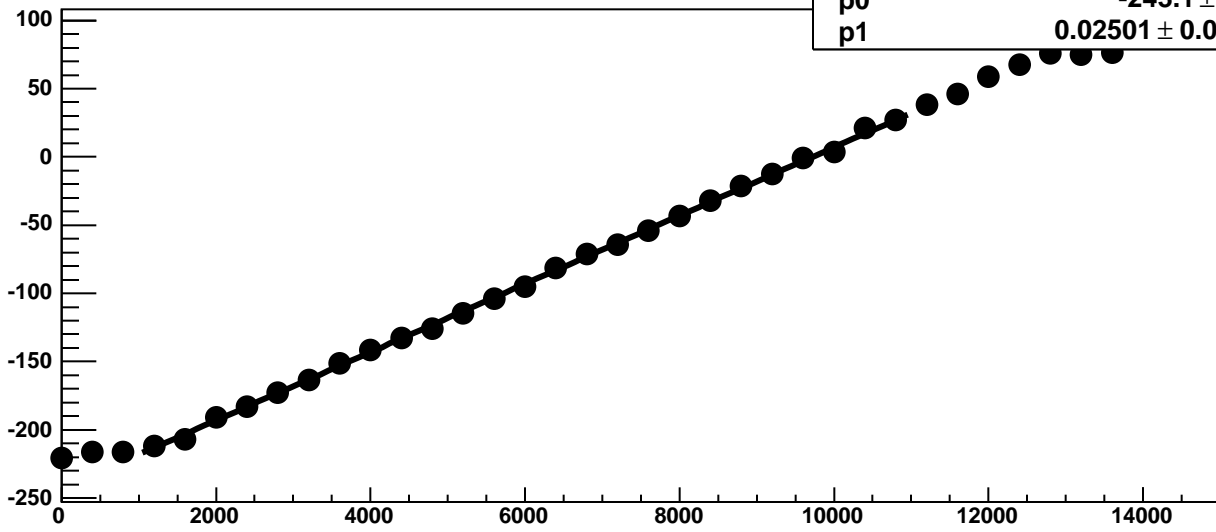
Chip 7, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



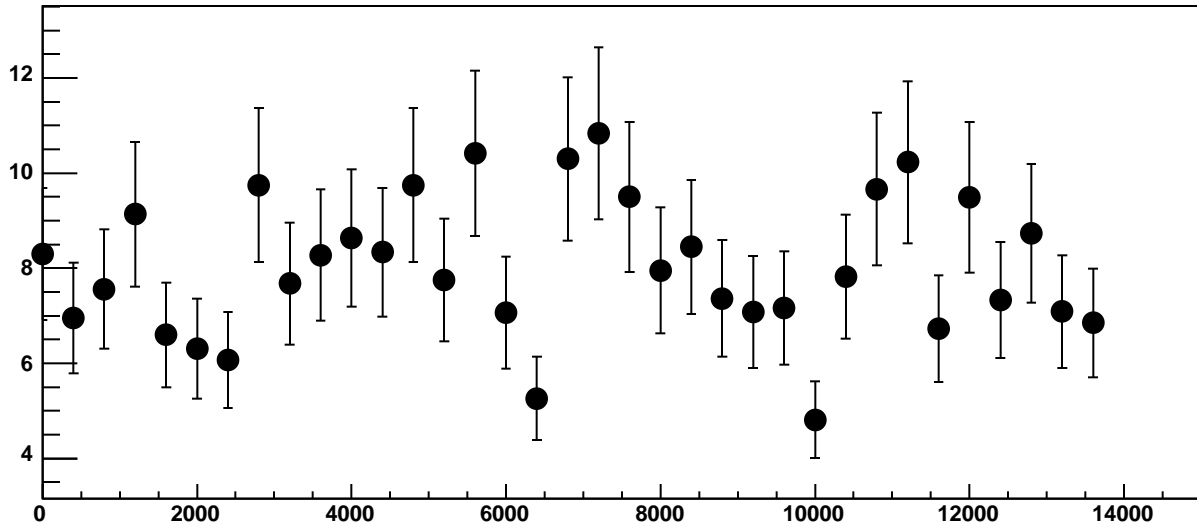
Chip 7, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



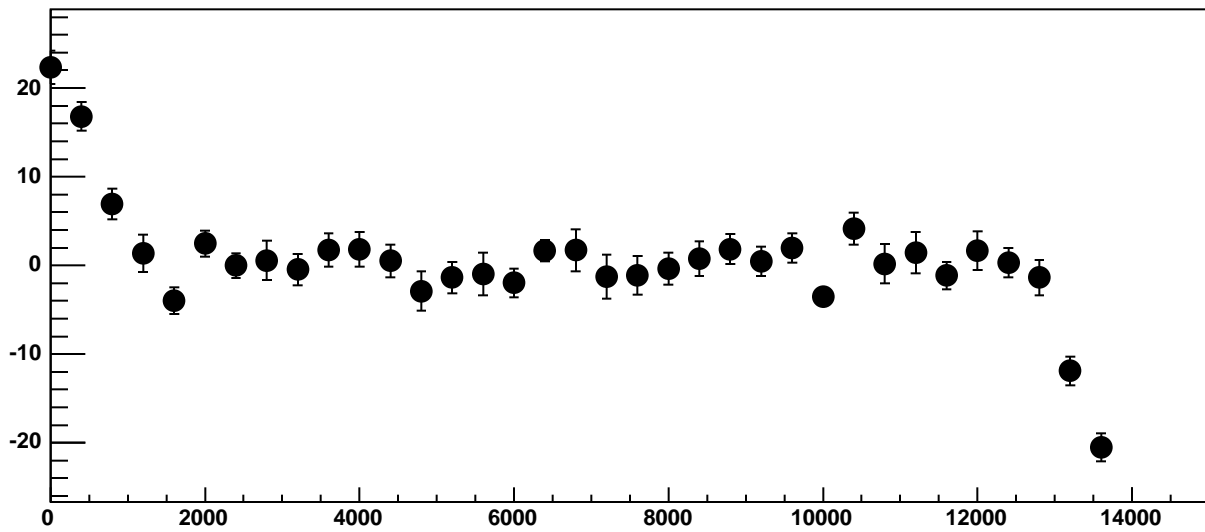
Chip 7, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



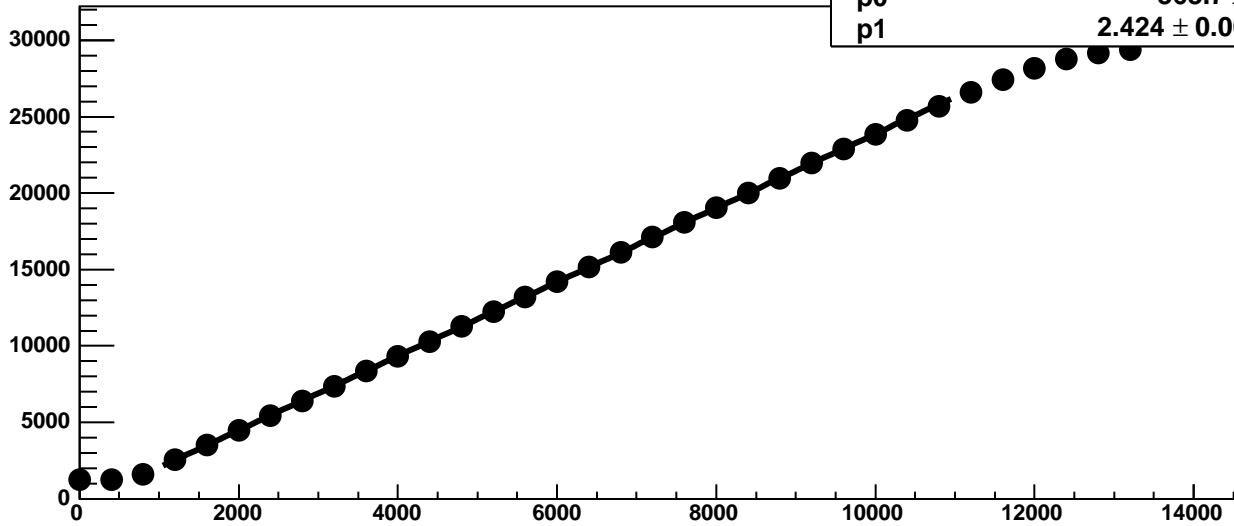
Chip 7, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 7, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

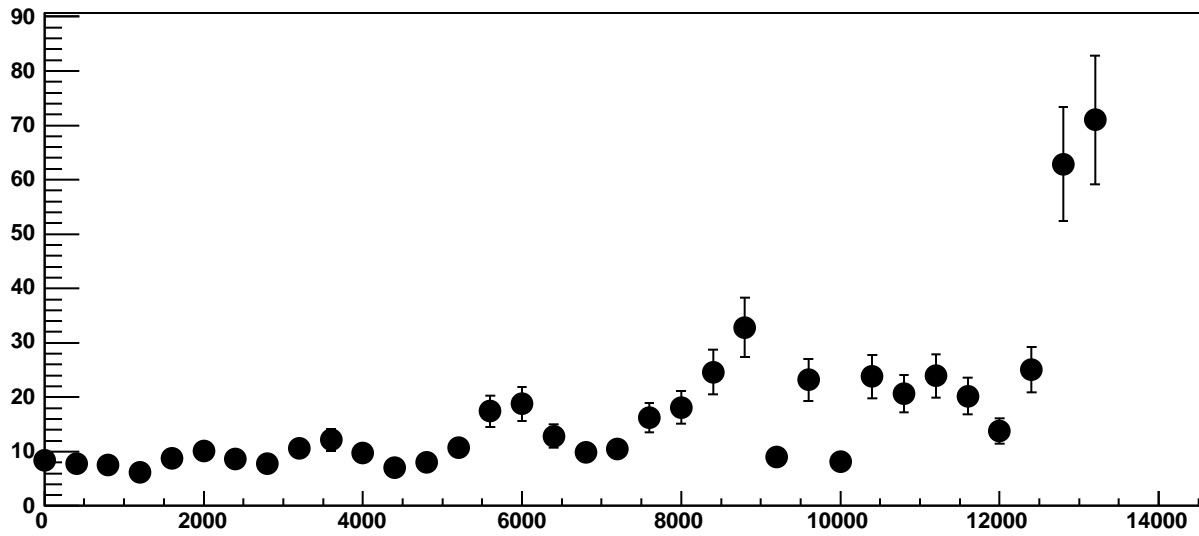


Chip 7, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC

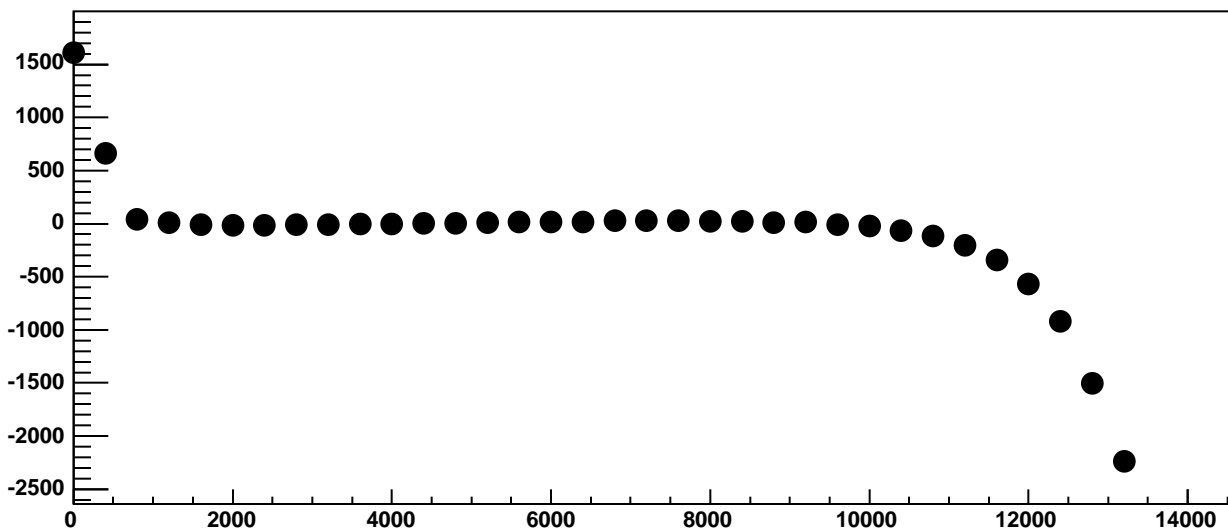


χ^2 / ndf 1645 / 23
p0 -363.7 ± 0.953
p1 2.424 ± 0.0001723

Chip 7, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC

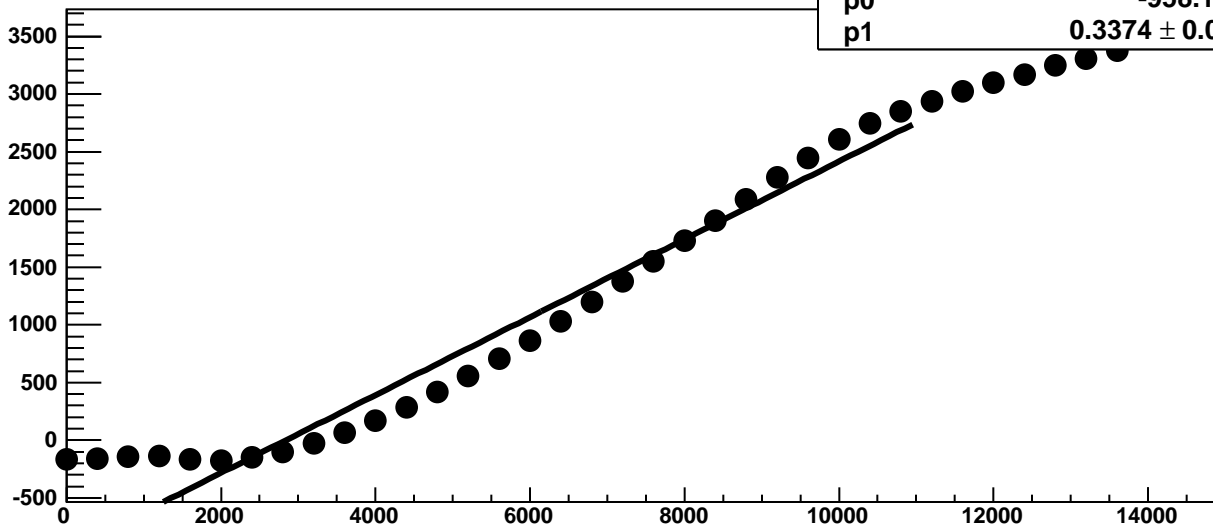


Chip 7, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC

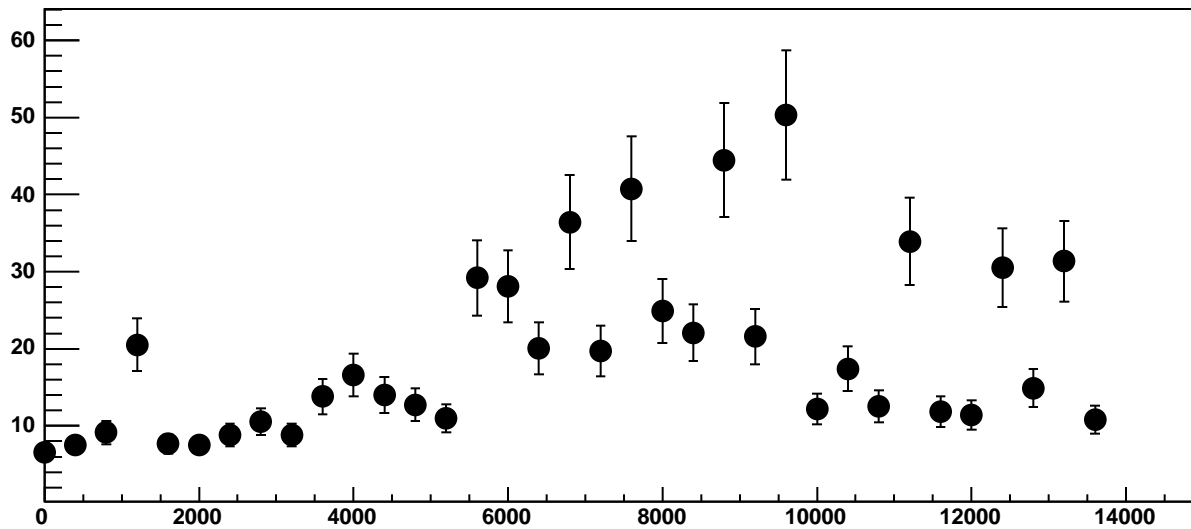


Chip 7, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

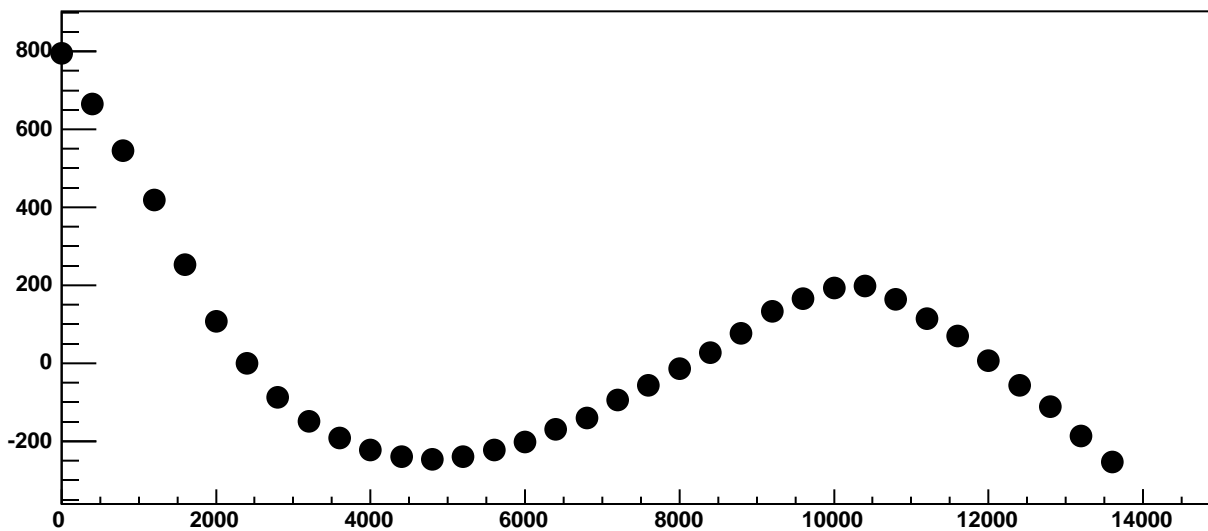
χ^2 / ndf 8.362e+04 / 23
p0 -958.1 ± 1.139
p1 0.3374 ± 0.0002166



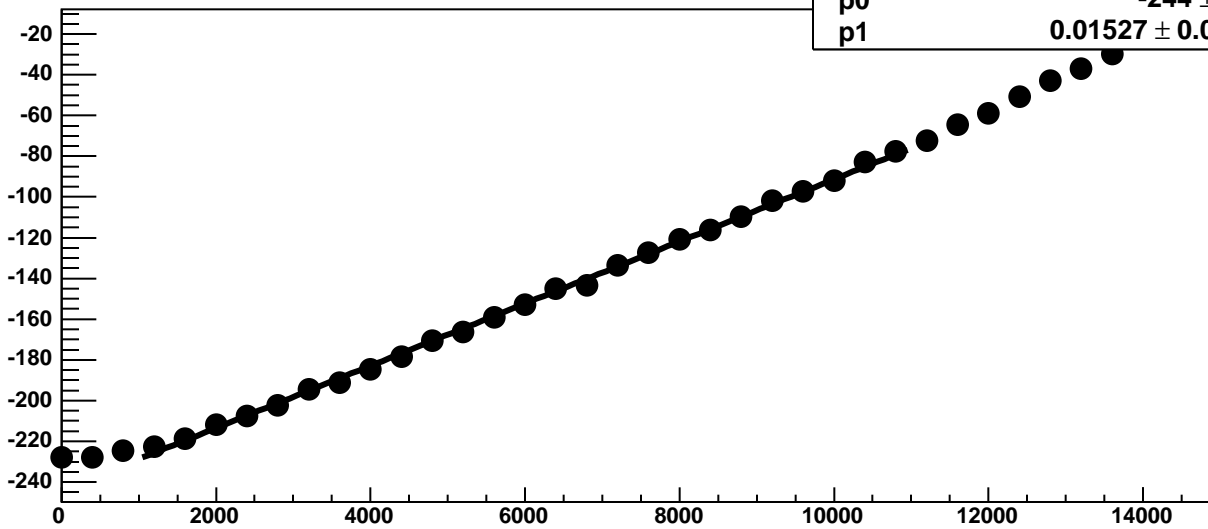
Chip 7, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

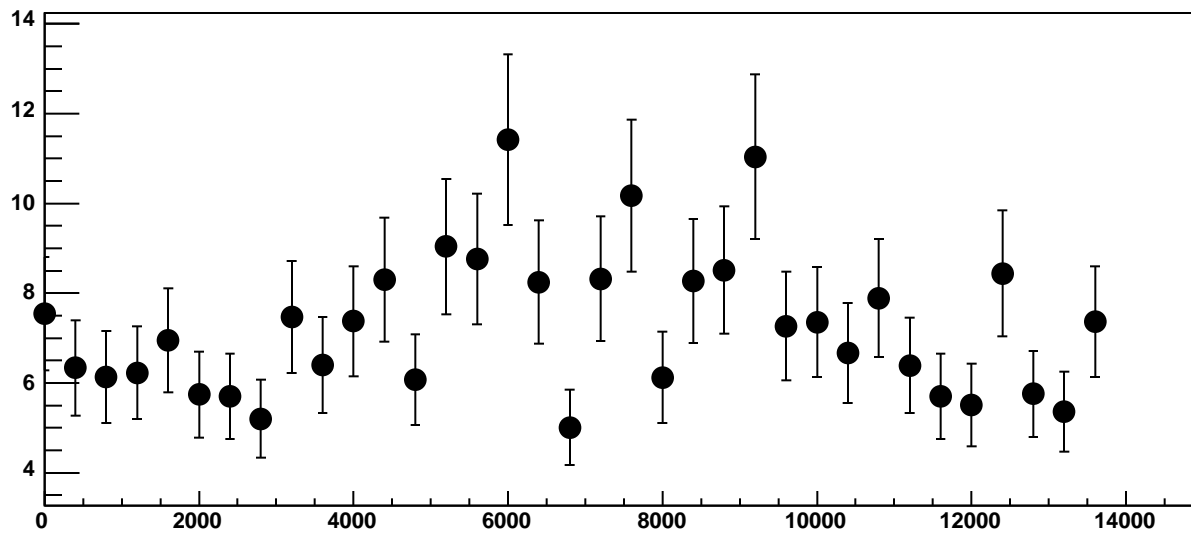


Chip 7, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

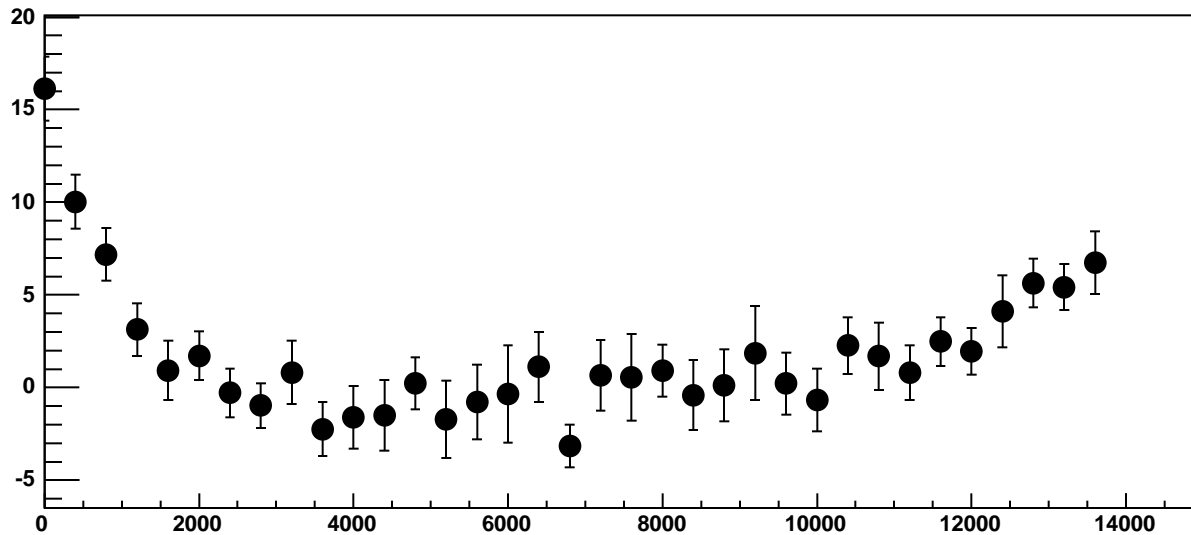


χ^2 / ndf 24.75 / 23
p0 -244 ± 0.6866
p1 0.01527 ± 0.0001096

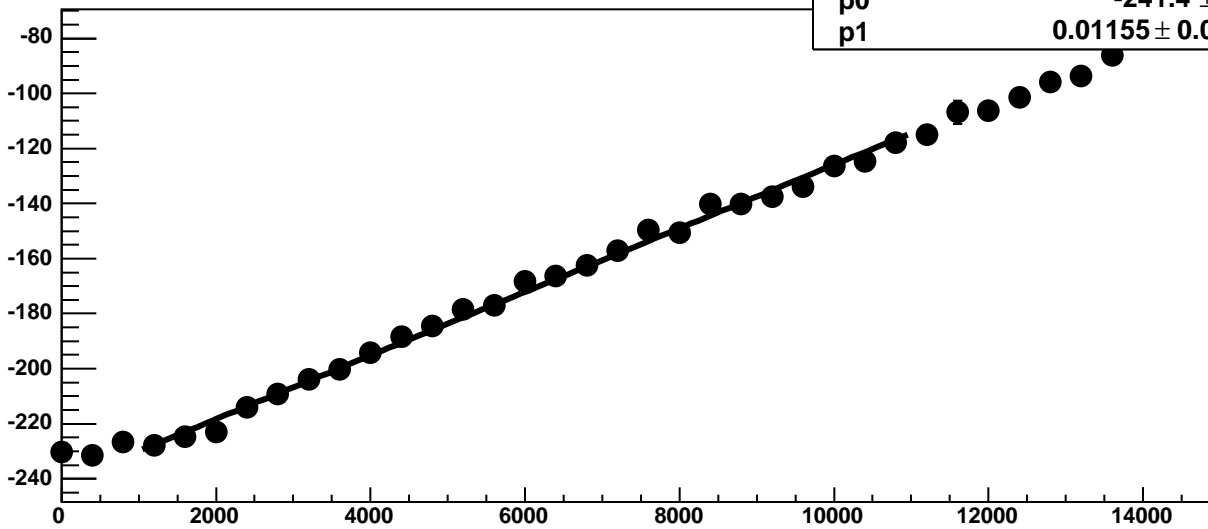
Chip 7, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

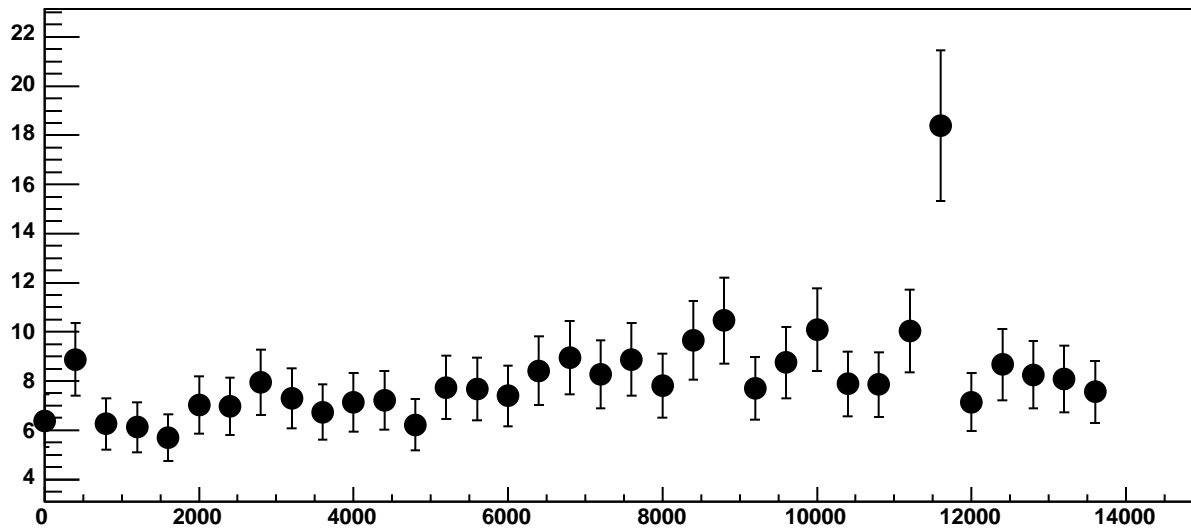


Chip 7, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

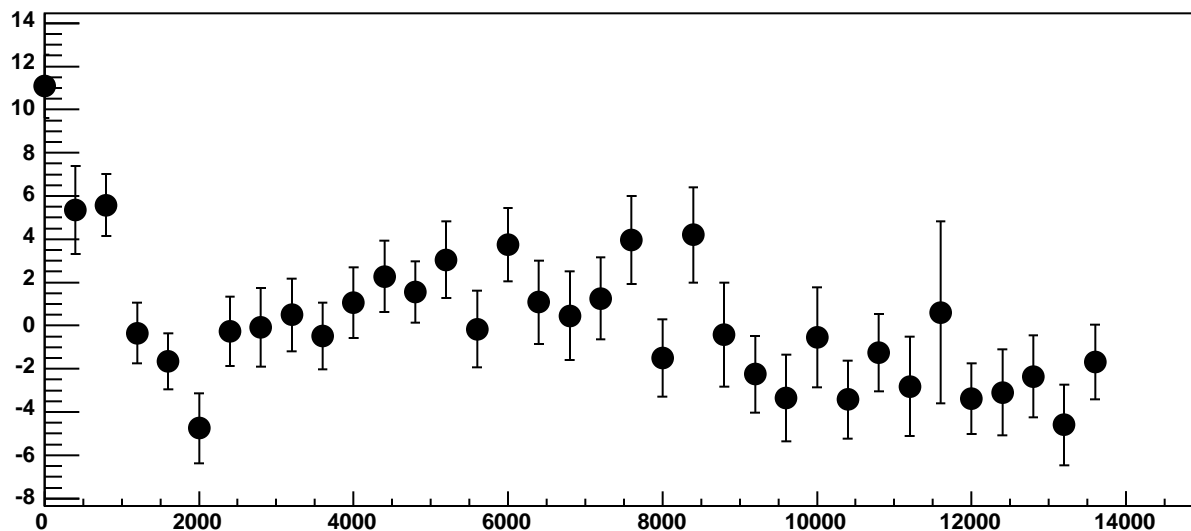


χ^2 / ndf 39.32 / 23
p0 -241.4 ± 0.7302
p1 0.01155 ± 0.0001193

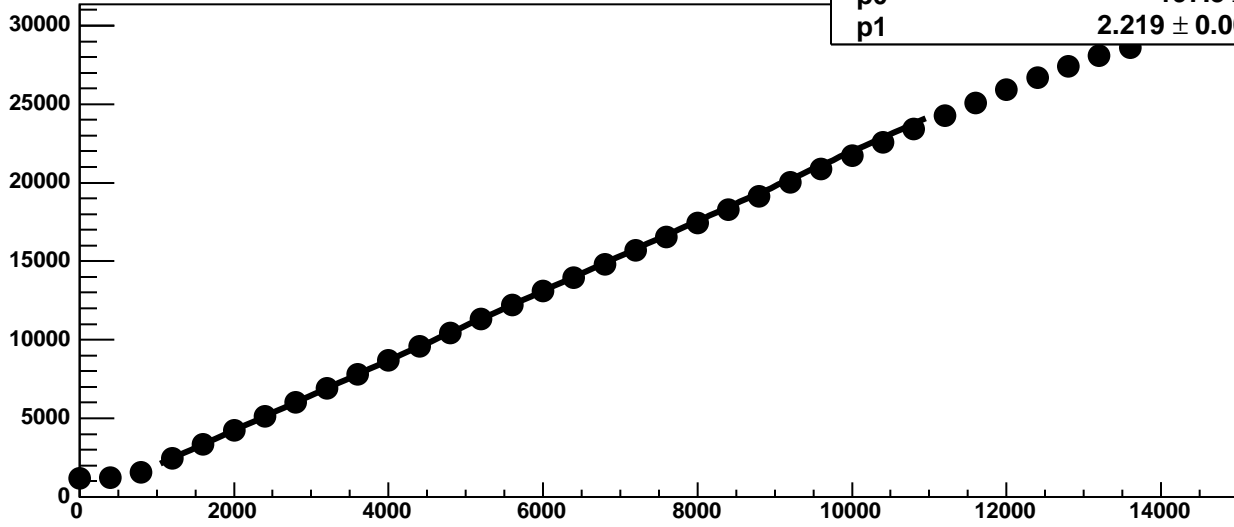
Chip 7, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



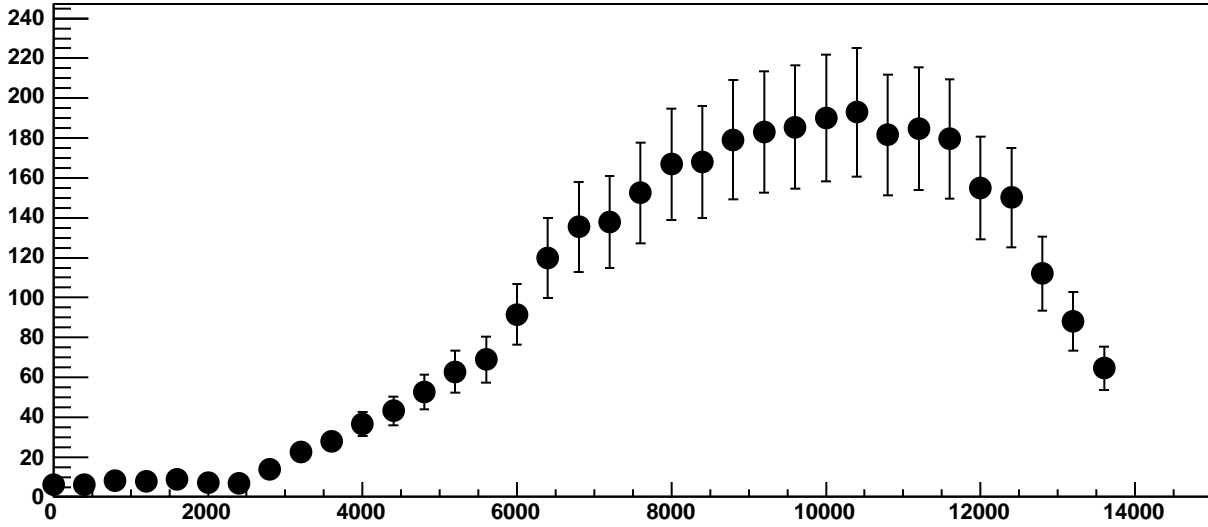
Chip 7, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



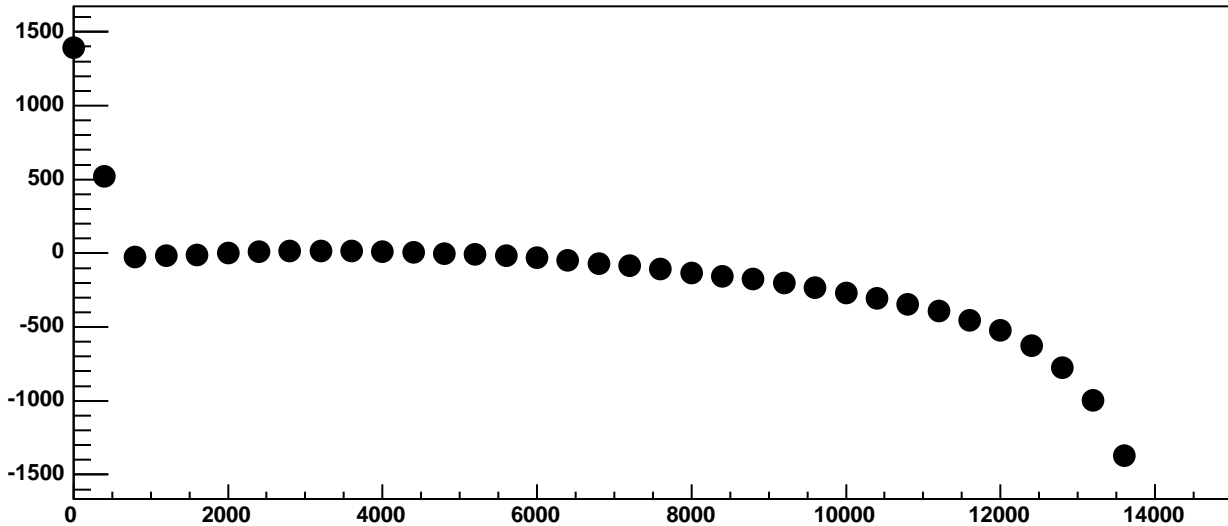
Chip 7, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC



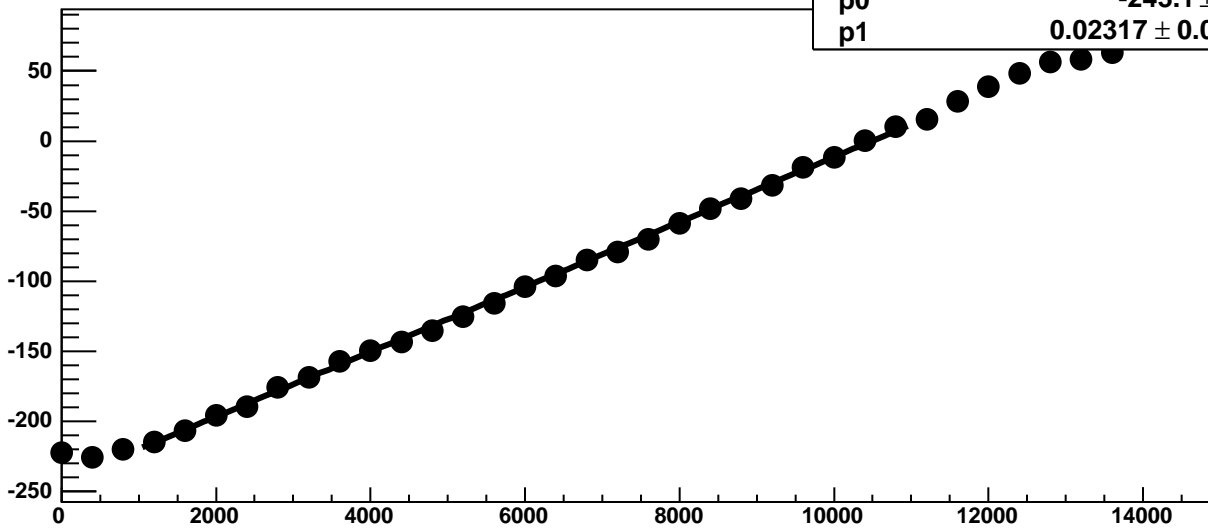
Chip 7, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC

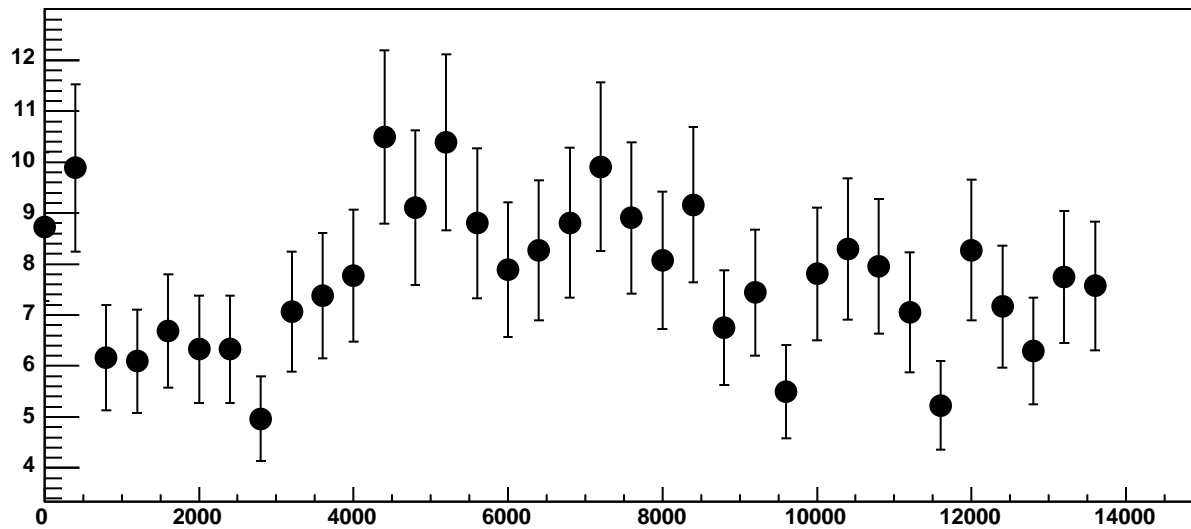


Chip 7, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

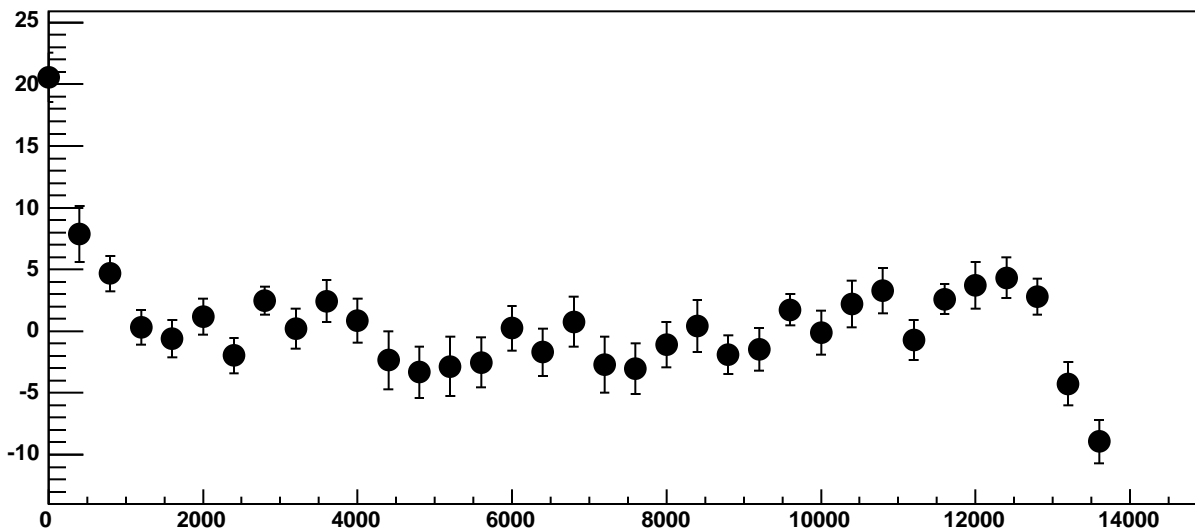


χ^2 / ndf 30.06 / 23
p0 -243.1 ± 0.7038
p1 0.02317 ± 0.0001095

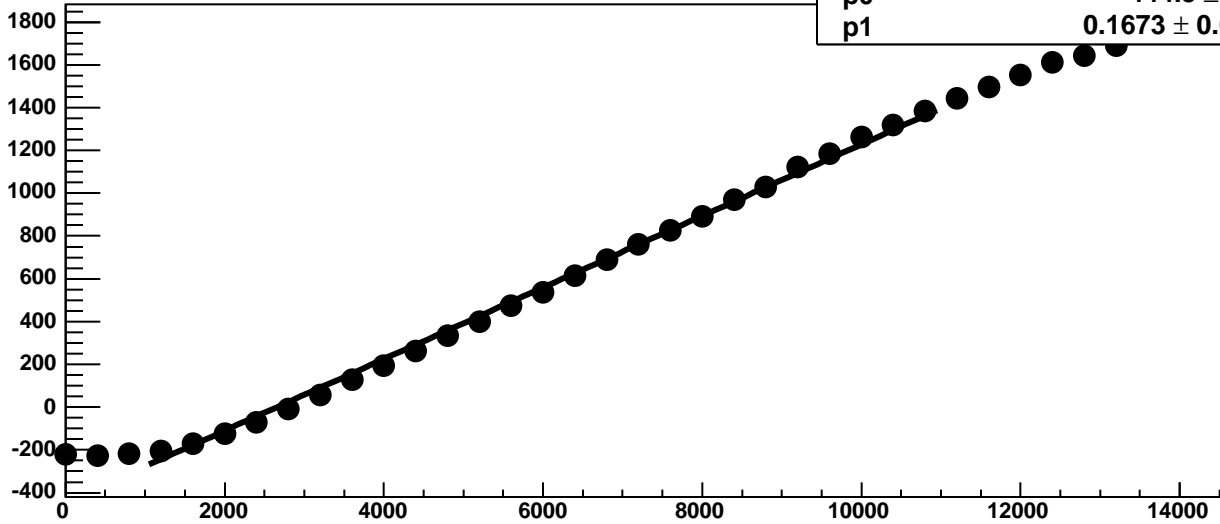
Chip 7, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



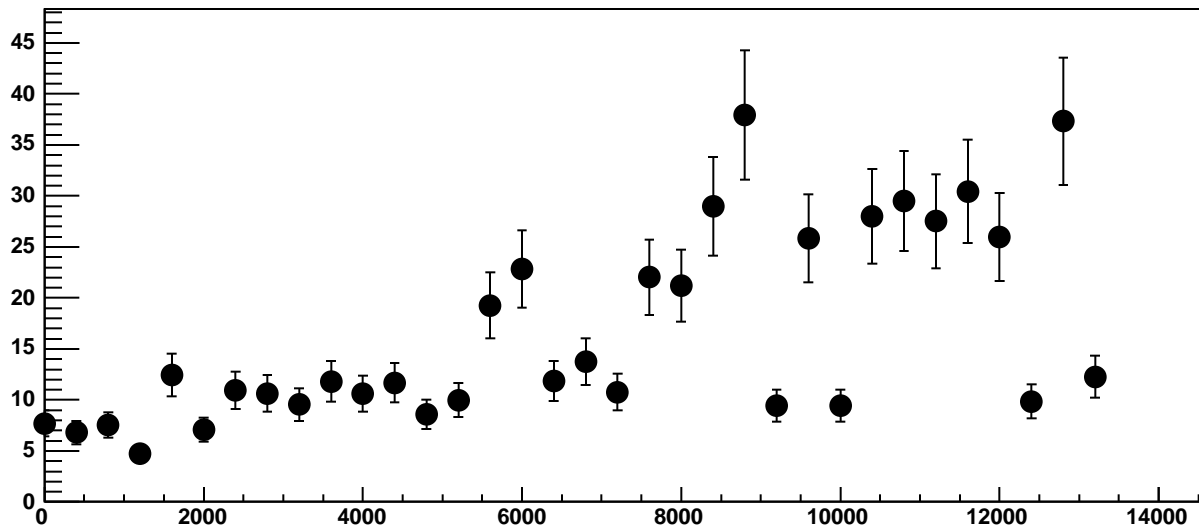
Chip 7, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



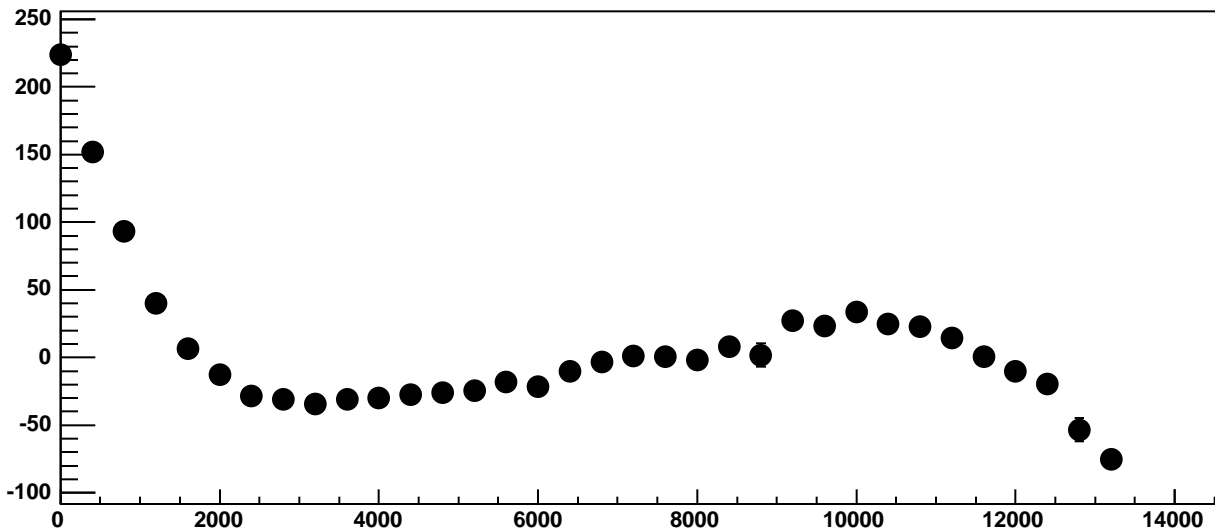
Chip 7, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



Chip 7, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

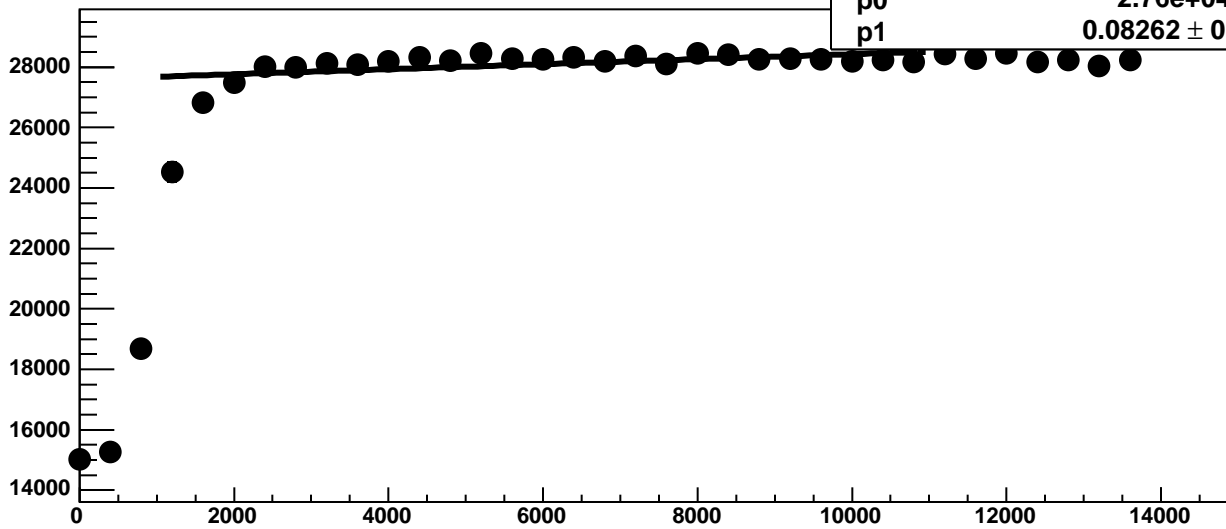


Chip 7, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

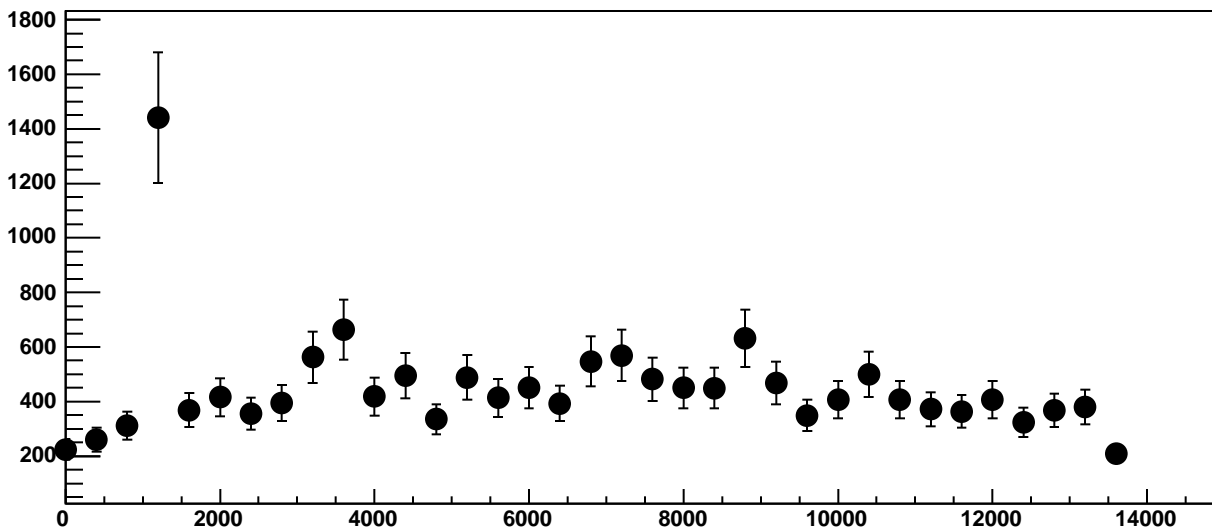


Chip 7, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC

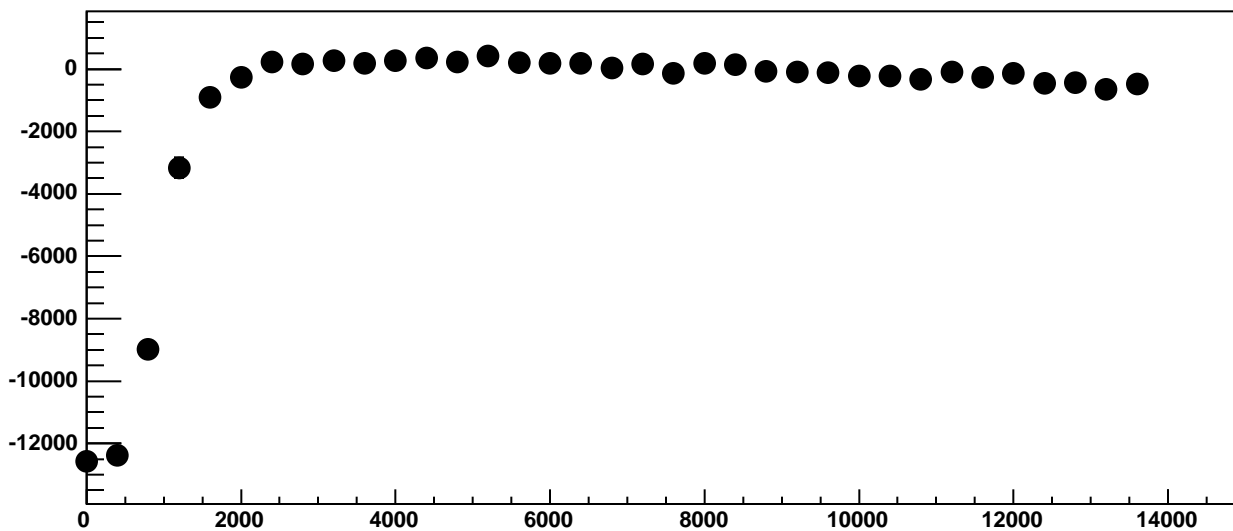
χ^2 / ndf 315.5 / 23
p0 $2.76\text{e}+04 \pm 47.3$
p1 0.08262 ± 0.007066



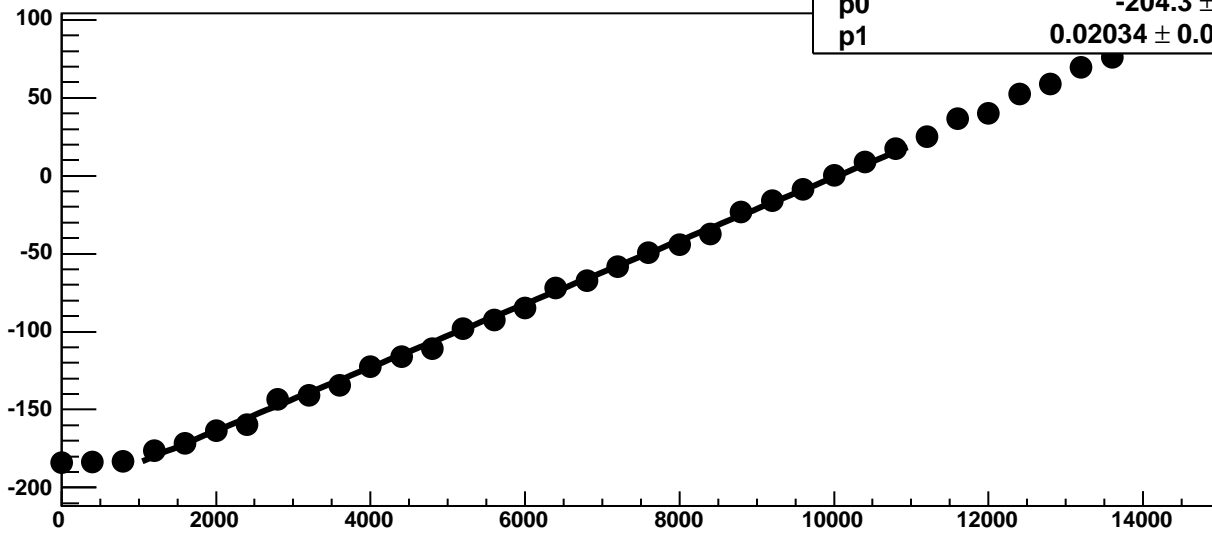
Chip 7, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC

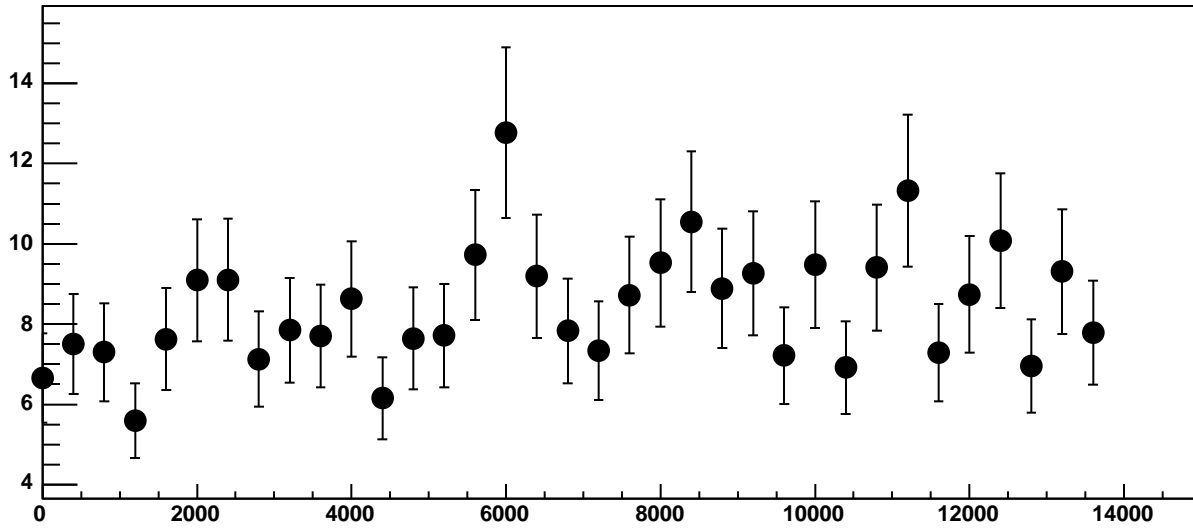


Chip 7, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

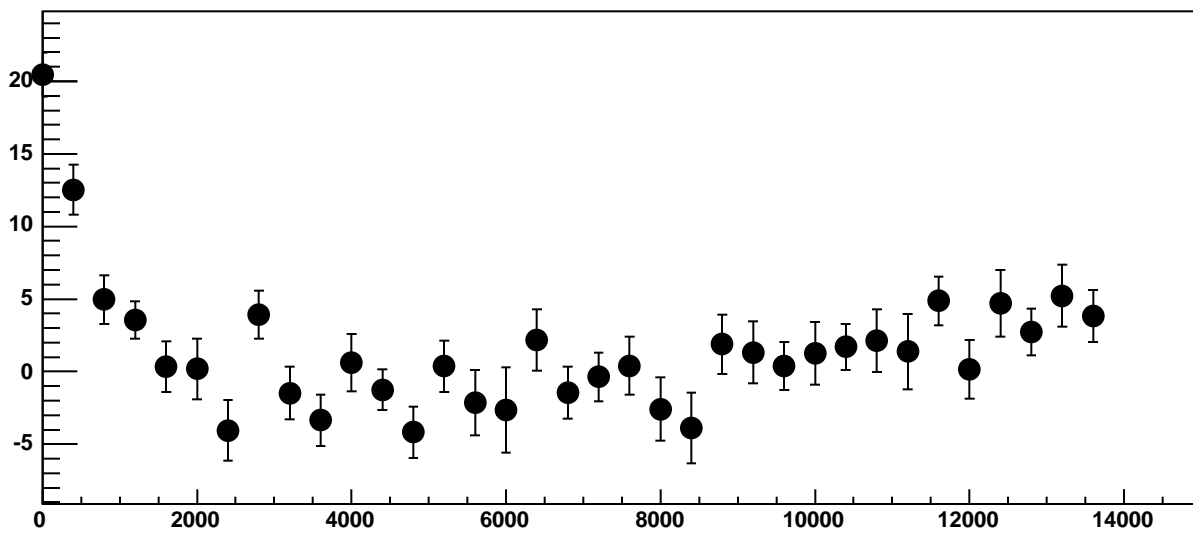


χ^2 / ndf 39.42 / 23
p0 -204.3 ± 0.7904
p1 0.02034 ± 0.0001239

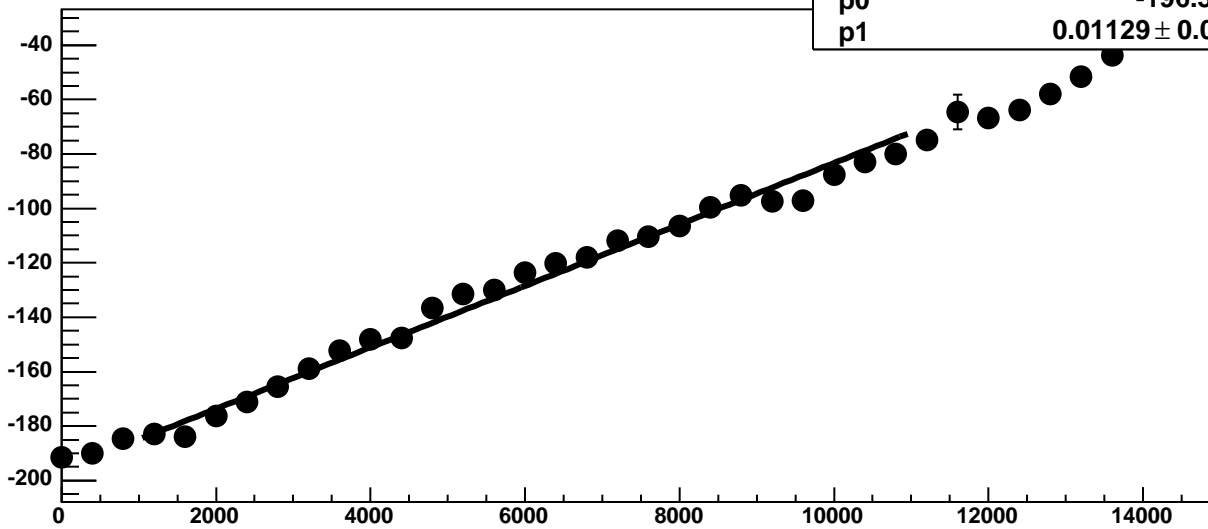
Chip 7, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



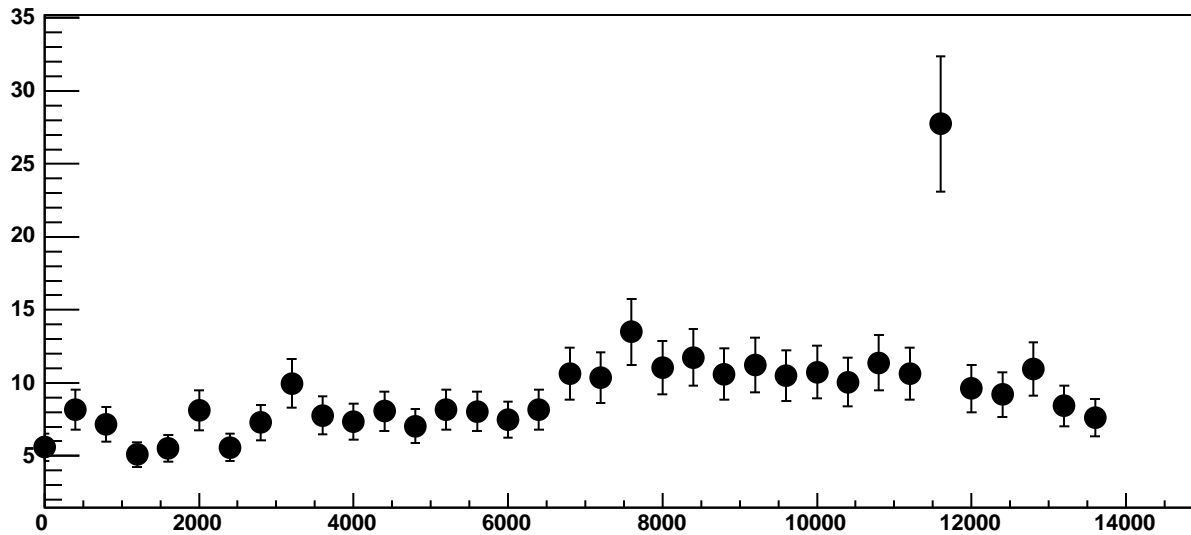
Chip 7, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



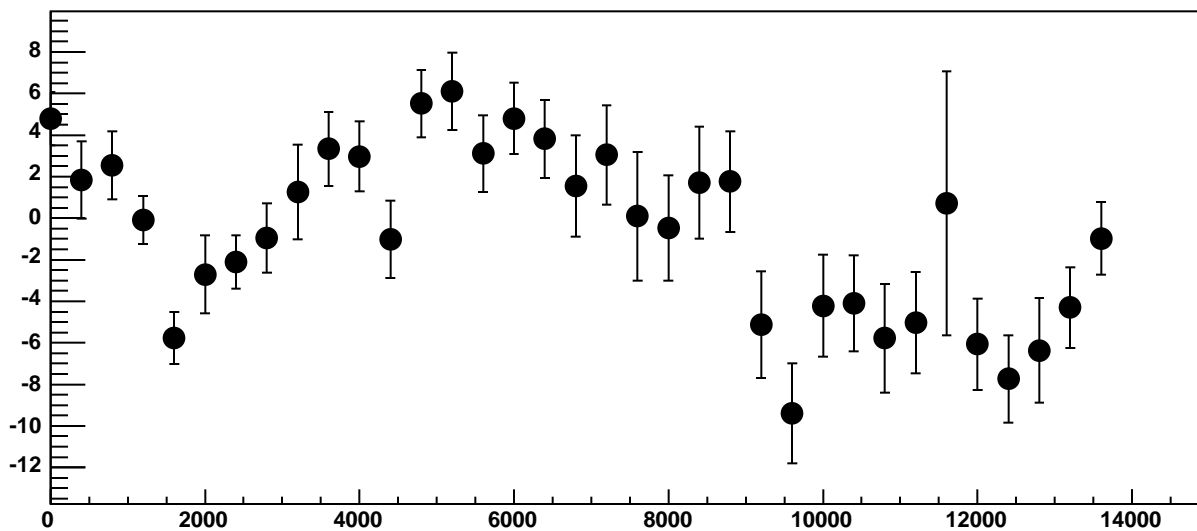
Chip 7, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



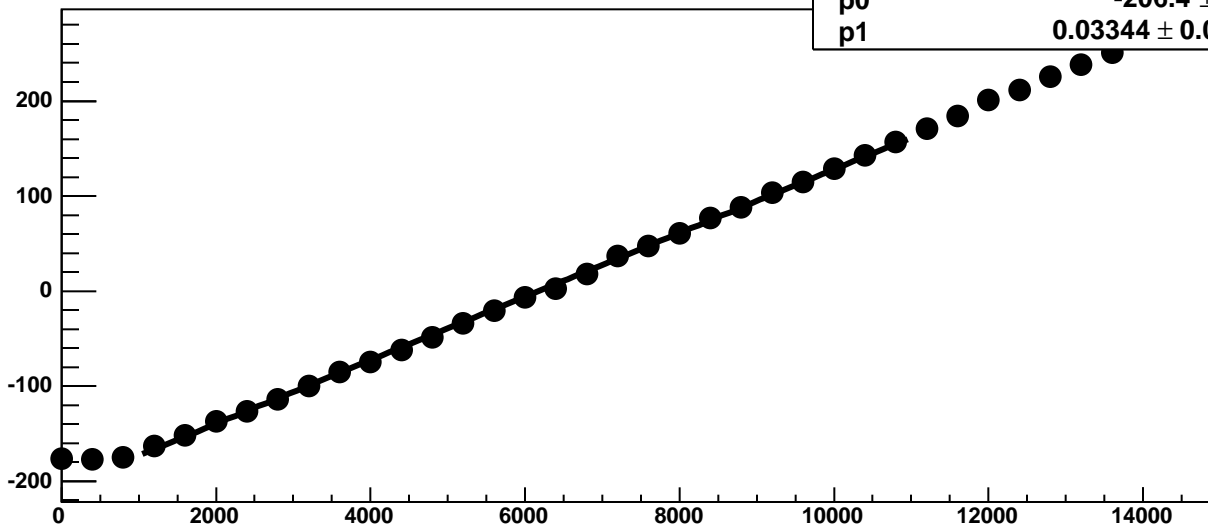
Chip 7, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

34.14 / 23

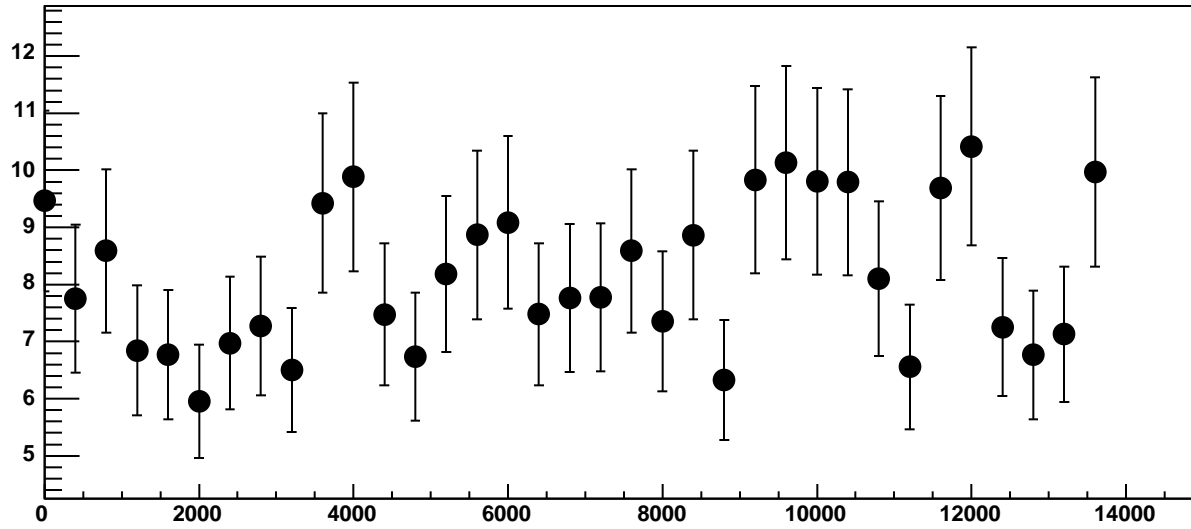
p0

-206.4 ± 0.7664

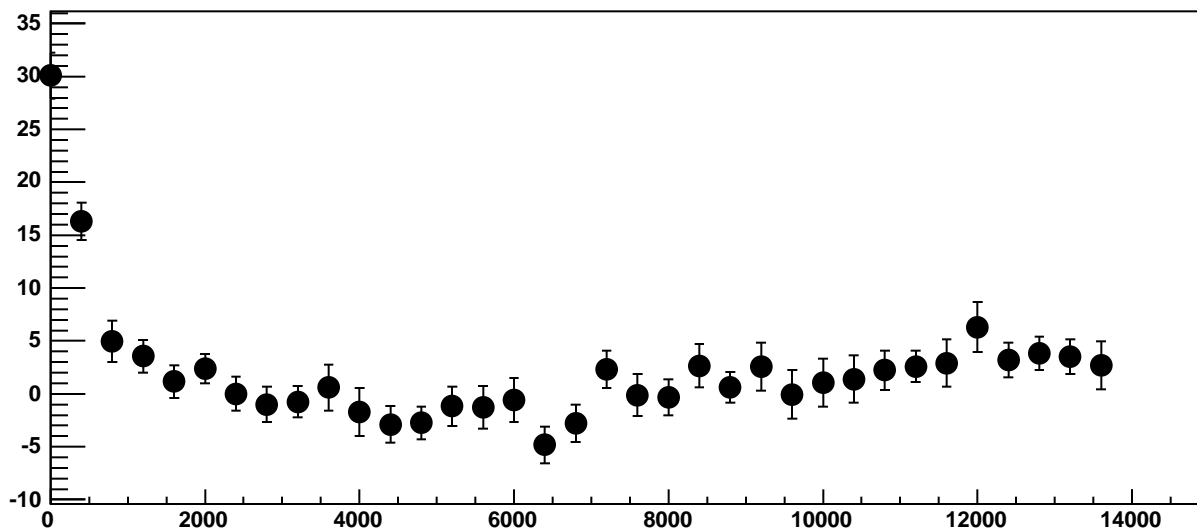
p1

0.03344 ± 0.0001231

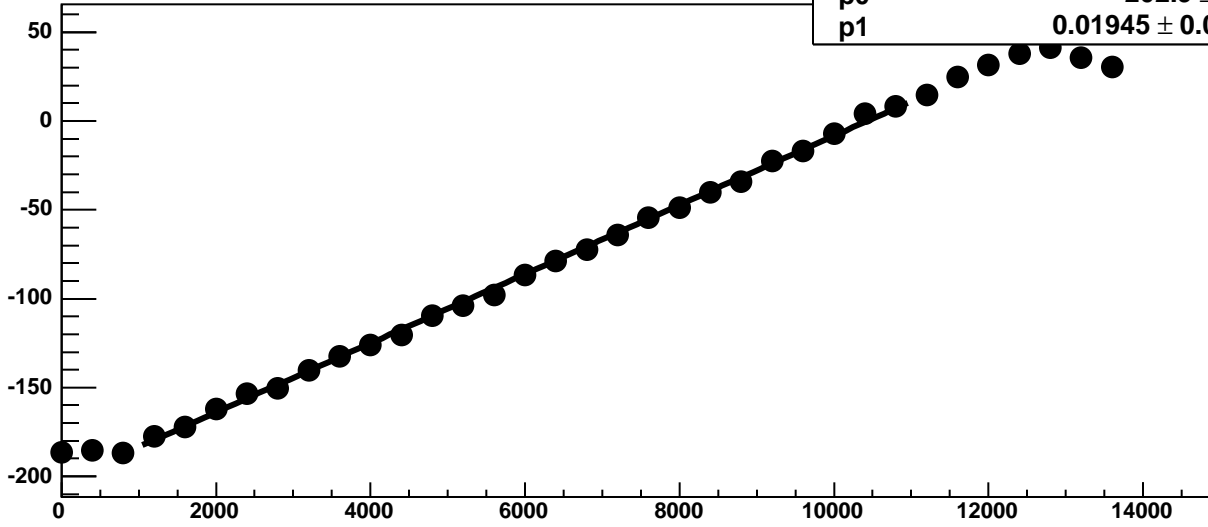
Chip 7, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

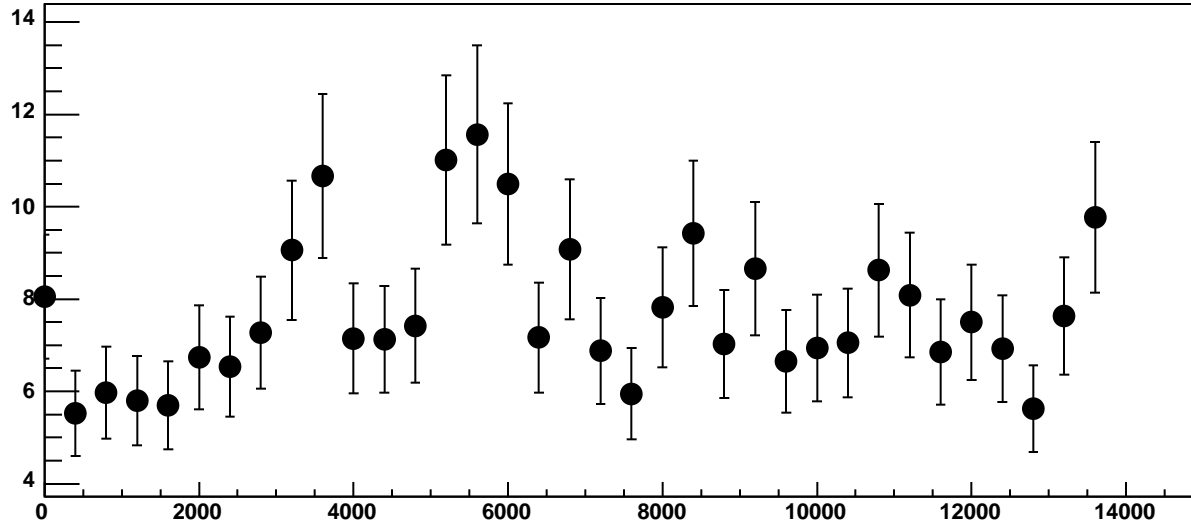


Chip 7, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

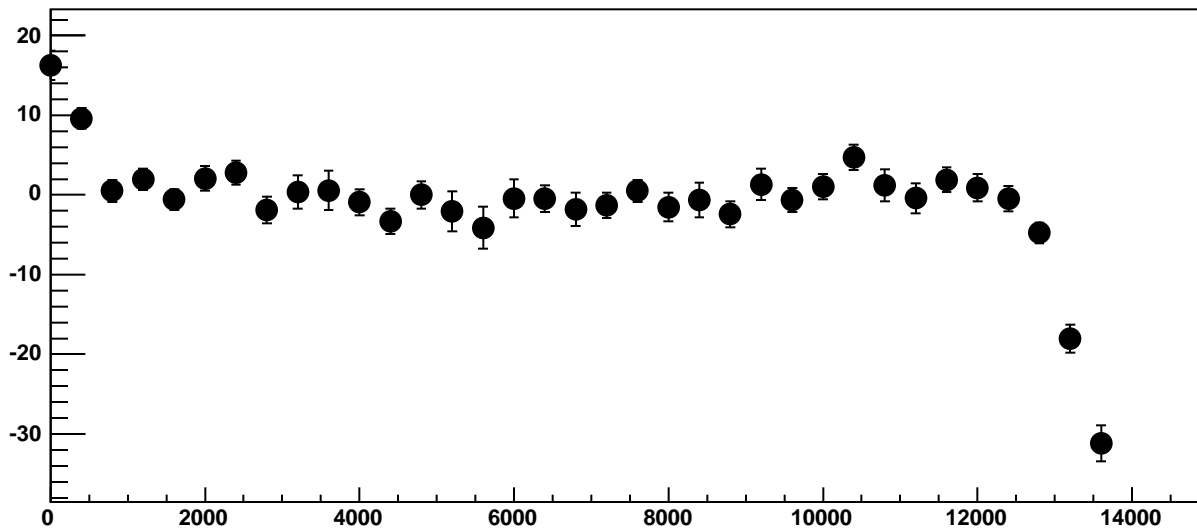


χ^2 / ndf 31.32 / 23
p0 -202.9 ± 0.7252
p1 0.01945 ± 0.0001109

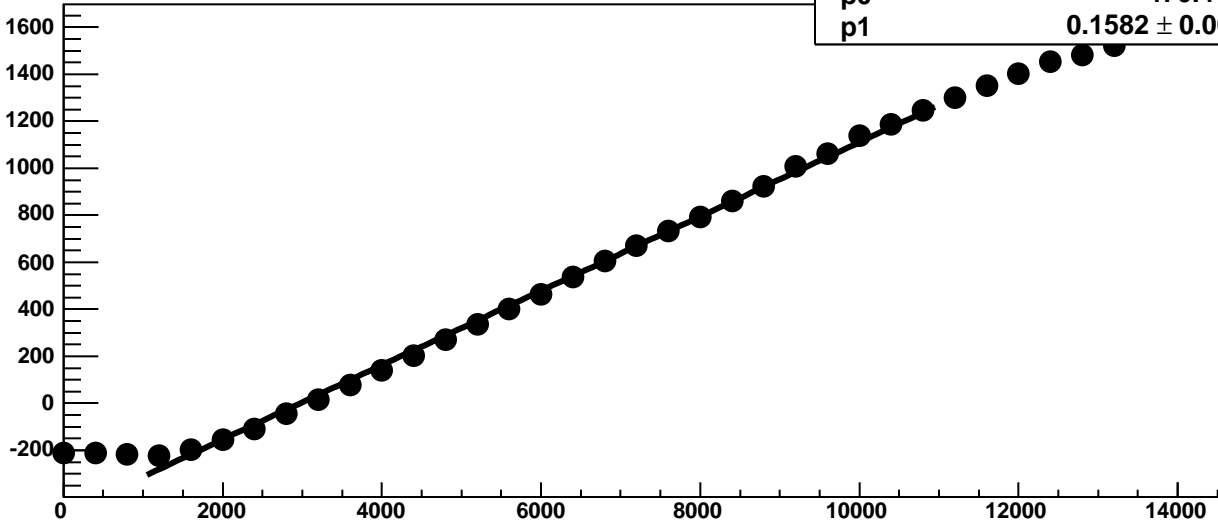
Chip 7, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



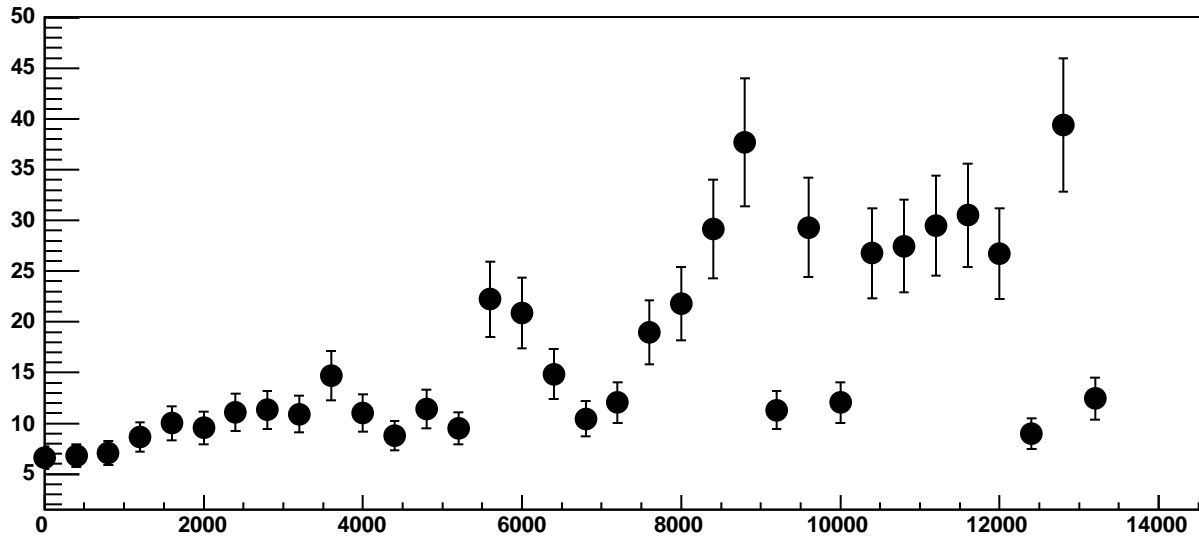
Chip 7, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



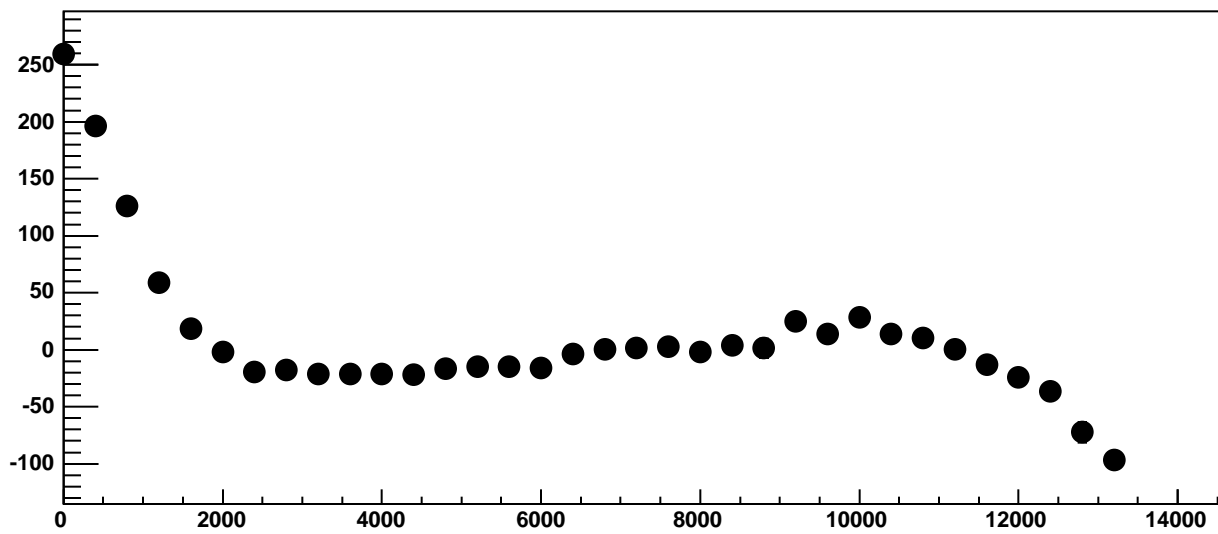
Chip 7, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



Chip 7, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

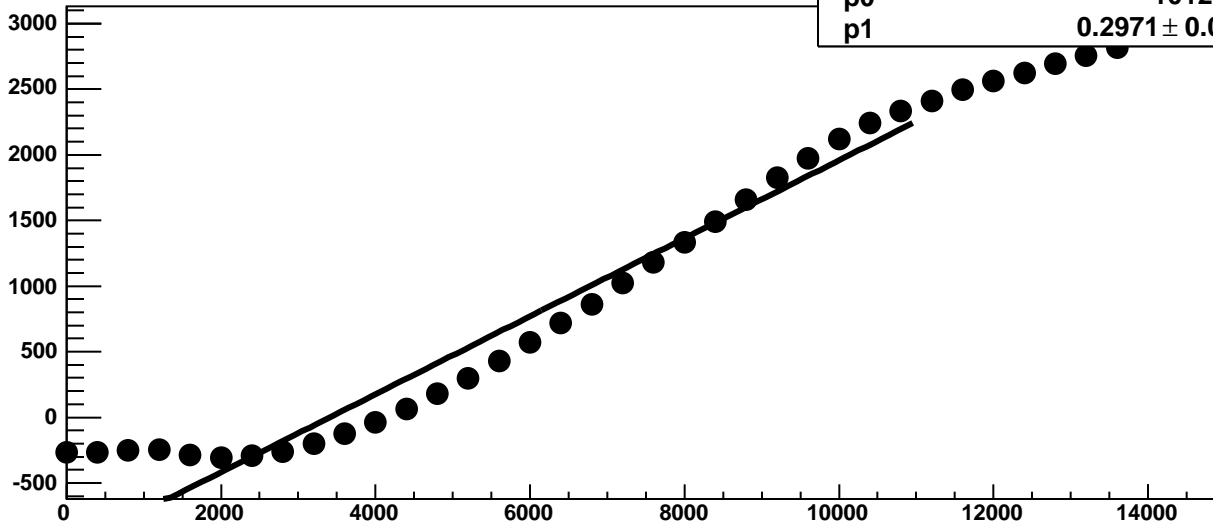


Chip 7, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

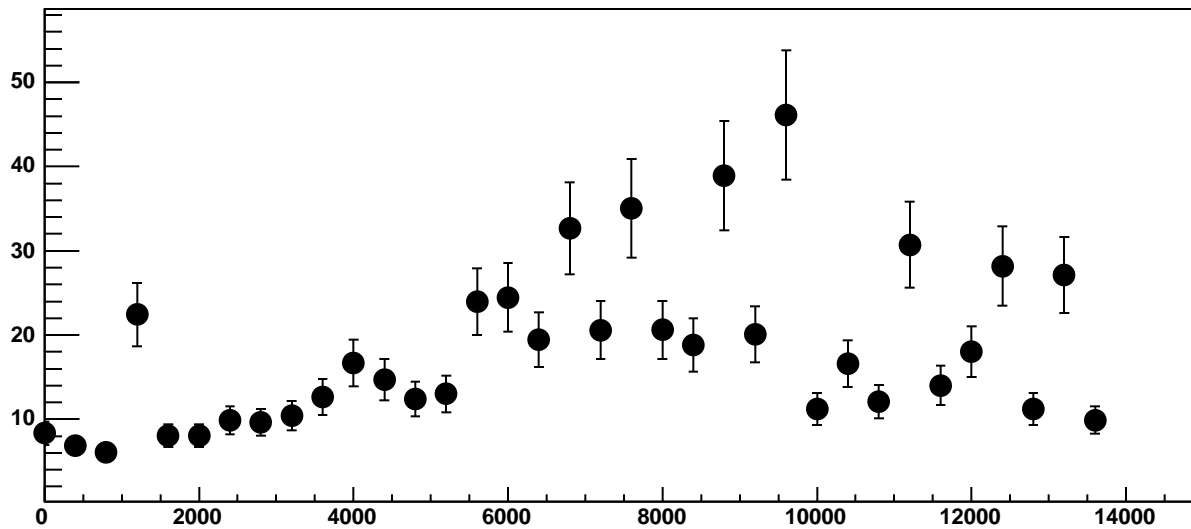


Chip 7, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC

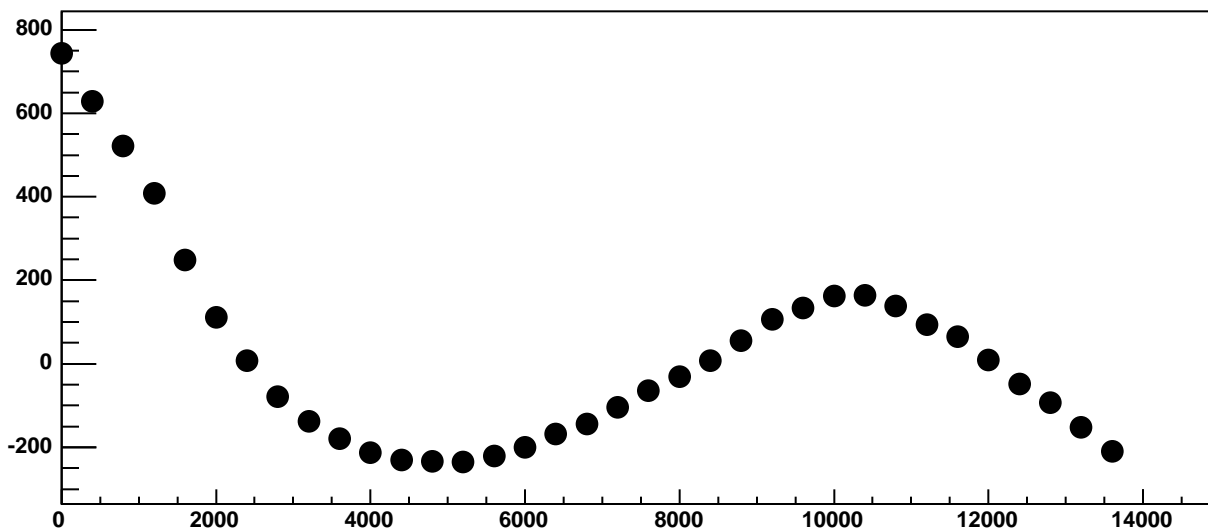
χ^2 / ndf 7.154e+04 / 23
p0 -1012 ± 1.177
p1 0.2971 ± 0.0002112



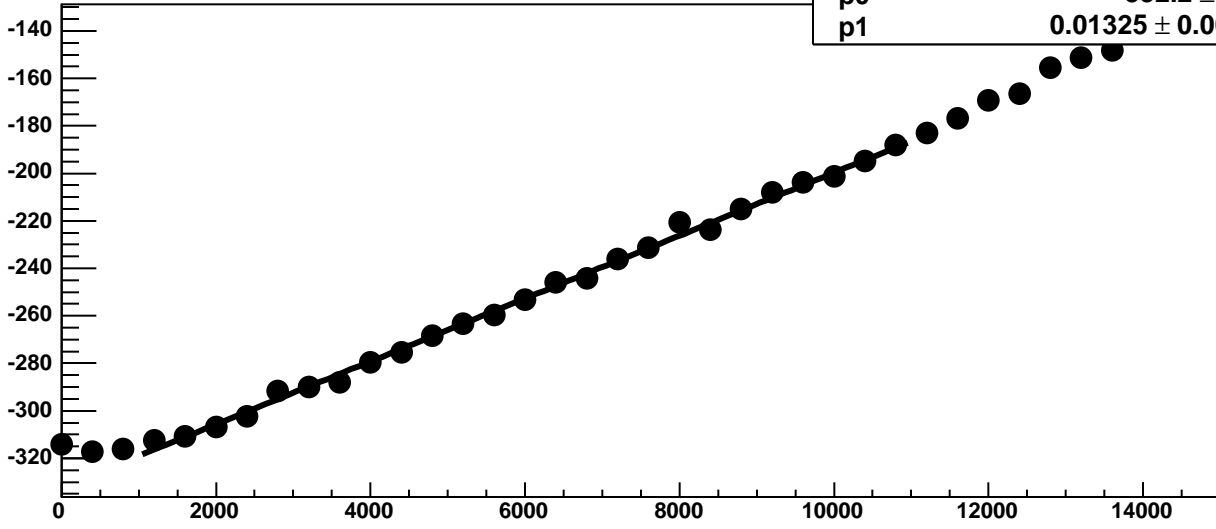
Chip 7, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



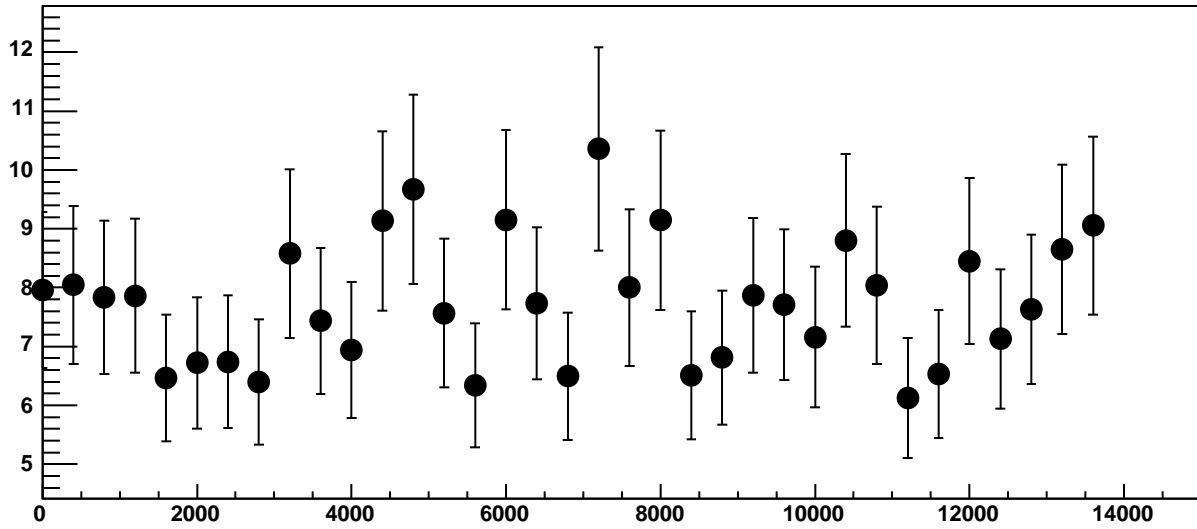
Chip 7, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC



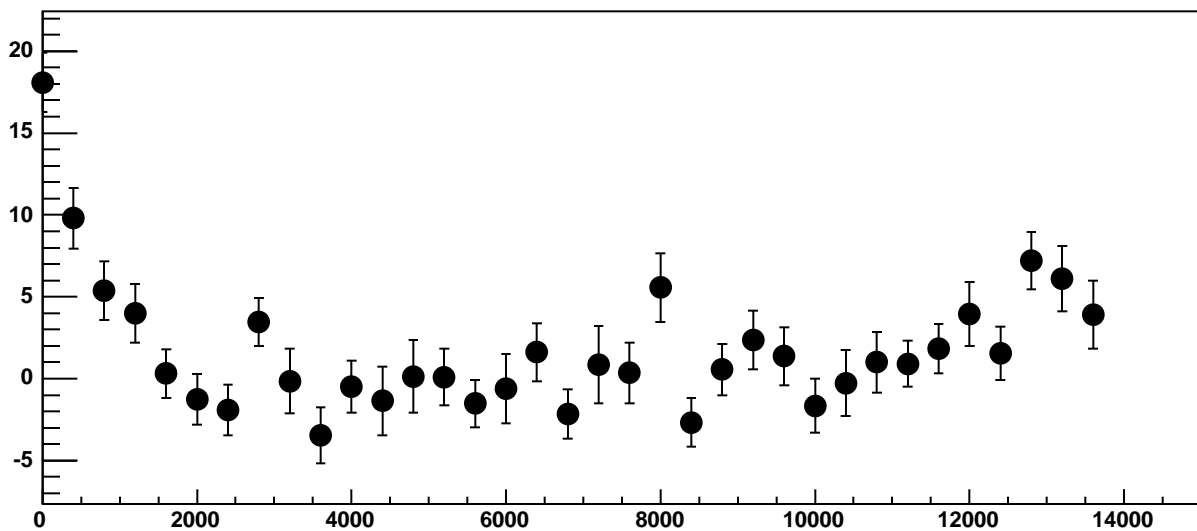
Chip 7, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



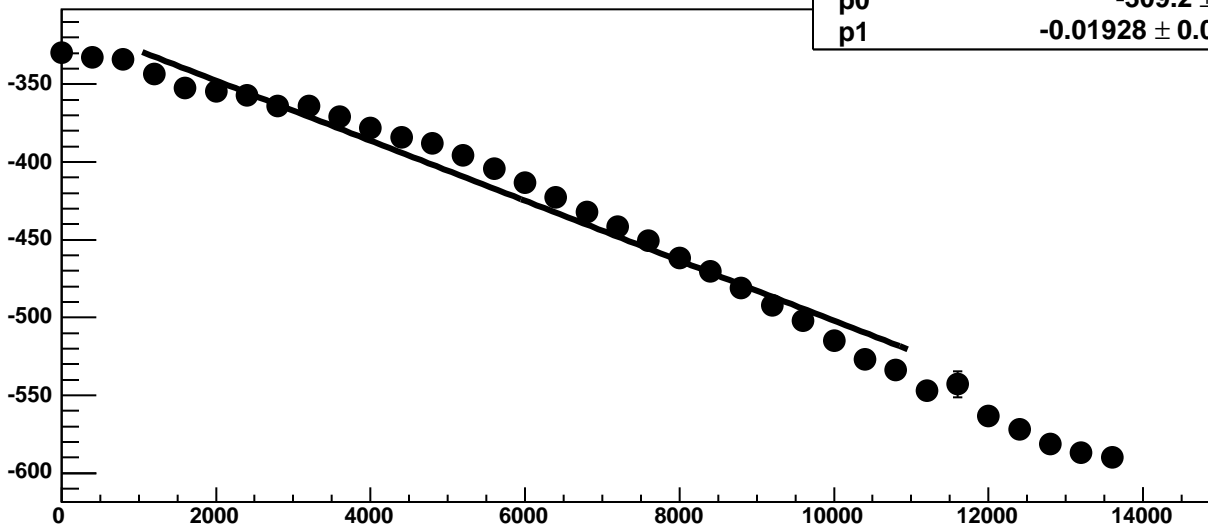
Chip 7, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

460 / 23

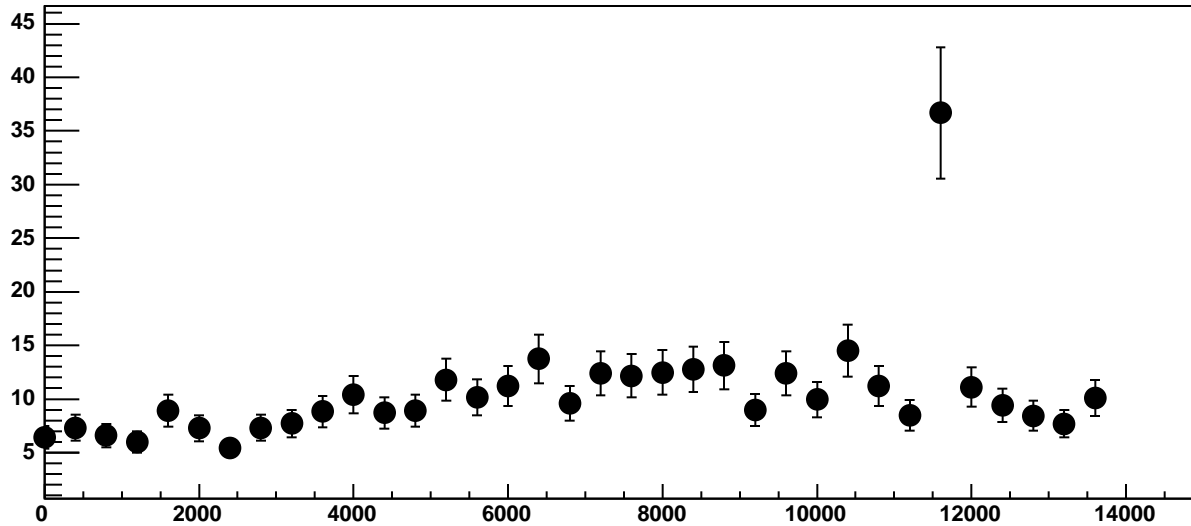
p0

-309.2 ± 0.8166

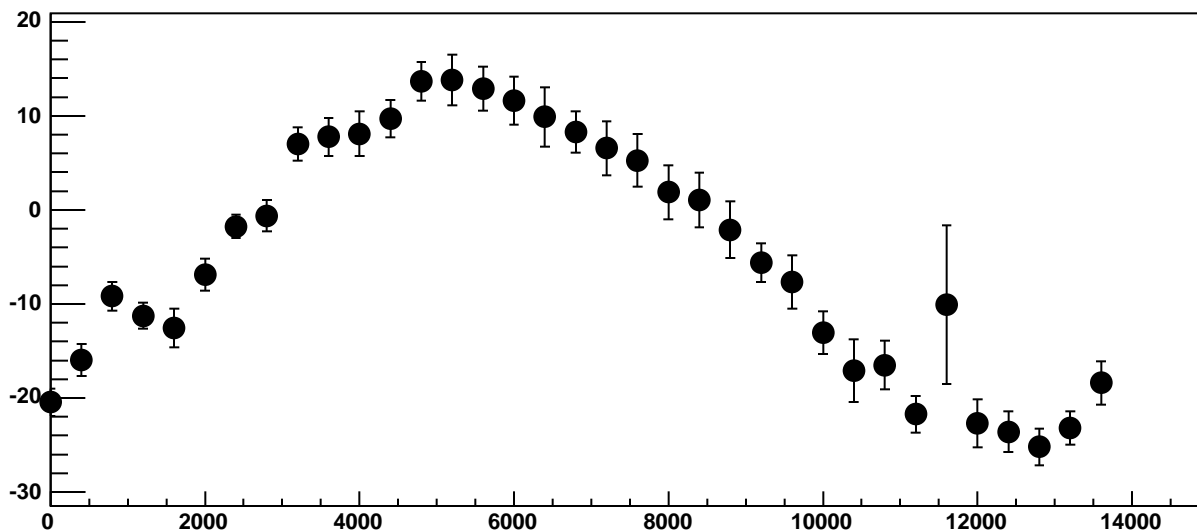
p1

-0.01928 ± 0.0001456

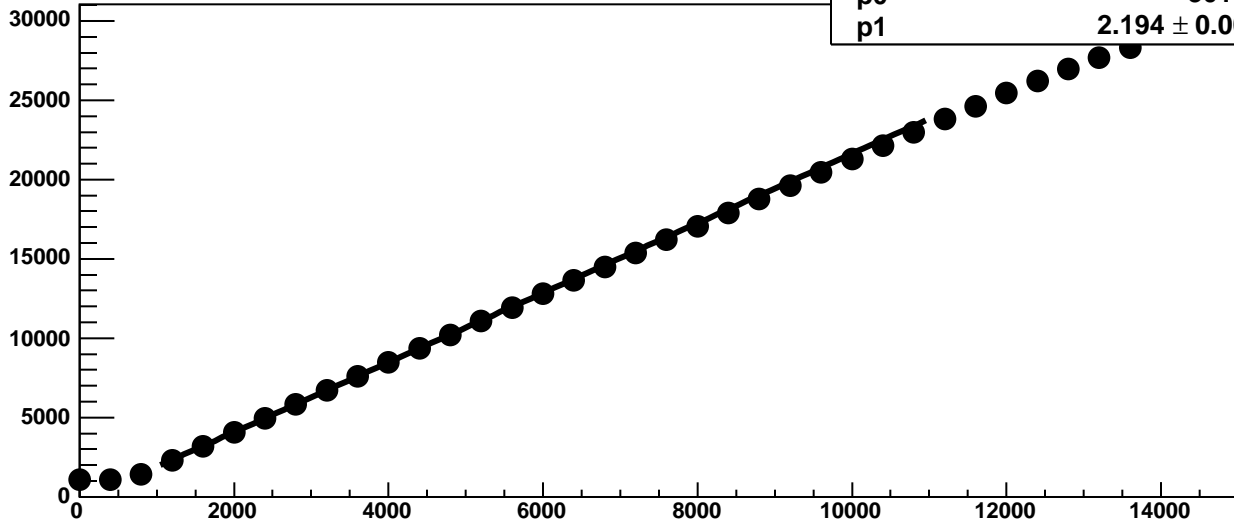
Chip 7, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



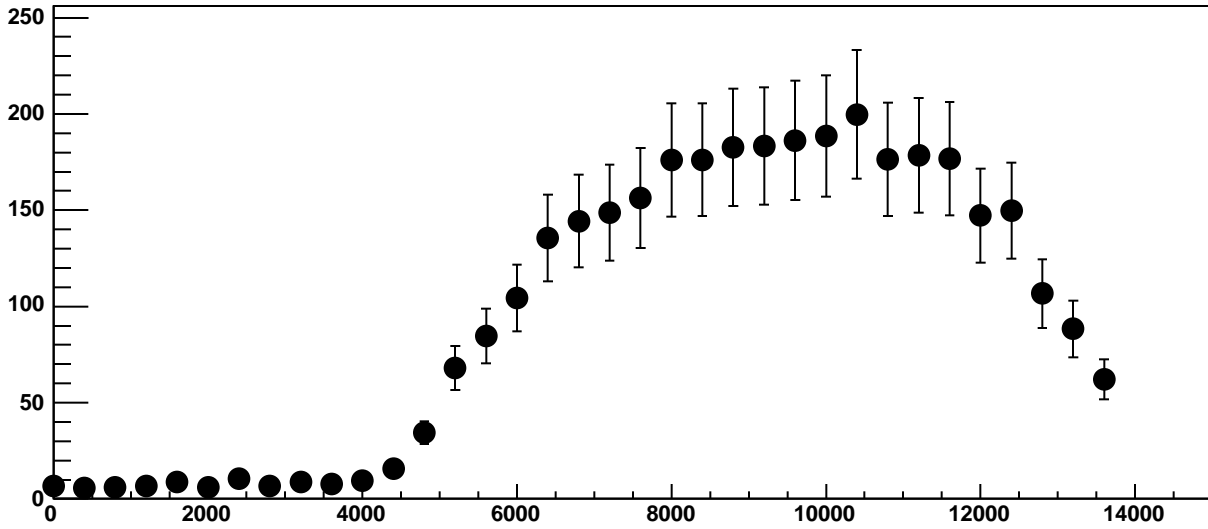
Chip 7, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



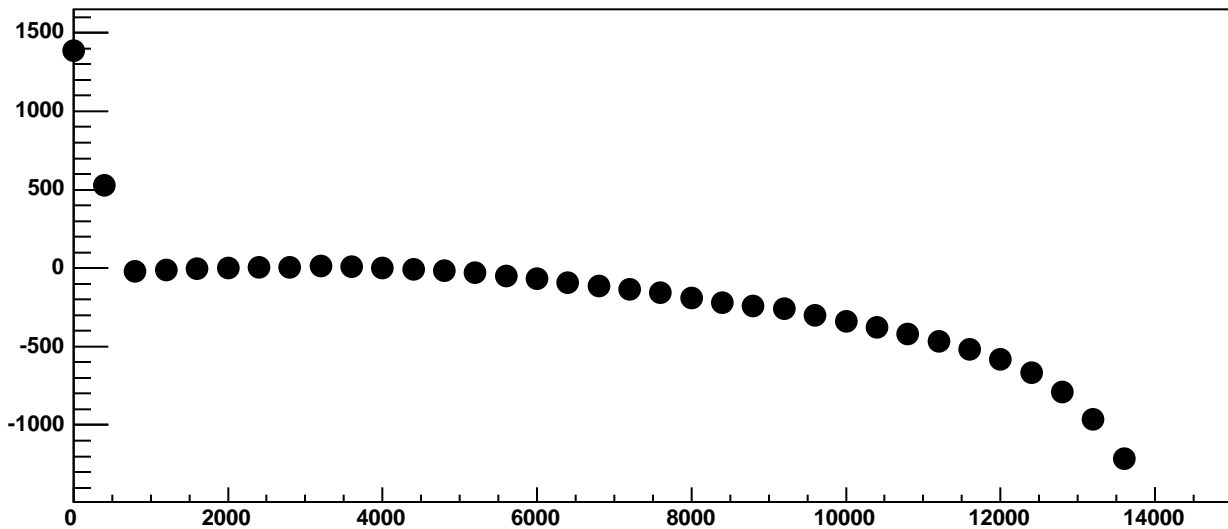
Chip 7, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC



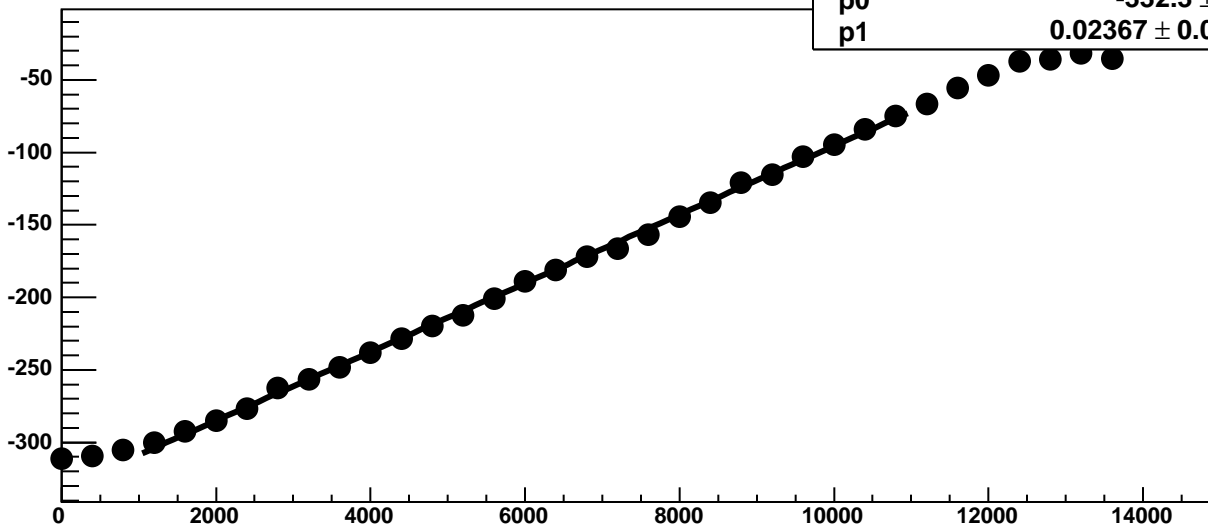
Chip 7, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



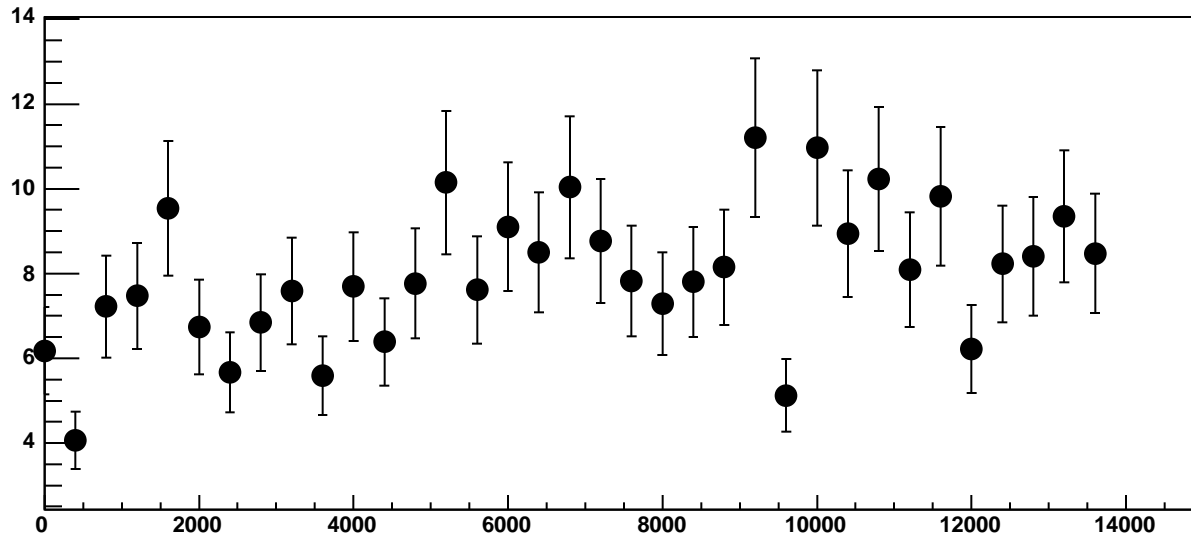
Chip 7, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC



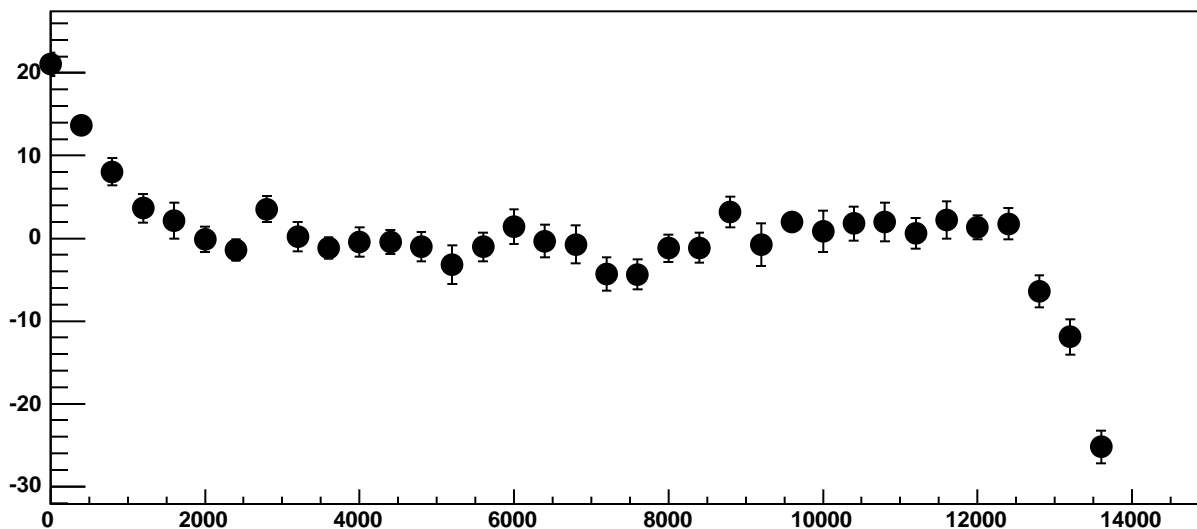
Chip 7, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



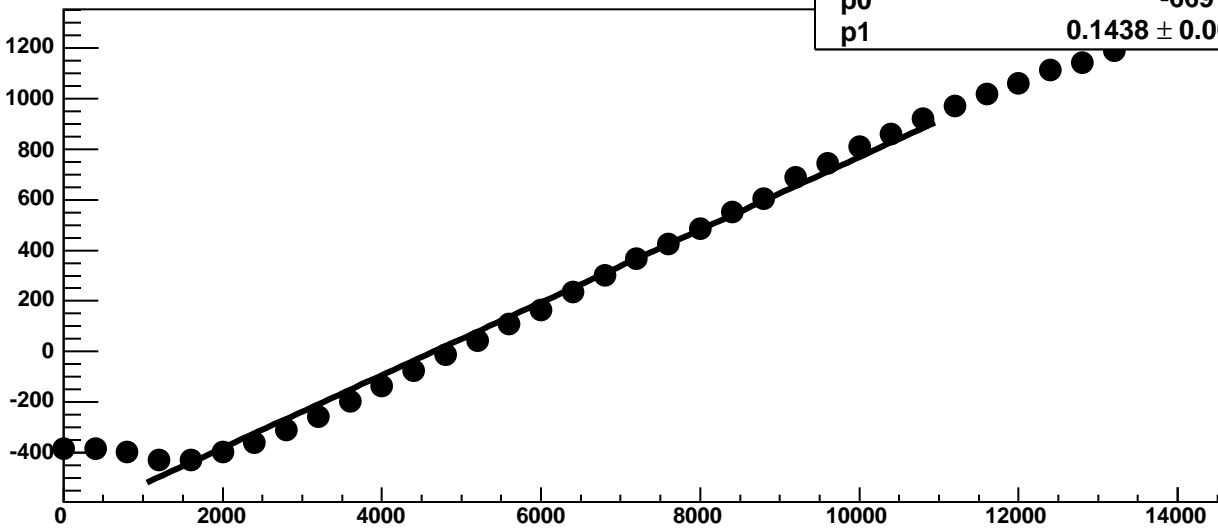
Chip 7, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



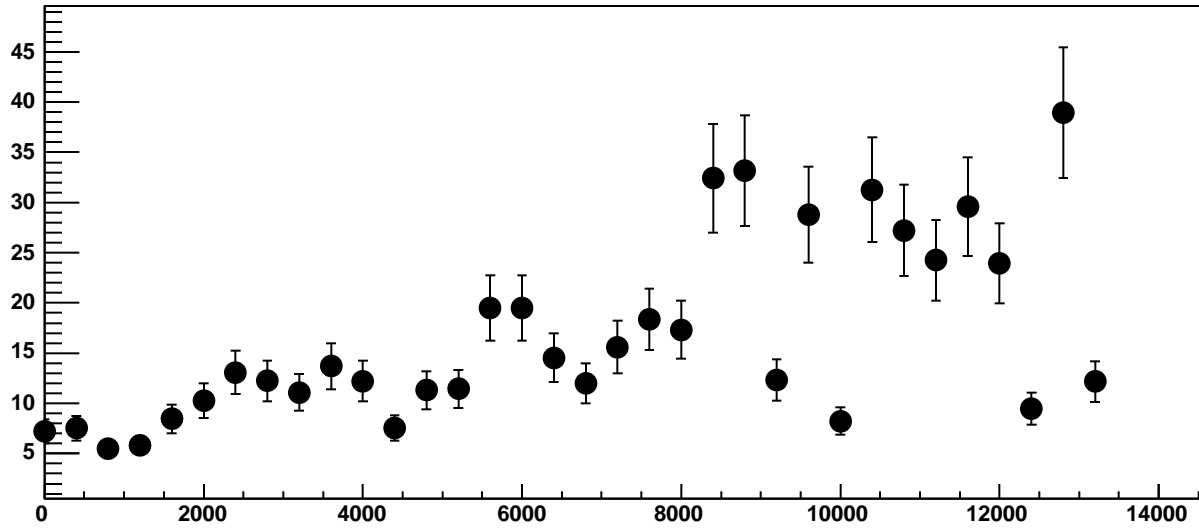
Chip 7, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



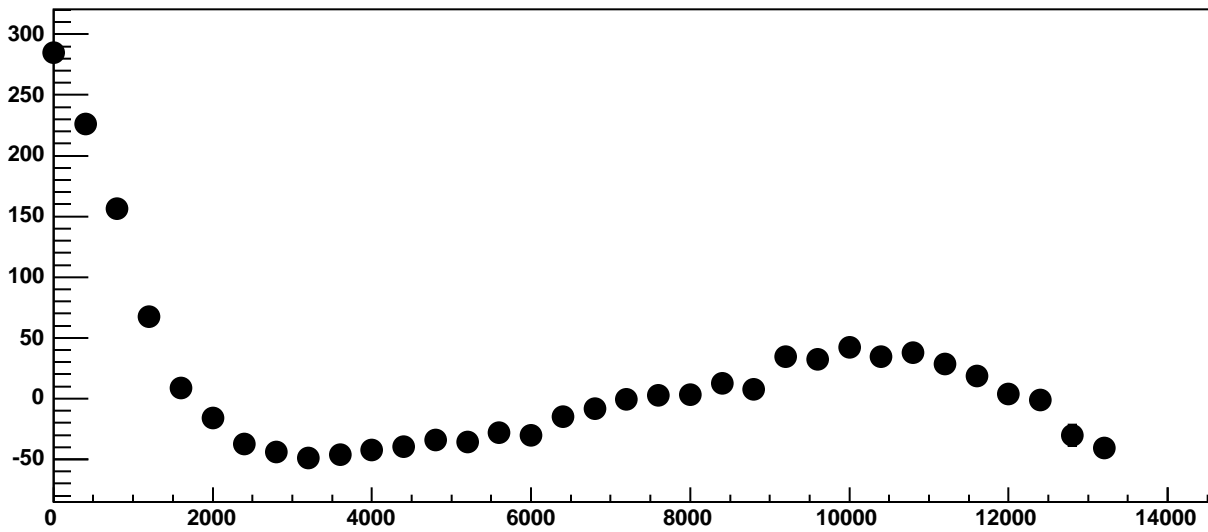
Chip 7, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



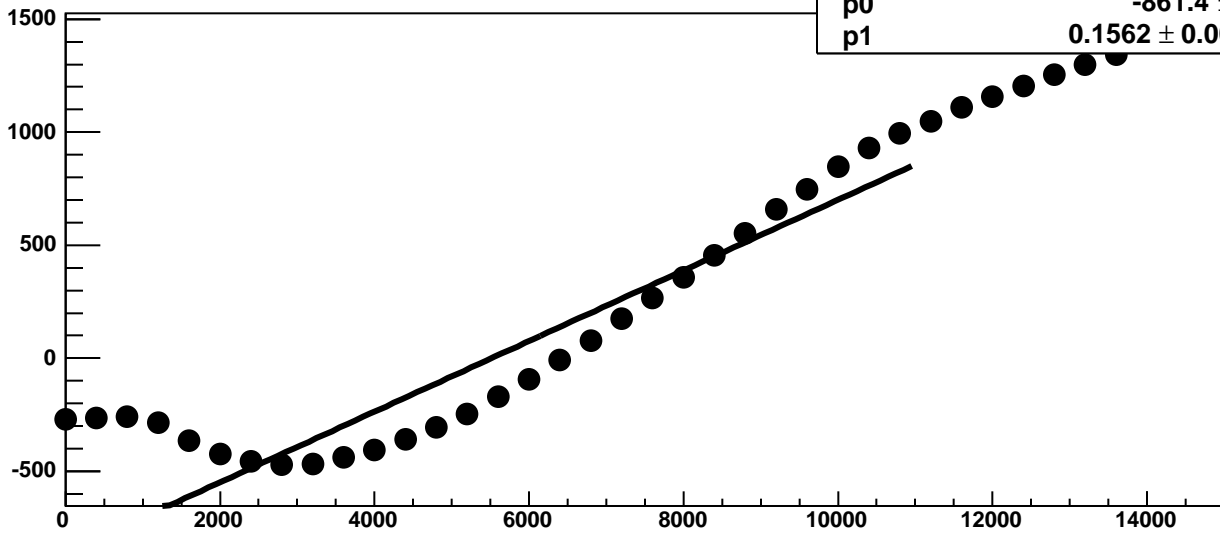
Chip 7, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 7, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

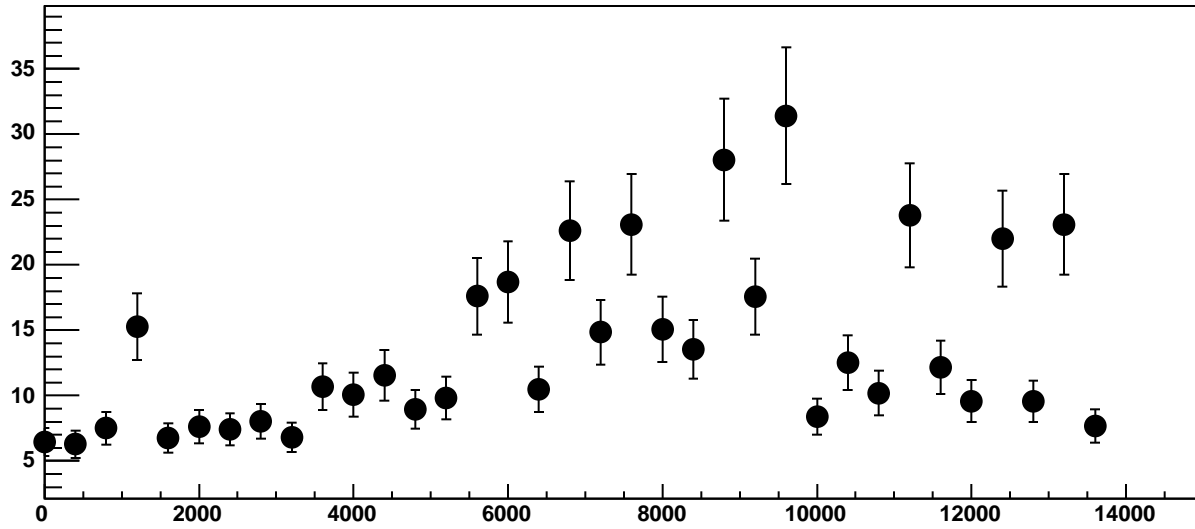


Chip 7, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

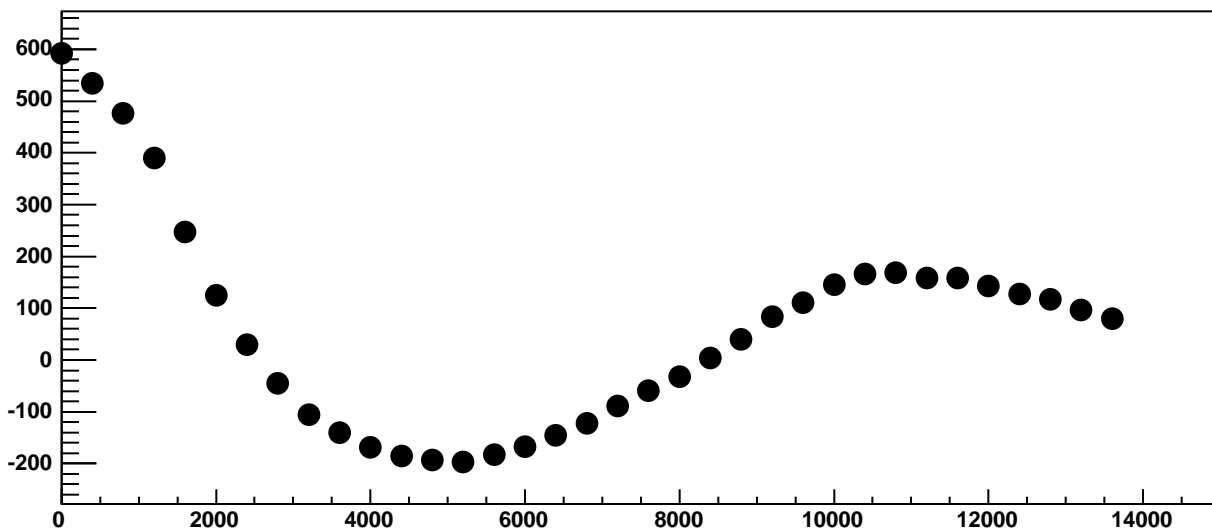


χ^2 / ndf 1.021e+05 / 23
p0 -861.4 ± 0.943
p1 0.1562 ± 0.0001668

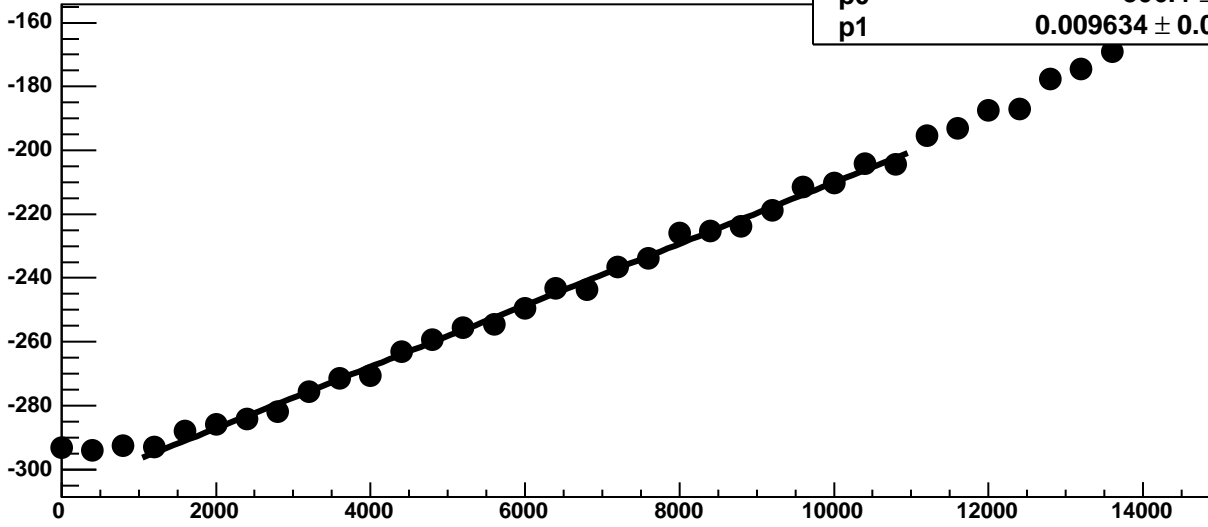
Chip 7, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



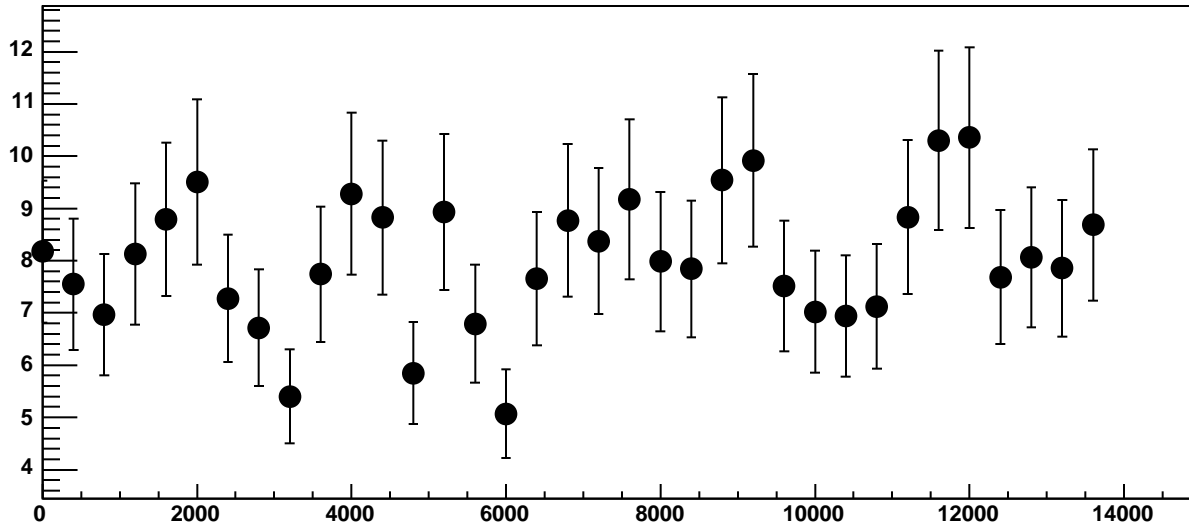
Chip 7, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



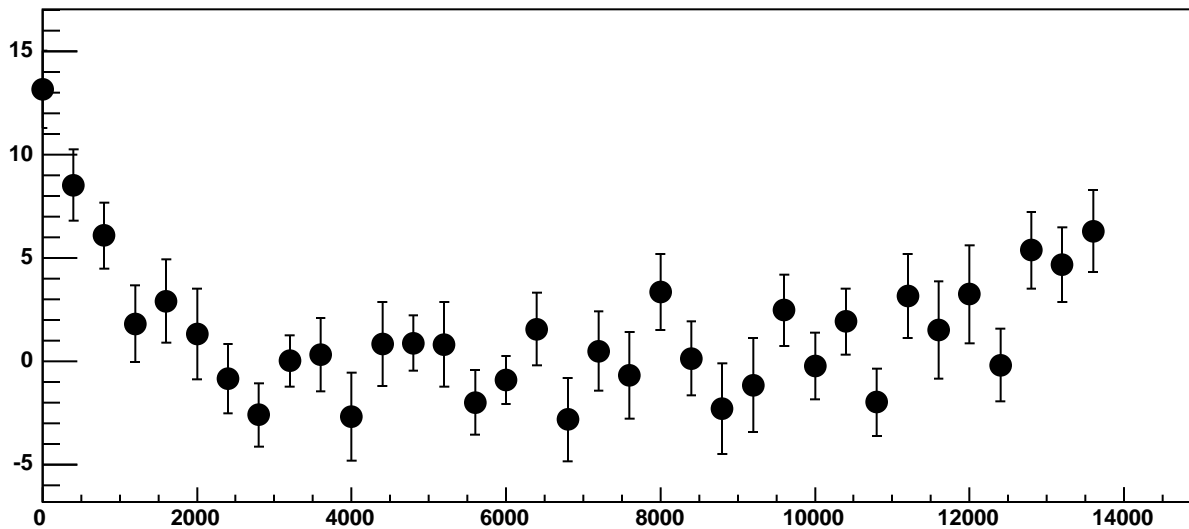
Chip 7, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC



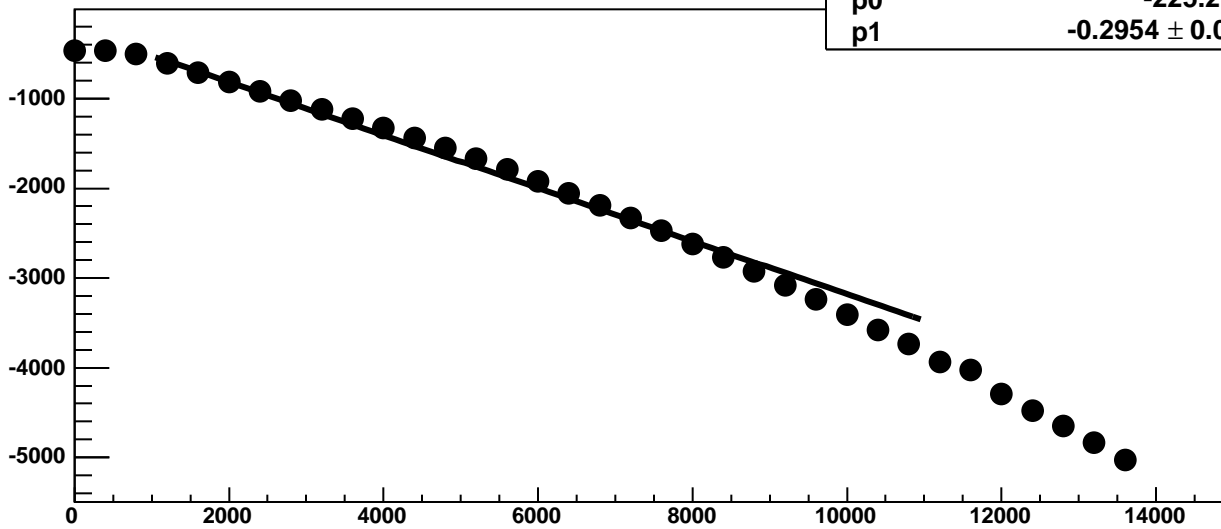
Chip 7, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



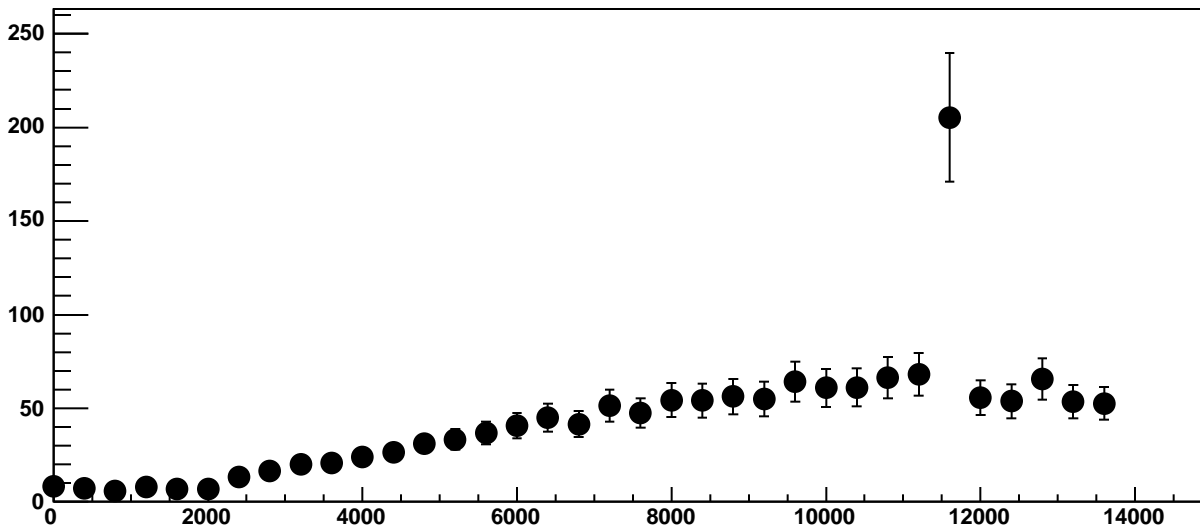
Chip 7, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC



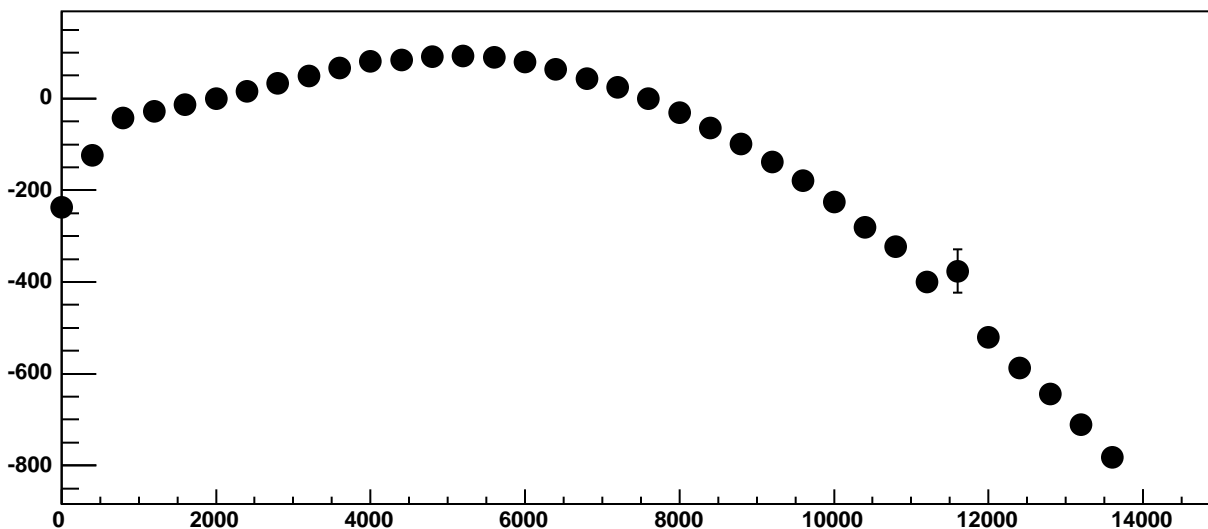
Chip 7, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



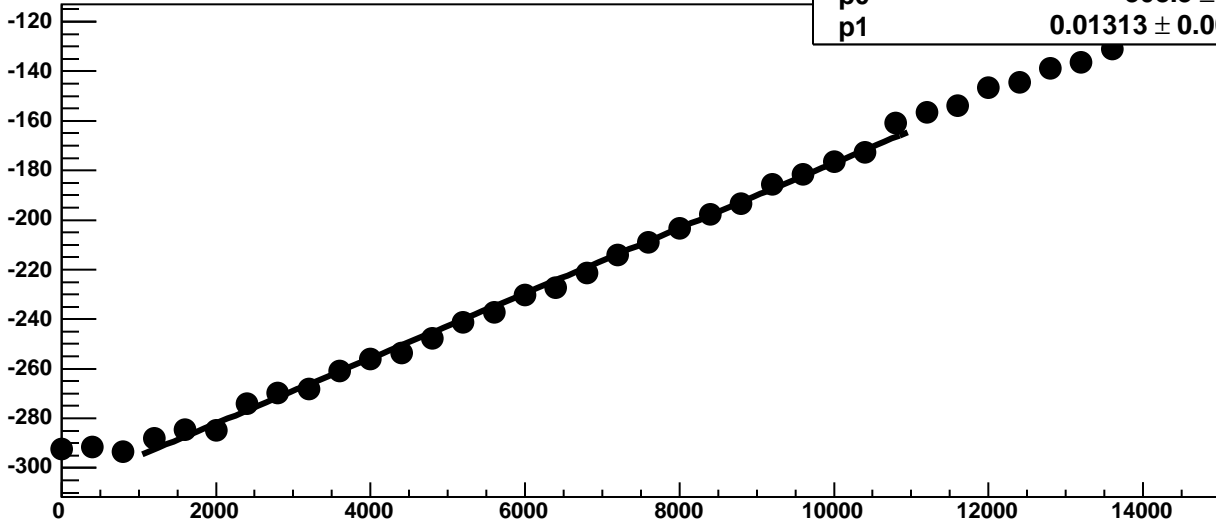
Chip 7, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



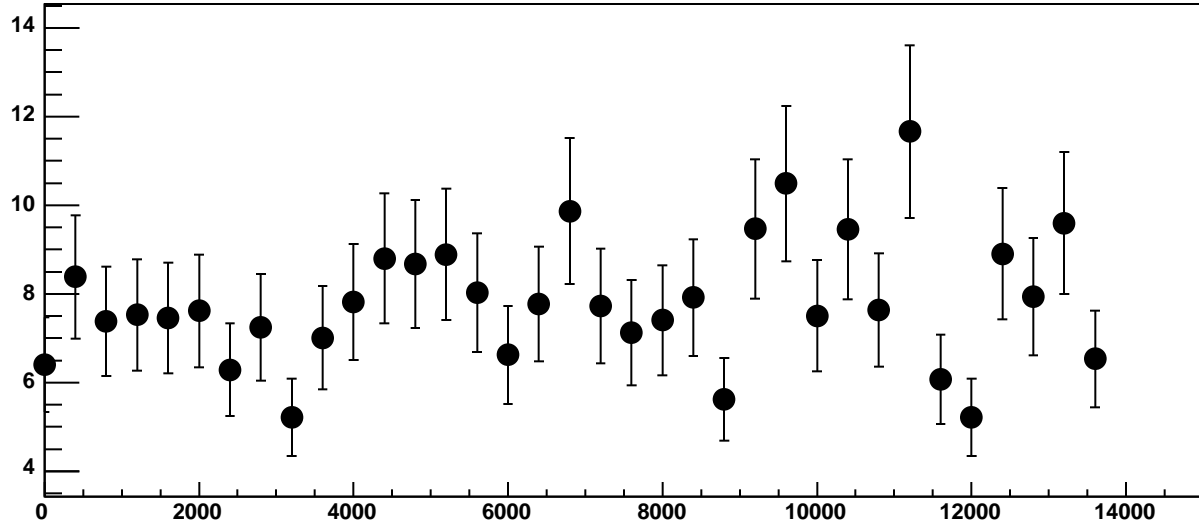
Chip 7, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



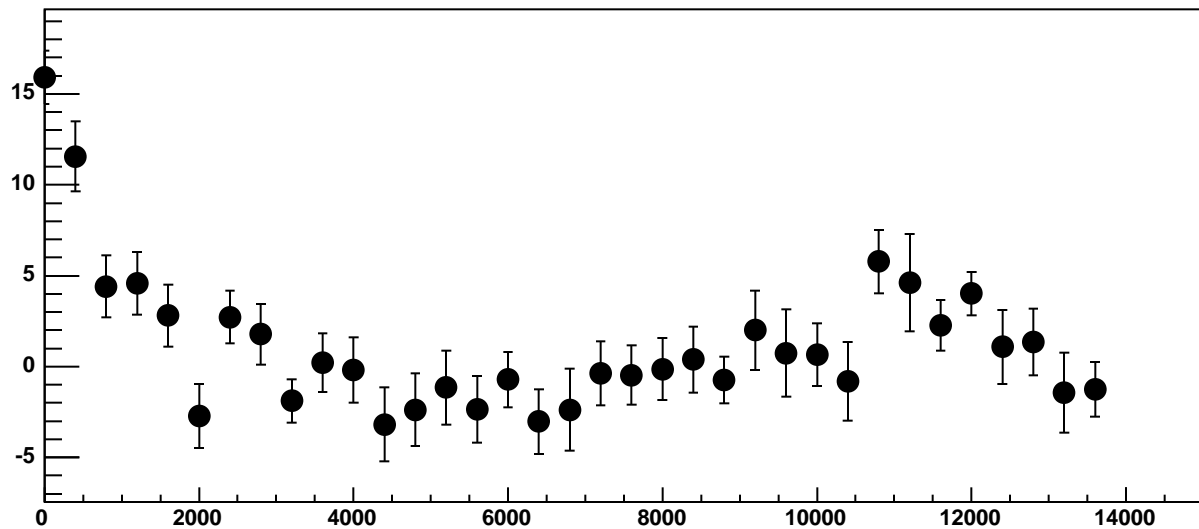
Chip 7, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



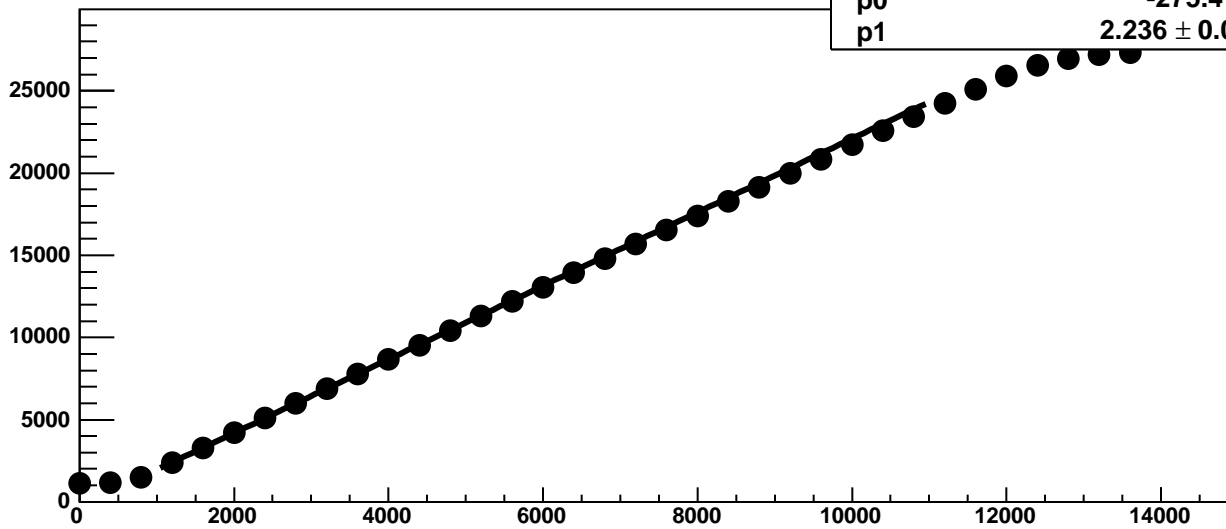
Chip 7, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



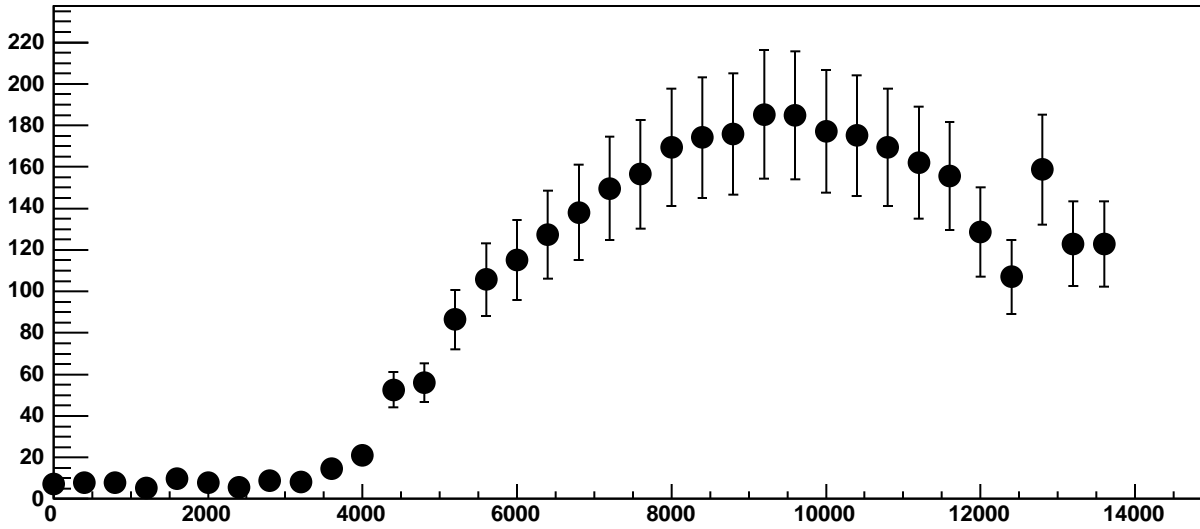
Chip 7, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



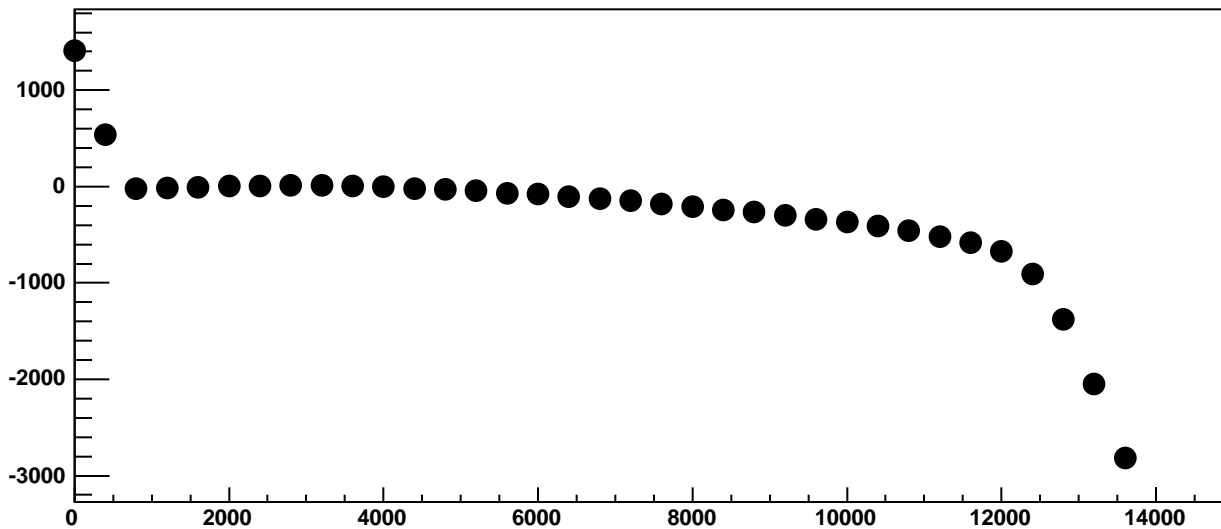
Chip 7, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC



Chip 7, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC

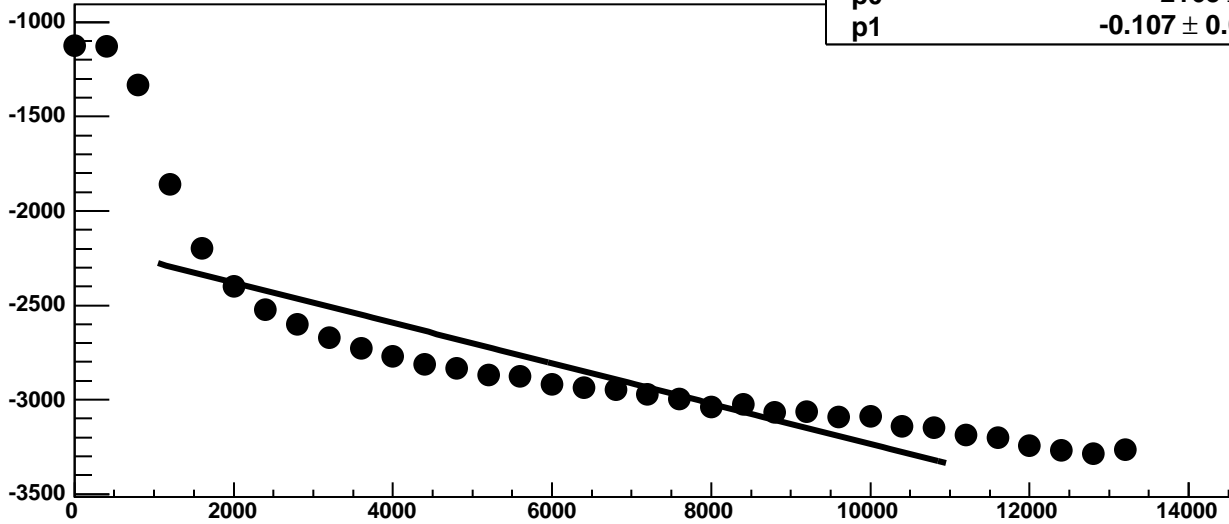


Chip 7, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC

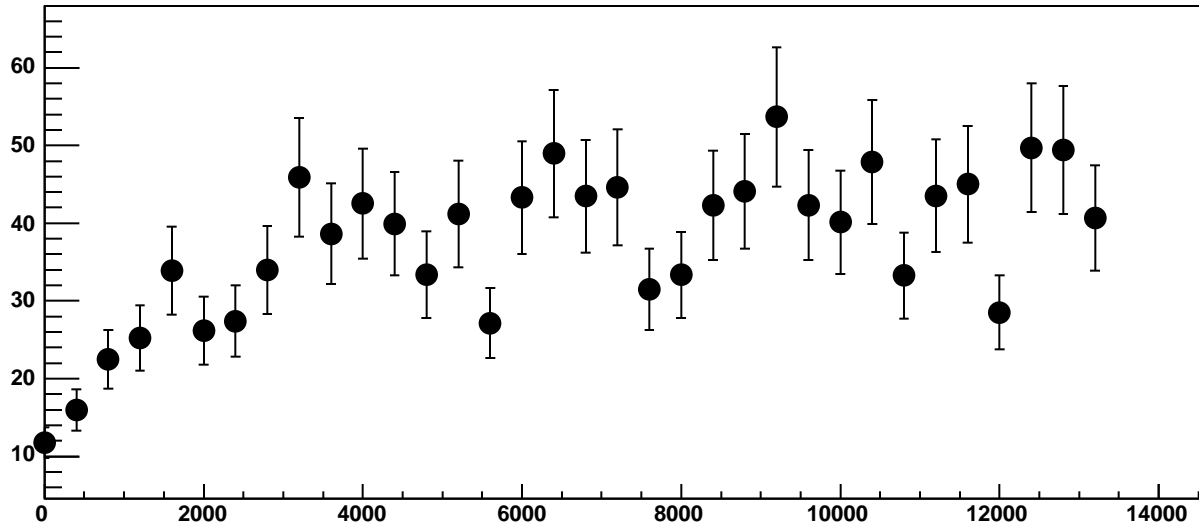


Chip 7, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

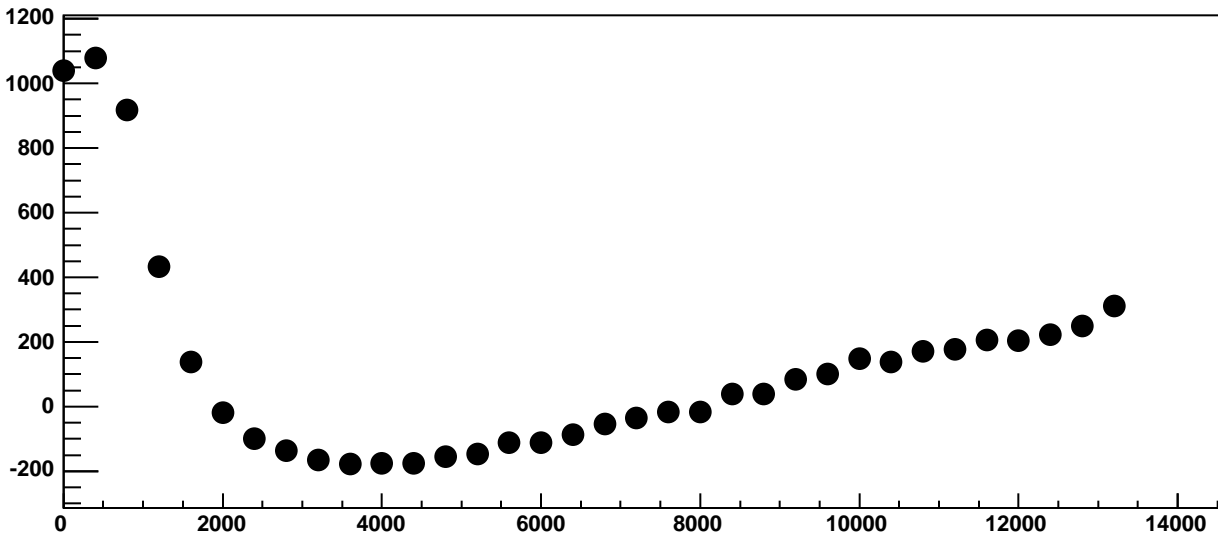
χ^2 / ndf 1.016e+04 / 23
p0 -2165 ± 3.386
p1 -0.107 ± 0.000553



Chip 7, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

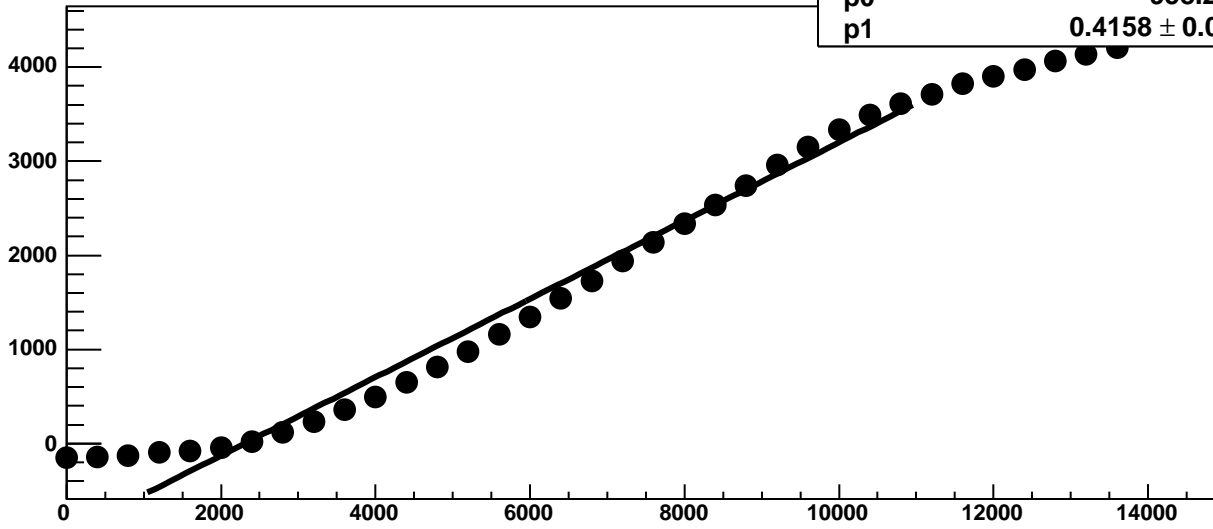


Chip 7, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

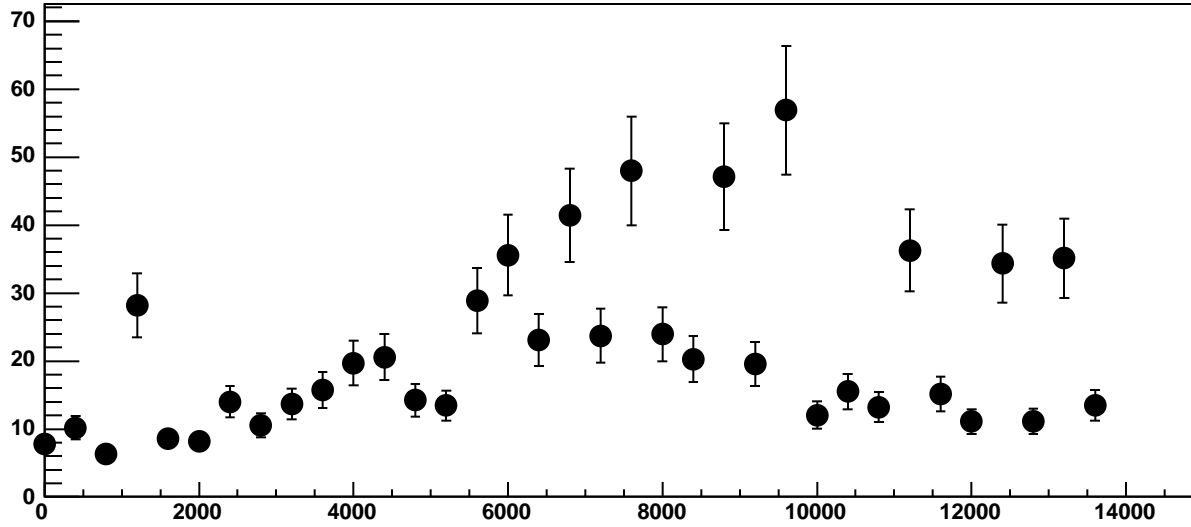


Chip 7, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

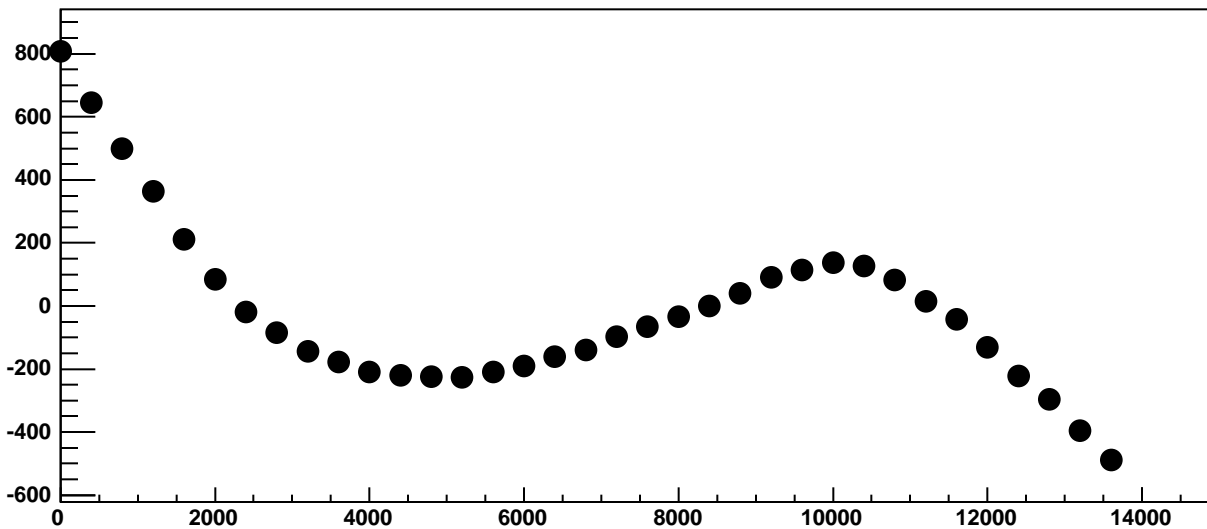
χ^2 / ndf 4.492e+04 / 23
p0 -958.2 ± 1.311
p1 0.4158 ± 0.0002272



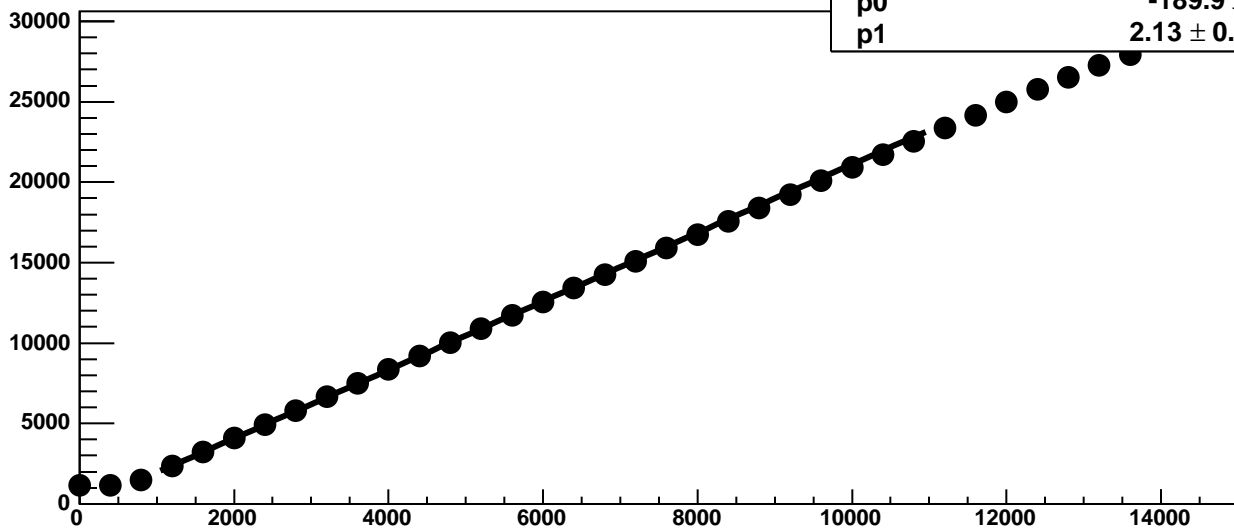
Chip 7, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



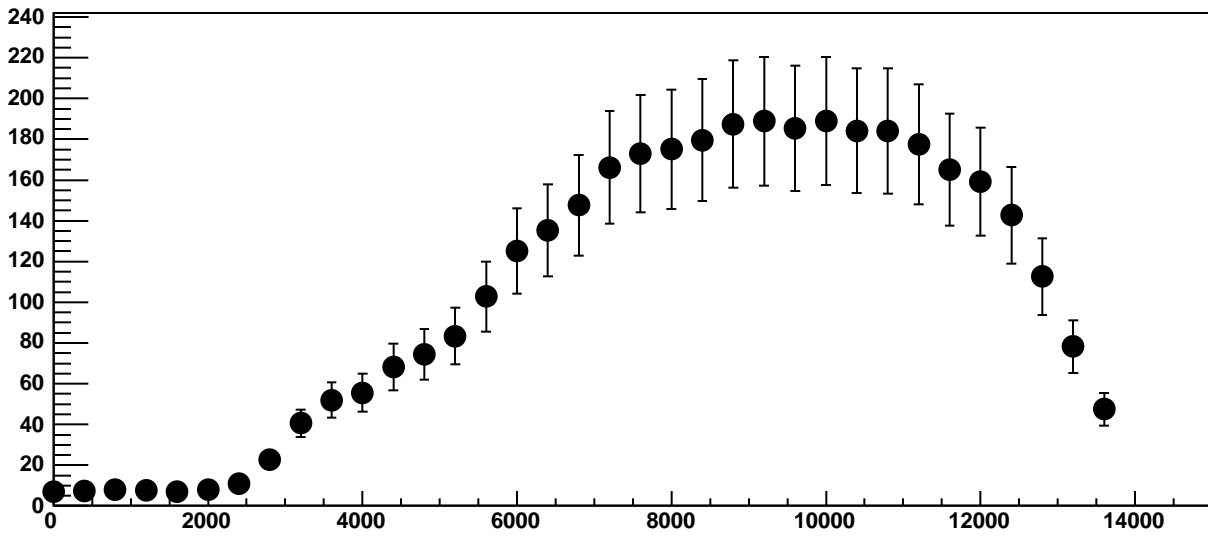
Chip 7, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



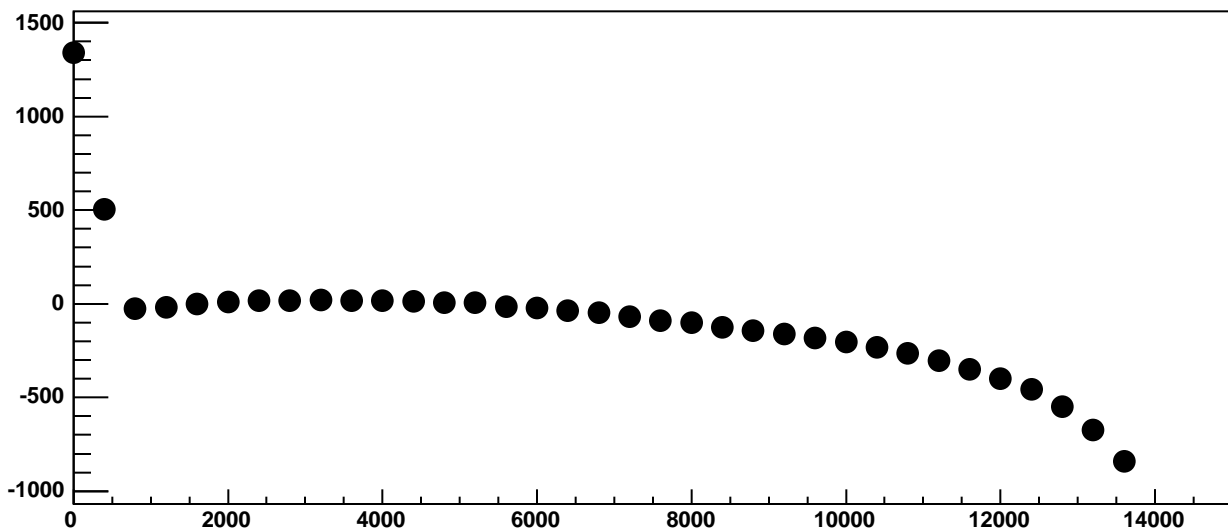
Chip 7, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC



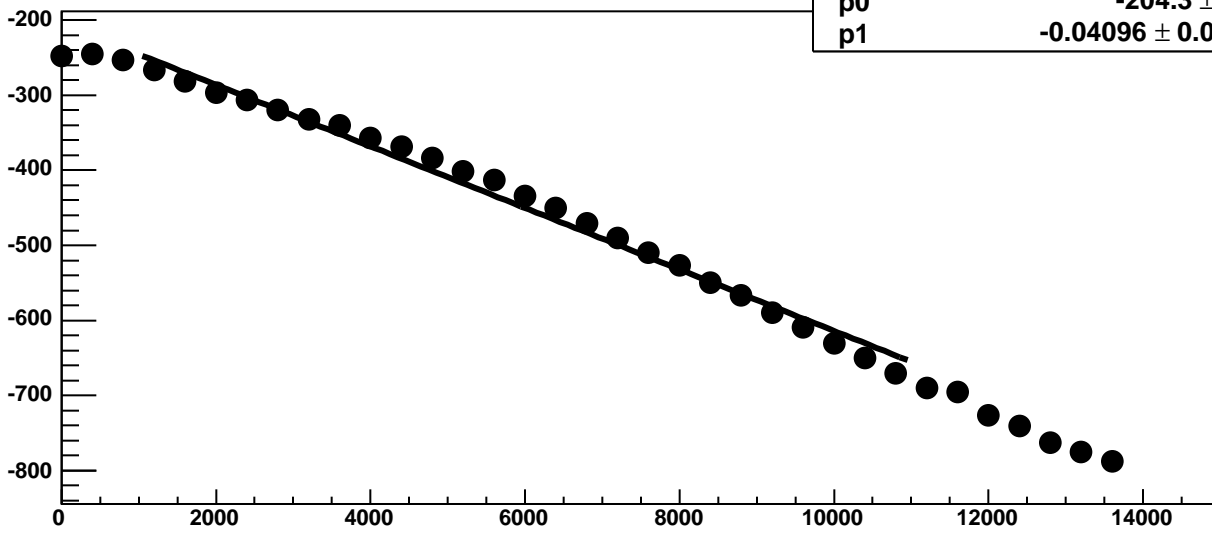
Chip 7, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC

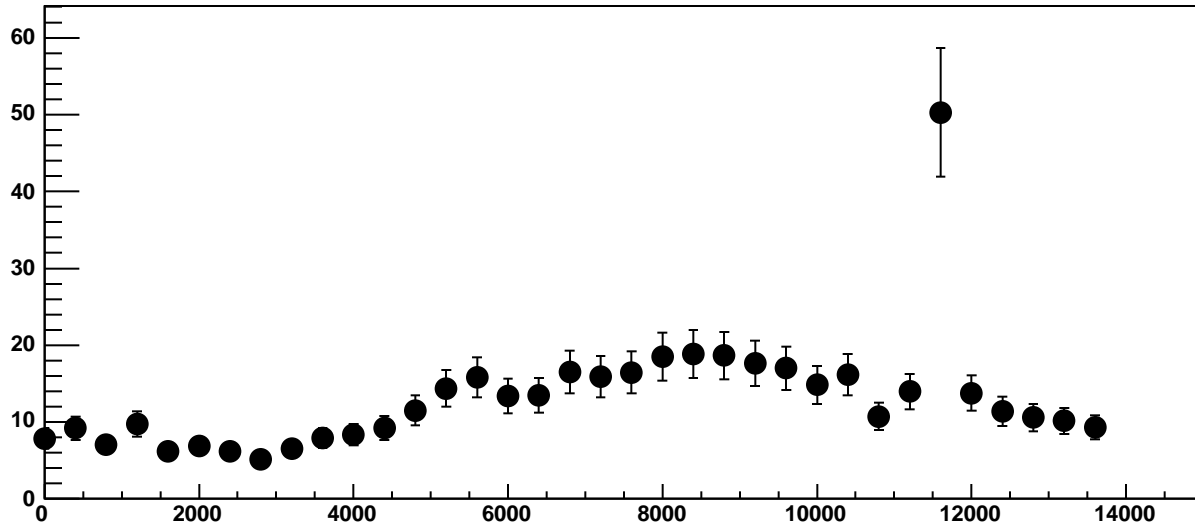


Chip 7, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

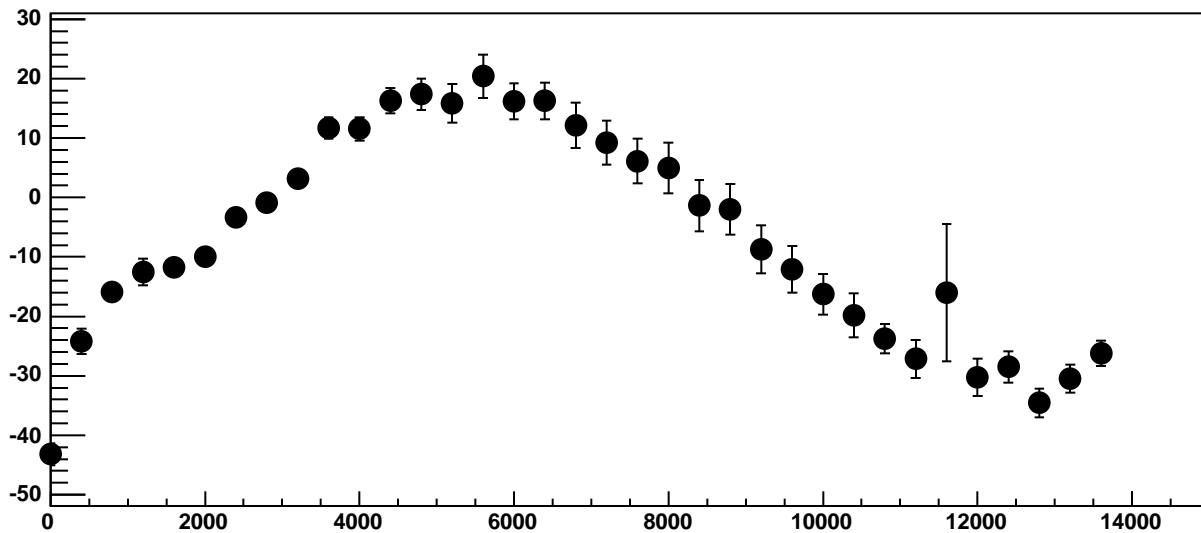


χ^2 / ndf 620.3 / 23
p0 -204.3 ± 0.8433
p1 -0.04096 ± 0.0001743

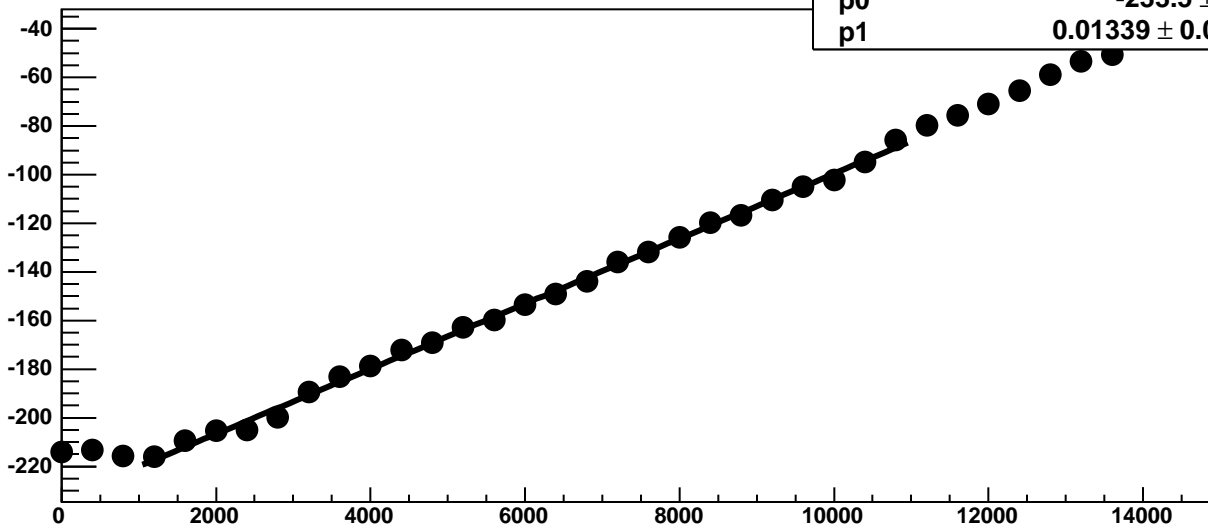
Chip 7, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

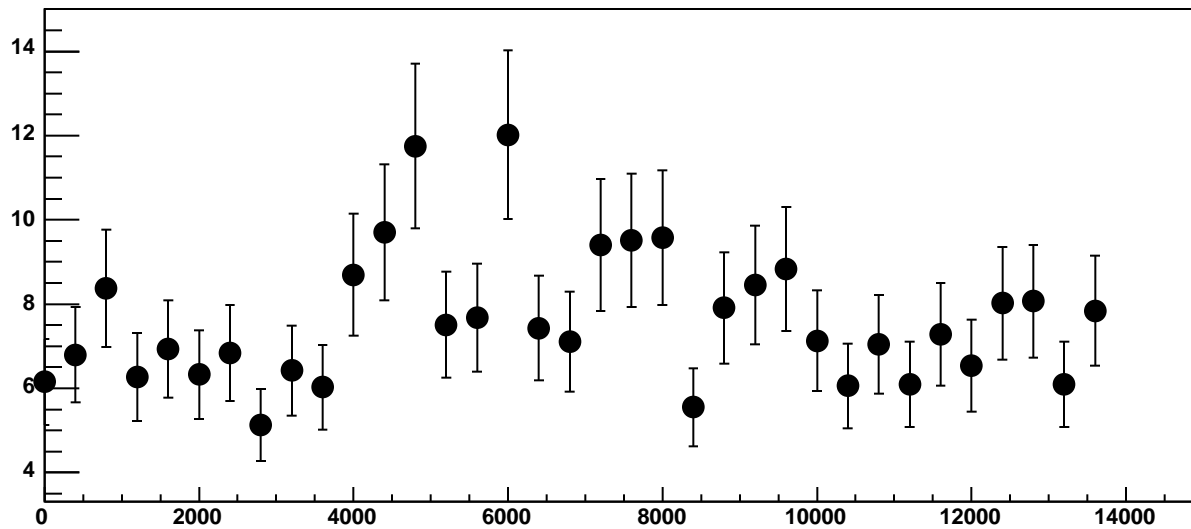


Chip 7, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

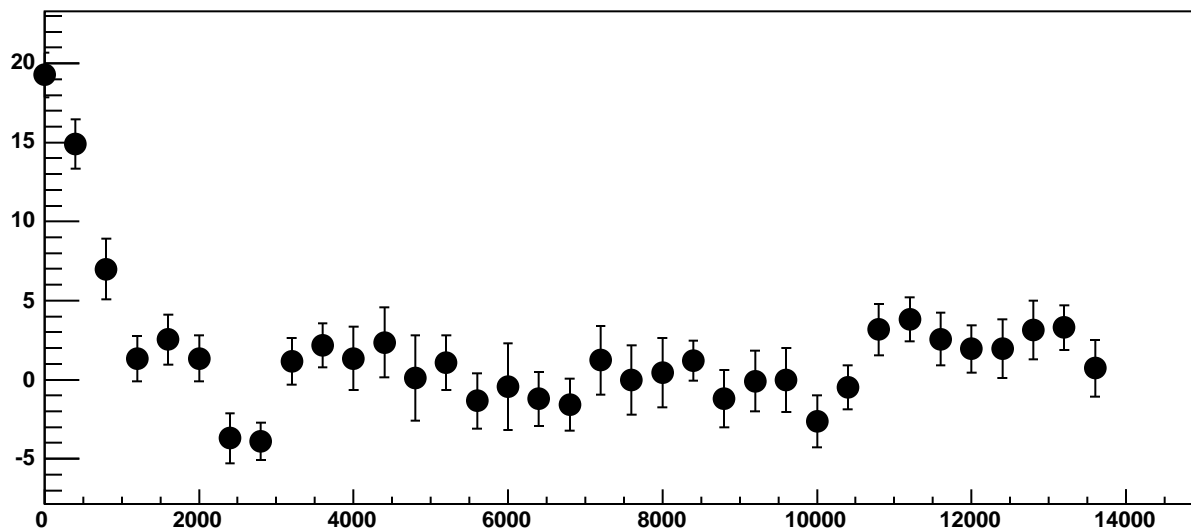


χ^2 / ndf 36.18 / 23
p0 -233.5 ± 0.7027
p1 0.01339 ± 0.0001081

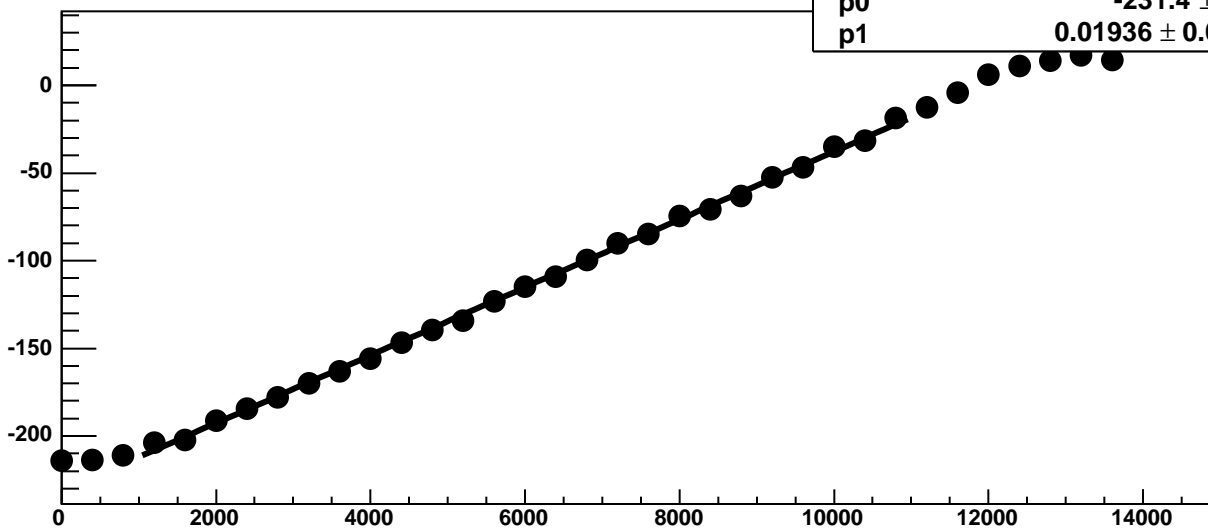
Chip 7, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

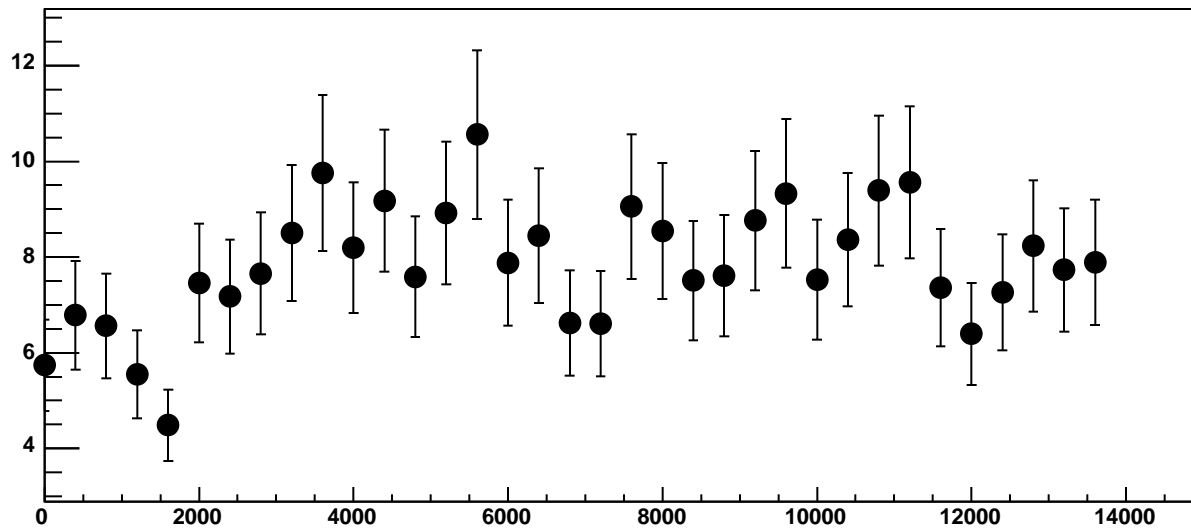


Chip 7, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC

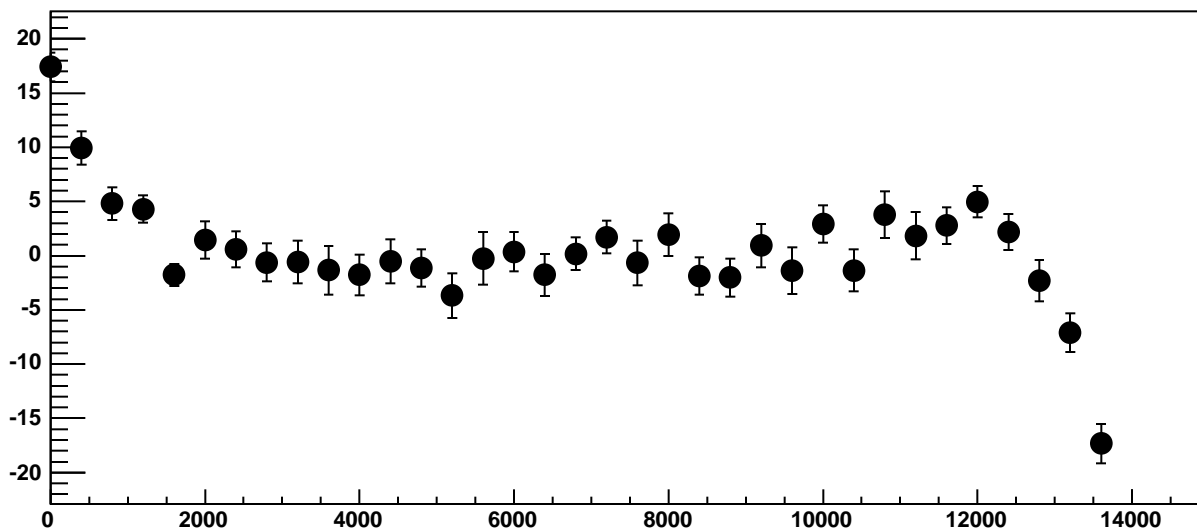


χ^2 / ndf 33.2 / 23
p0 -231.4 ± 0.7085
p1 0.01936 ± 0.0001141

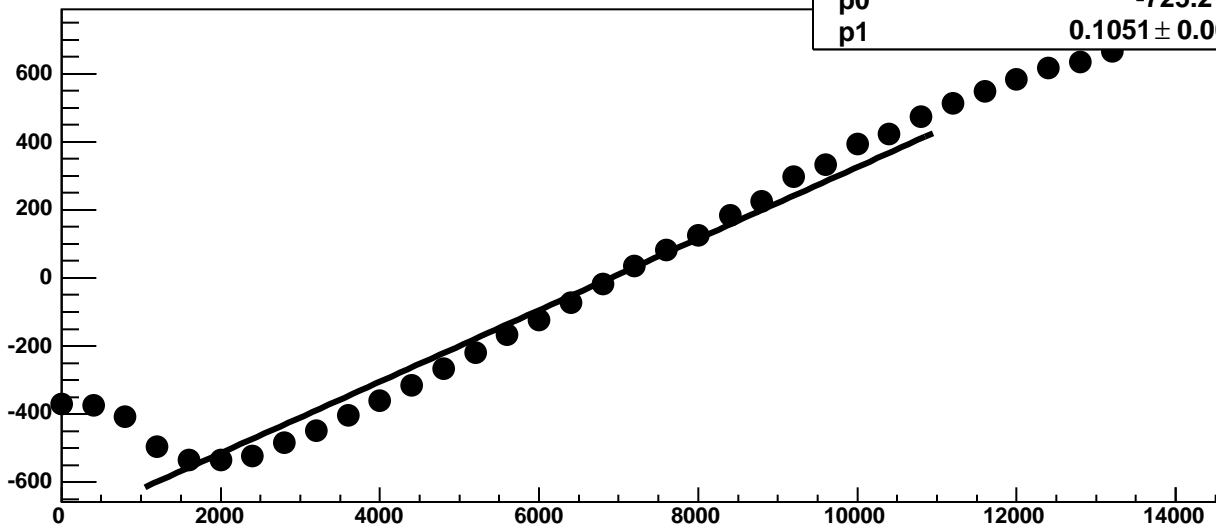
Chip 7, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



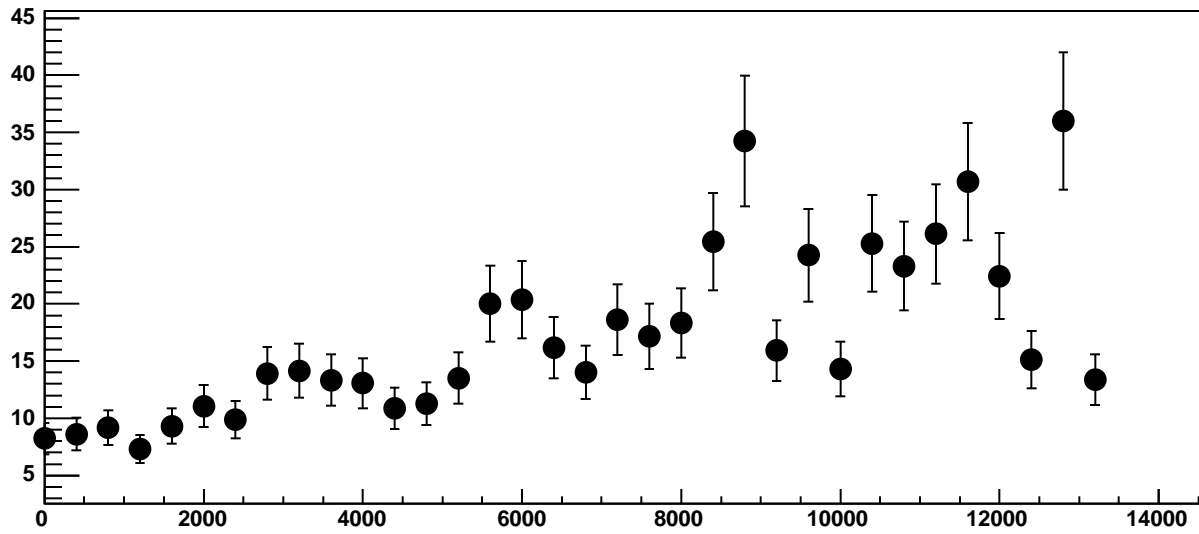
Chip 7, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC



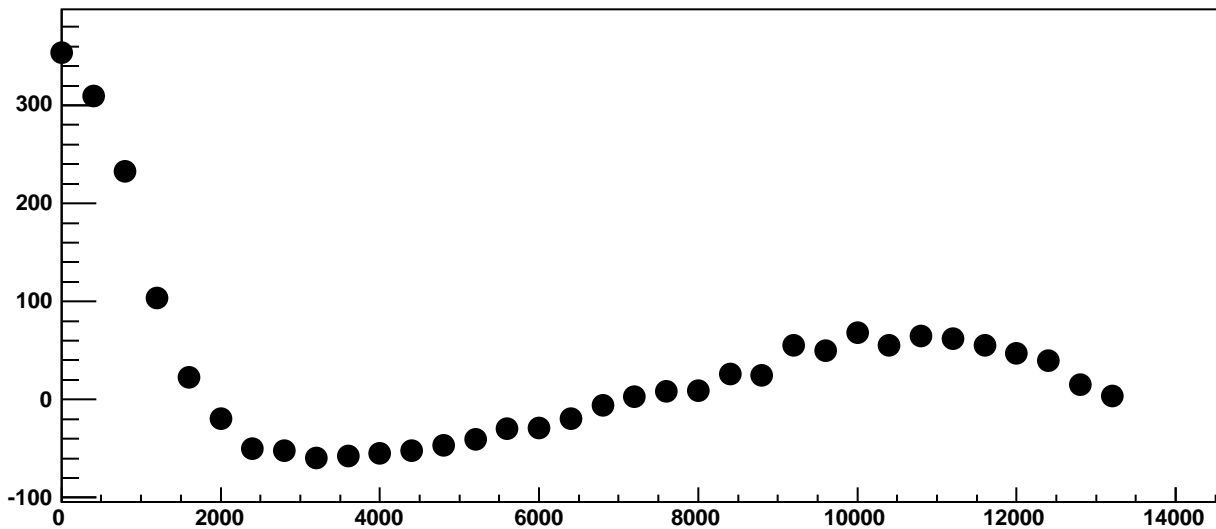
Chip 7, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



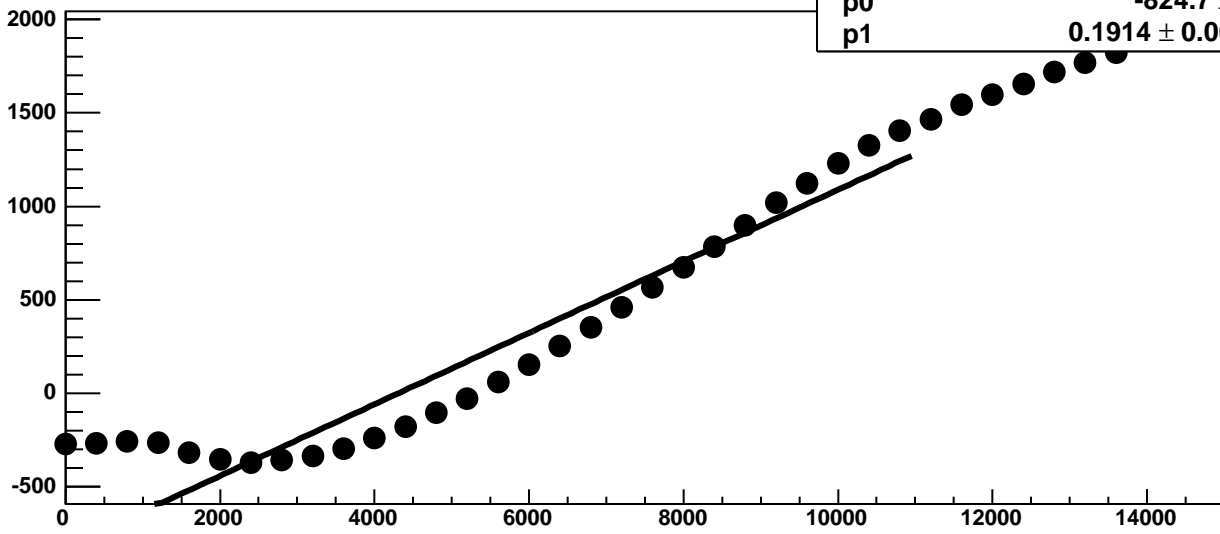
Chip 7, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



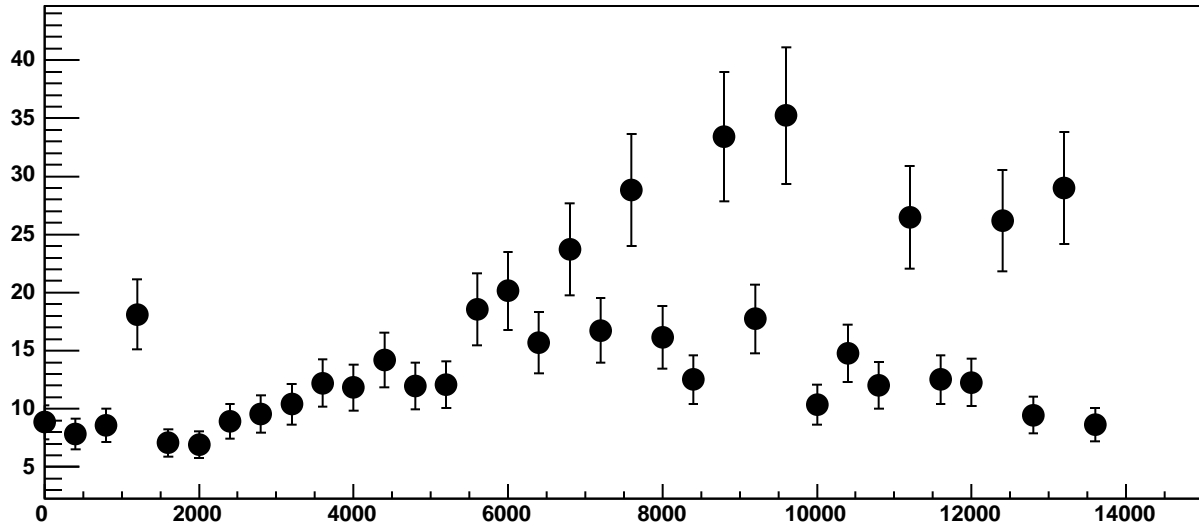
Chip 7, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



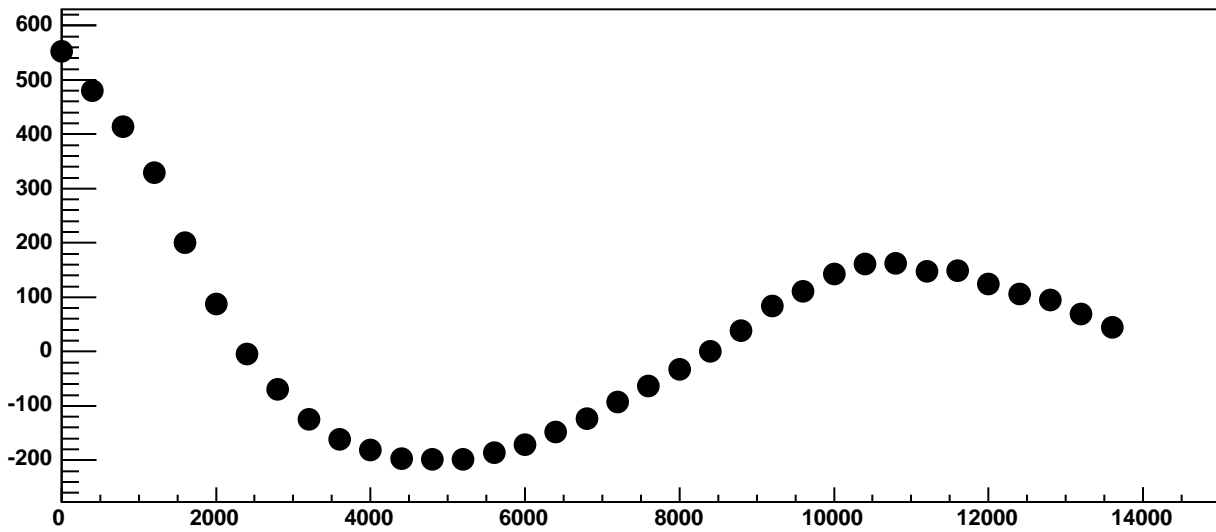
Chip 7, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



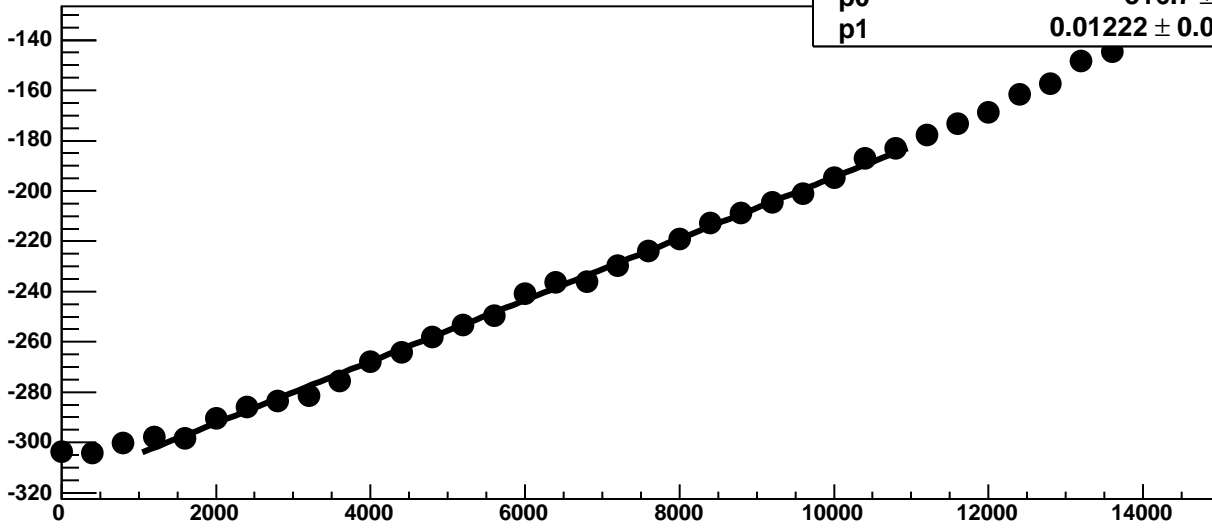
Chip 7, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



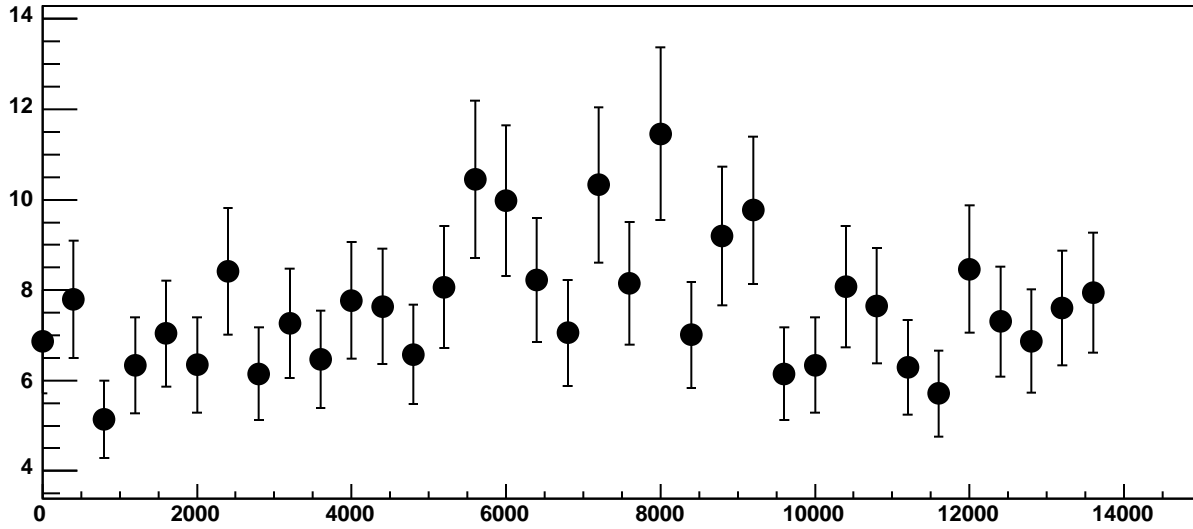
Chip 7, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



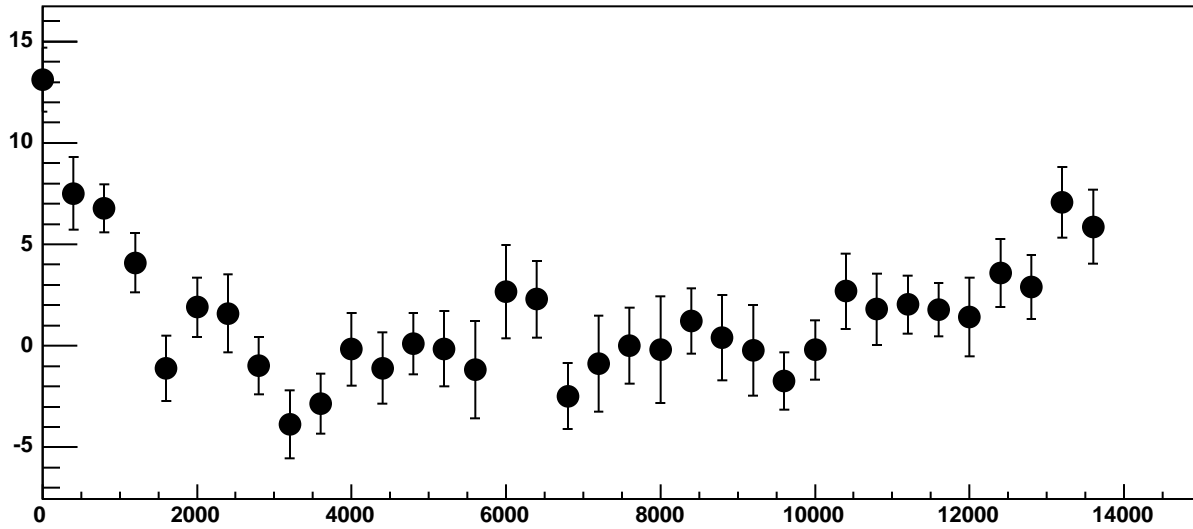
Chip 7, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC



Chip 7, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC

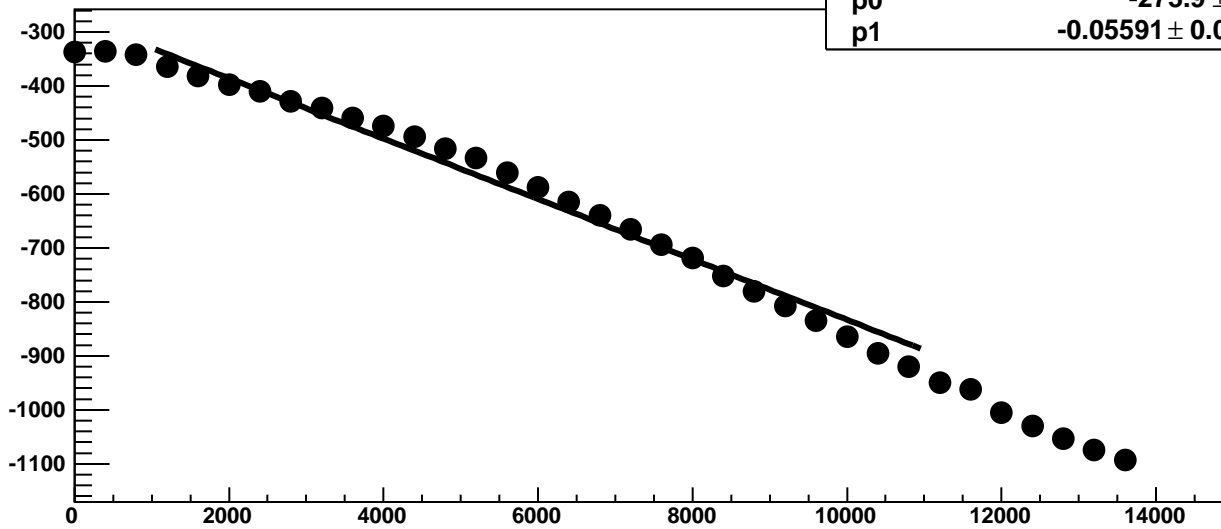


Chip 7, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC

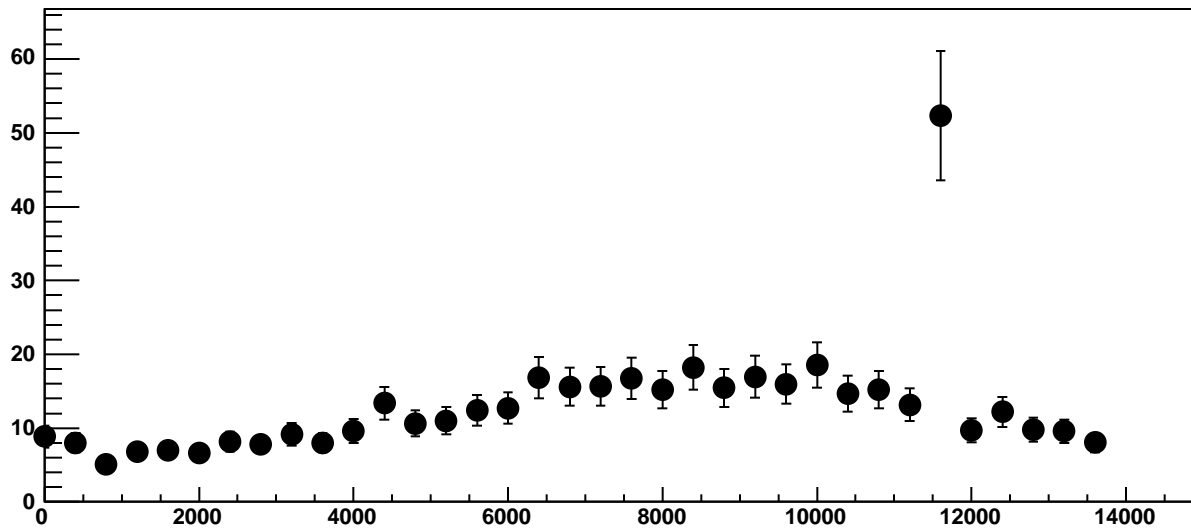


Chip 7, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

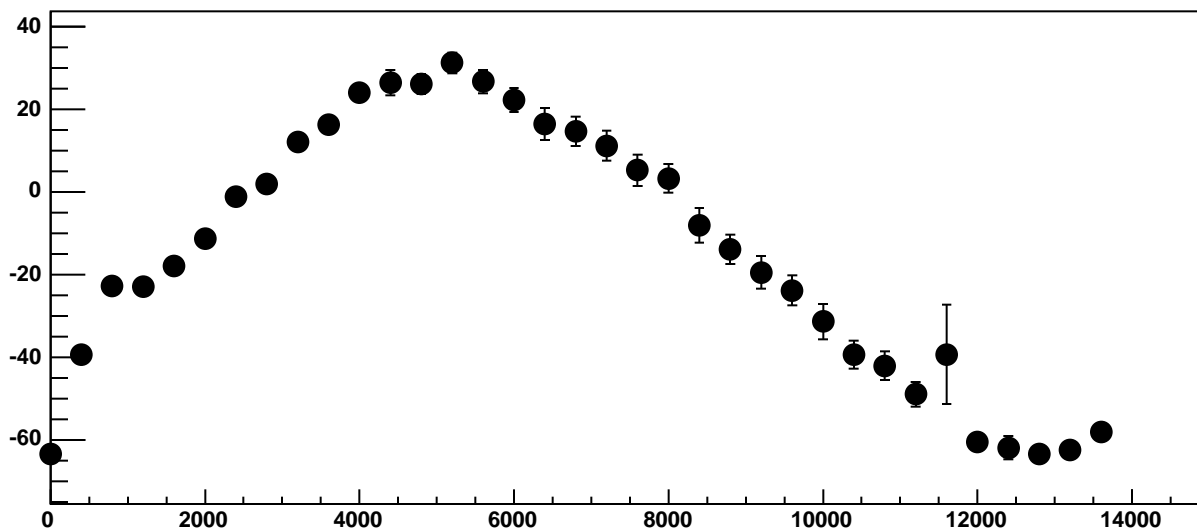
χ^2 / ndf 1580 / 23
p0 -273.9 ± 0.9126
p1 -0.05591 ± 0.0001829



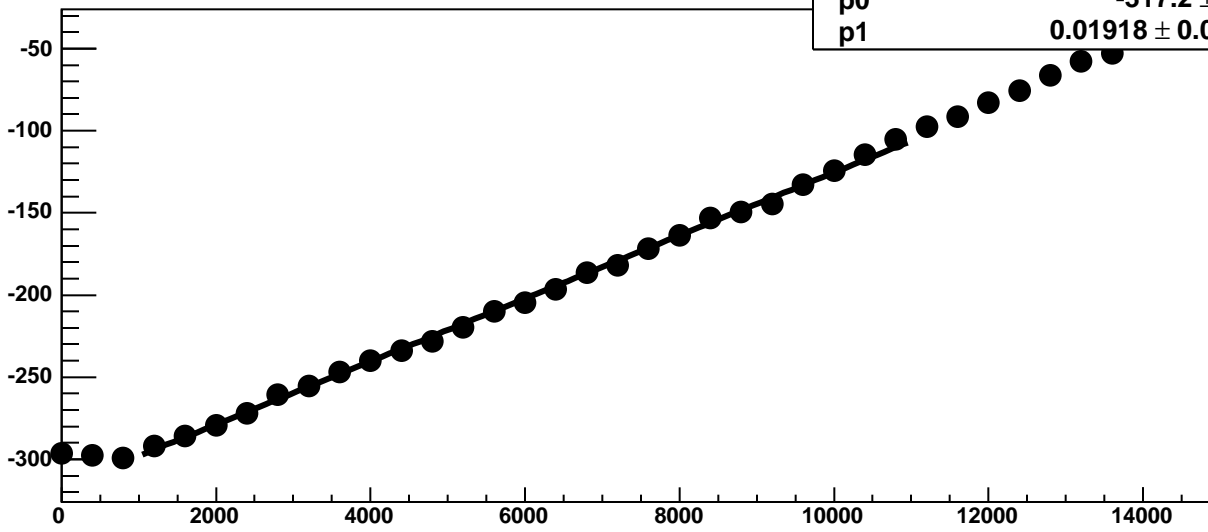
Chip 7, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

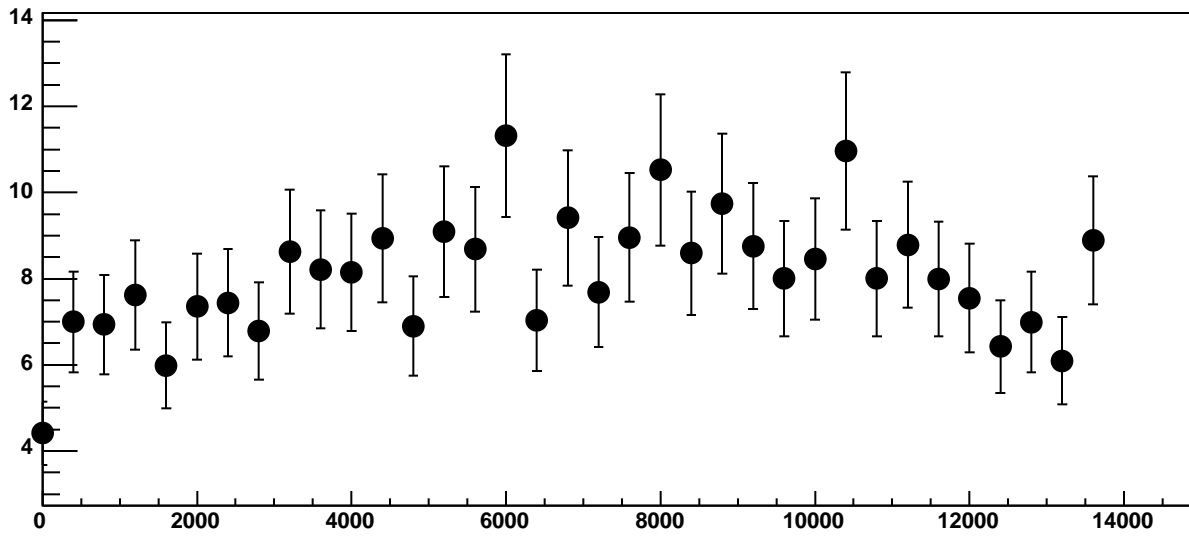


Chip 7, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

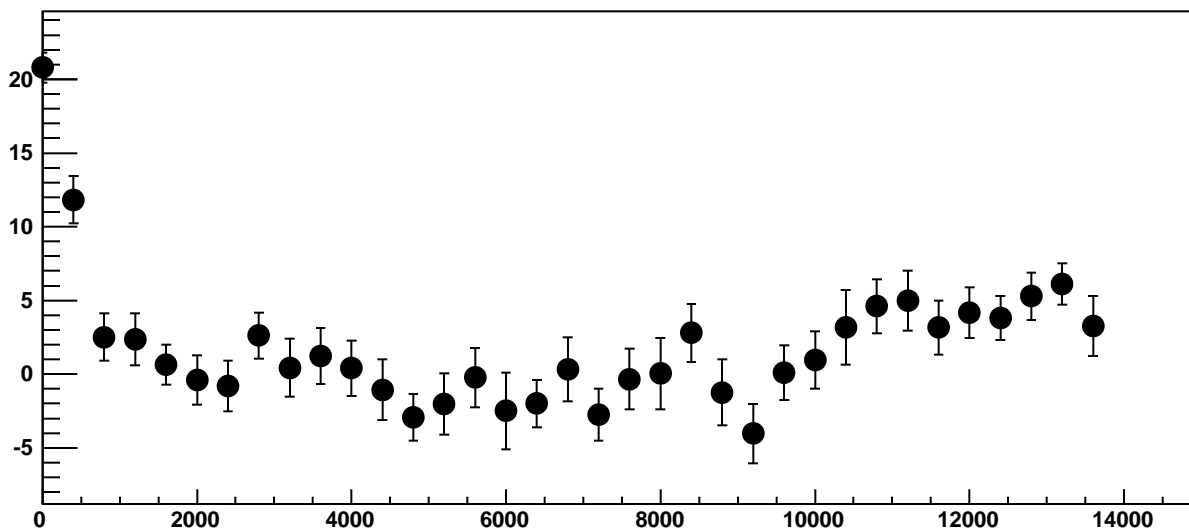


χ^2 / ndf 29.82 / 23
p0 -317.2 ± 0.7954
p1 0.01918 ± 0.0001268

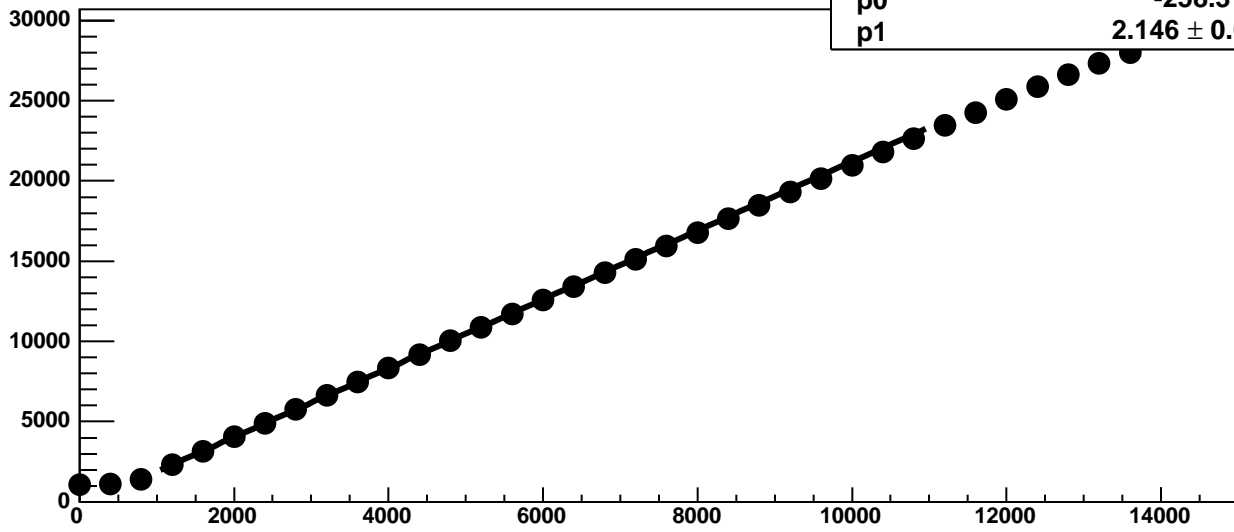
Chip 7, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

372.4 / 23

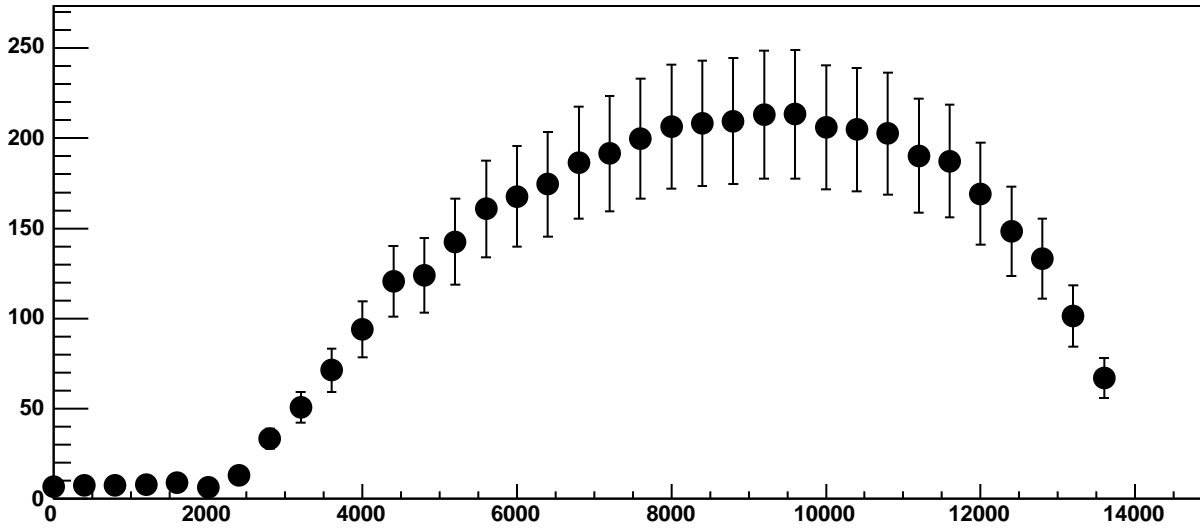
p0

-258.3 ± 2.641

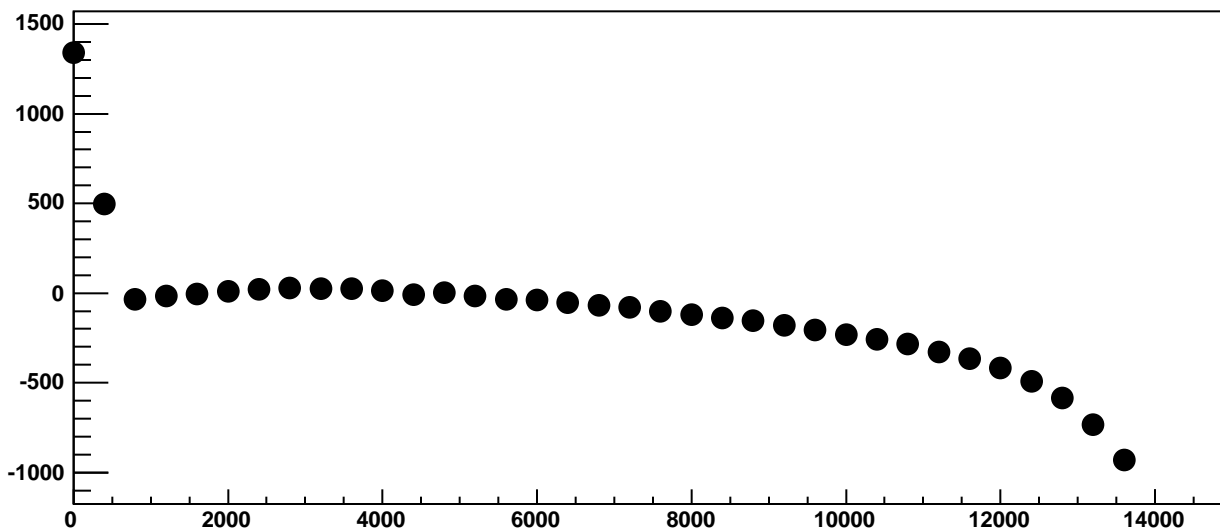
p1

2.146 ± 0.001355

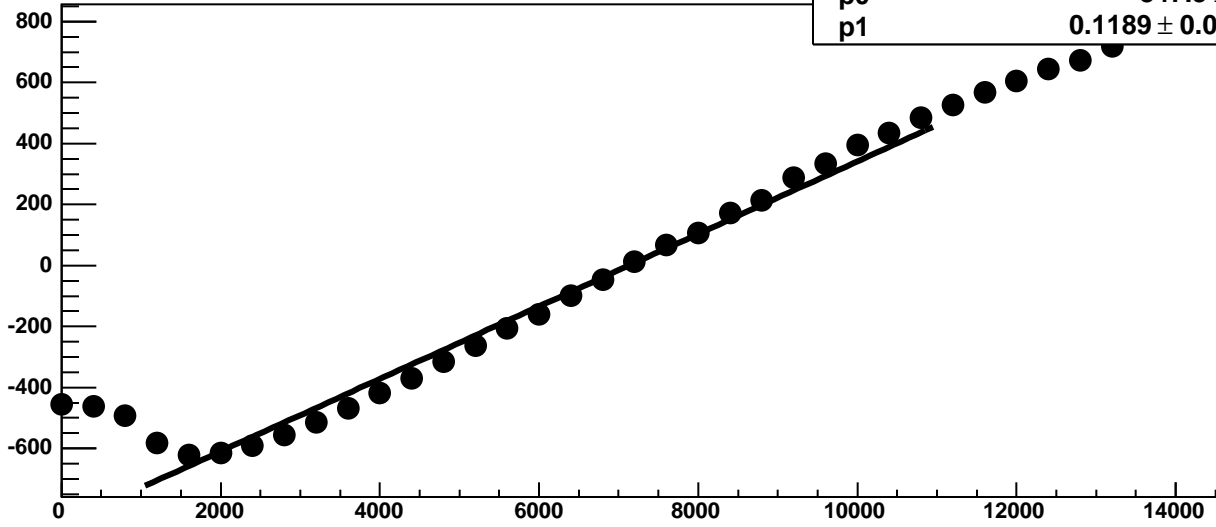
Chip 7, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

5804 / 23

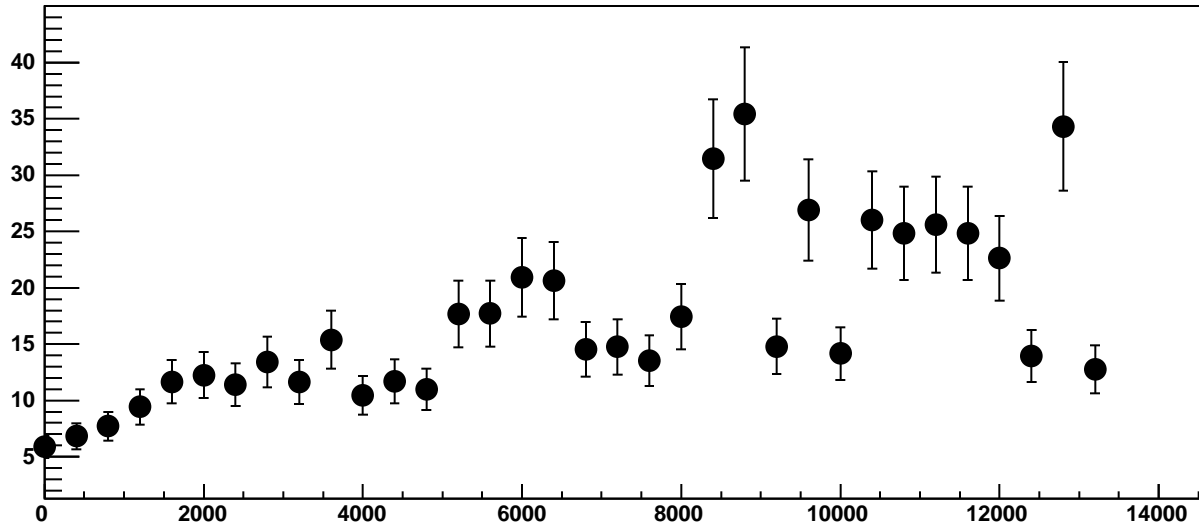
p0

-847.3 ± 1.333

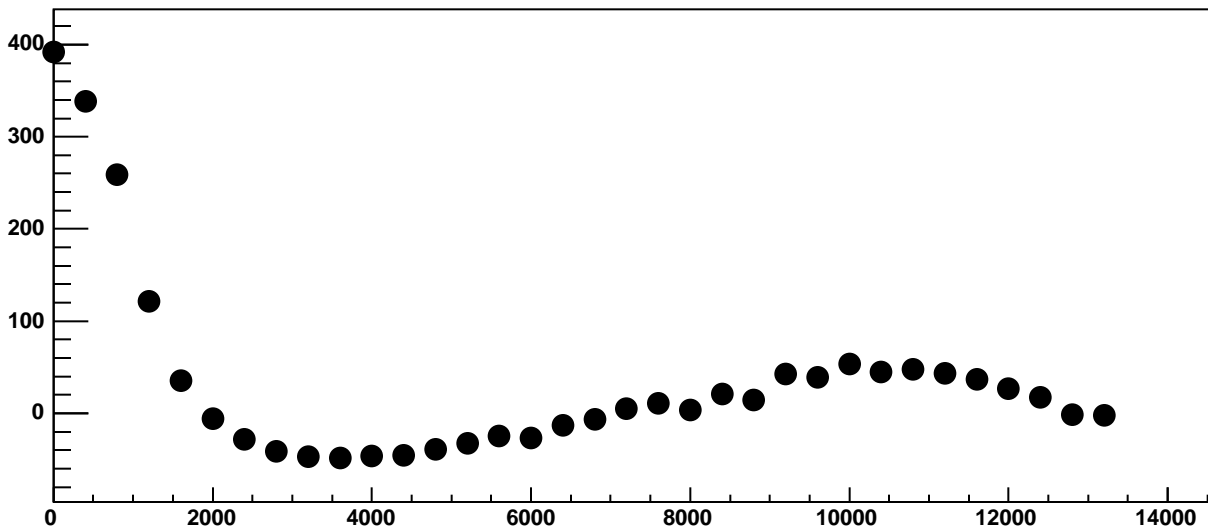
p1

0.1189 ± 0.0002431

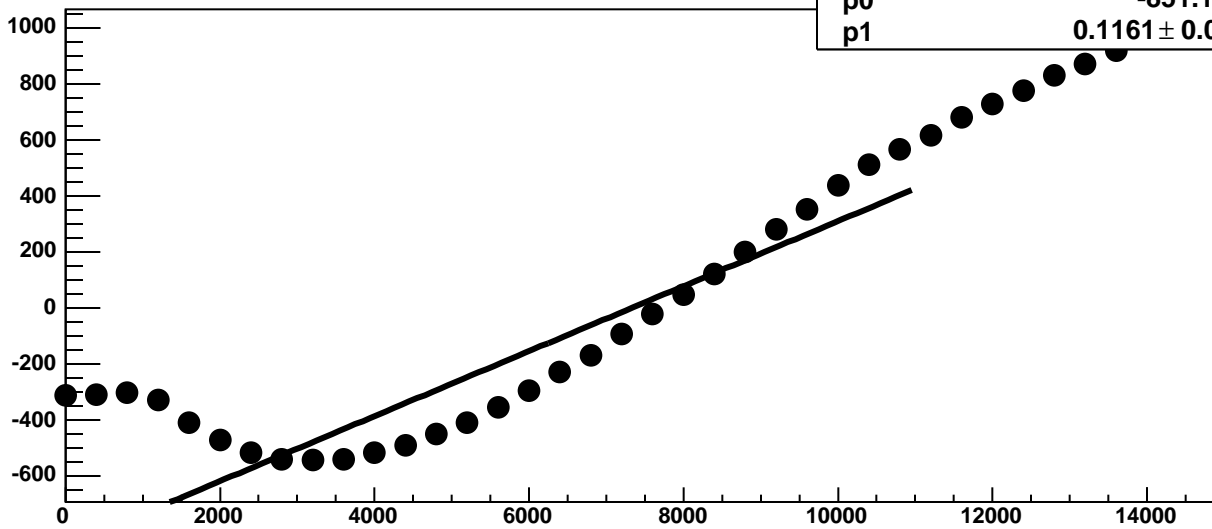
Chip 7, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



Chip 7, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

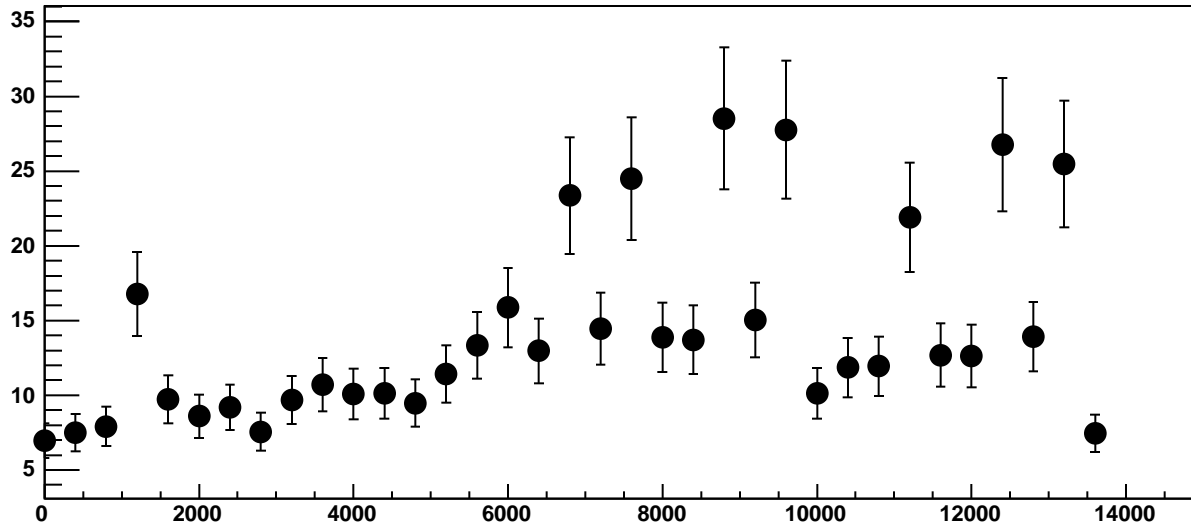


Chip 7, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

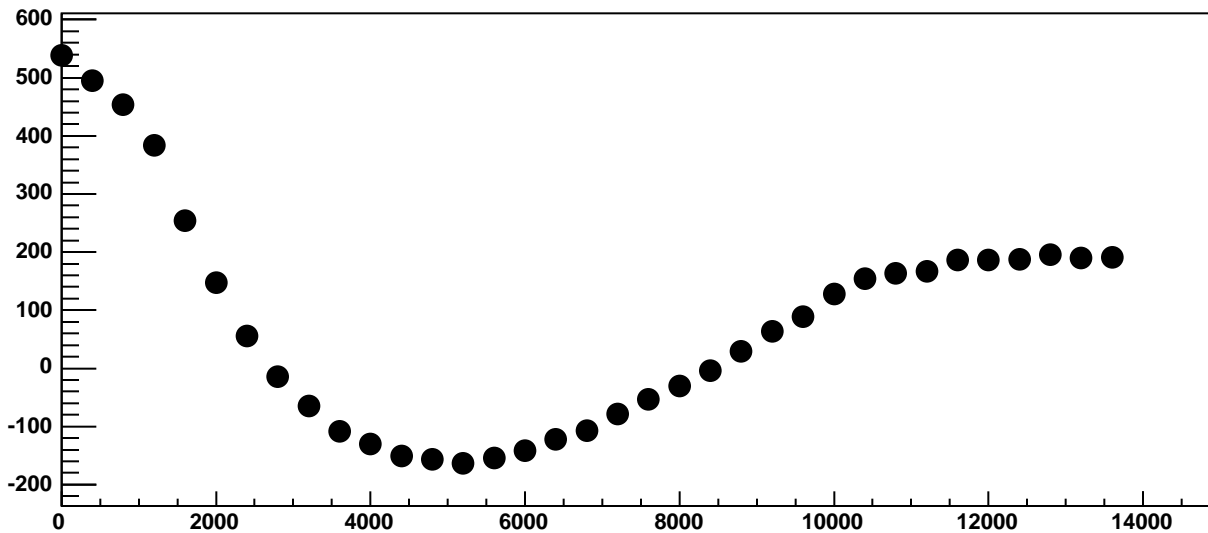


χ^2 / ndf 6.586e+04 / 23
p0 -851.1 ± 1.097
p1 0.1161 ± 0.0001873

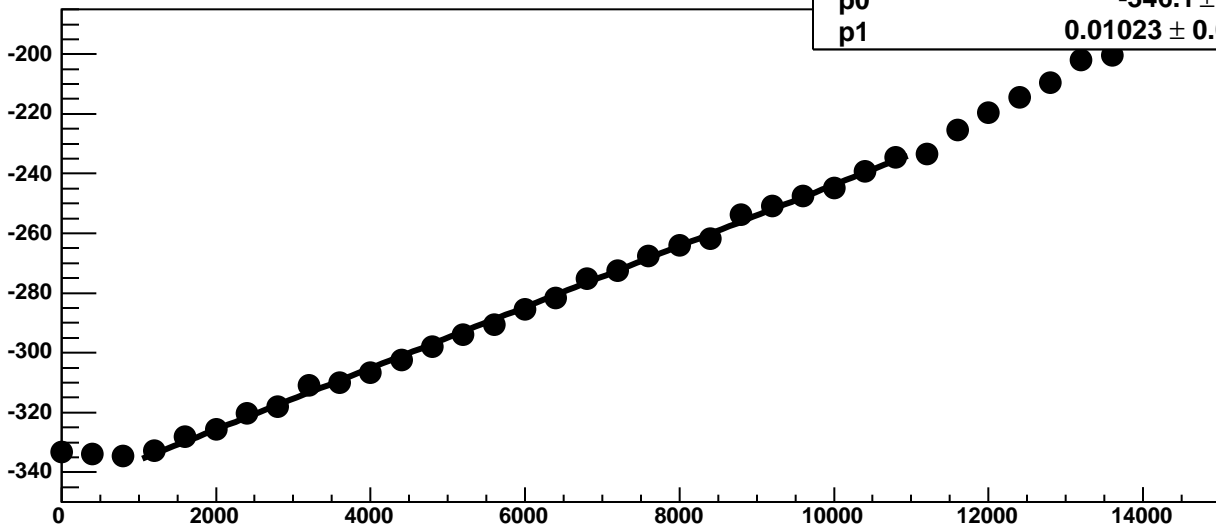
Chip 7, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

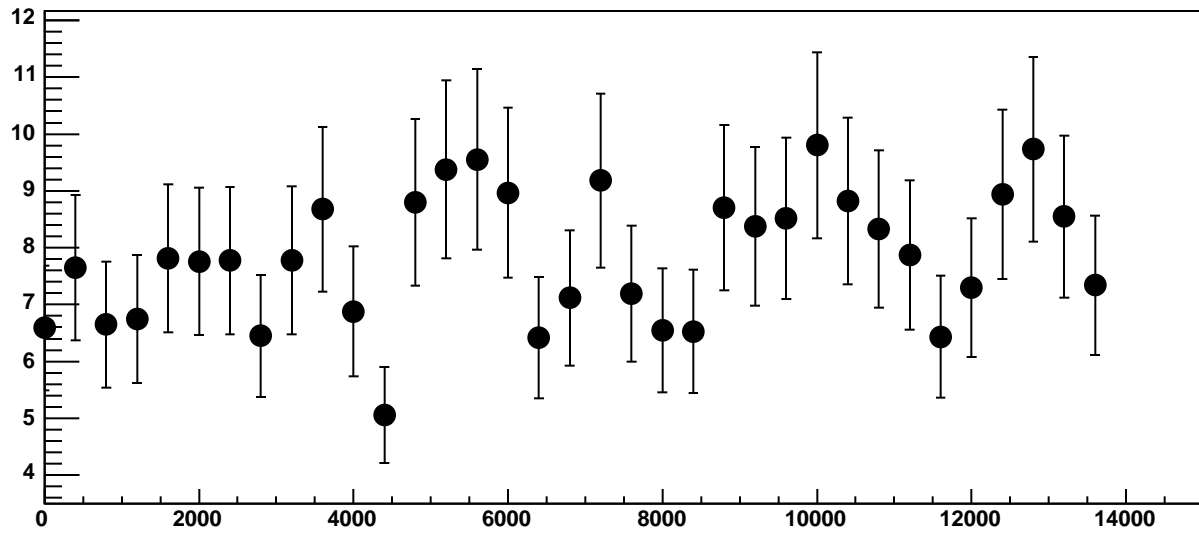


Chip 7, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

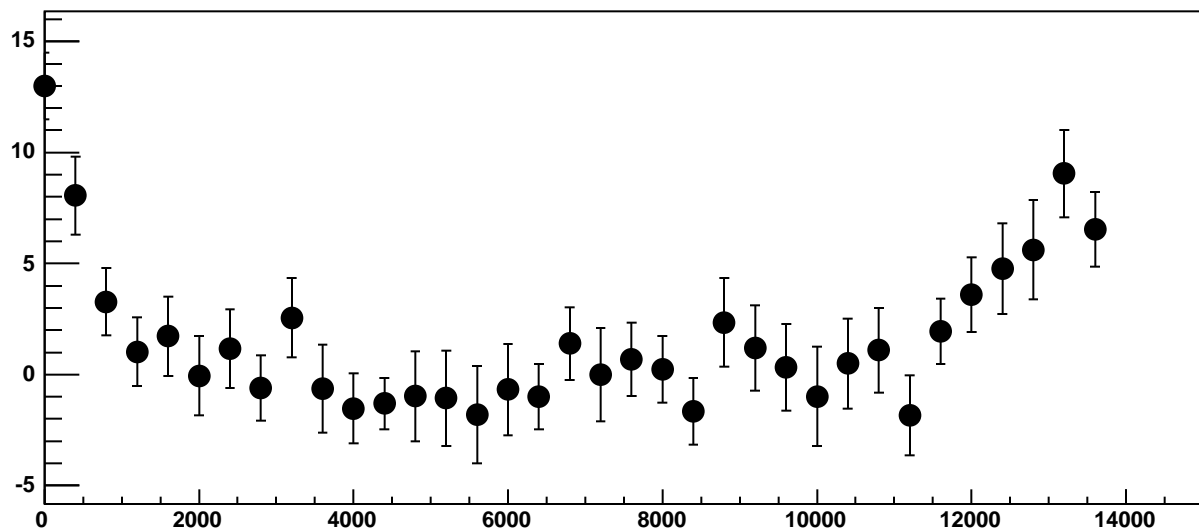


χ^2 / ndf 12.56 / 23
p0 -346.1 ± 0.7908
p1 0.01023 ± 0.000124

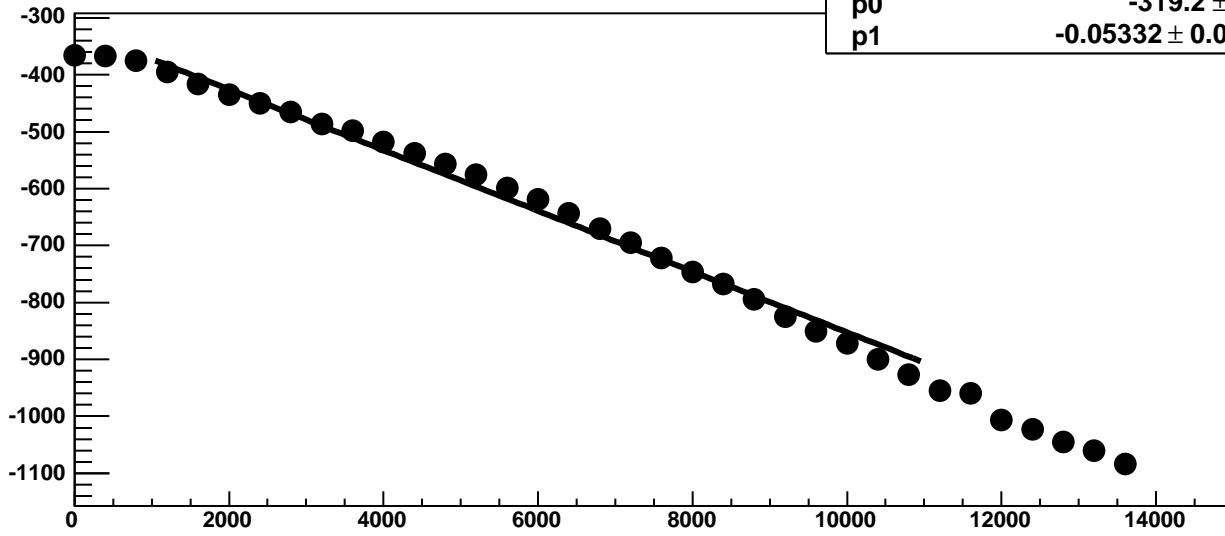
Chip 7, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

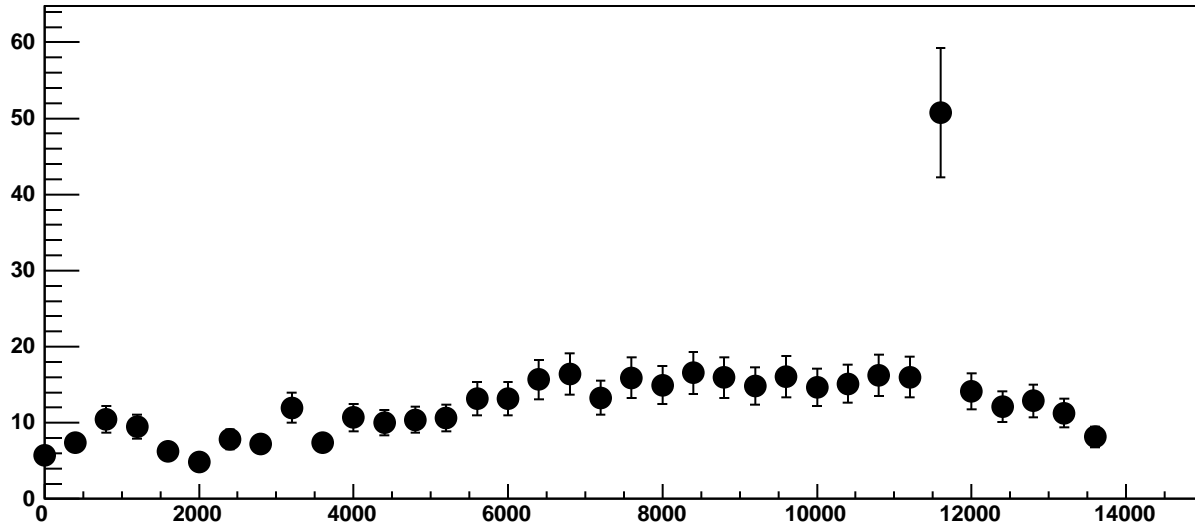


Chip 7, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

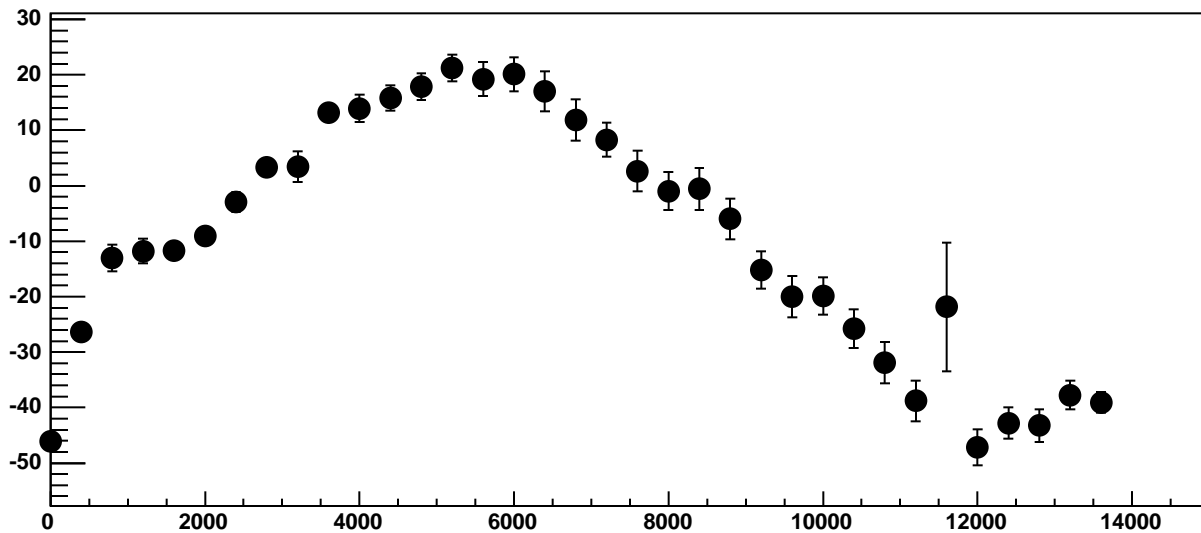


χ^2 / ndf 781.4 / 23
p0 -319.2 ± 0.8717
p1 -0.05332 ± 0.0001766

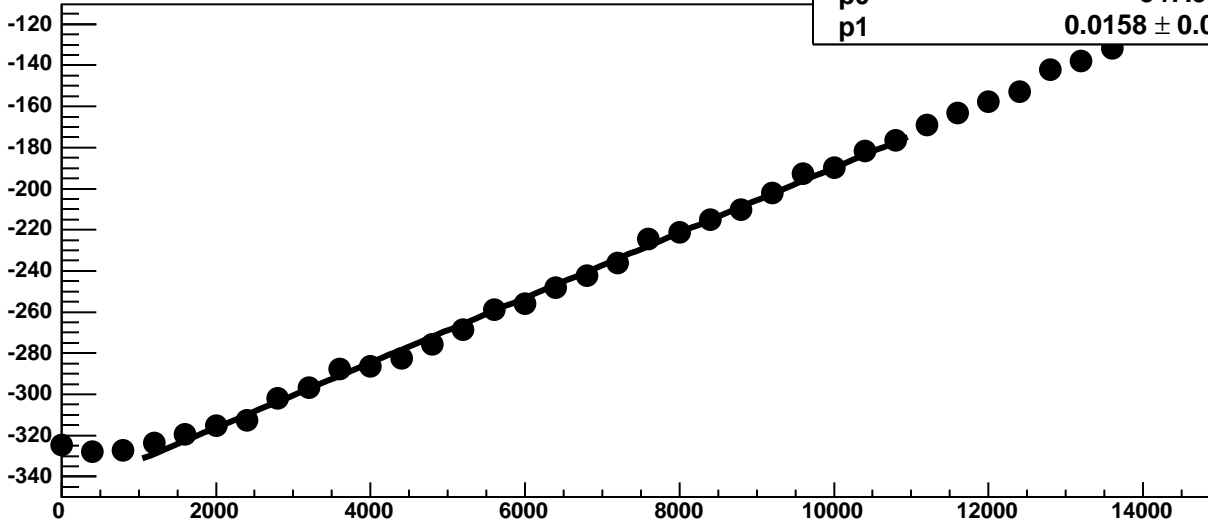
Chip 7, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



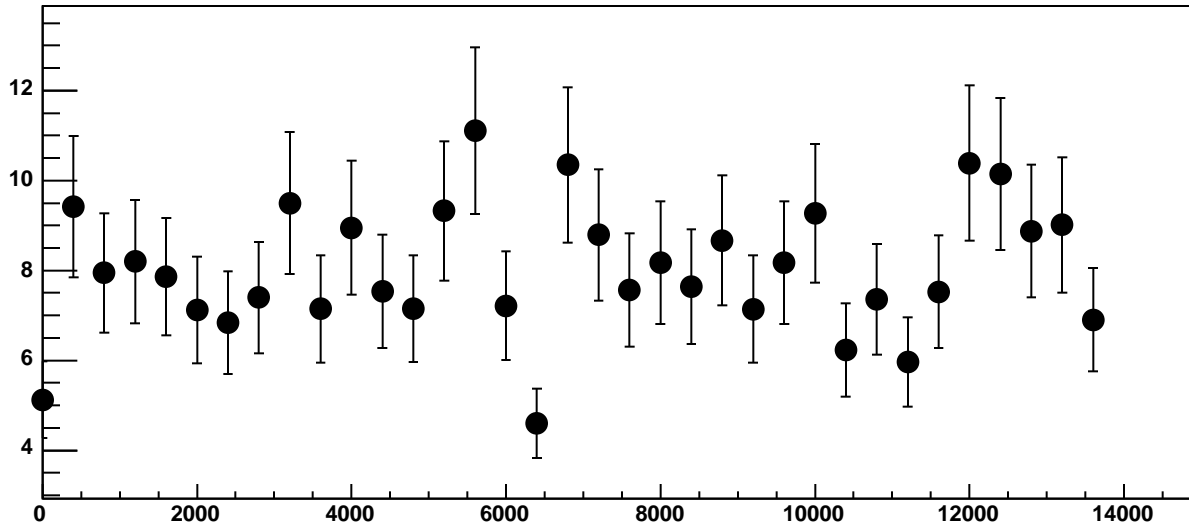
Chip 7, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC



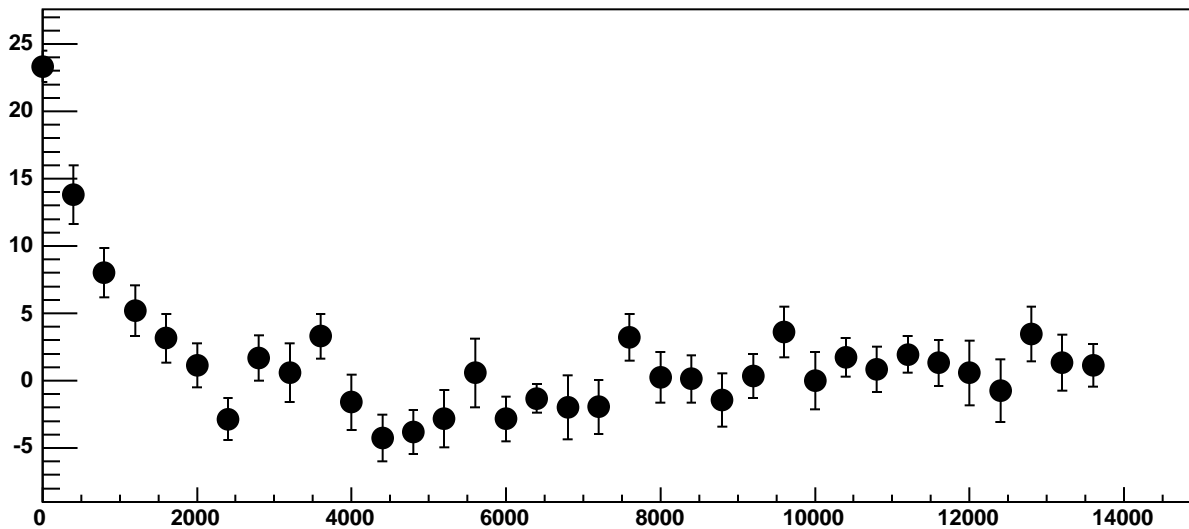
Chip 7, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



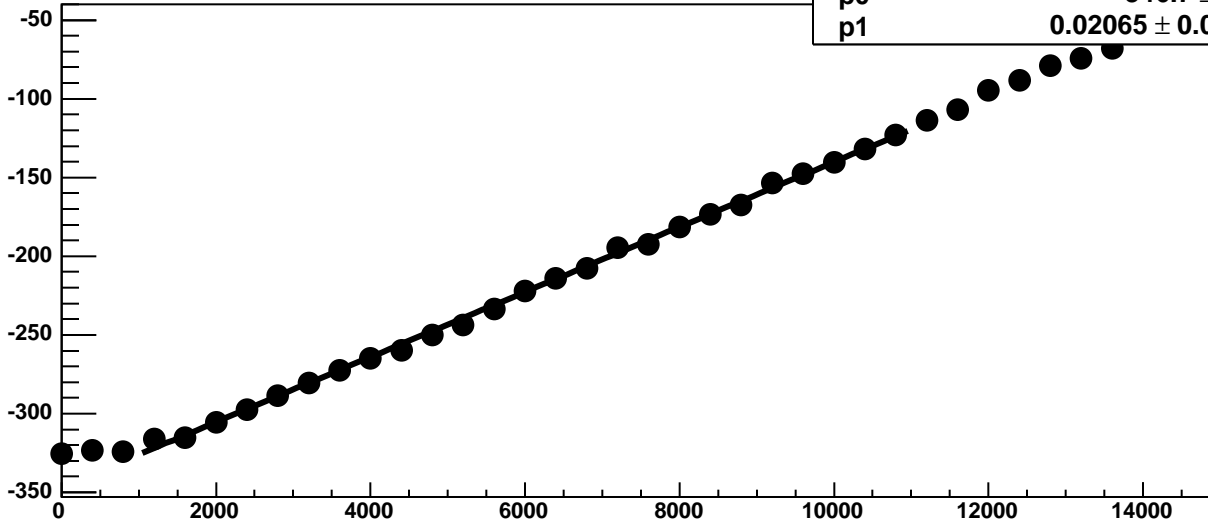
Chip 7, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

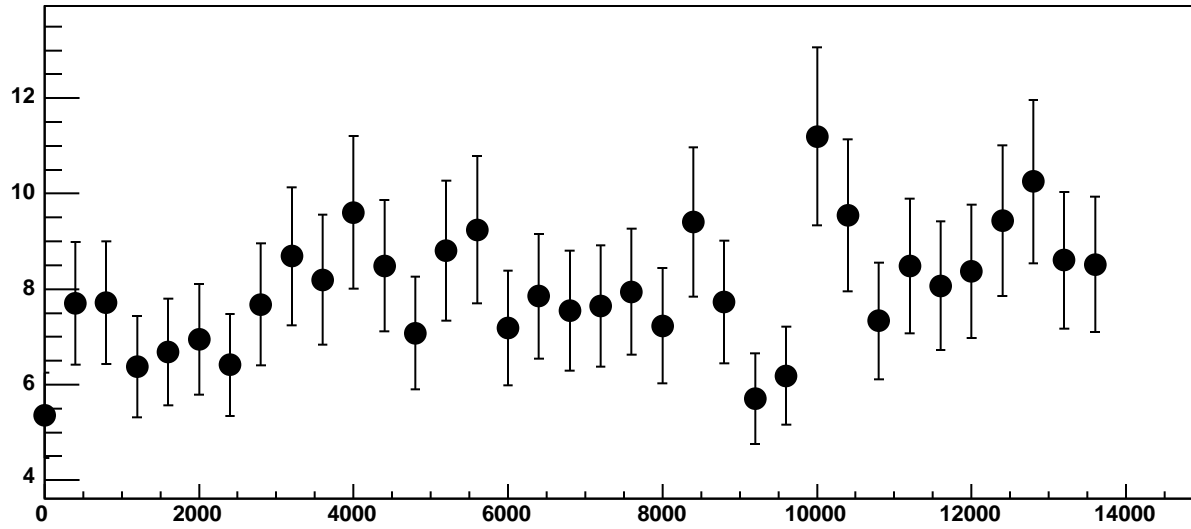


Chip 7, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

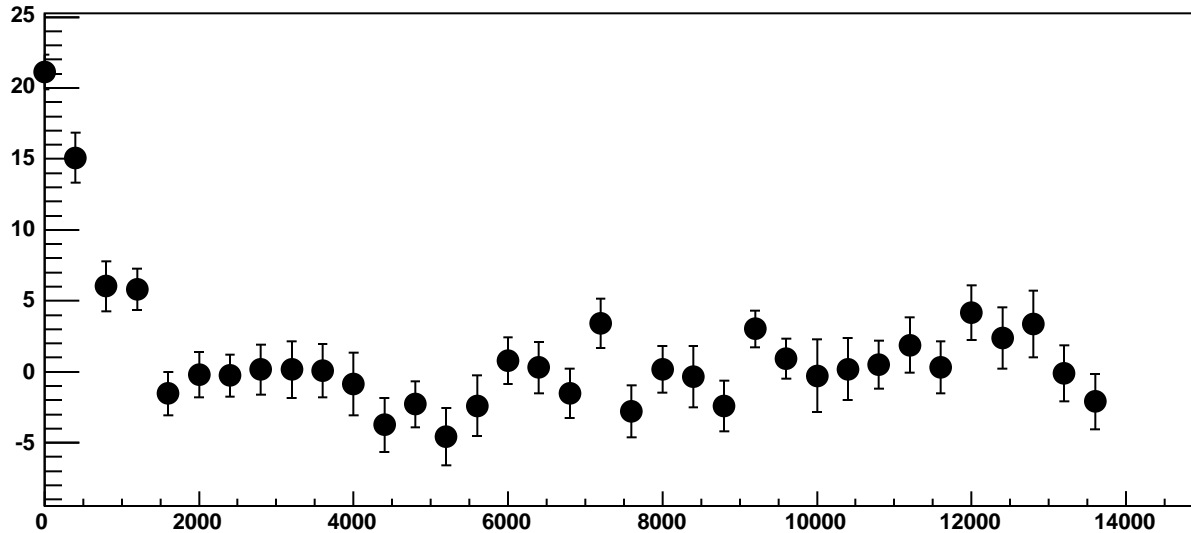


χ^2 / ndf 44.23 / 23
p0 -346.7 ± 0.7658
p1 0.02065 ± 0.0001163

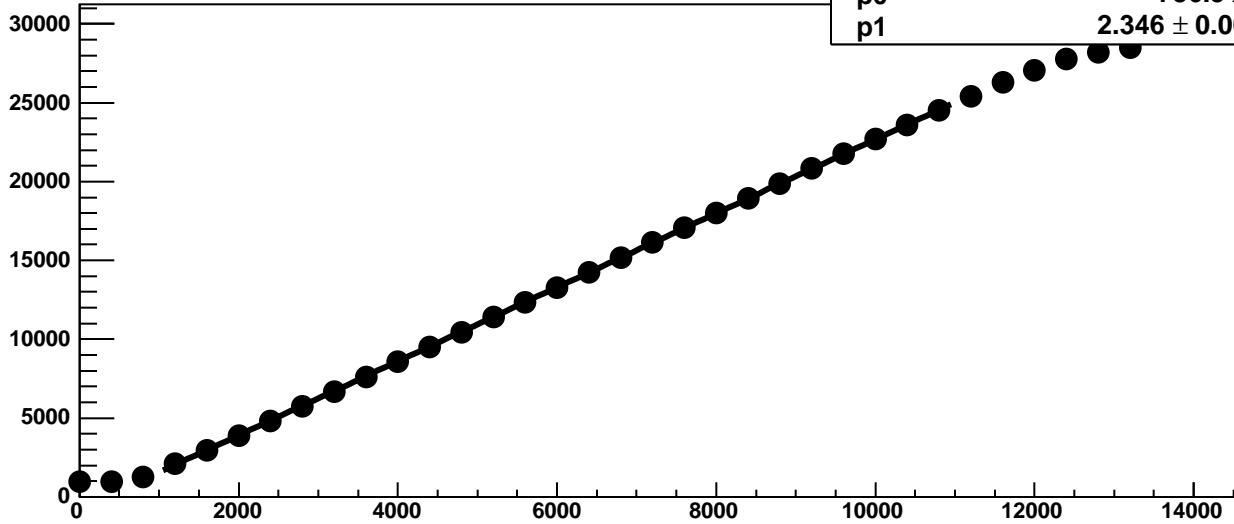
Chip 7, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



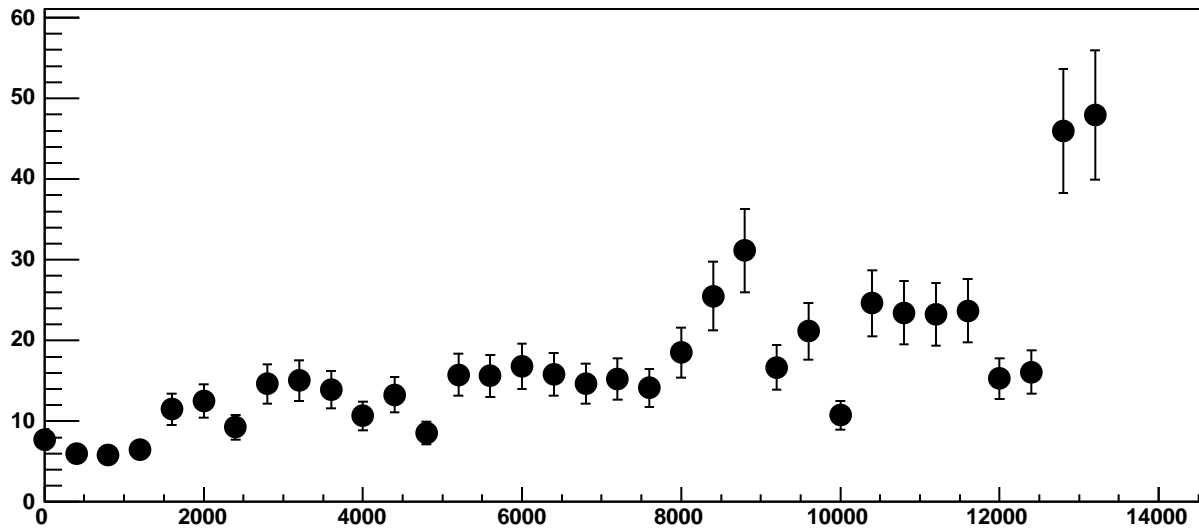
Chip 7, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



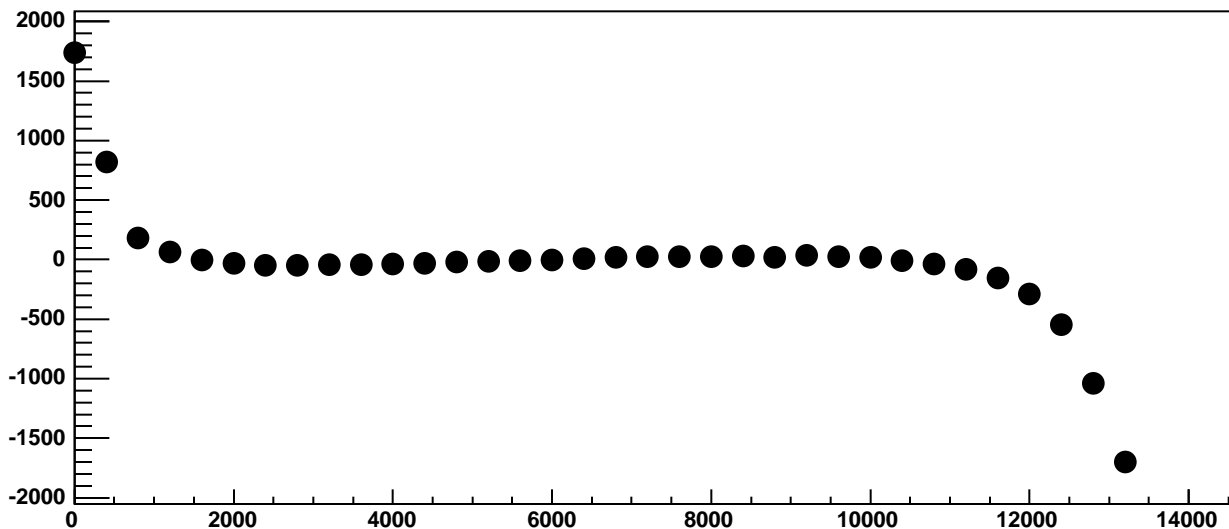
Chip 7, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC



Chip 7, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC

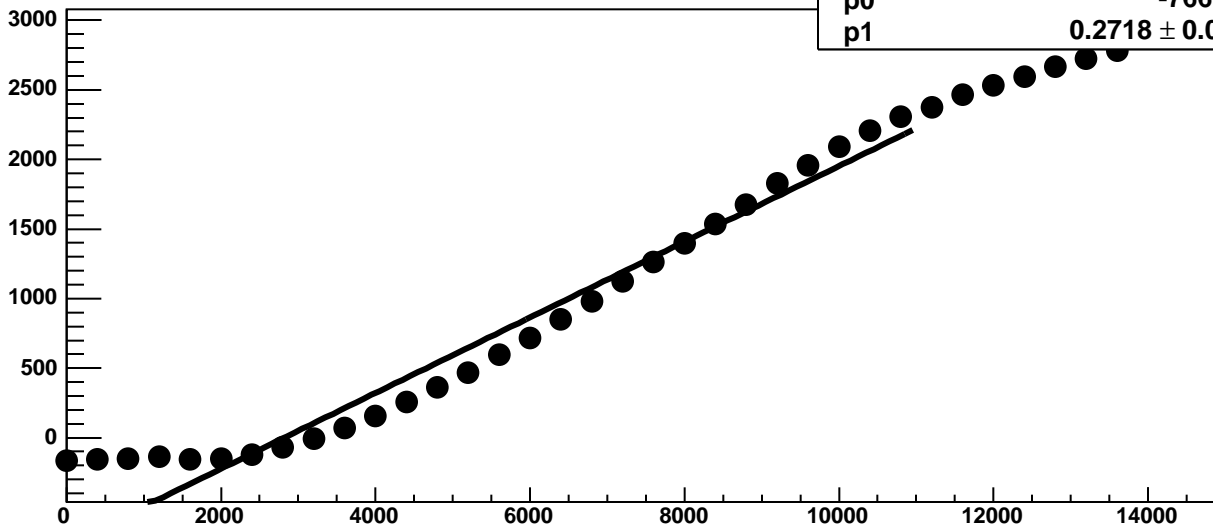


Chip 7, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC

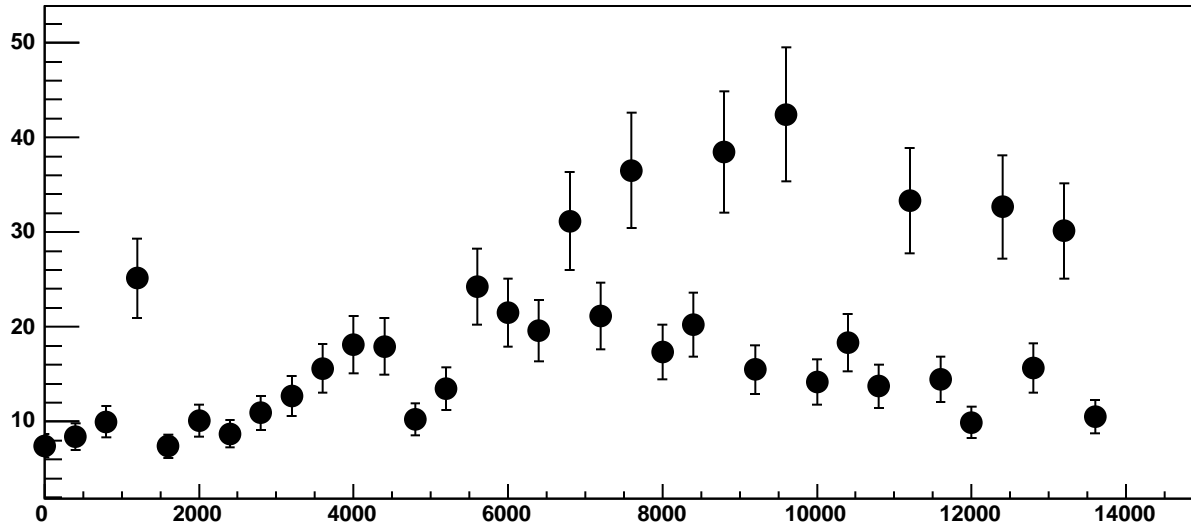


Chip 7, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

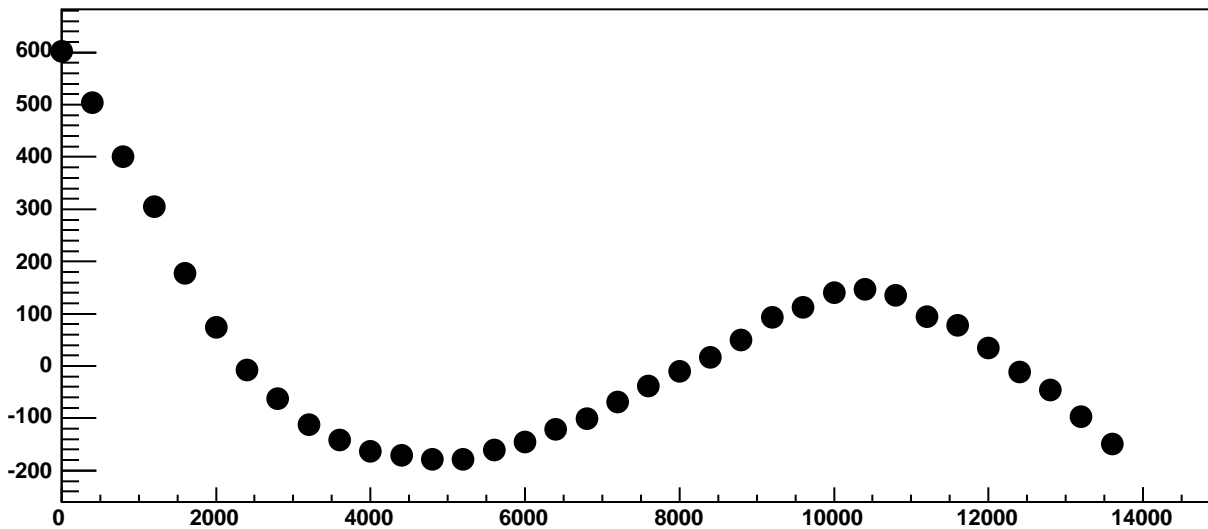
χ^2 / ndf 3.942e+04 / 23
p0 -766 ± 1.237
p1 0.2718 ± 0.0002252



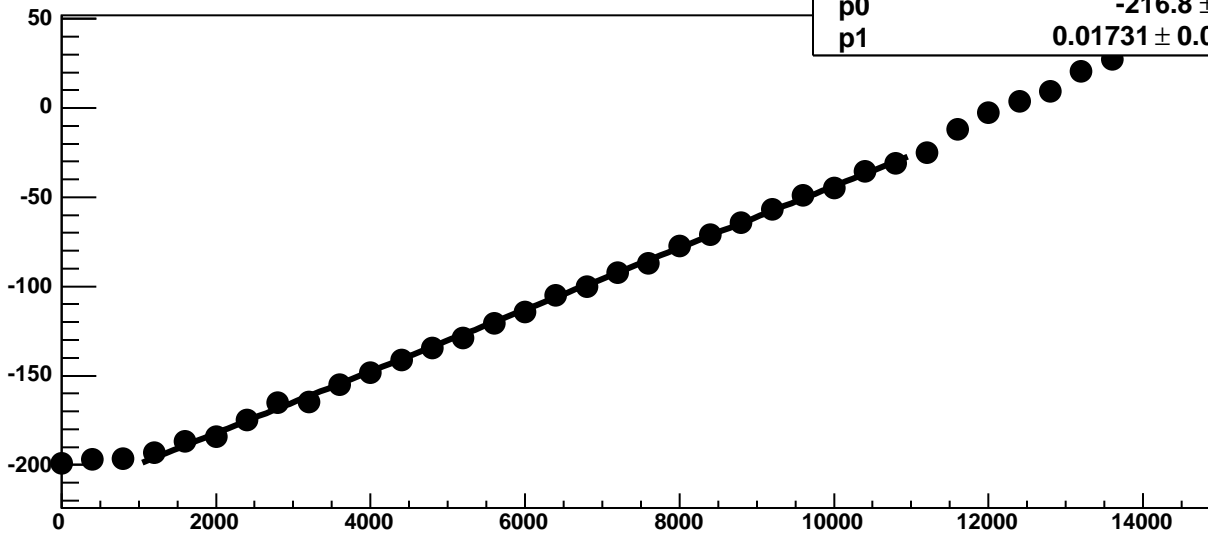
Chip 7, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



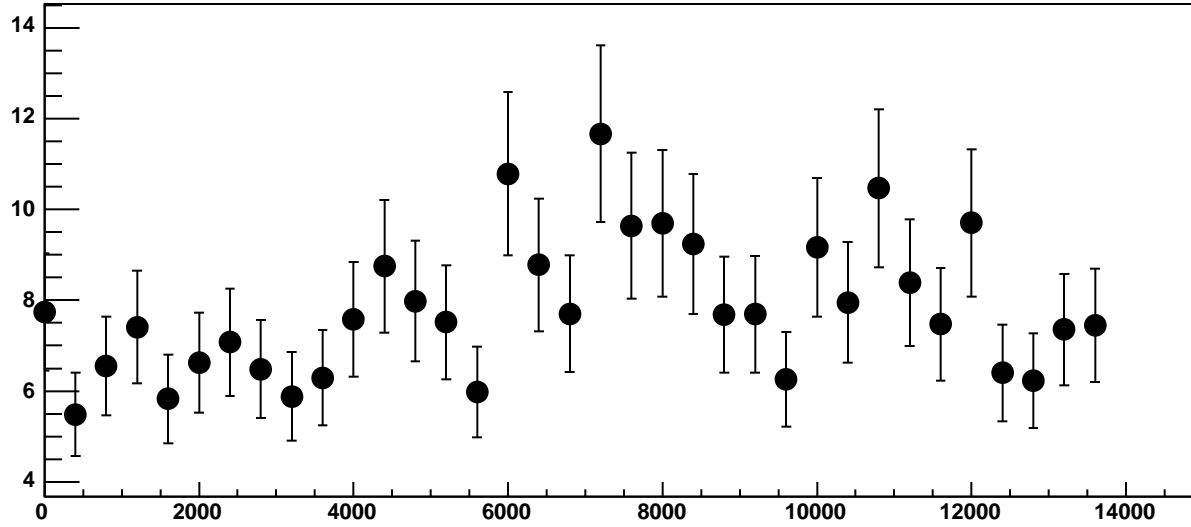
Chip 7, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



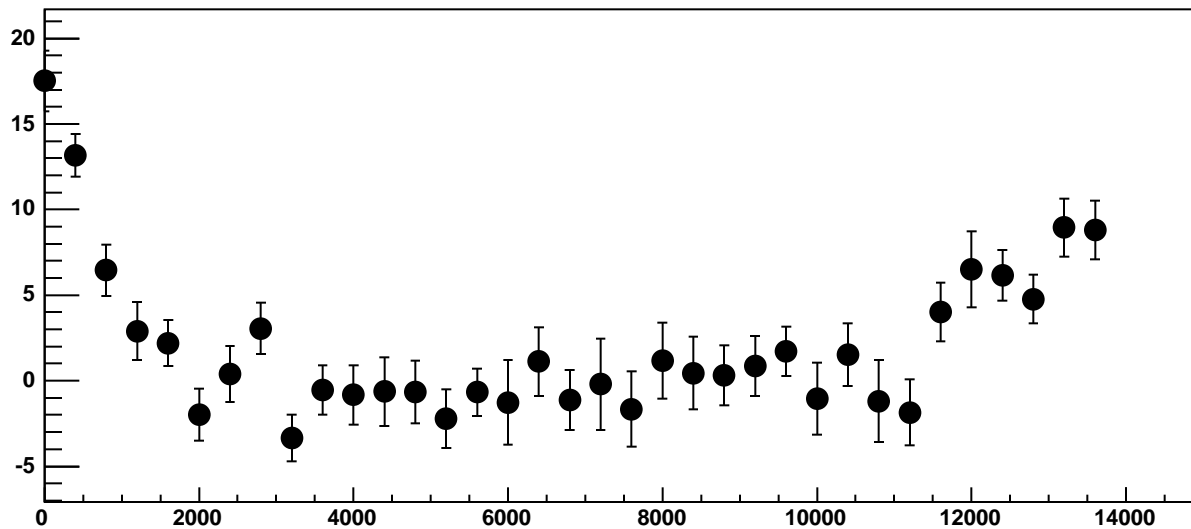
Chip 7, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



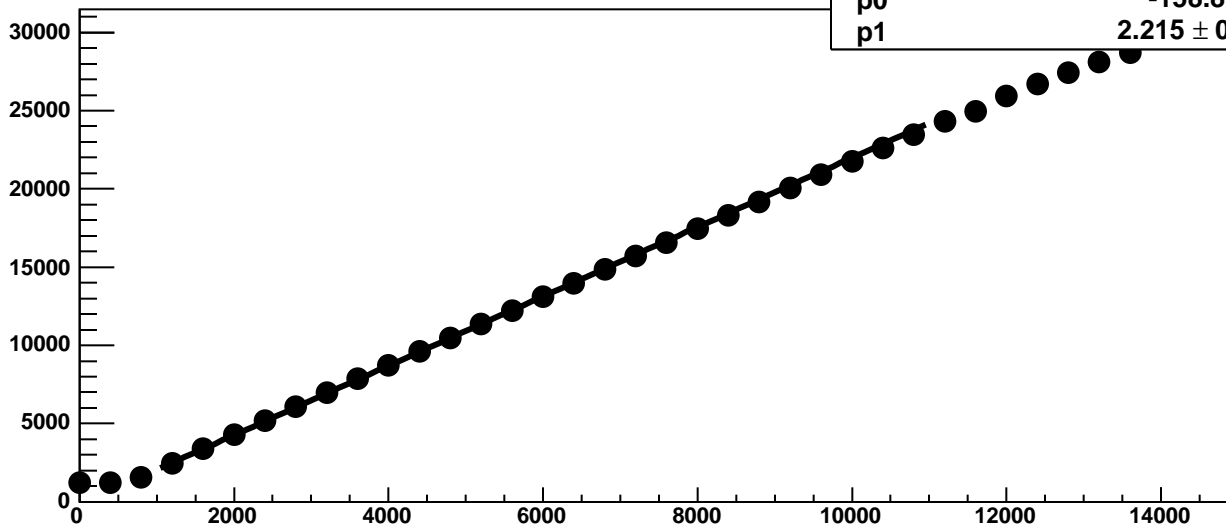
Chip 7, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



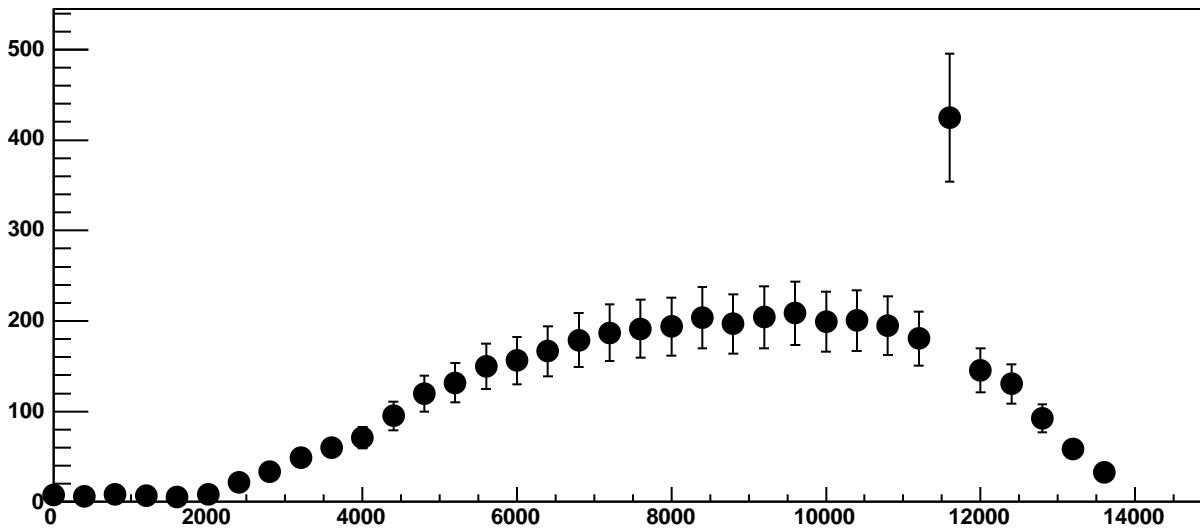
Chip 7, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



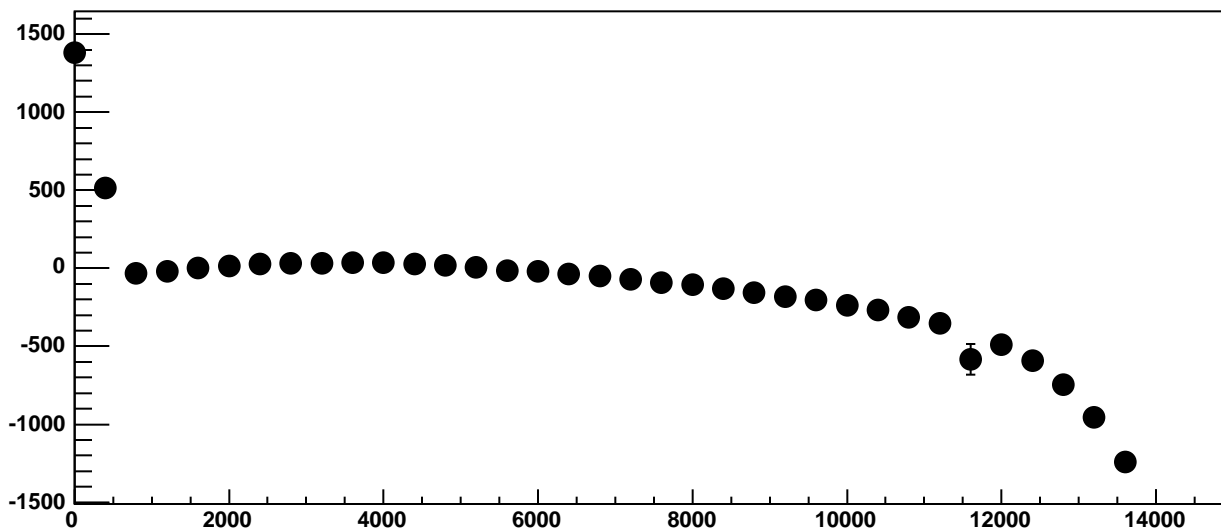
Chip 7, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC



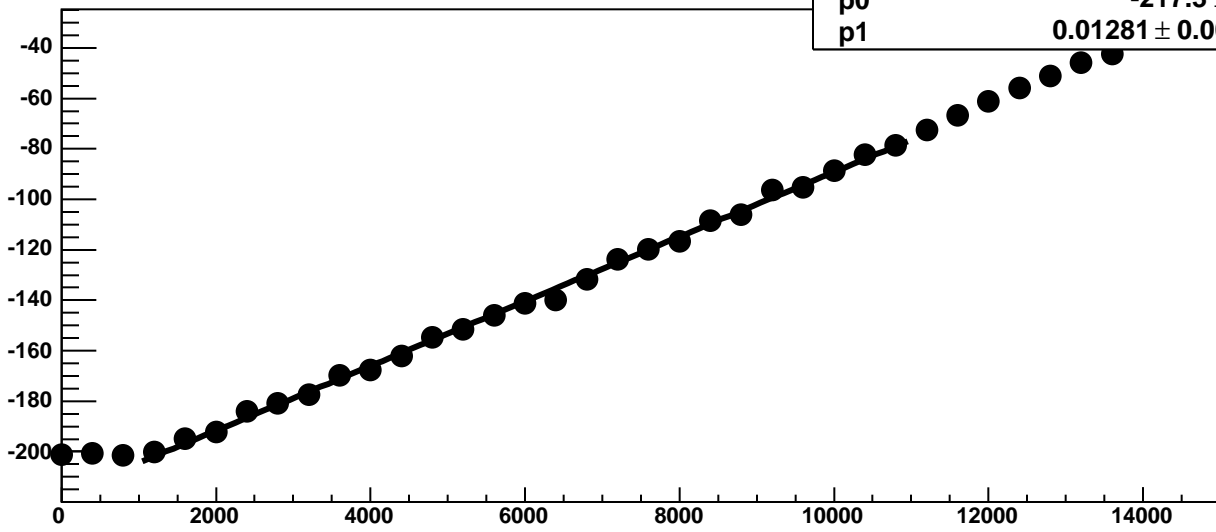
Chip 7, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC



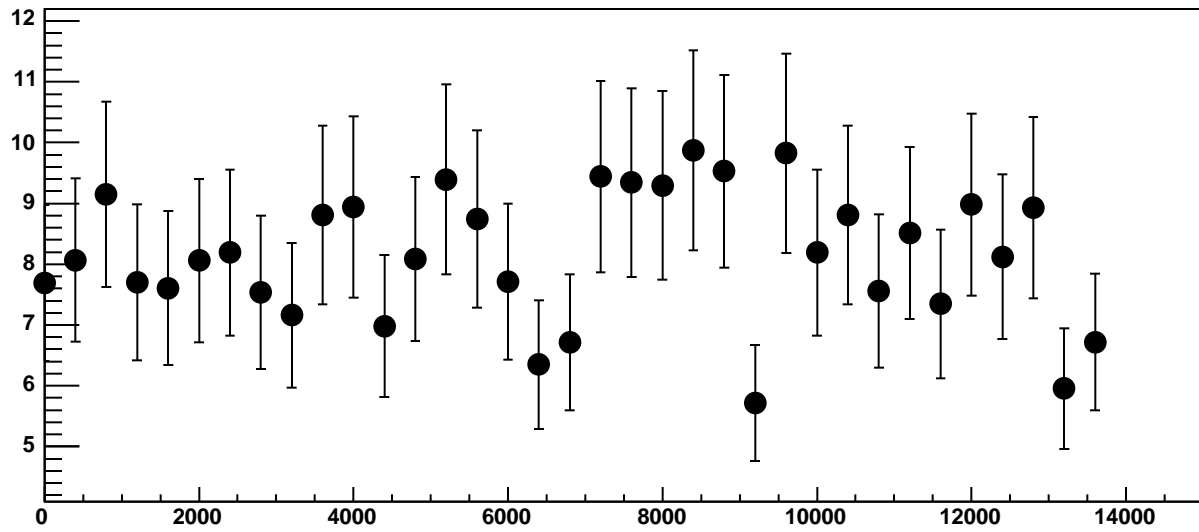
Chip 7, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC



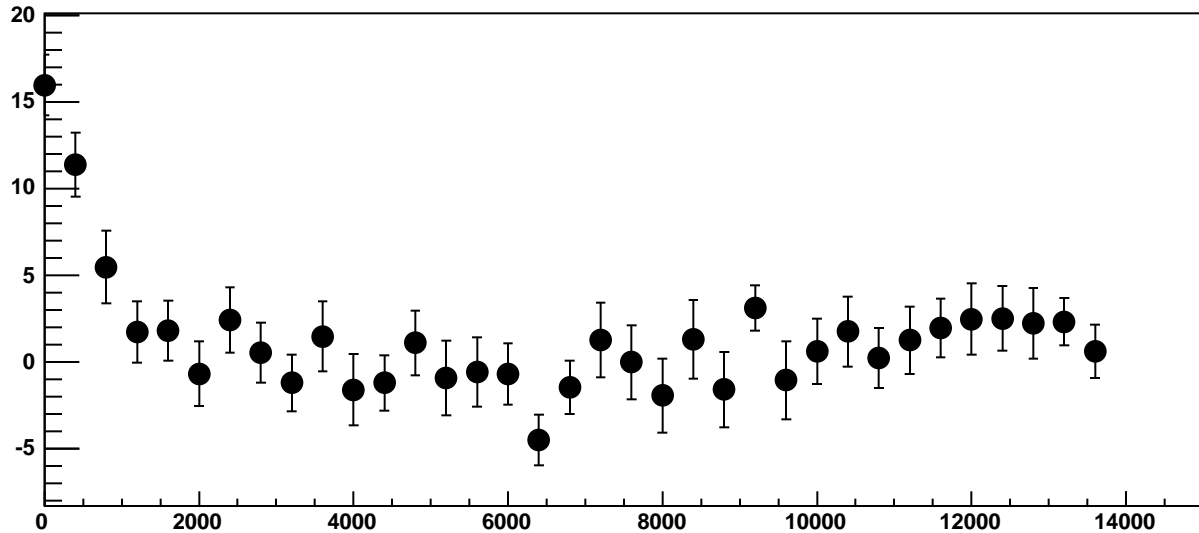
Chip 7, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



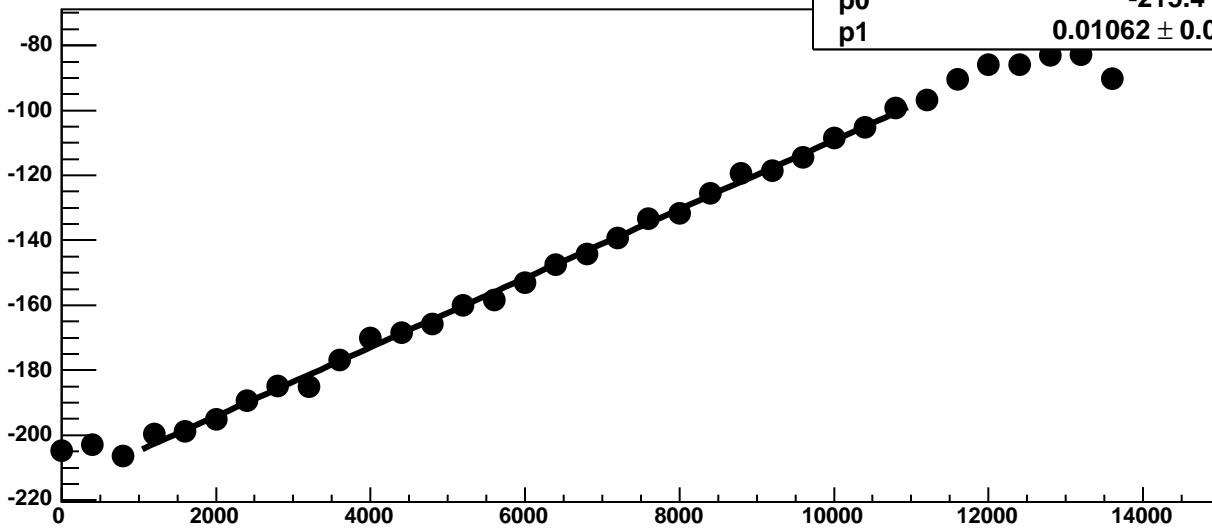
Chip 7, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

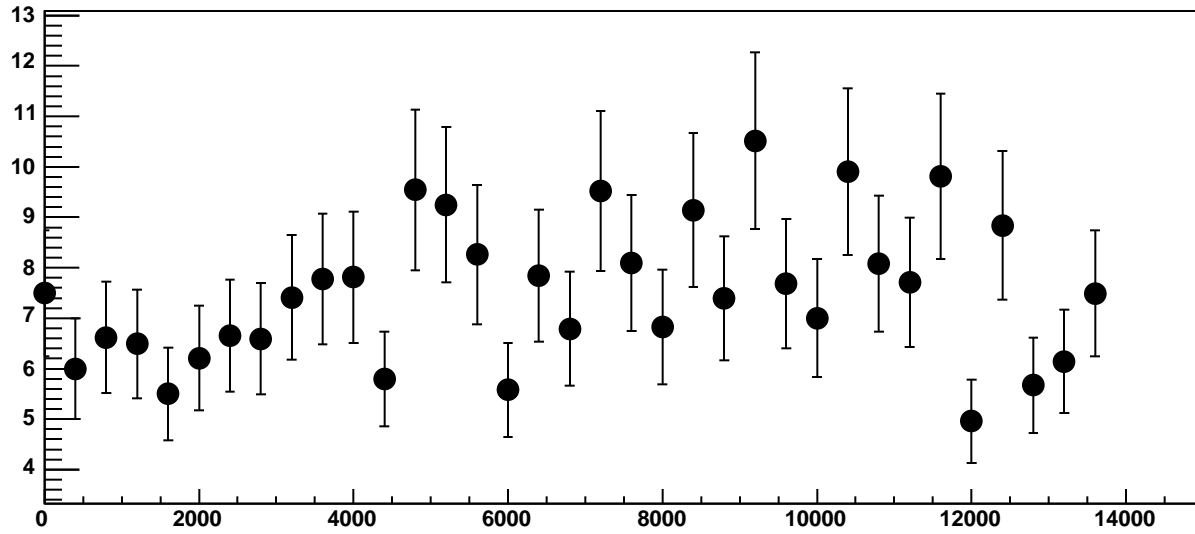


Chip 7, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

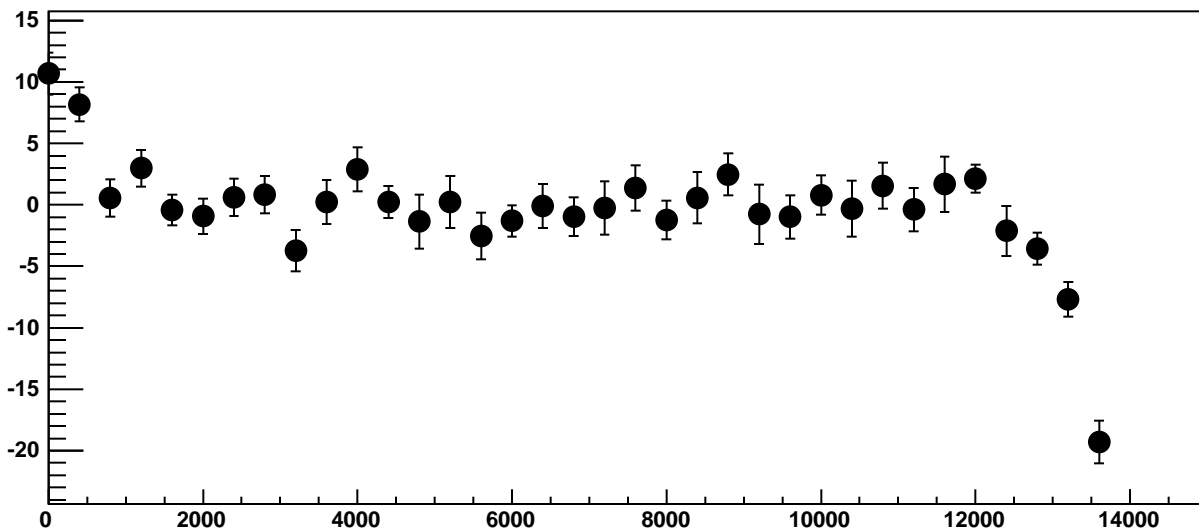


χ^2 / ndf 20.84 / 23
p0 -215.4 ± 0.714
p1 0.01062 ± 0.0001152

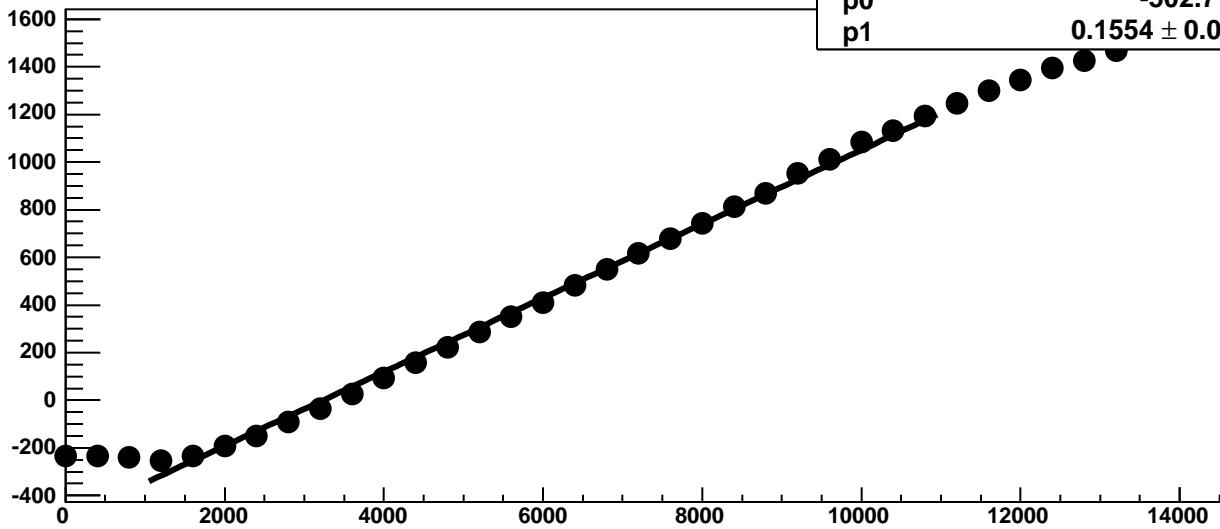
Chip 7, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



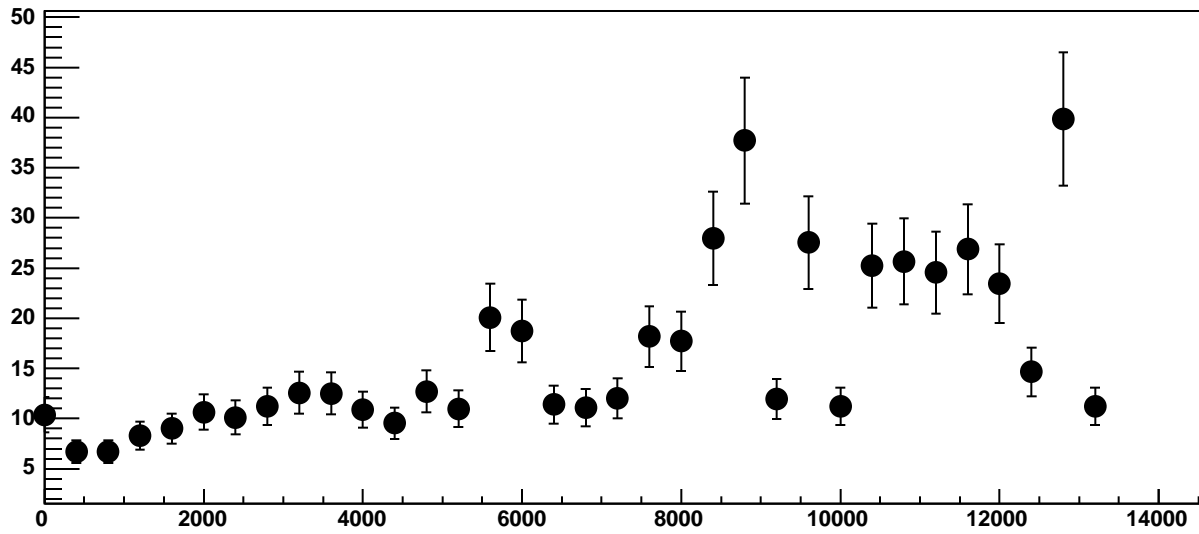
Chip 7, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



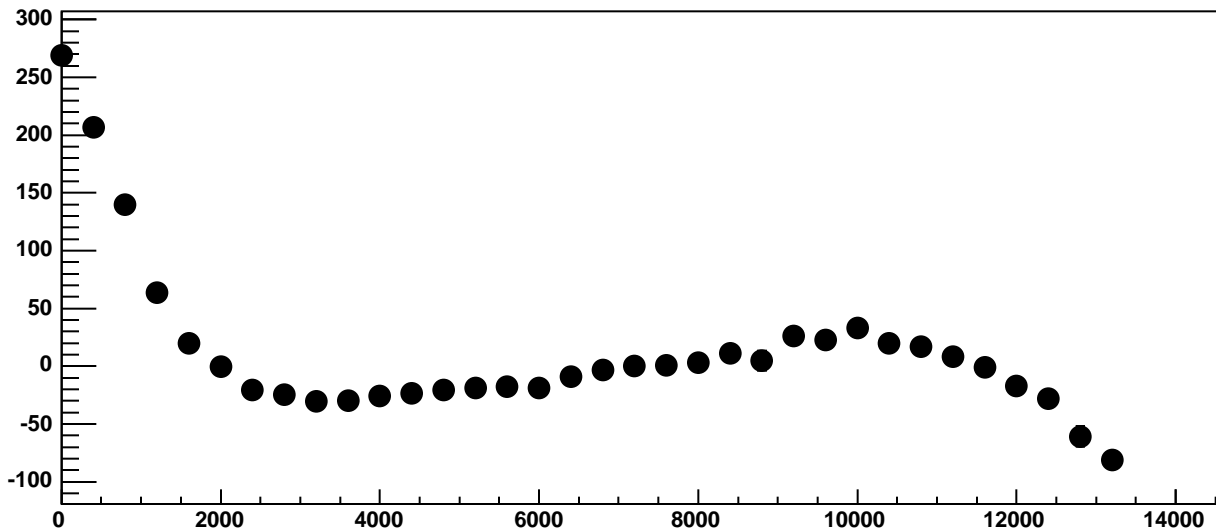
Chip 7, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC



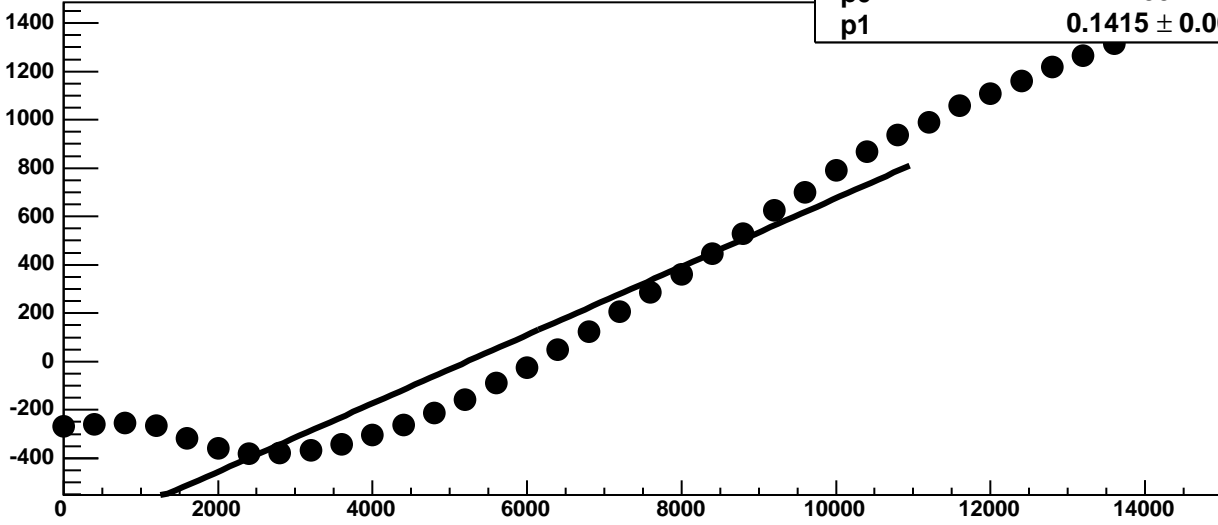
Chip 7, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



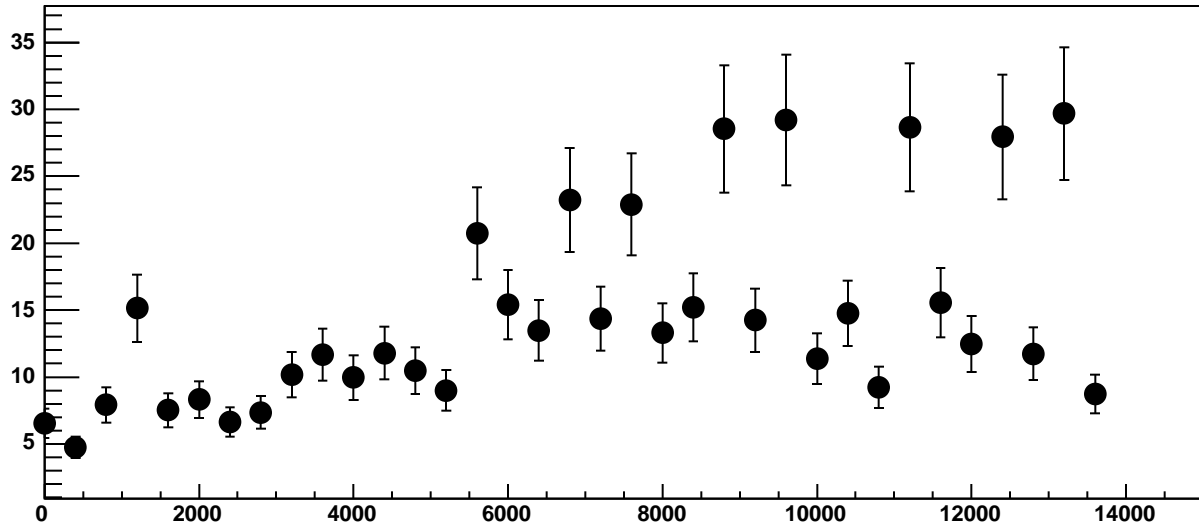
Chip 7, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC



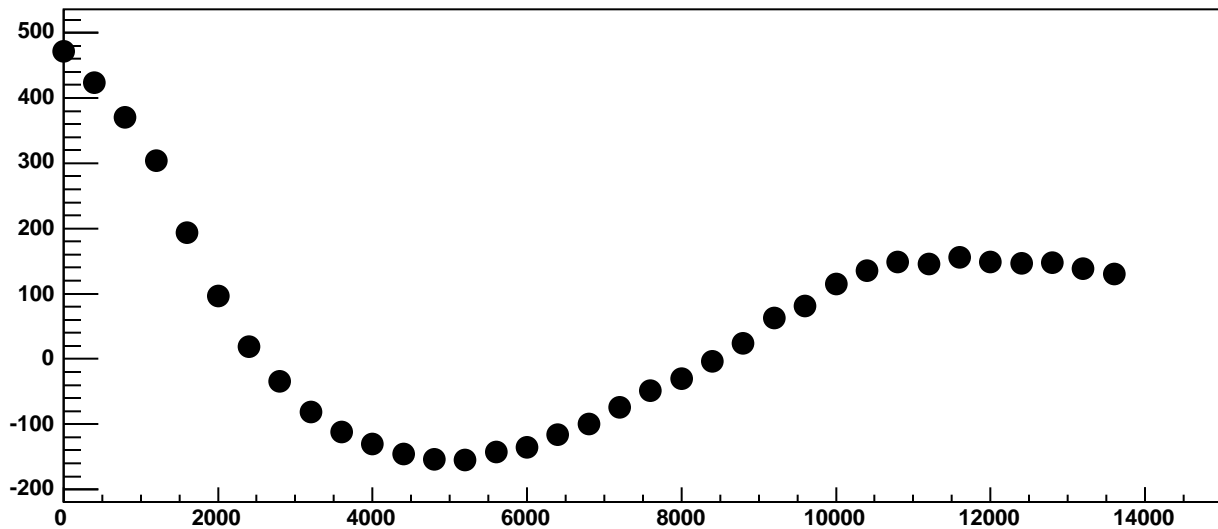
Chip 7, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



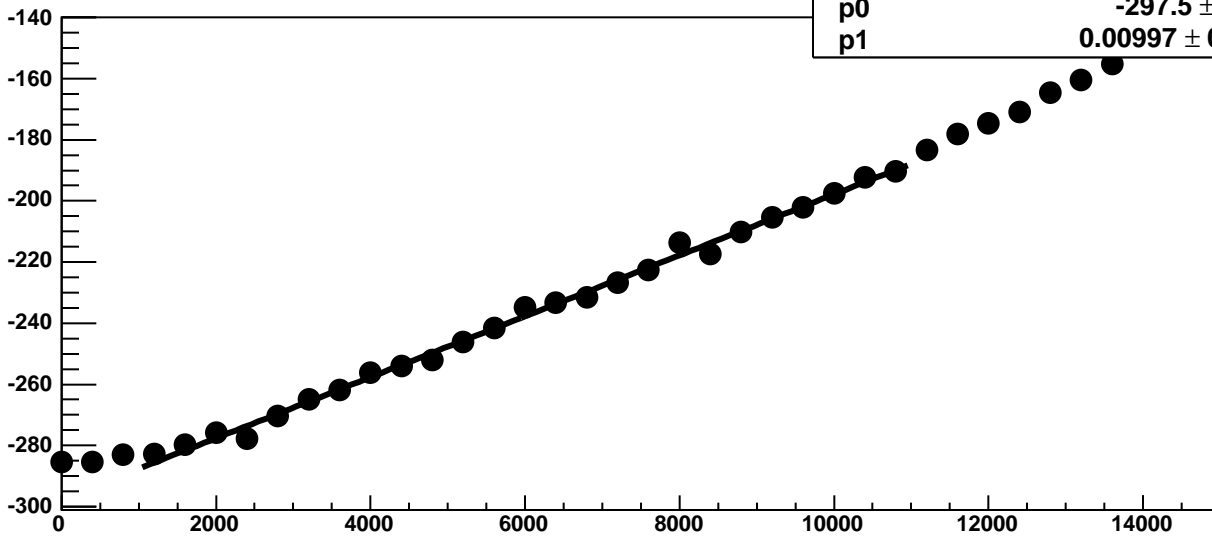
Chip 7, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



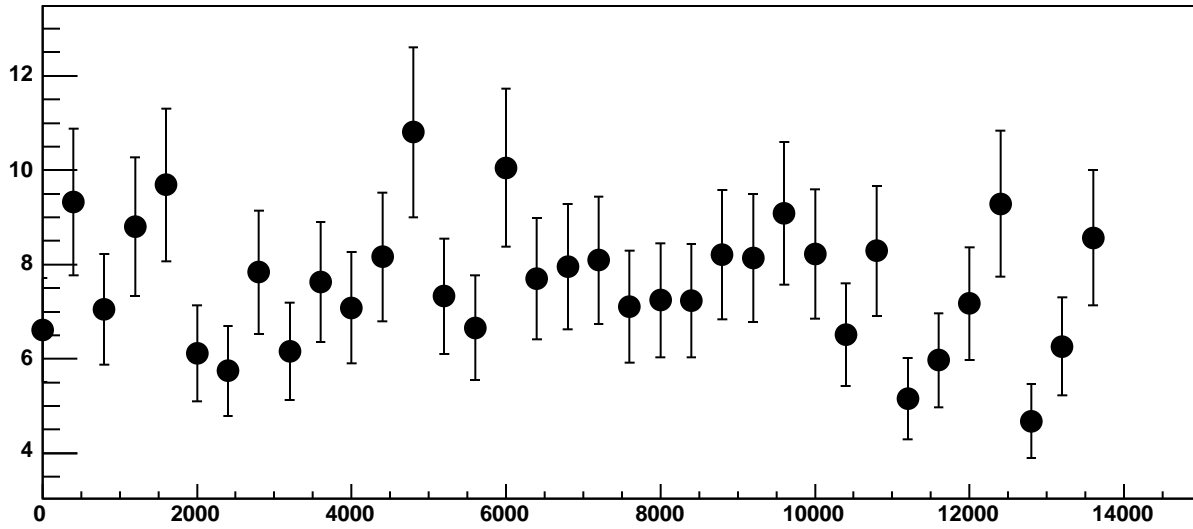
Chip 7, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



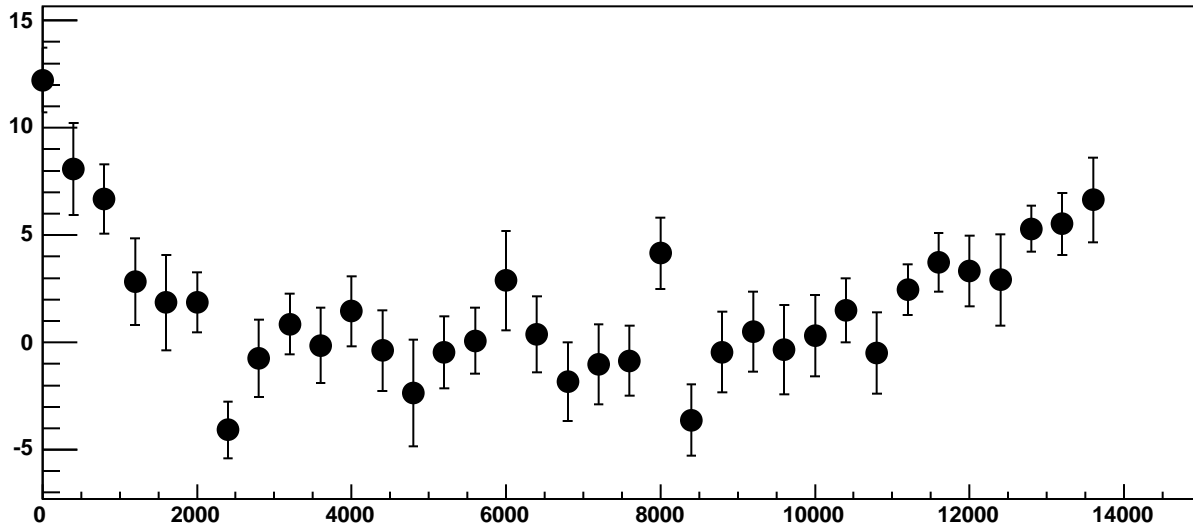
Chip 7, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



Chip 7, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

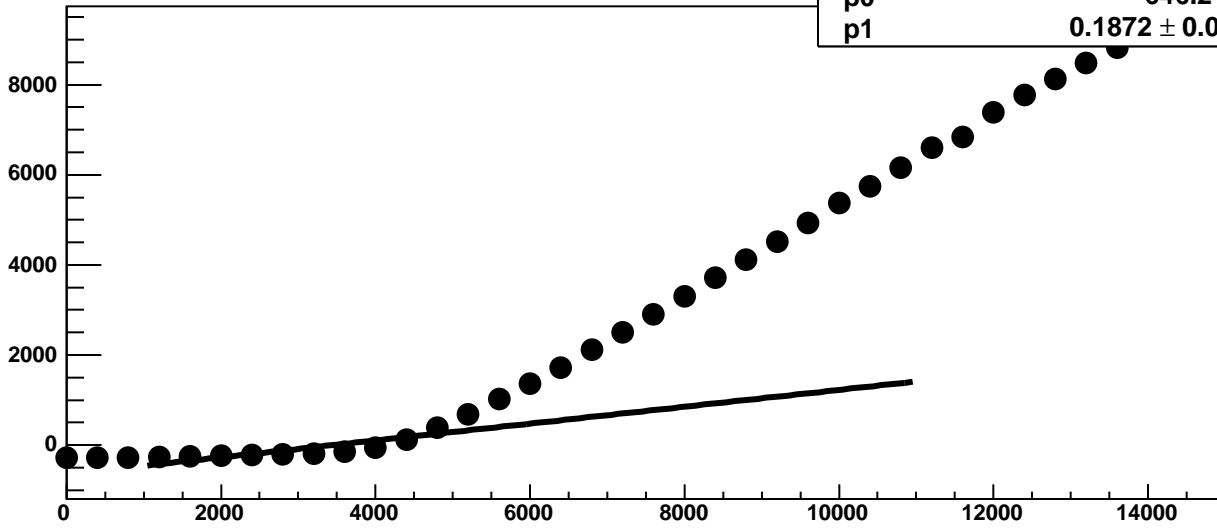


Chip 7, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

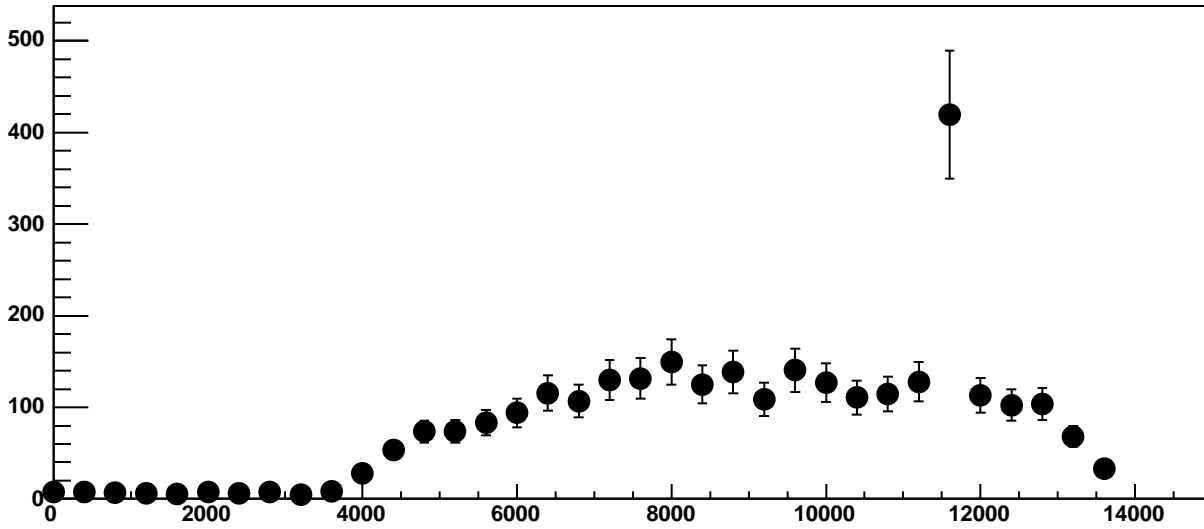


Chip 7, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC

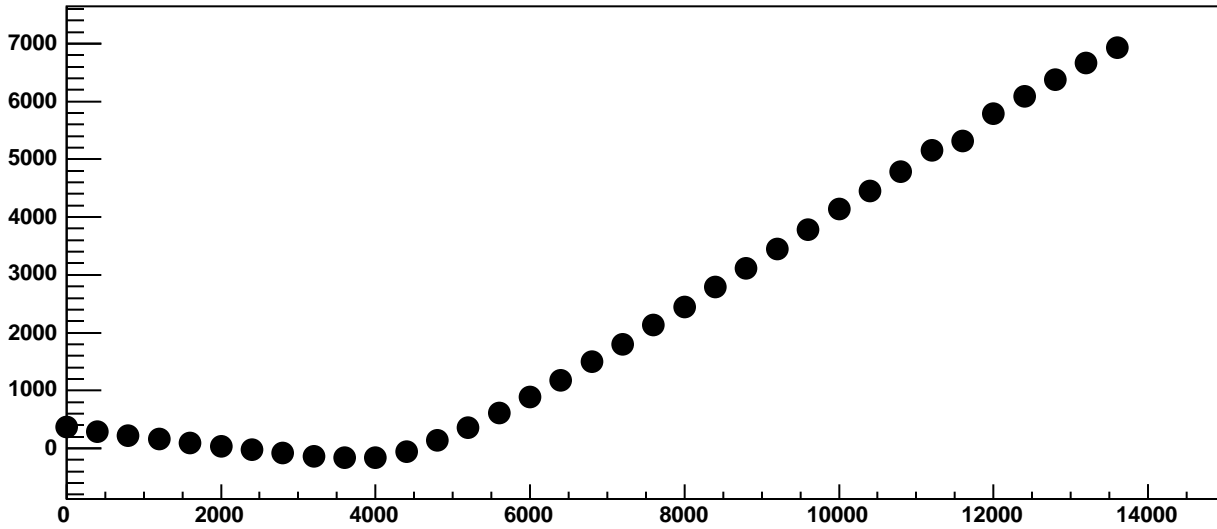
χ^2 / ndf 2.051e+05 / 23
p0 -646.2 ± 1.465
p1 0.1872 ± 0.0005697



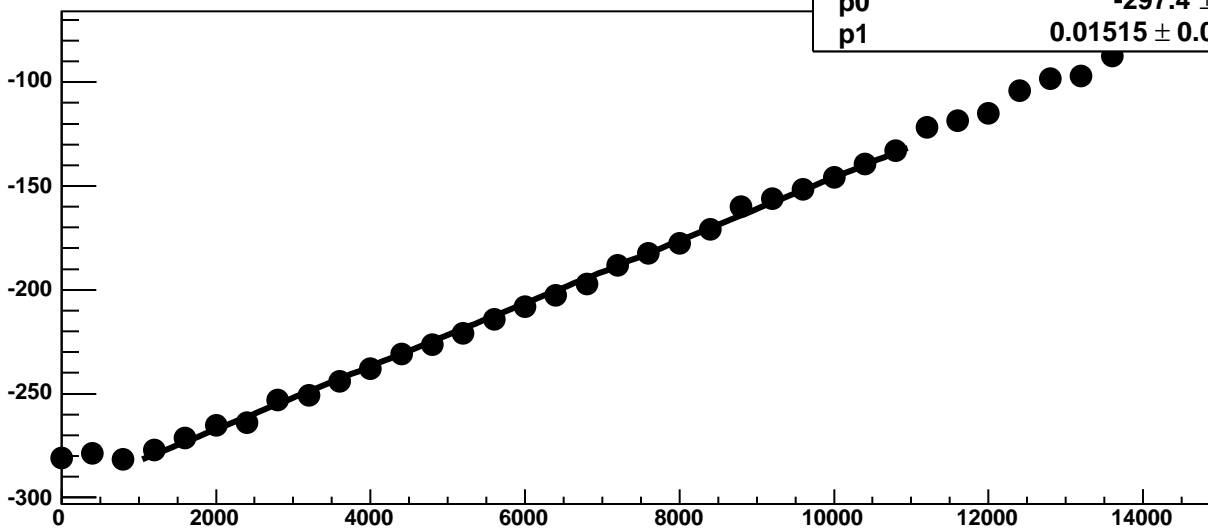
Chip 7, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC

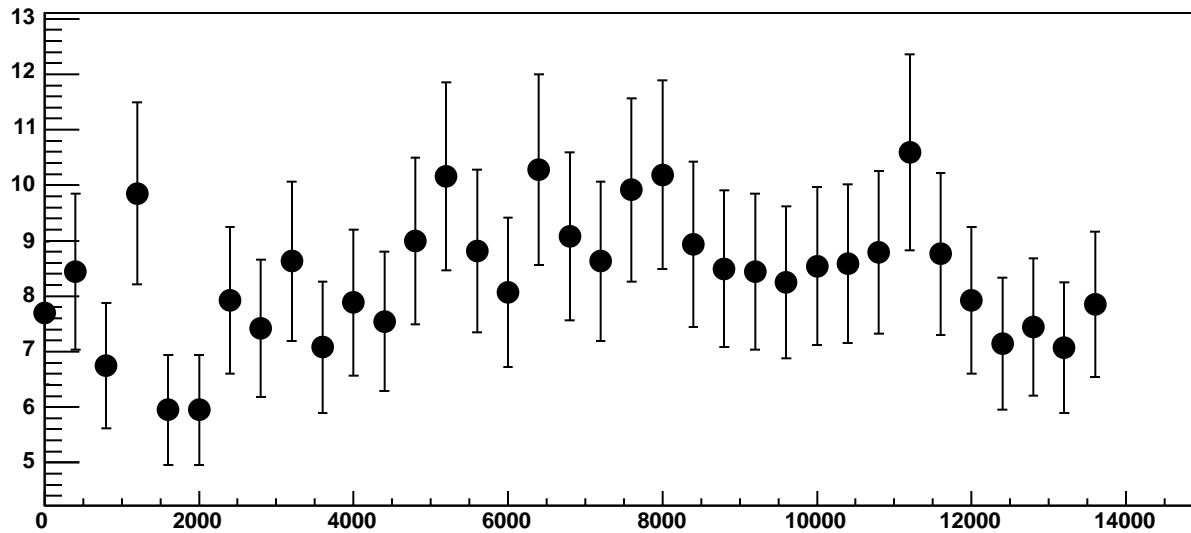


Chip 7, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

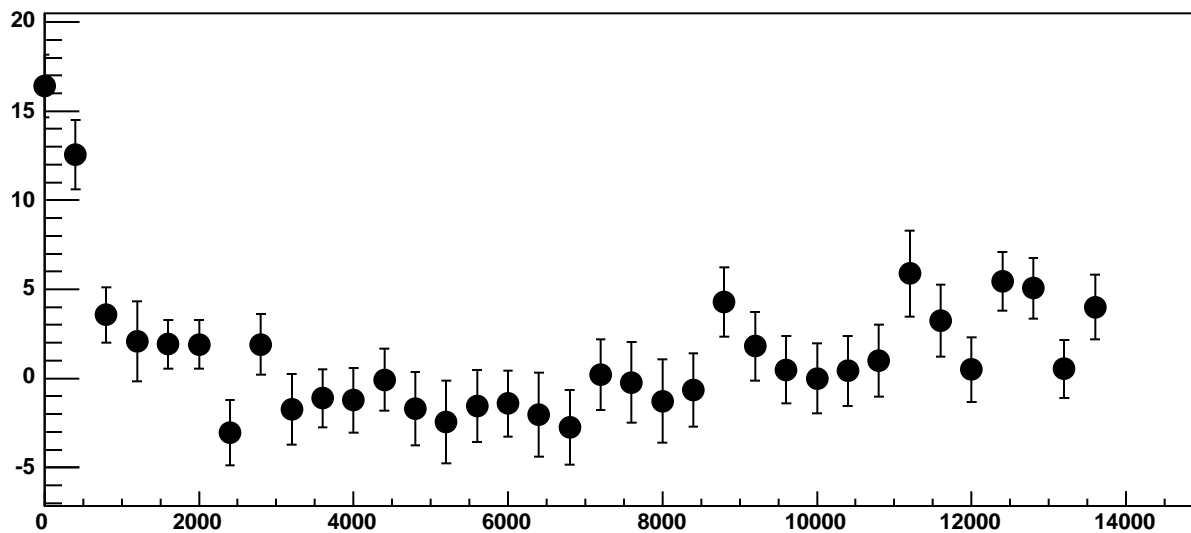


χ^2 / ndf 22.45 / 23
p0 -297.4 ± 0.7963
p1 0.01515 ± 0.0001264

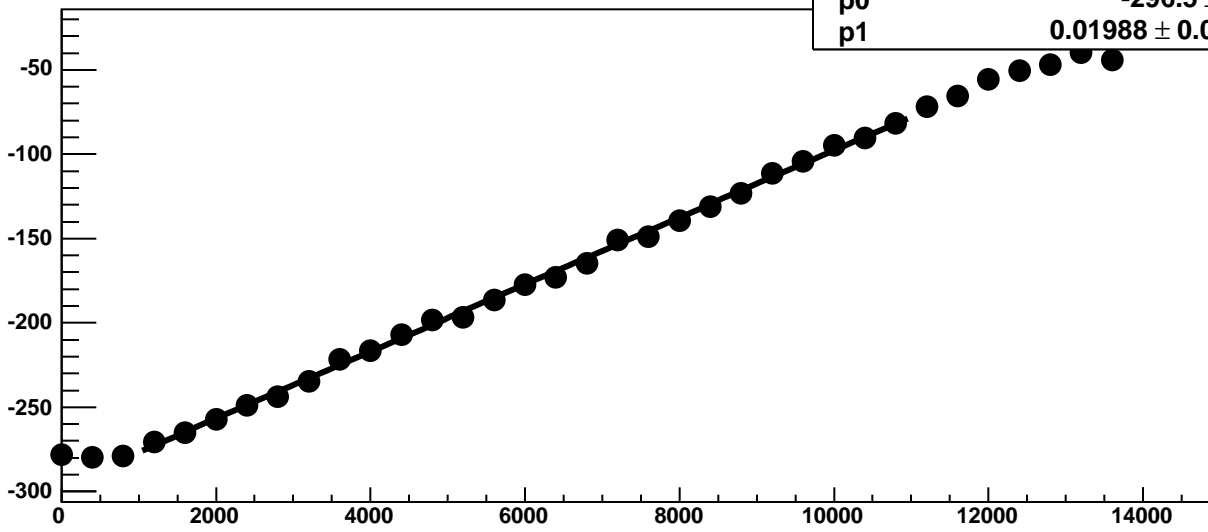
Chip 7, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

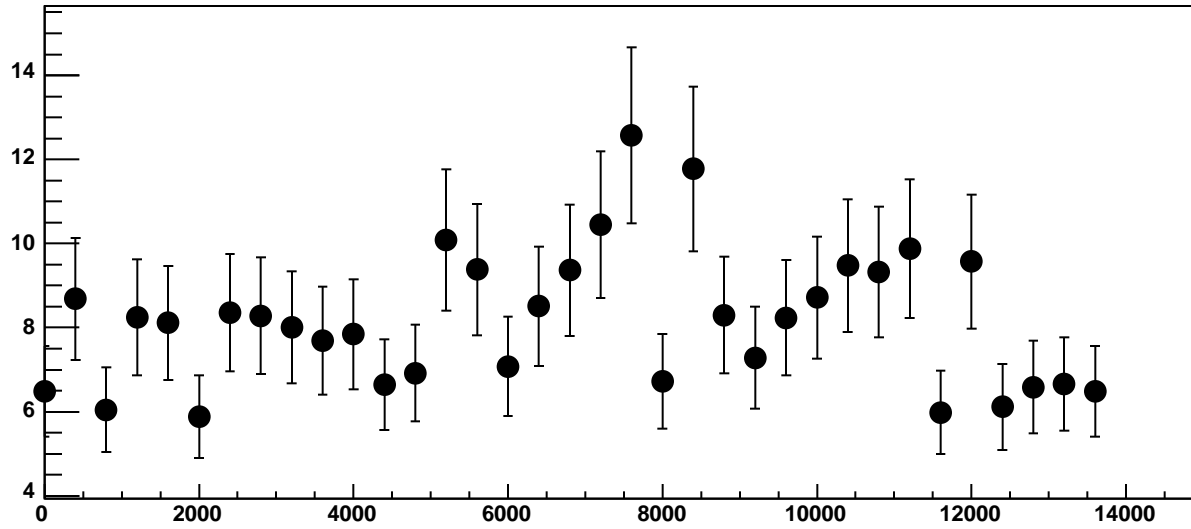


Chip 7, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

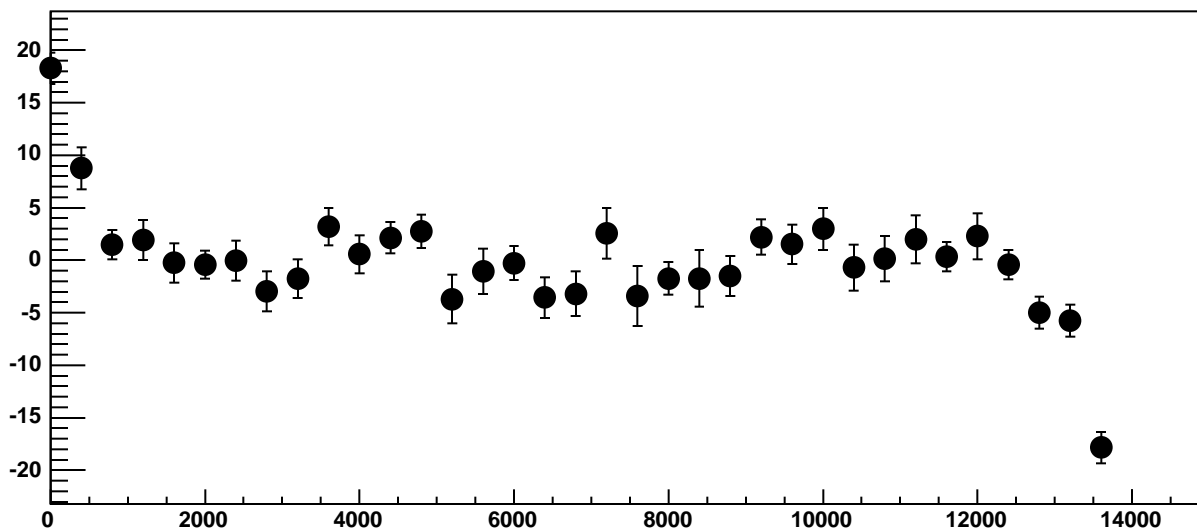


χ^2 / ndf 30.89 / 23
p0 -296.5 ± 0.8221
p1 0.01988 ± 0.0001298

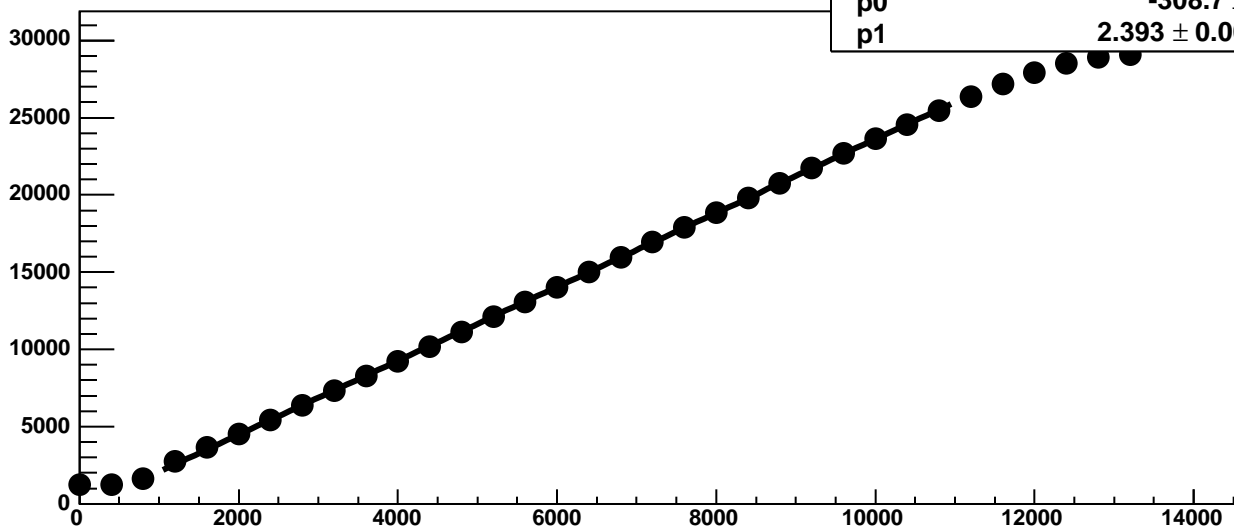
Chip 7, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



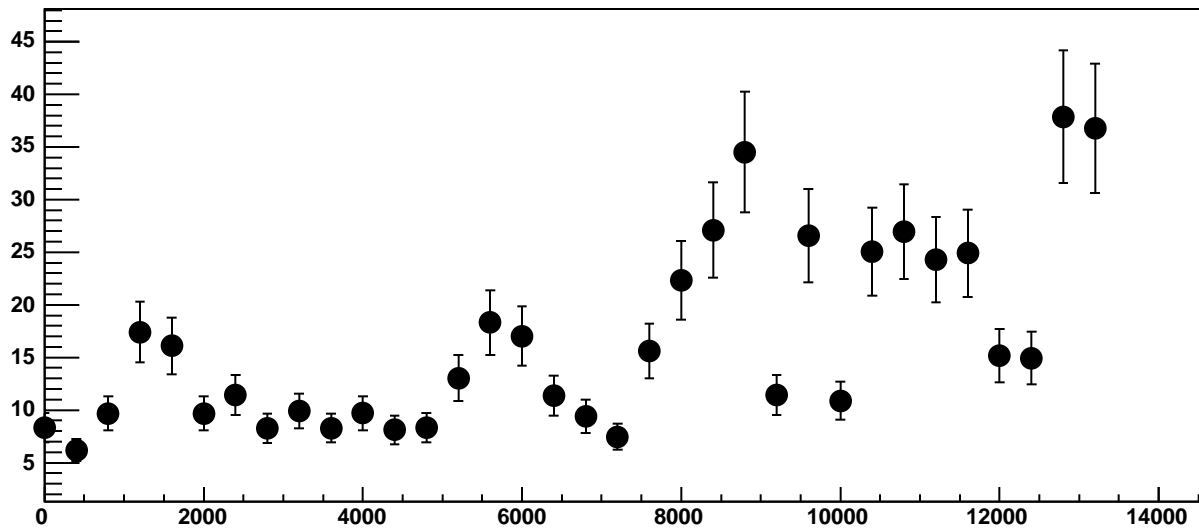
Chip 7, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



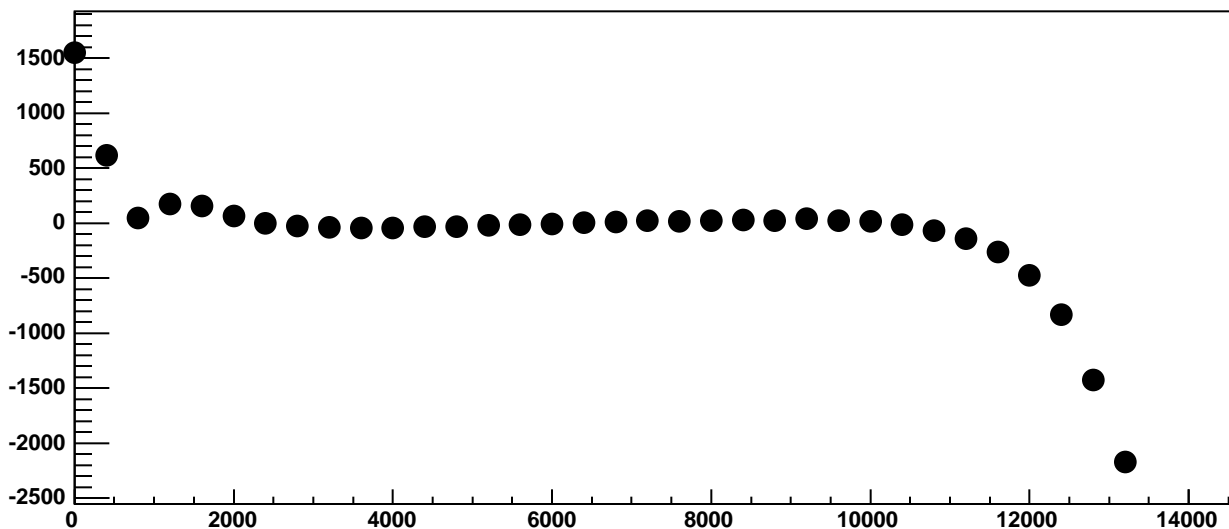
Chip 7, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC



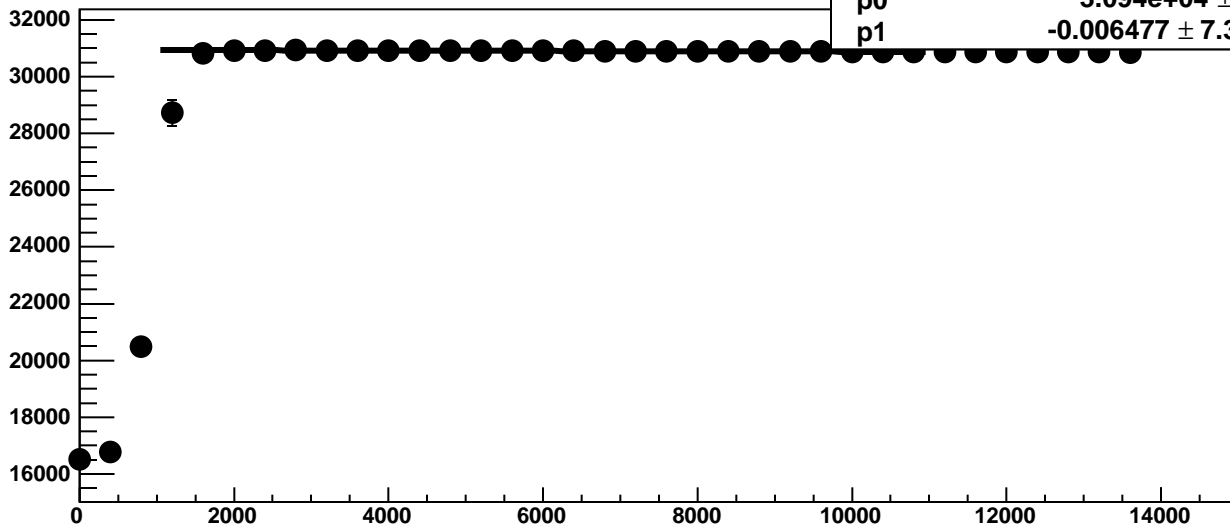
Chip 7, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC



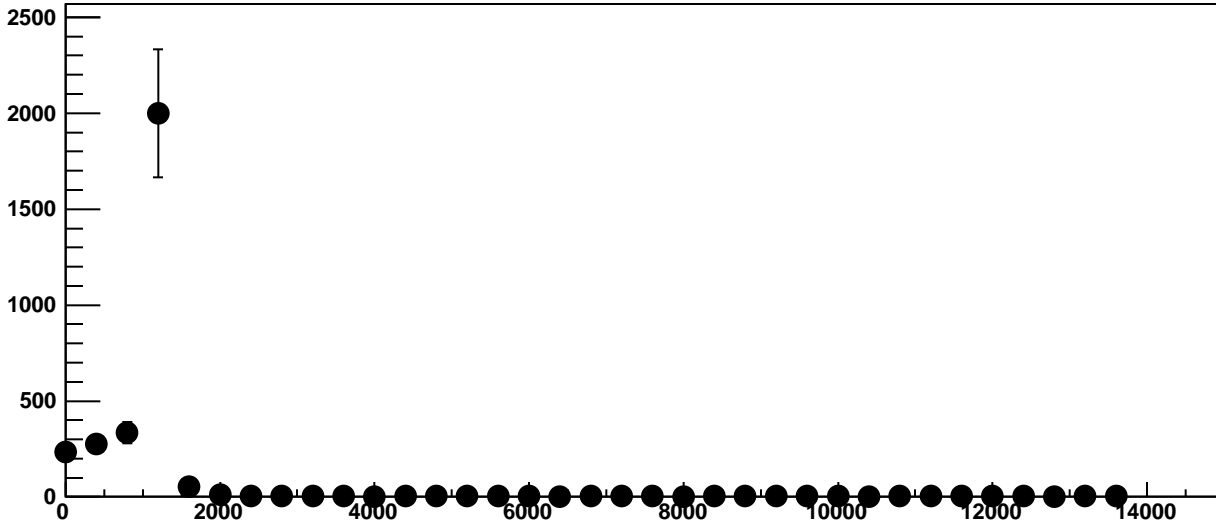
Chip 7, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC



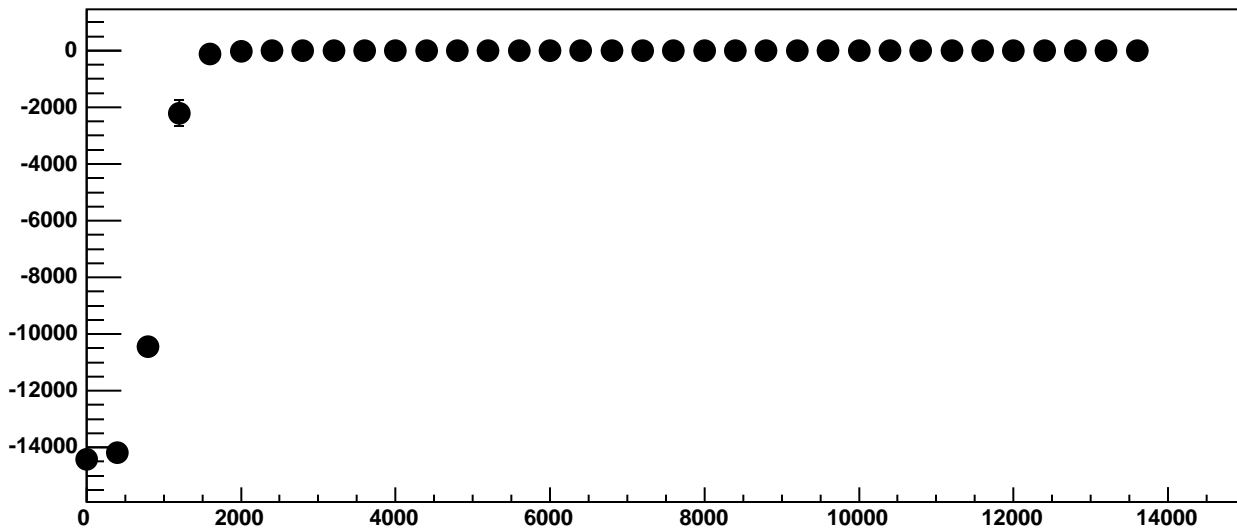
Chip 8, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



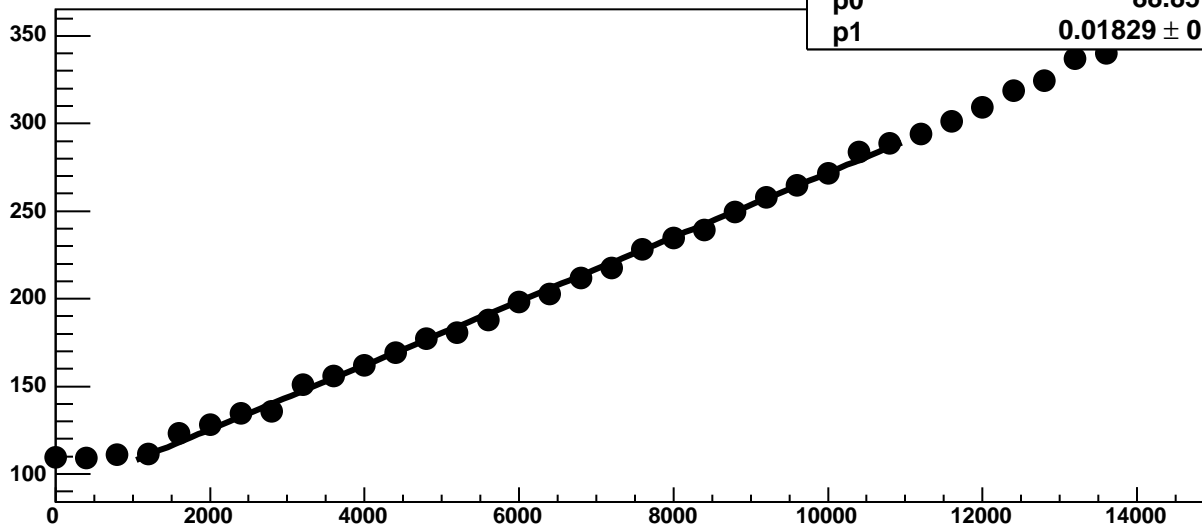
Chip 8, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC

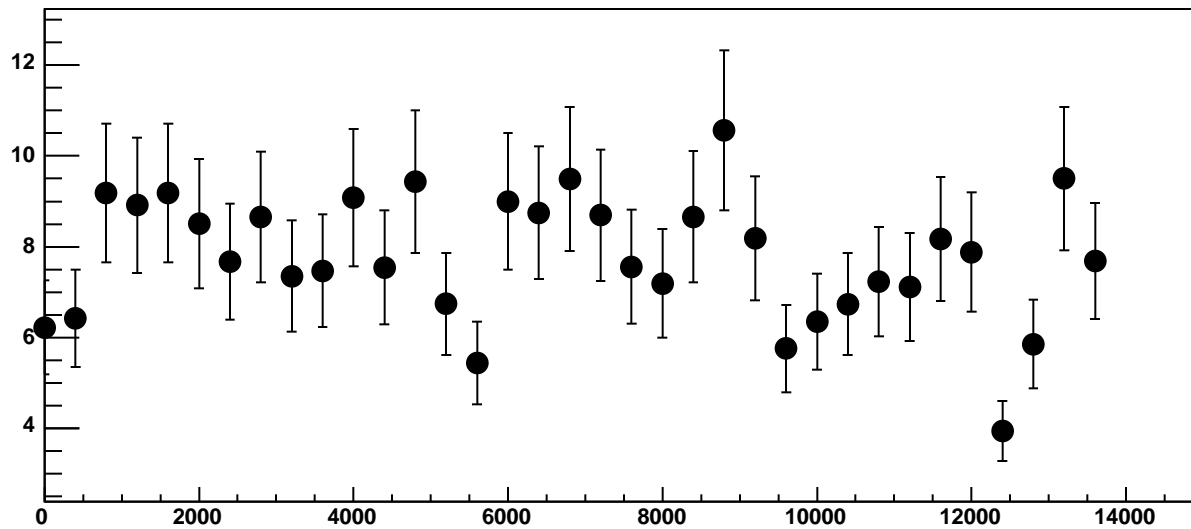


Chip 8, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

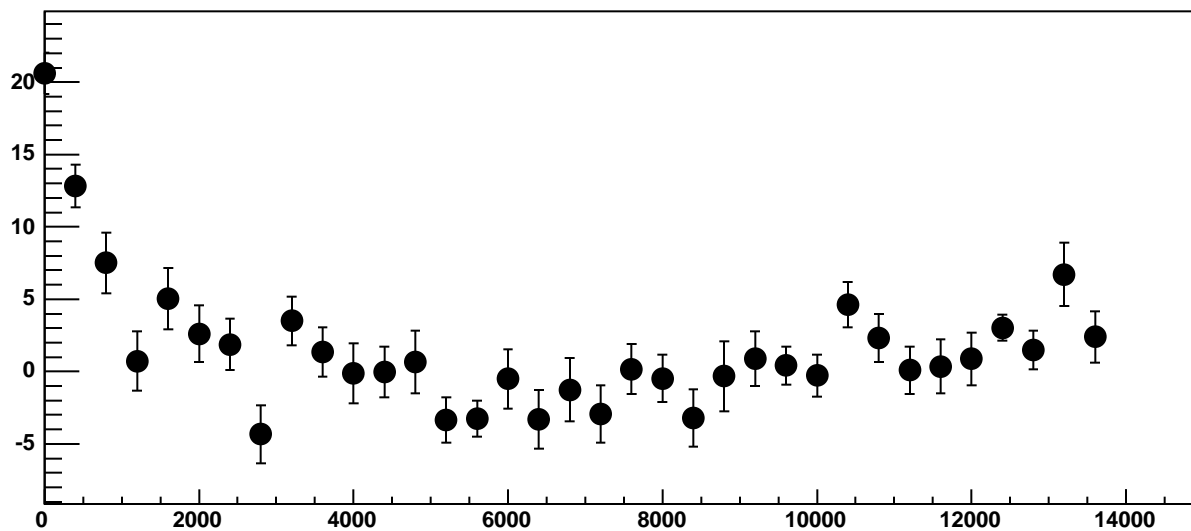


χ^2 / ndf 49.23 / 23
p0 88.85 ± 0.848
p1 0.01829 ± 0.000122

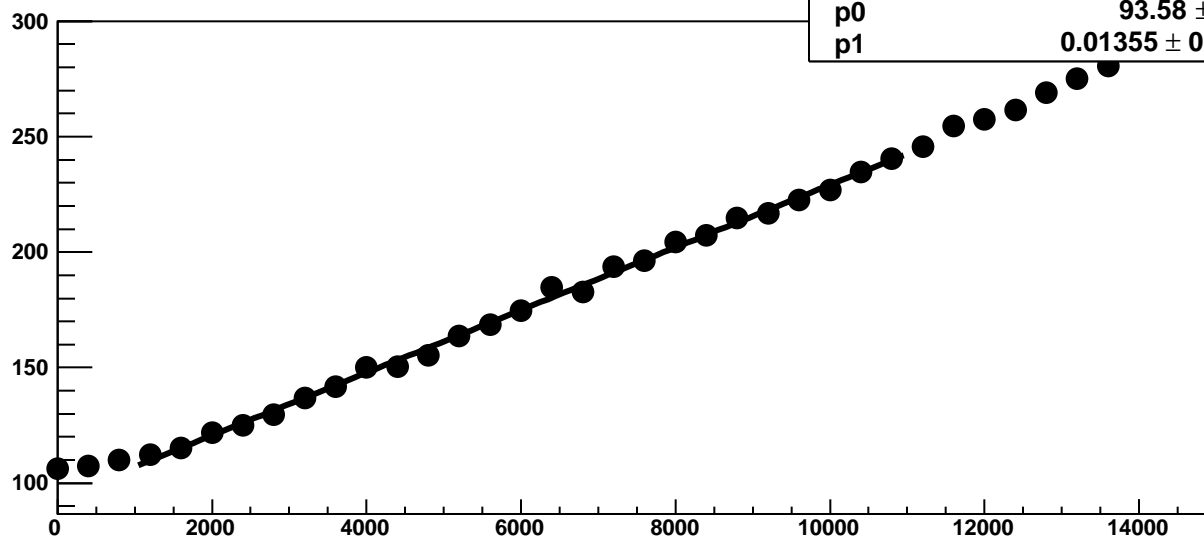
Chip 8, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

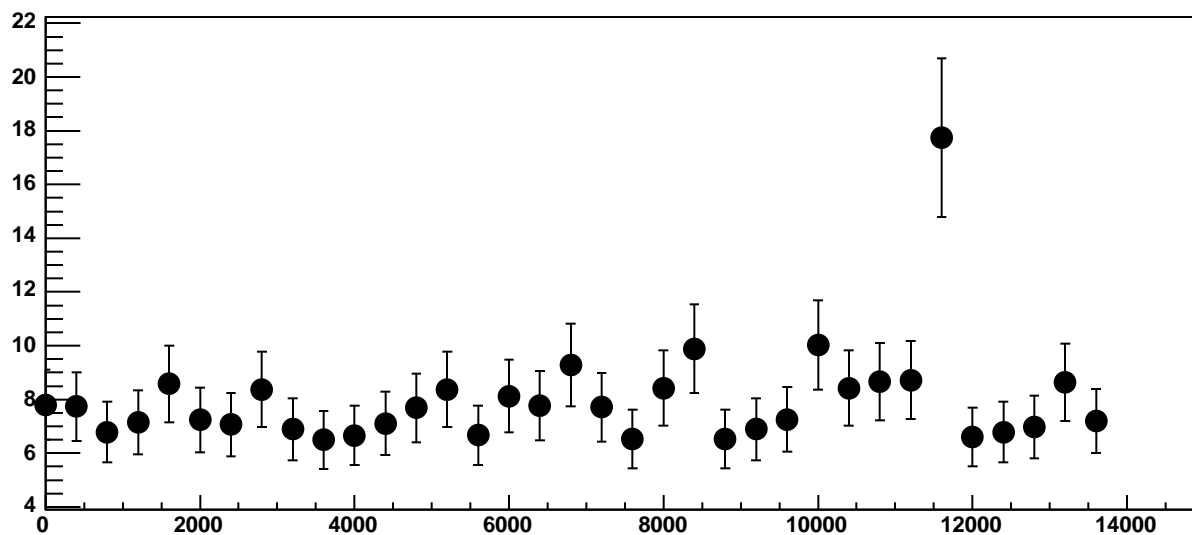


Chip 8, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

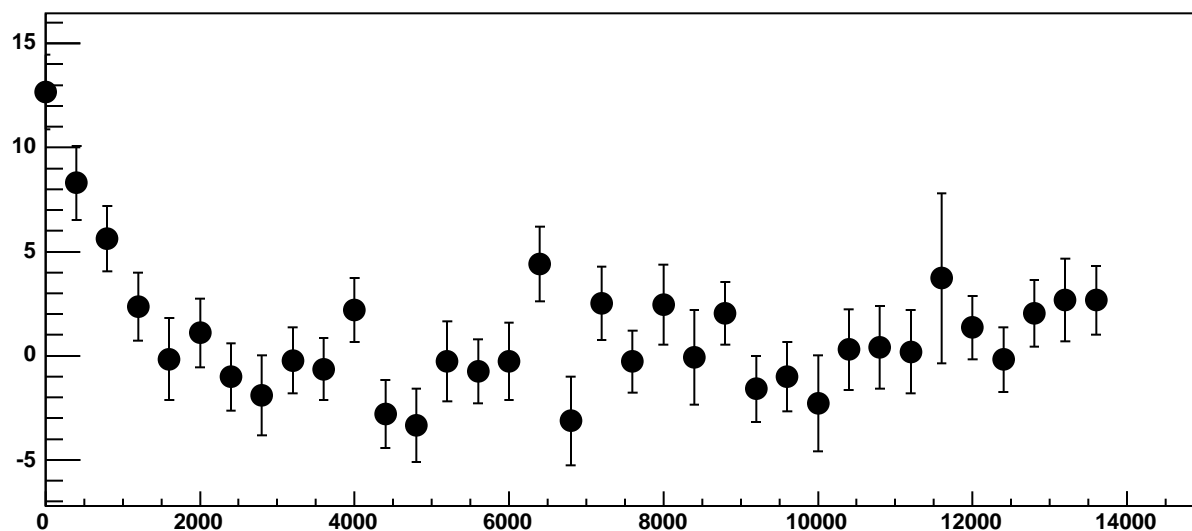


χ^2 / ndf 29.2 / 23
p0 93.58 ± 0.7933
p1 0.01355 ± 0.000123

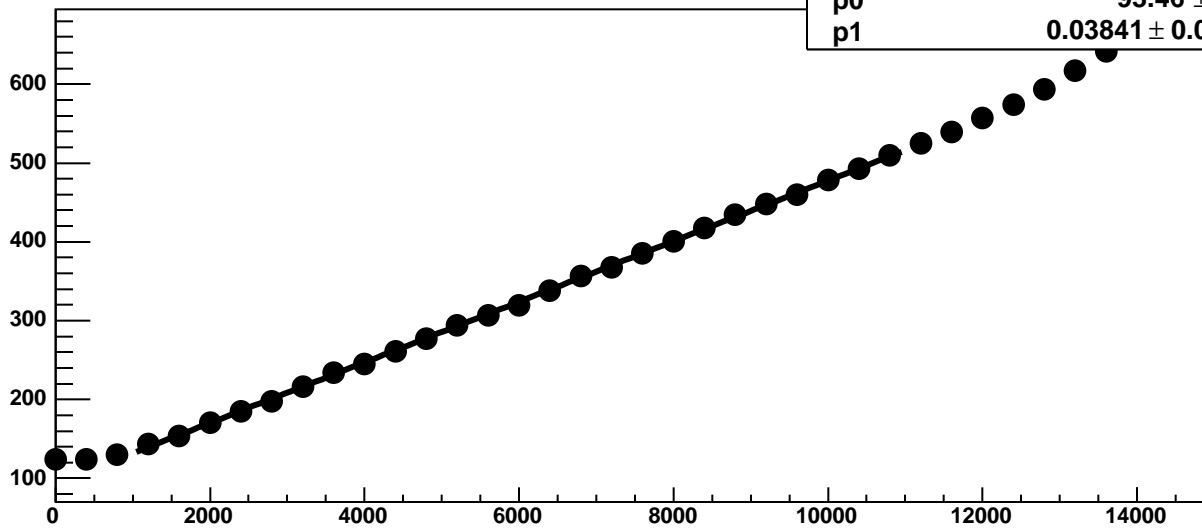
Chip 8, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

24.51 / 23

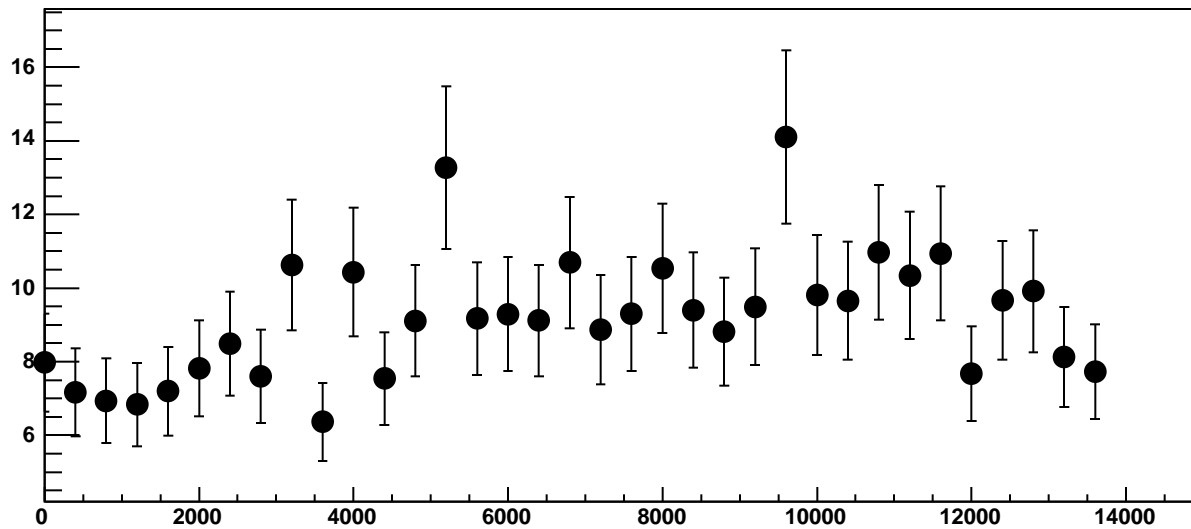
p0

93.46 ± 0.8555

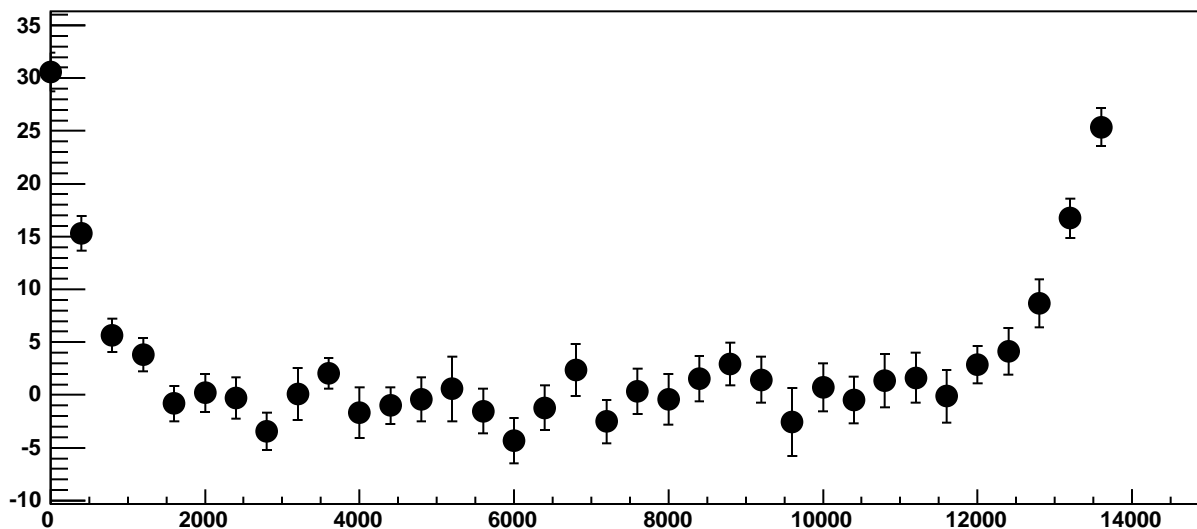
p1

0.03841 ± 0.0001404

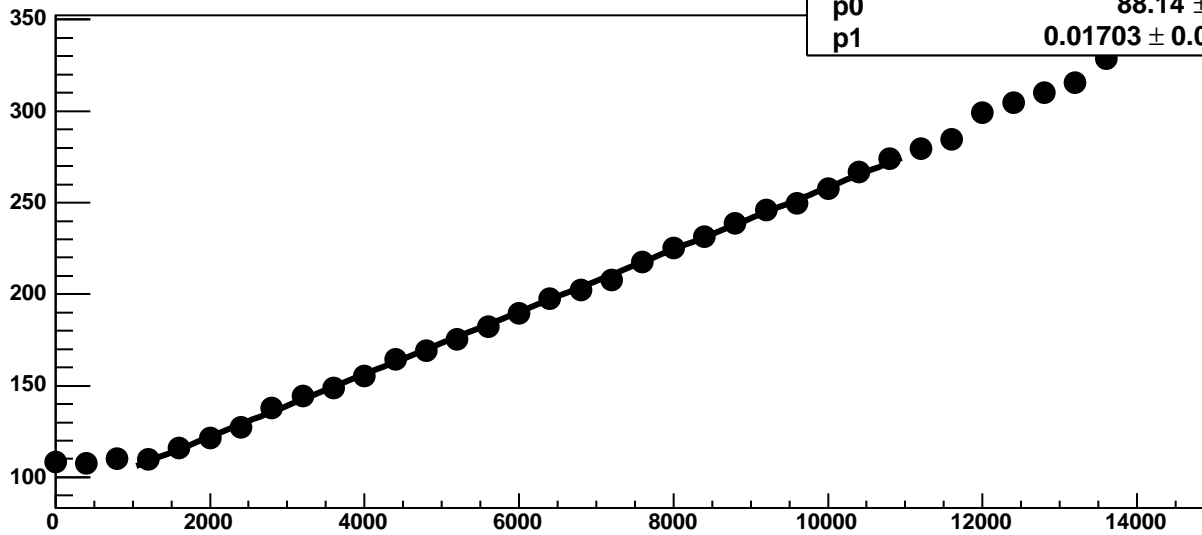
Chip 8, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC

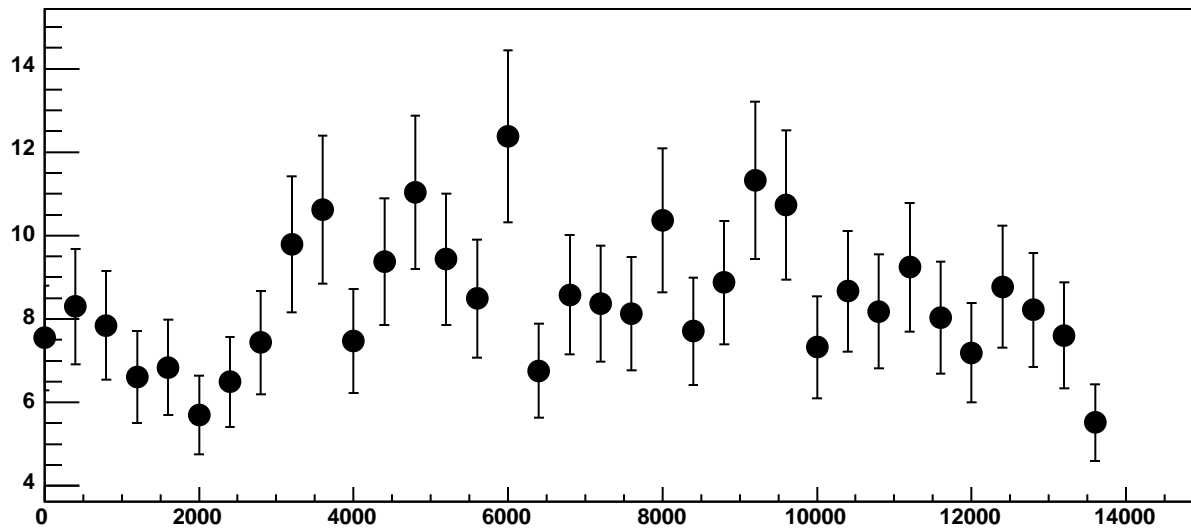


Chip 8, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

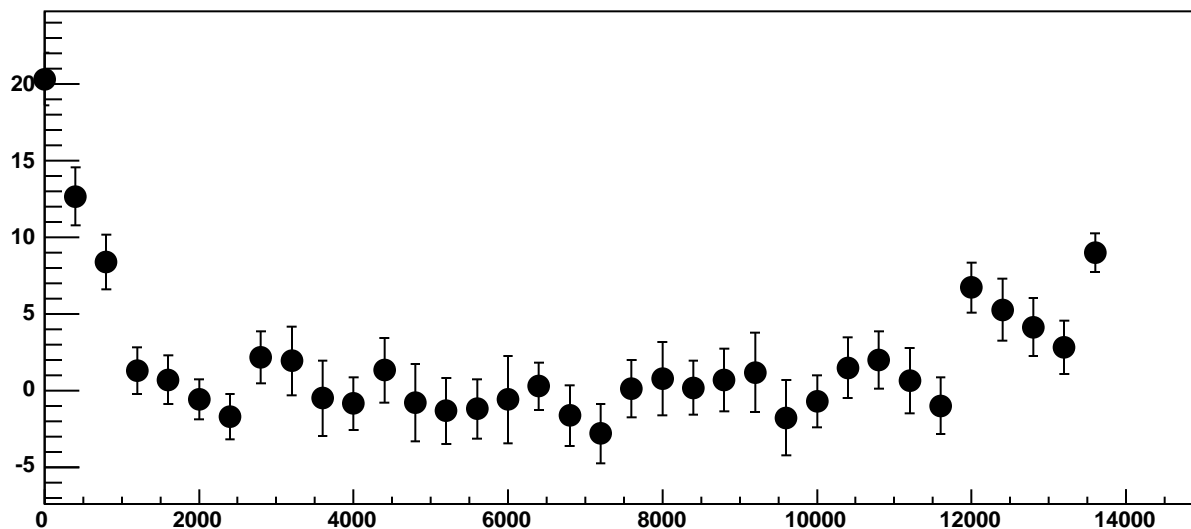


χ^2 / ndf 12.08 / 23
p0 88.14 ± 0.7735
p1 0.01703 ± 0.0001228

Chip 8, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

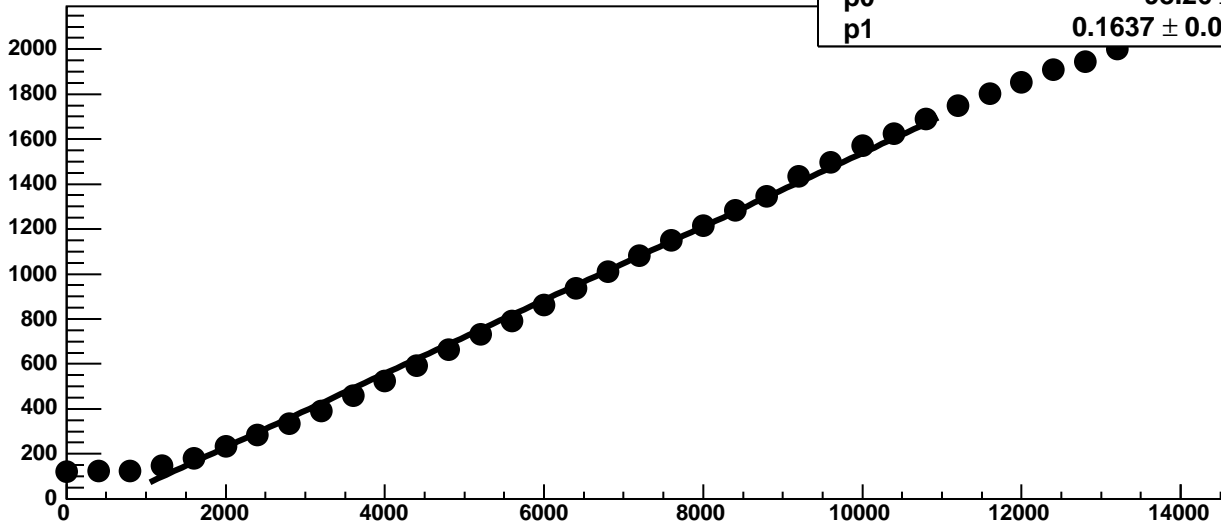


Chip 8, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

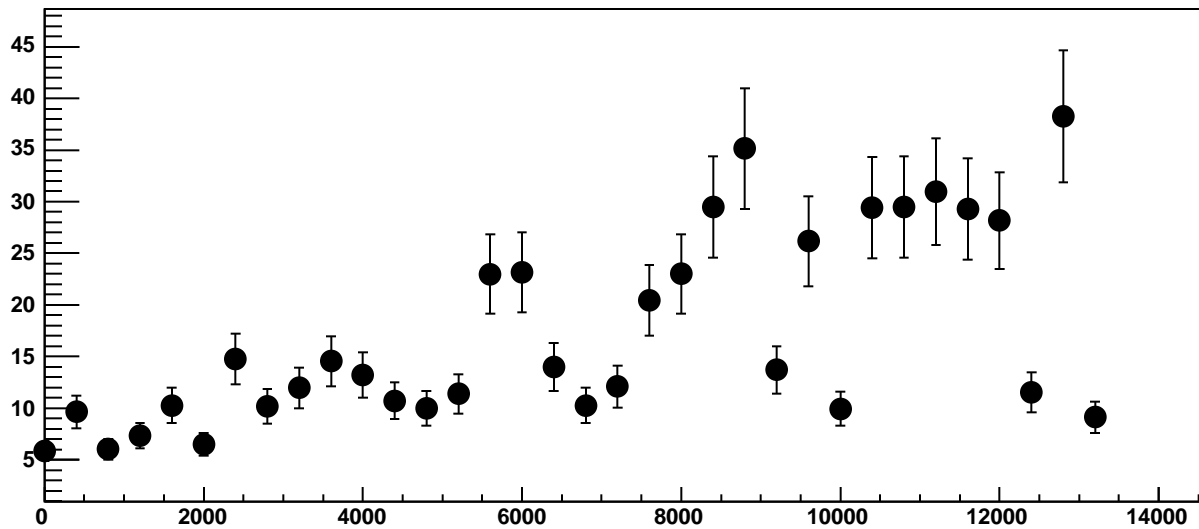


Chip 8, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

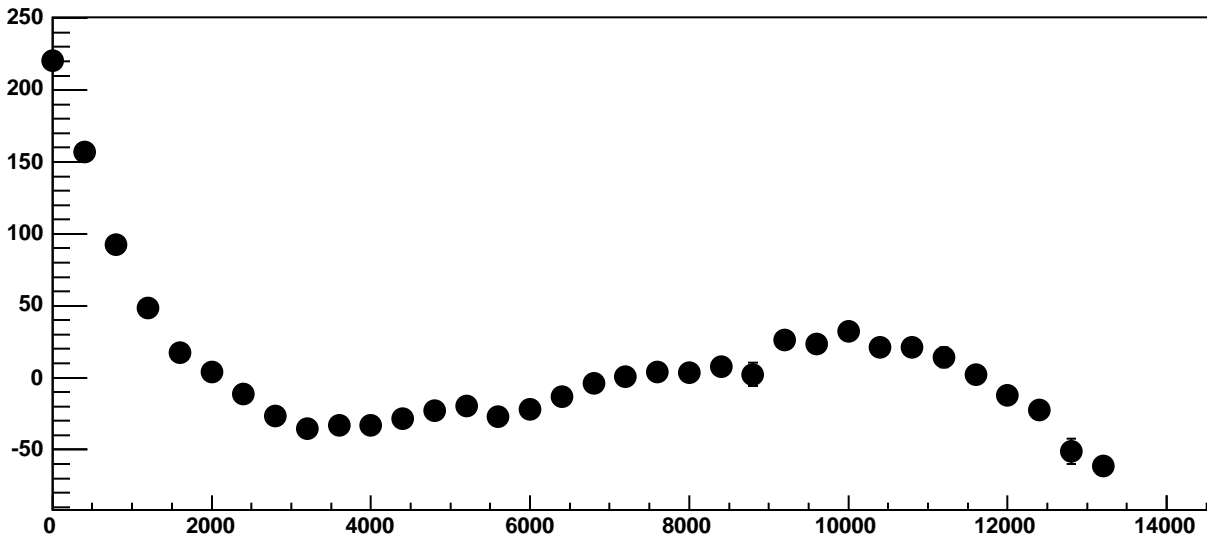
χ^2 / ndf 2079 / 23
p0 -98.26 ± 1.082
p1 0.1637 ± 0.0002051



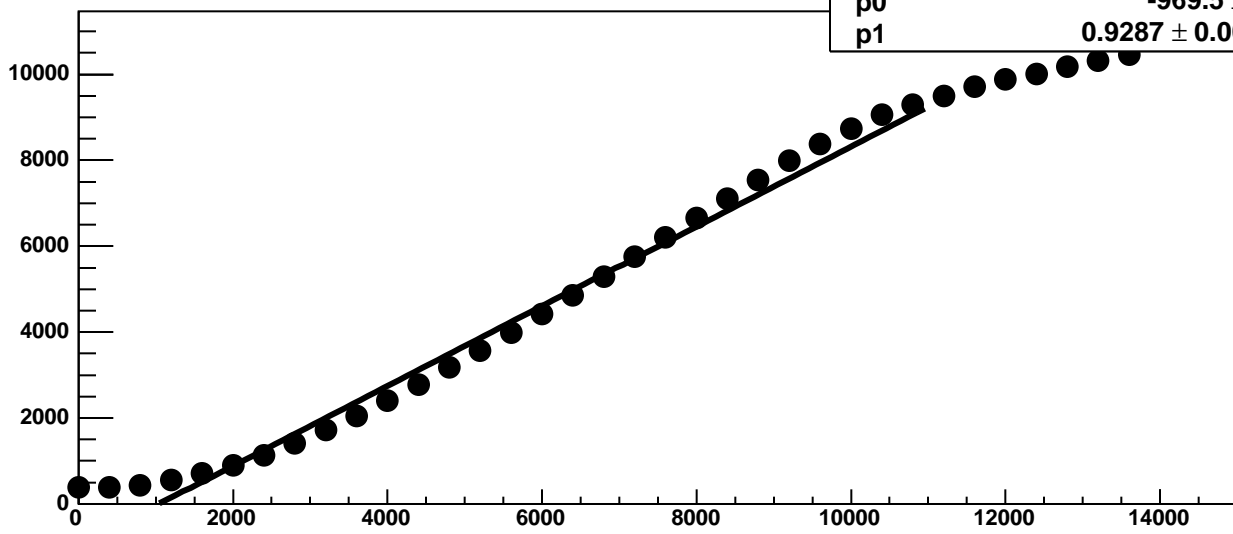
Chip 8, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



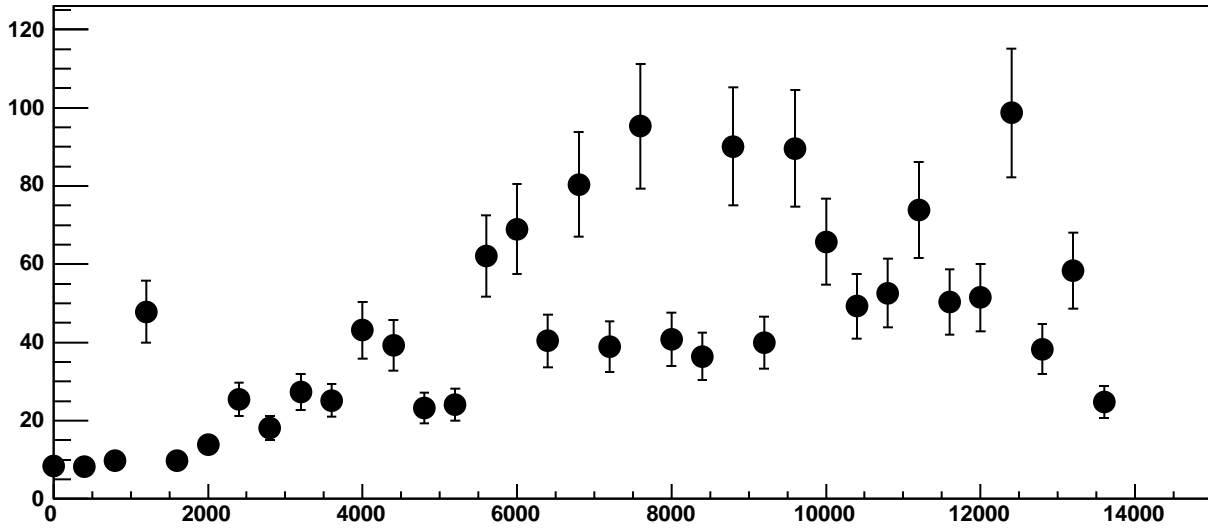
Chip 8, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



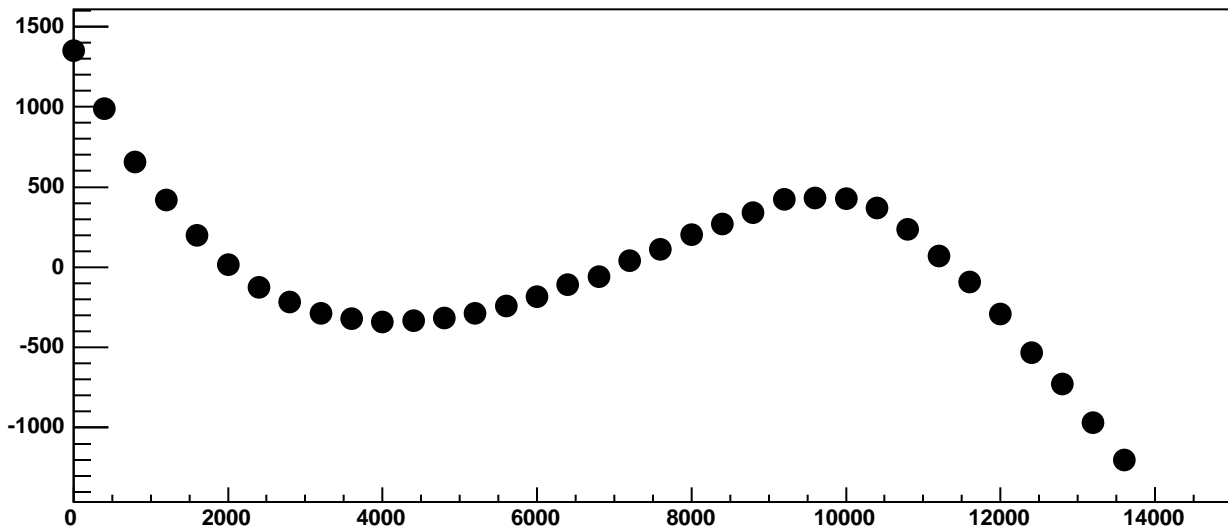
Chip 8, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC



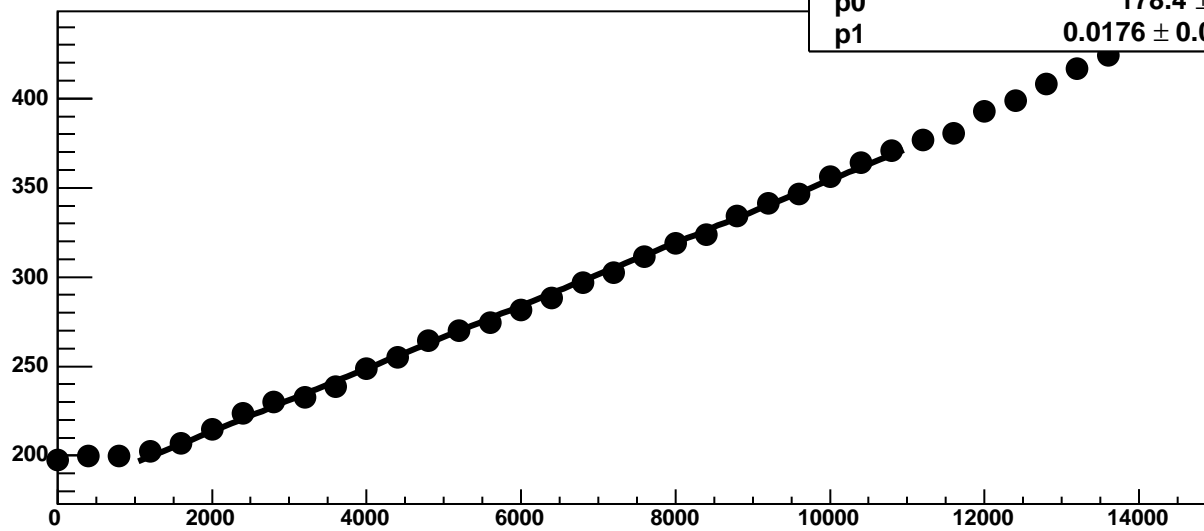
Chip 8, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC

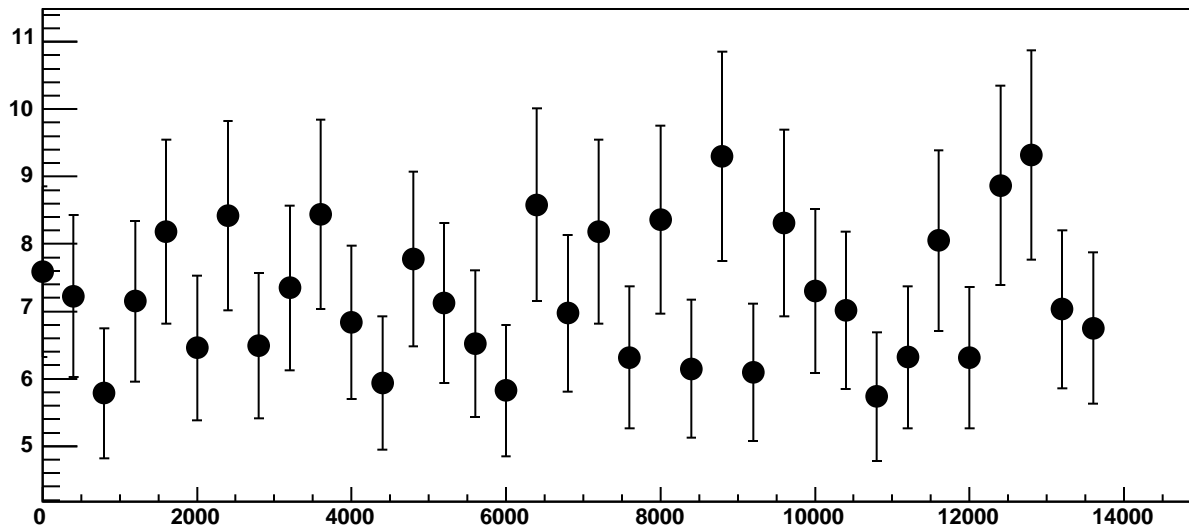


Chip 8, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

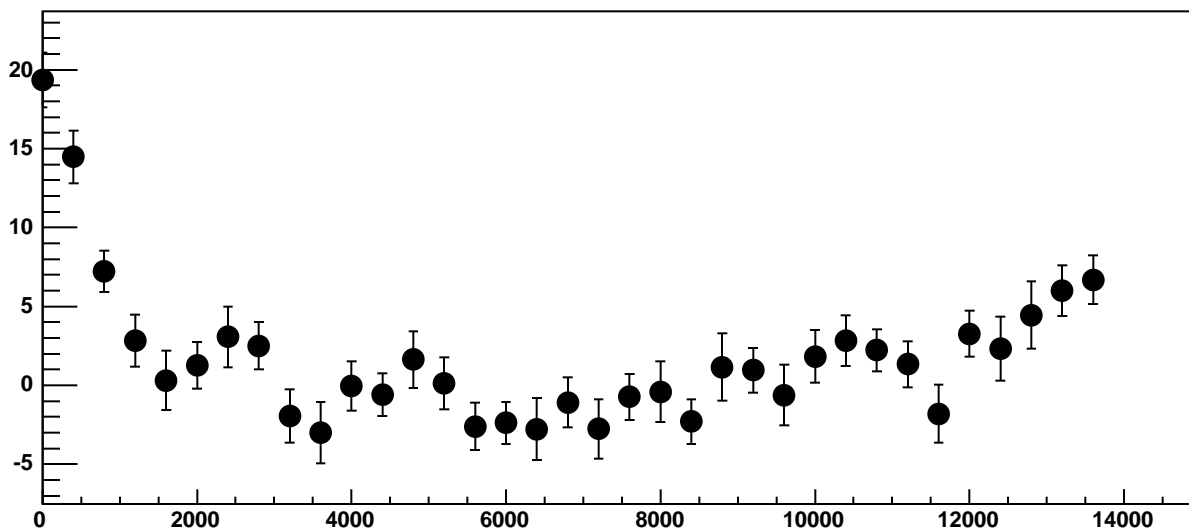


χ^2 / ndf 35.55 / 23
p0 178.4 ± 0.7536
p1 0.0176 ± 0.0001116

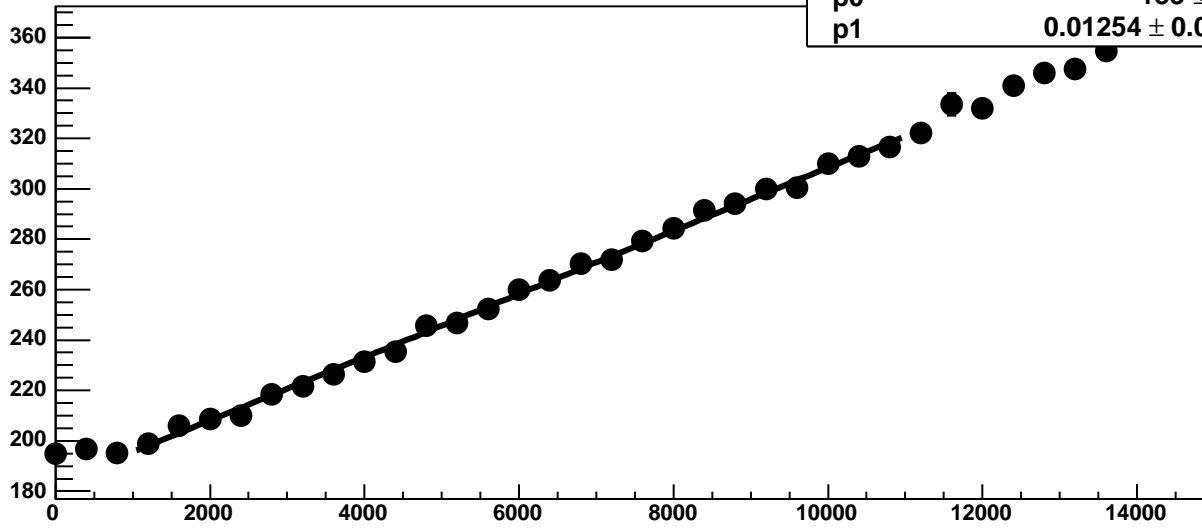
Chip 8, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



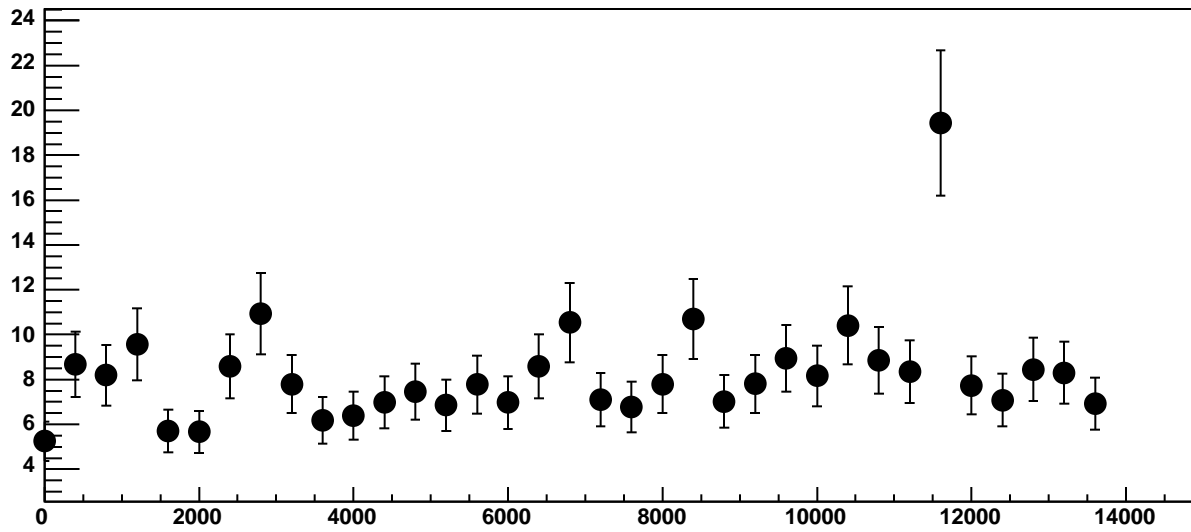
Chip 8, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



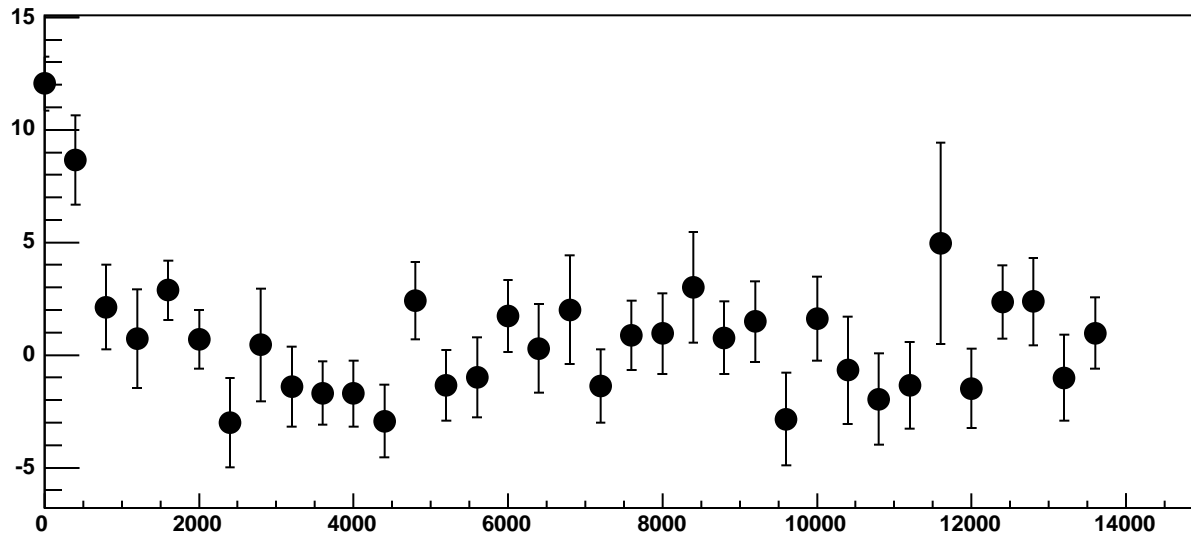
Chip 8, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



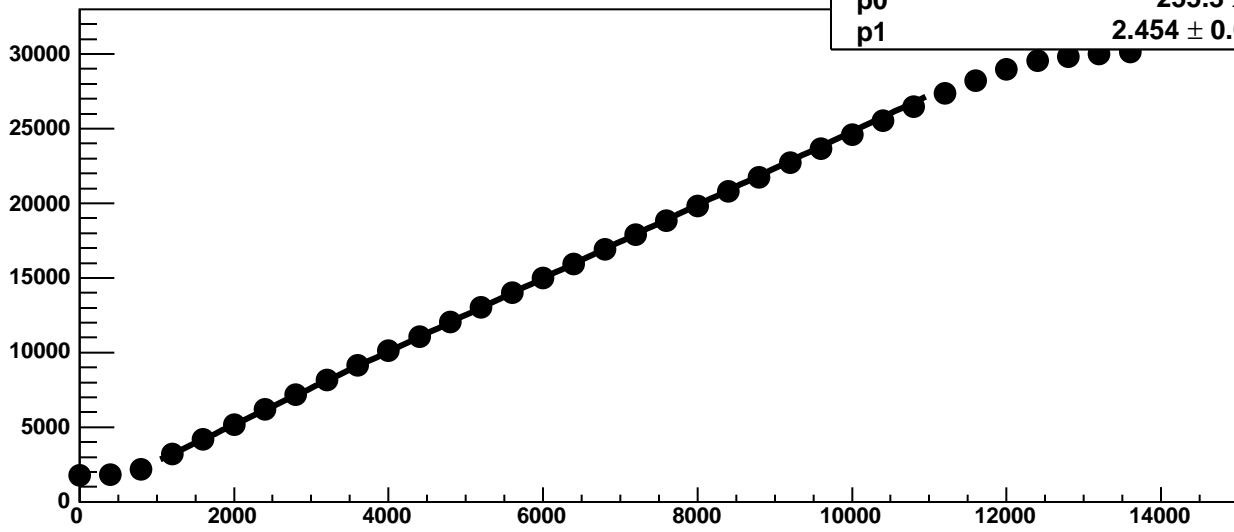
Chip 8, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



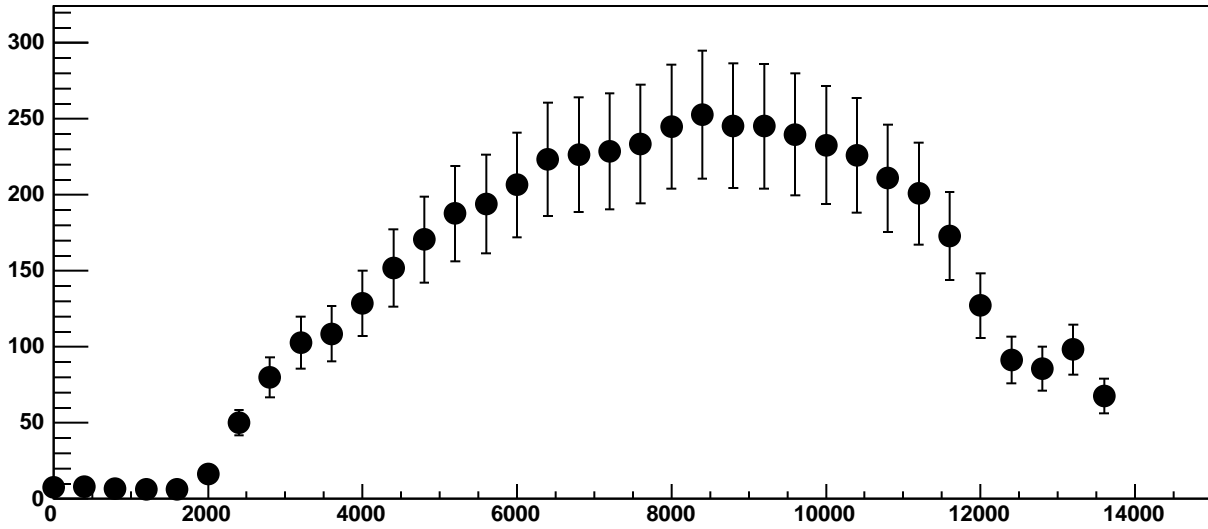
Chip 8, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



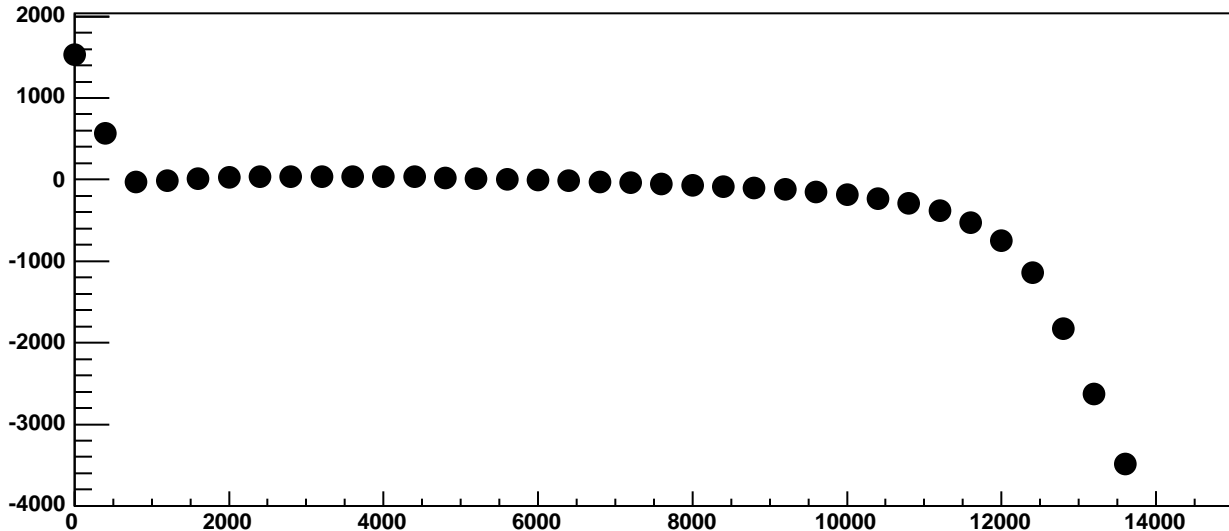
Chip 8, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC



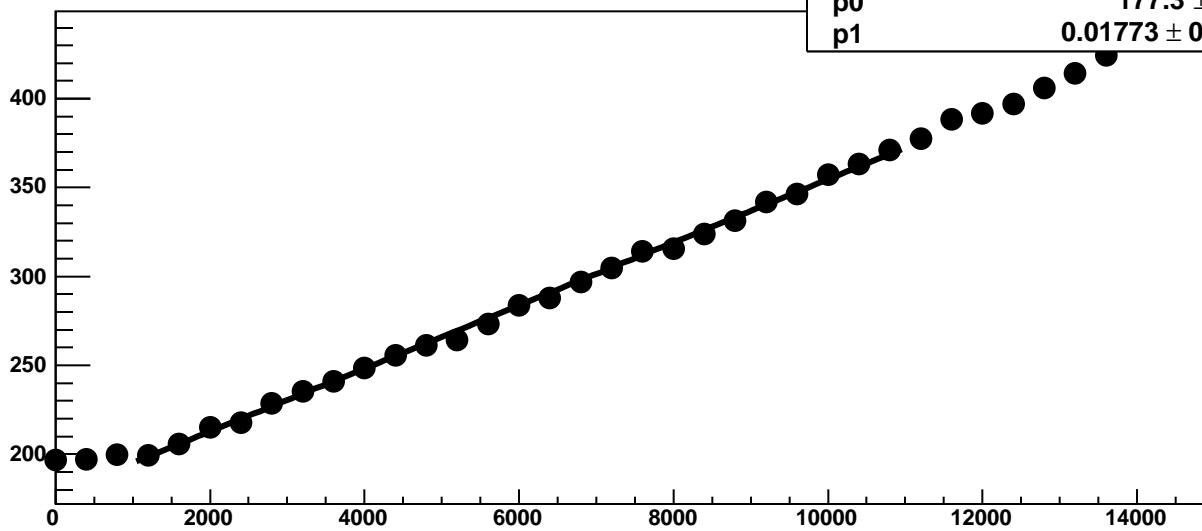
Chip 8, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC

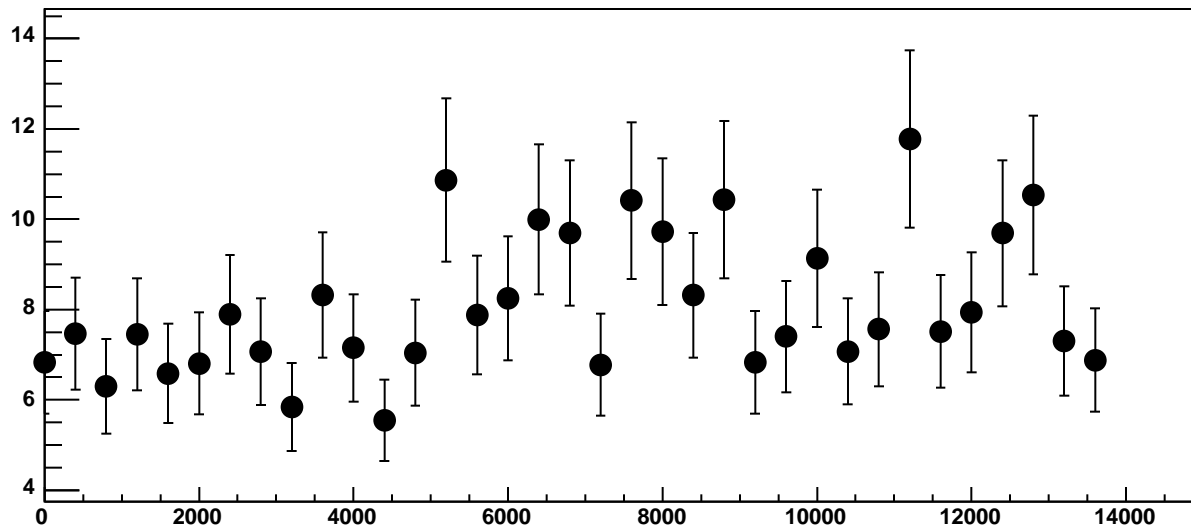


Chip 8, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

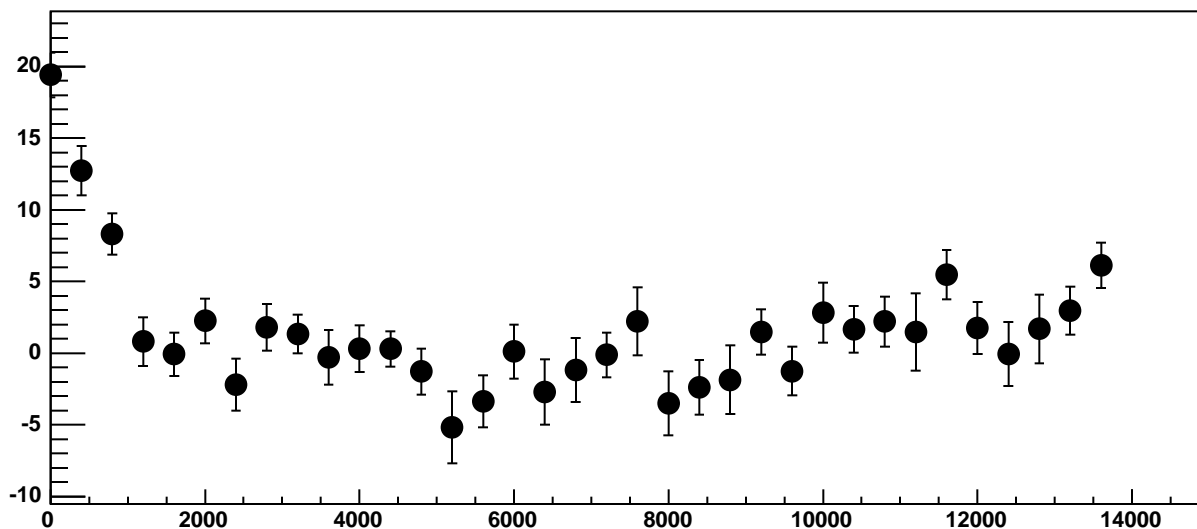


χ^2 / ndf 27.68 / 23
p0 177.3 ± 0.7519
p1 0.01773 ± 0.000118

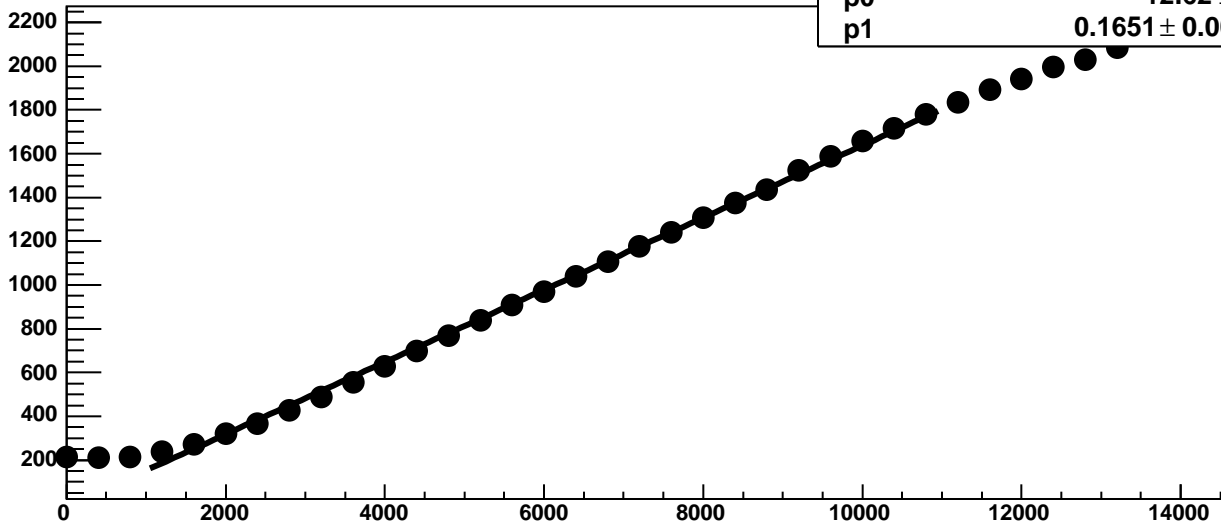
Chip 8, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



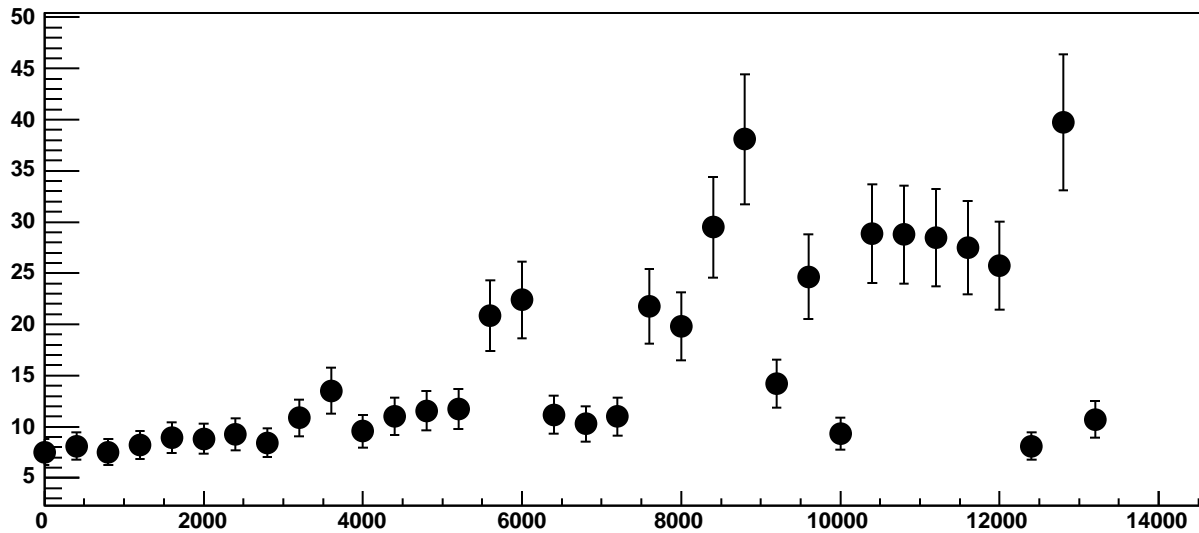
Chip 8, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



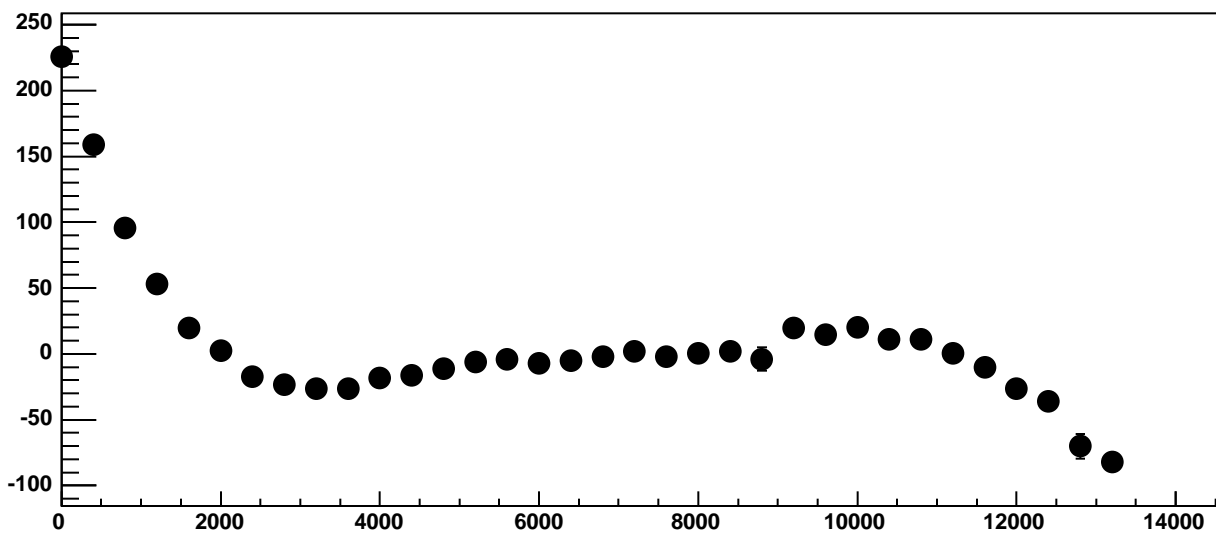
Chip 8, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



Chip 8, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

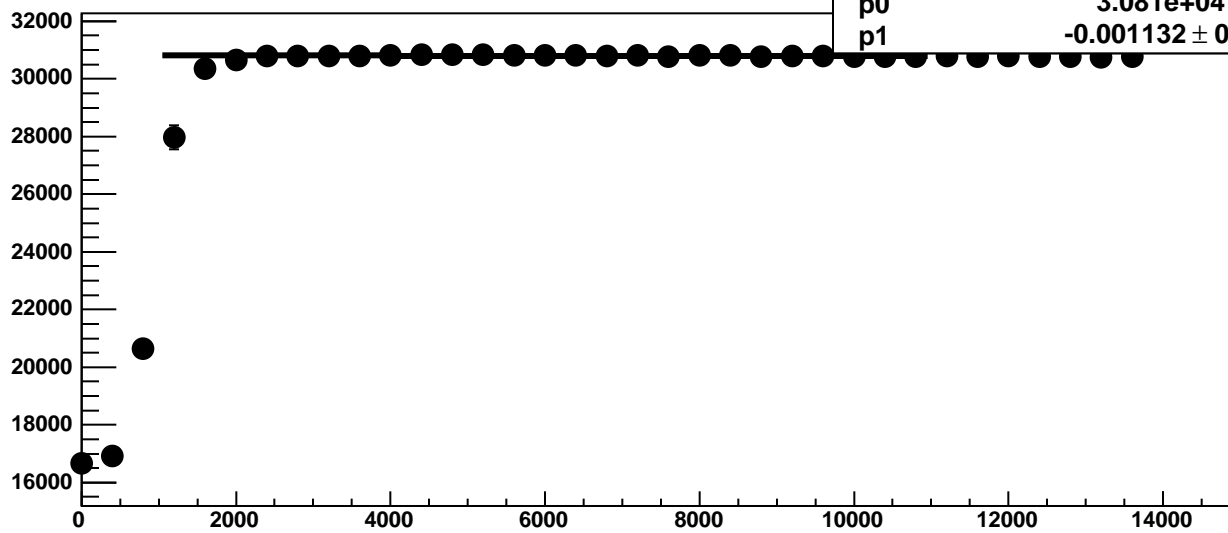


Chip 8, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

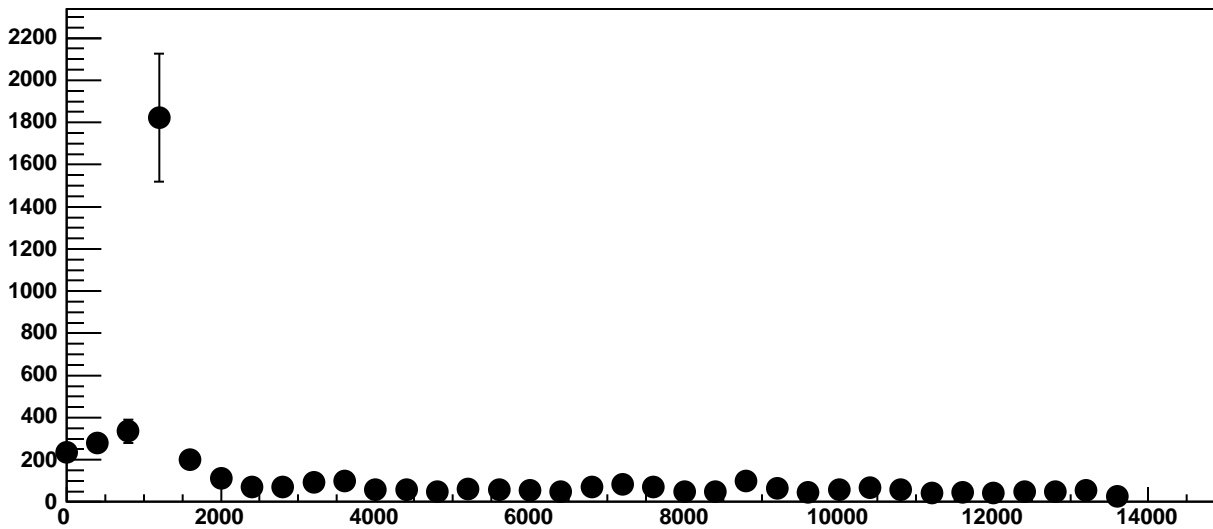


Chip 8, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC

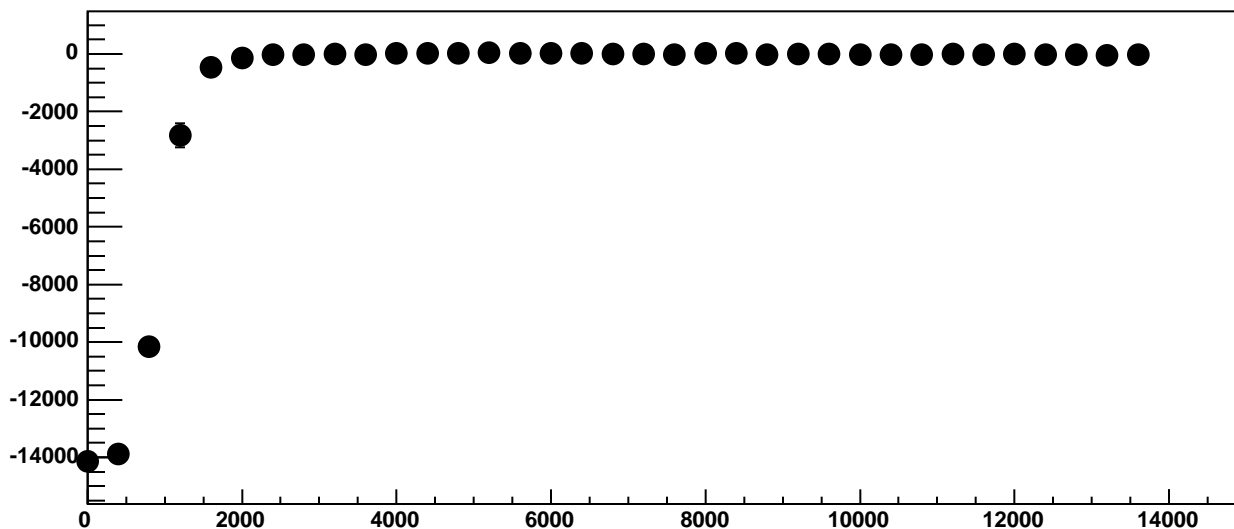
χ^2 / ndf 219.4 / 23
p0 $3.081\text{e}+04 \pm 8.622$
p1 -0.001132 ± 0.001196



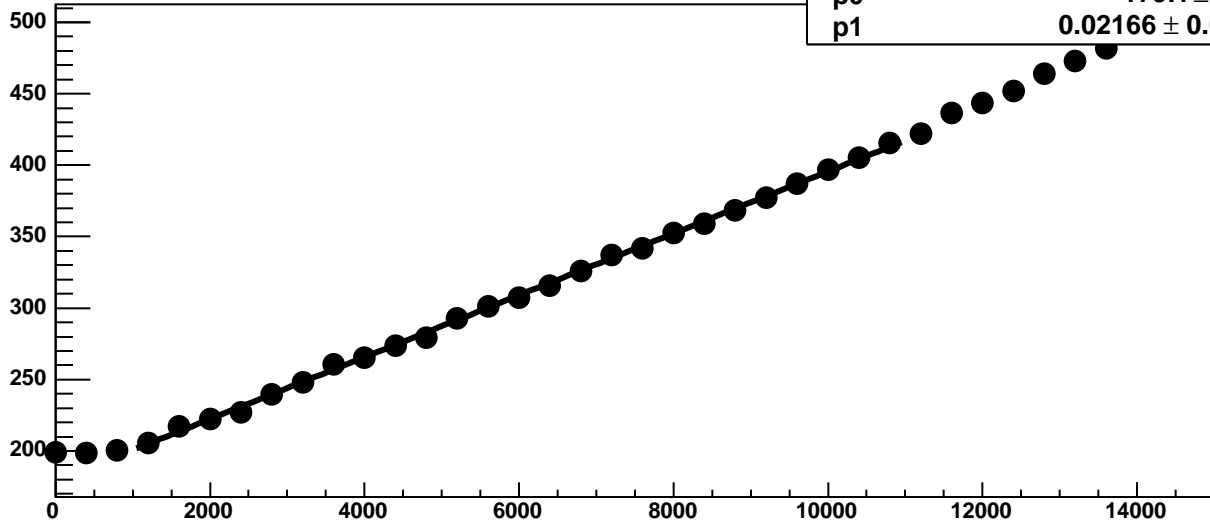
Chip 8, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

31.53 / 23

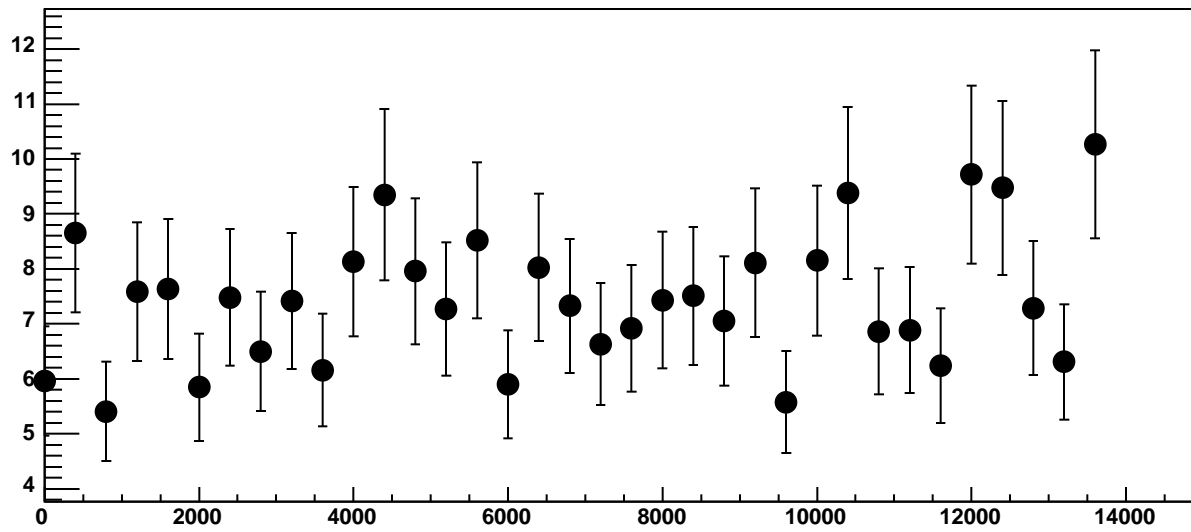
p0

179.1 ± 0.7544

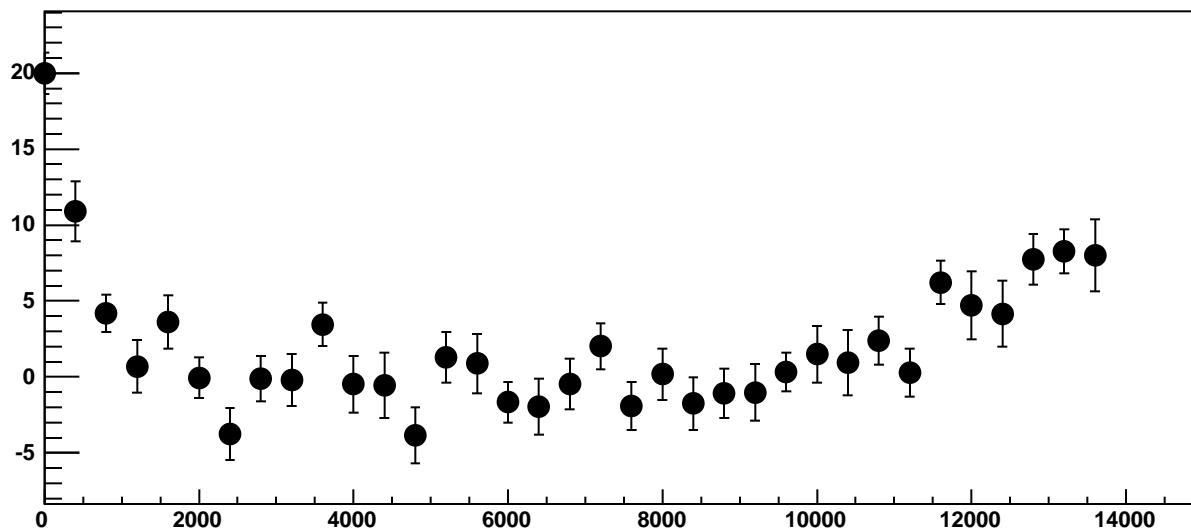
p1

0.02166 ± 0.000114

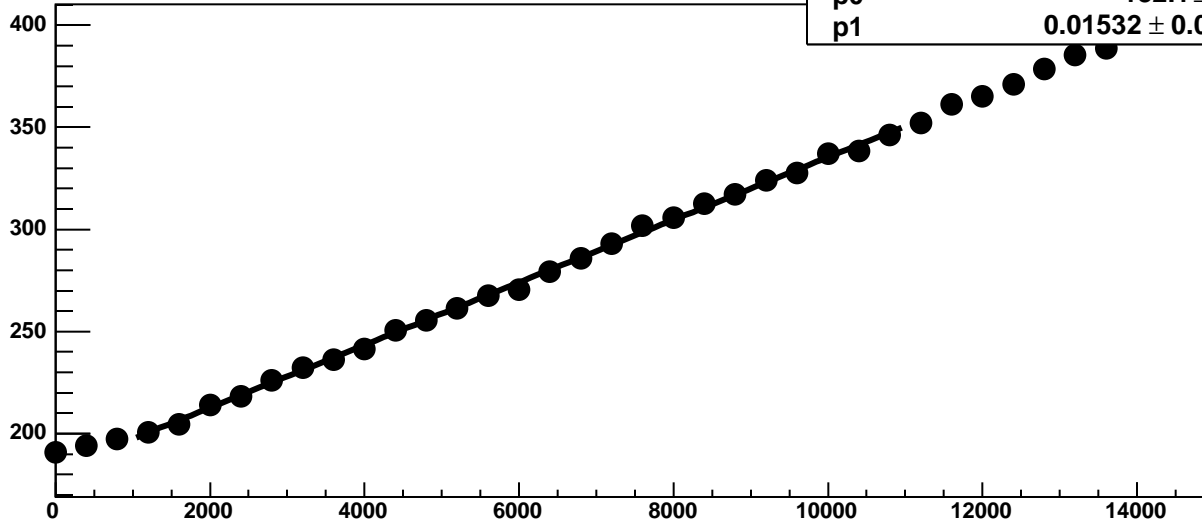
Chip 8, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

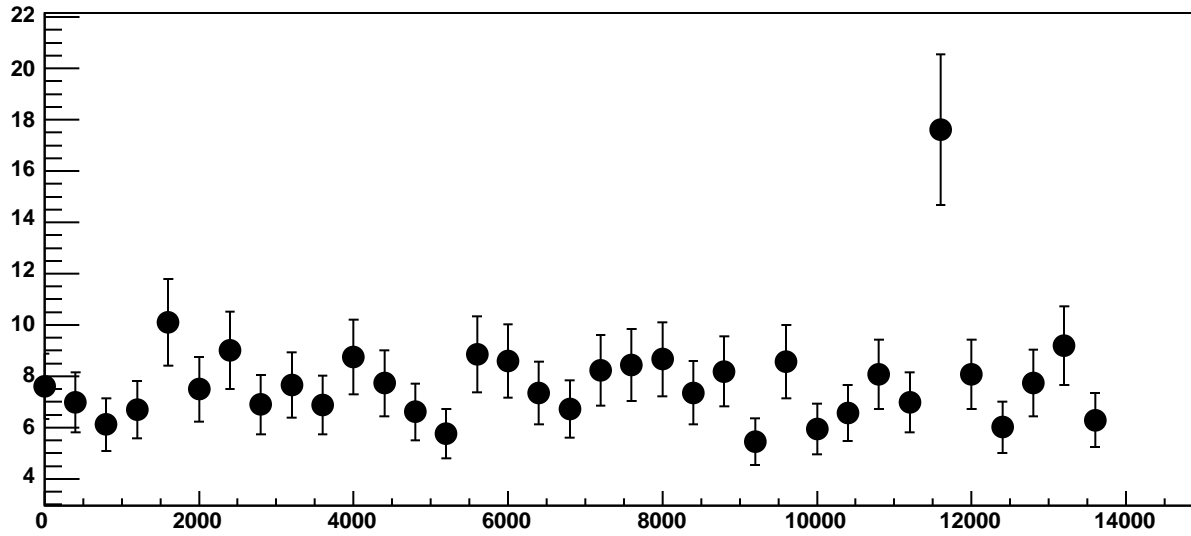


Chip 8, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

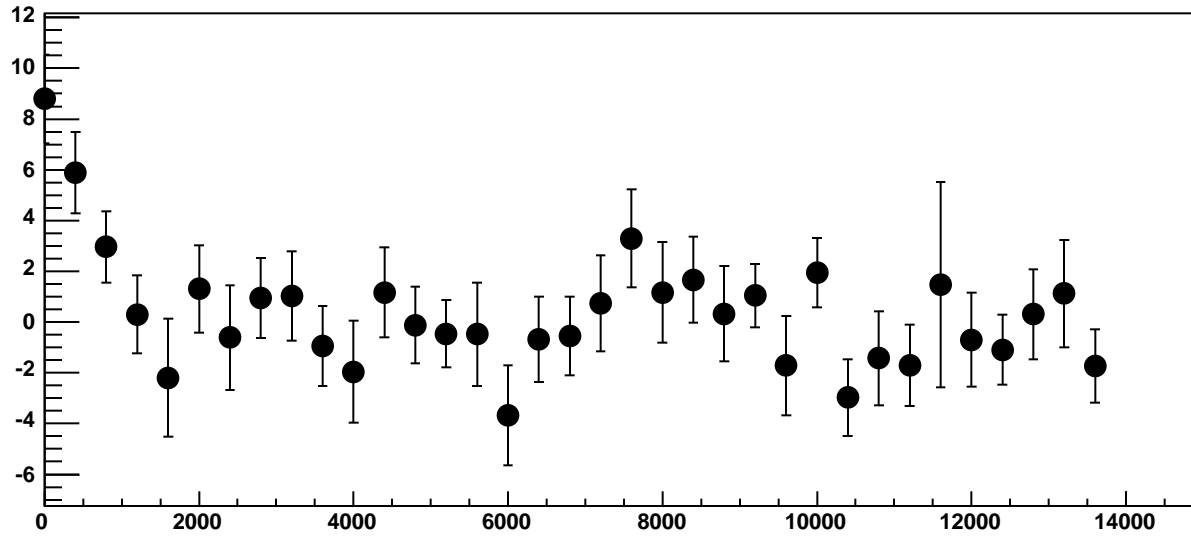


χ^2 / ndf 20.35 / 23
p0 182.1 ± 0.7953
p1 0.01532 ± 0.0001165

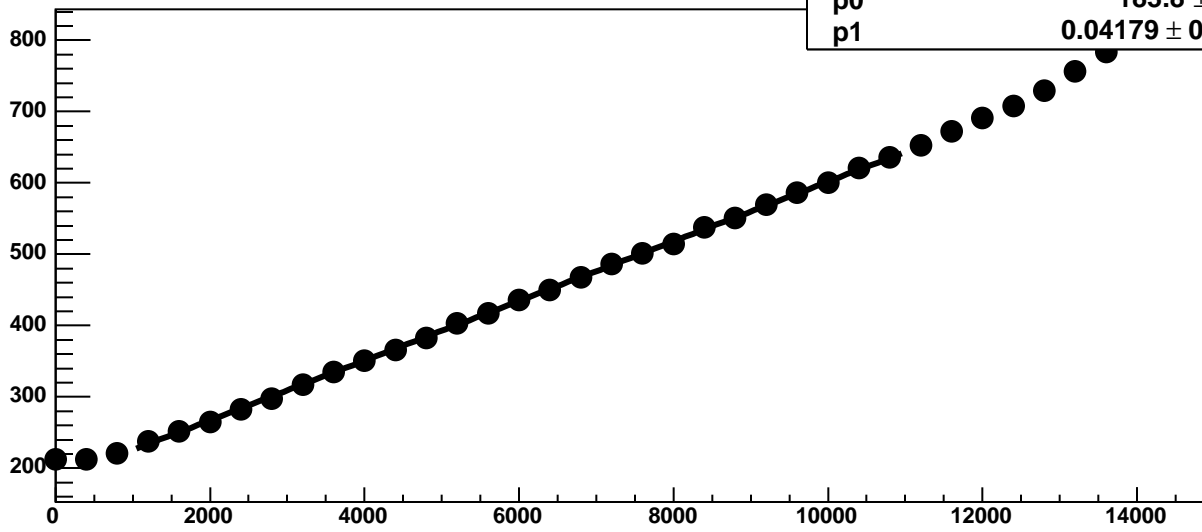
Chip 8, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

19.34 / 23

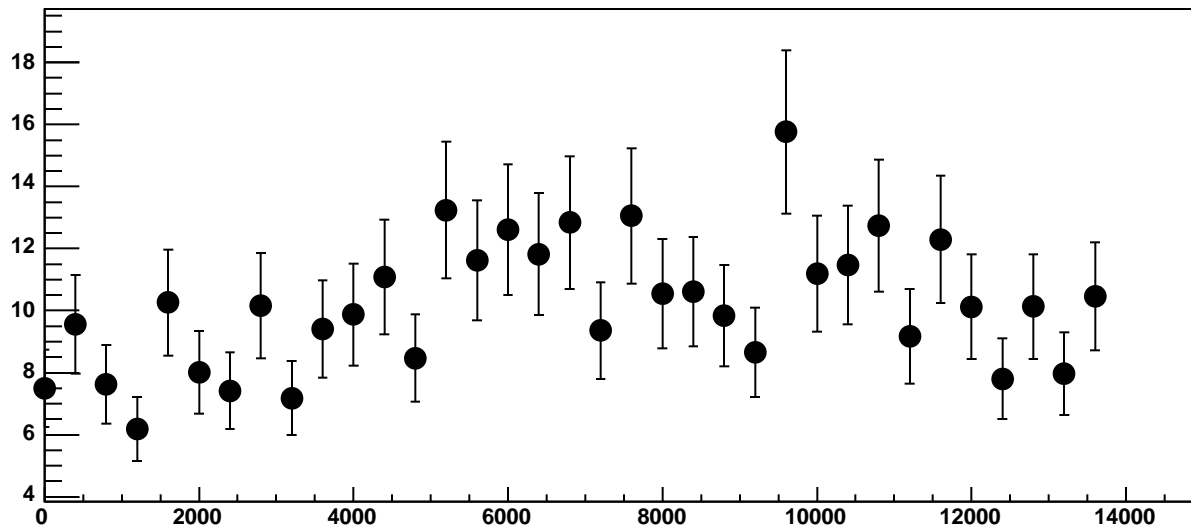
p0

183.8 ± 0.9019

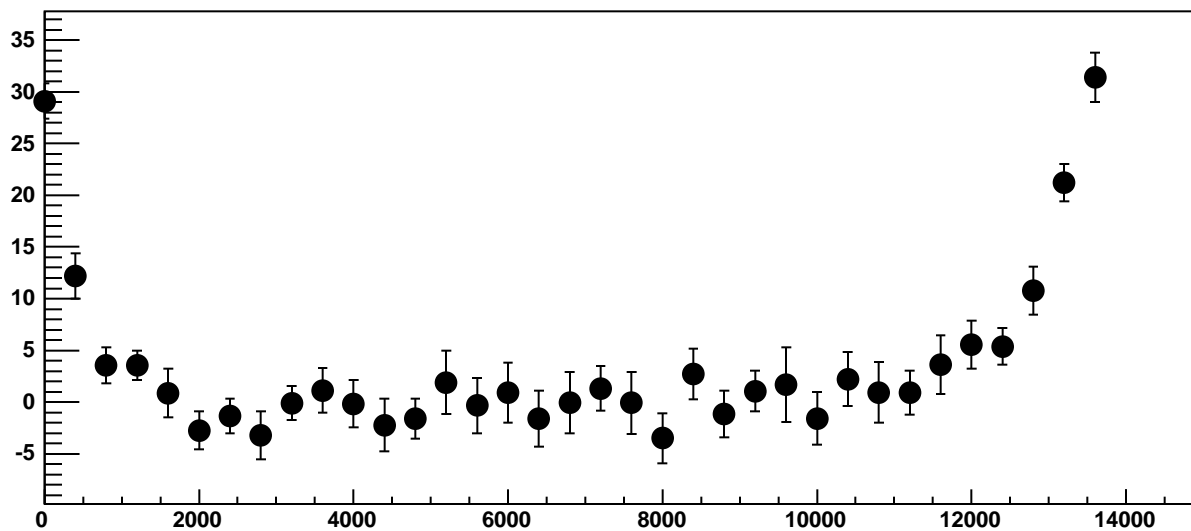
p1

0.04179 ± 0.000151

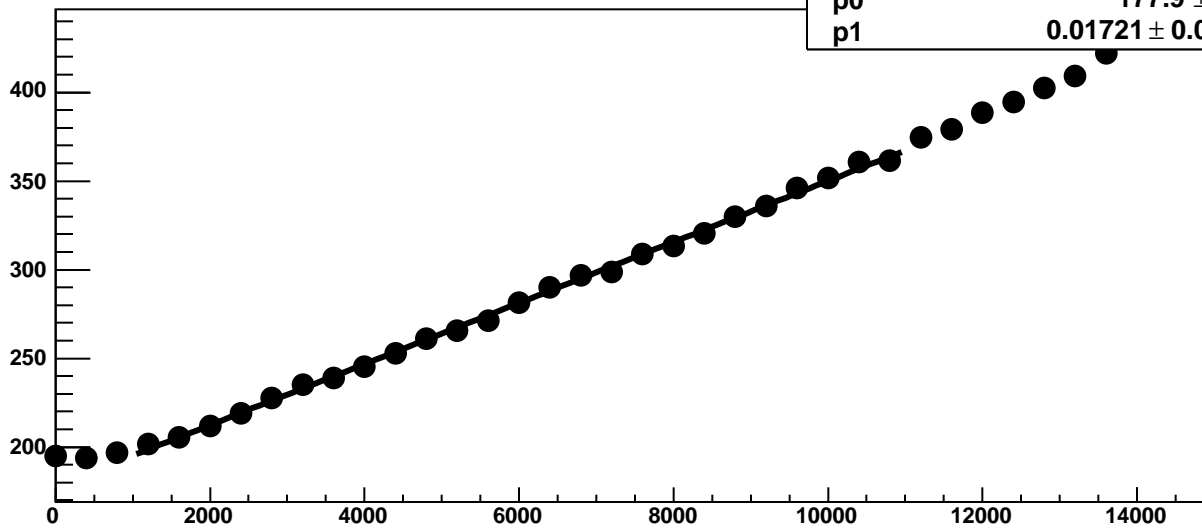
Chip 8, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

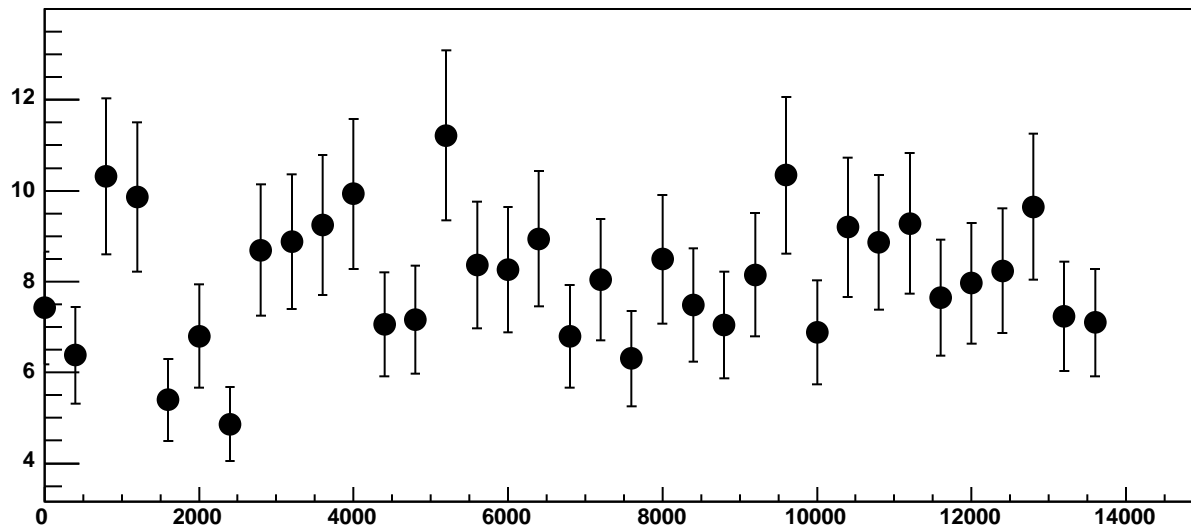


Chip 8, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

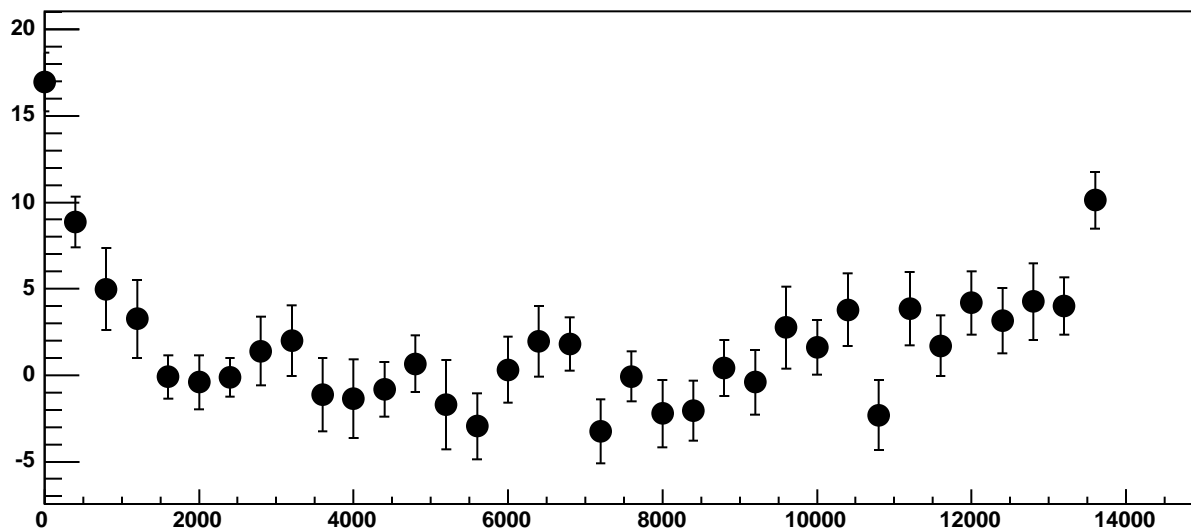


χ^2 / ndf 22.59 / 23
p0 177.9 ± 0.7576
p1 0.01721 ± 0.0001188

Chip 8, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

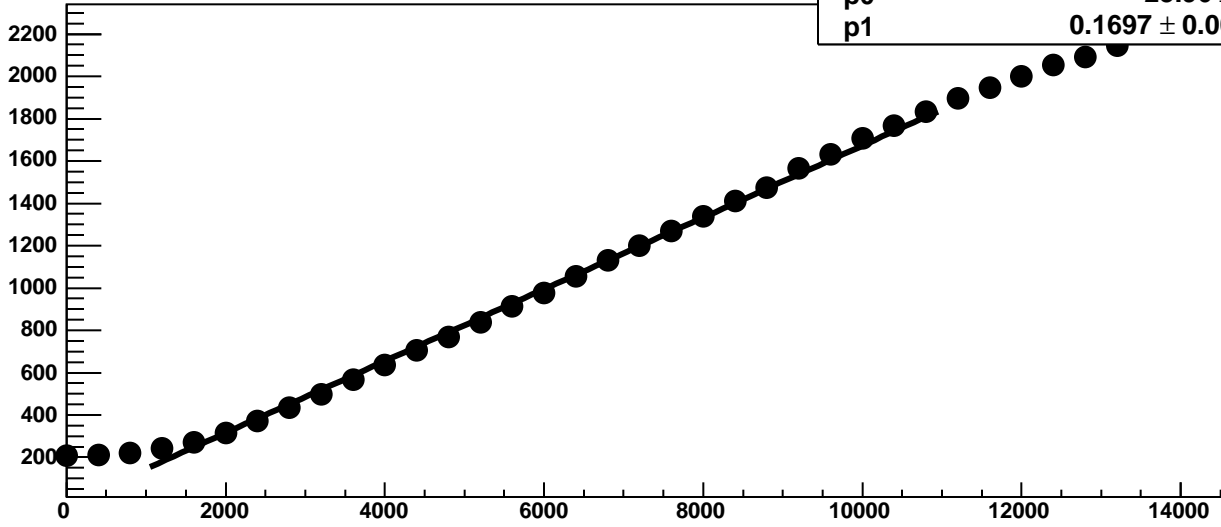


Chip 8, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

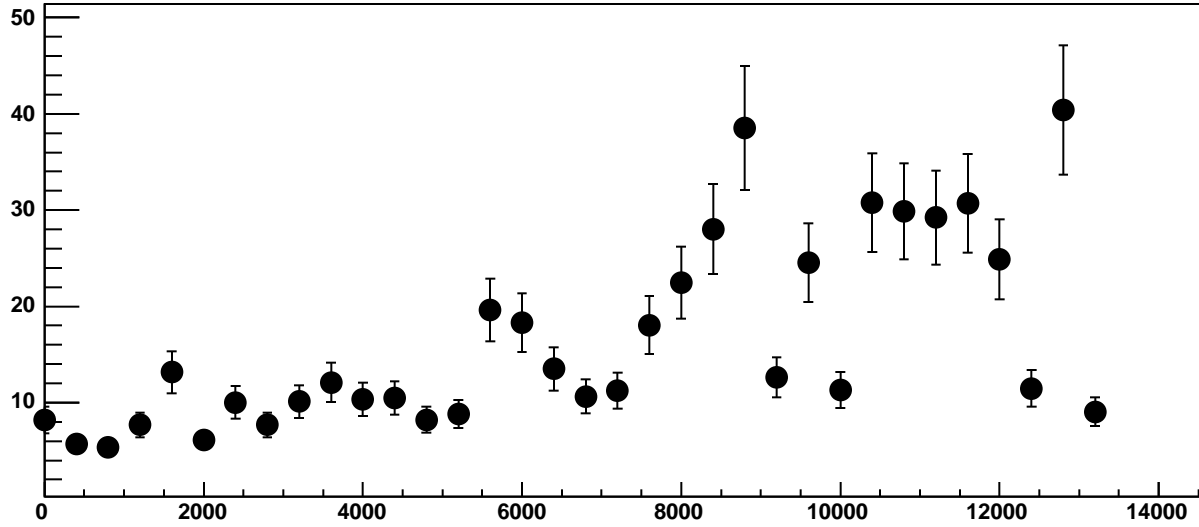


Chip 8, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

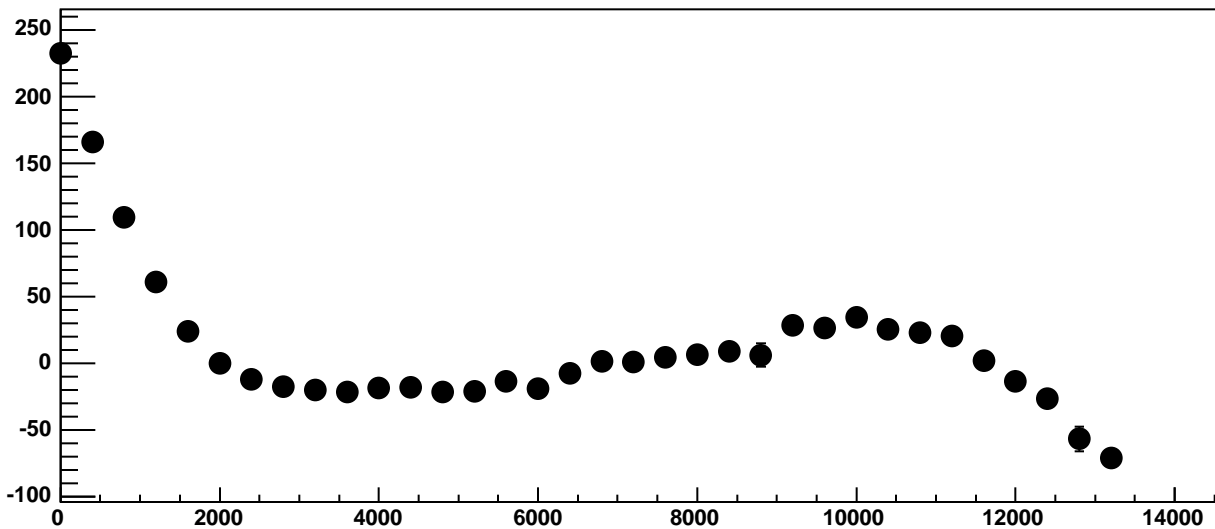
χ^2 / ndf 2254 / 23
p0 -23.96 ± 1.043
p1 0.1697 ± 0.0002058



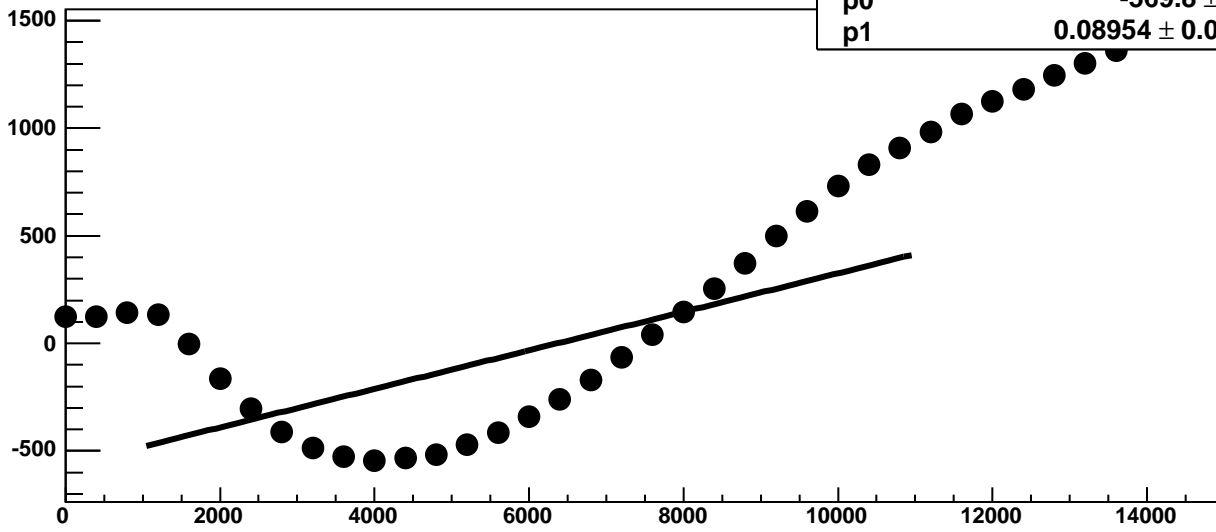
Chip 8, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

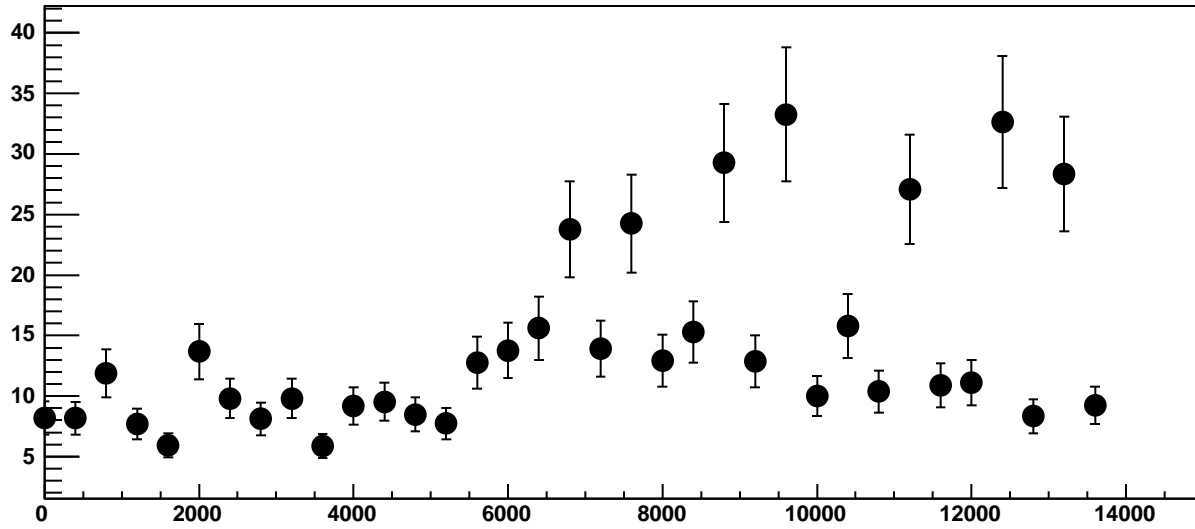


Chip 8, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

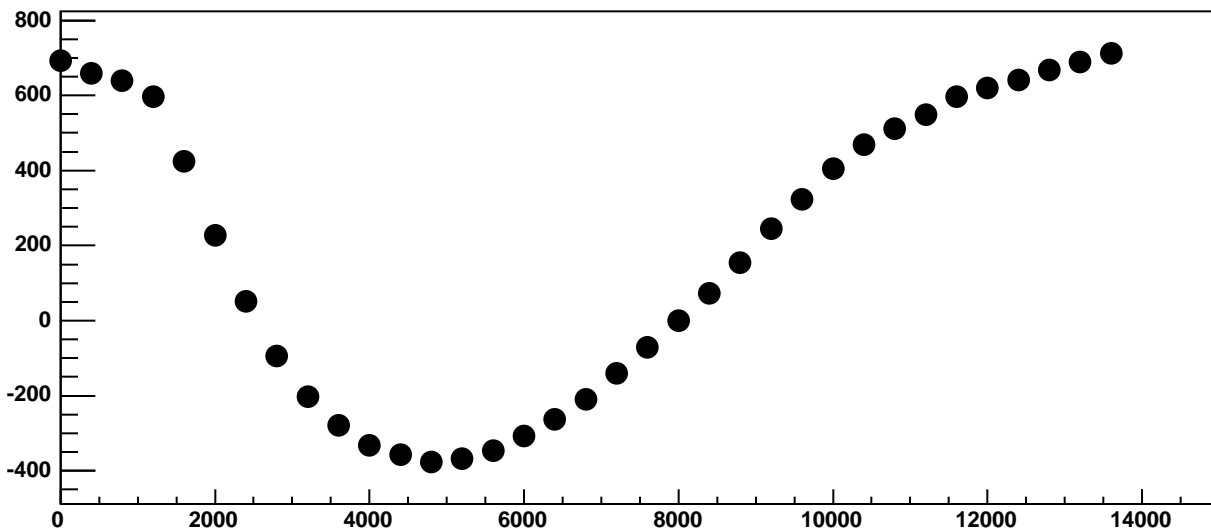


χ^2 / ndf 5.398e+05 / 23
p0 -569.8 ± 0.9166
p1 0.08954 ± 0.0001699

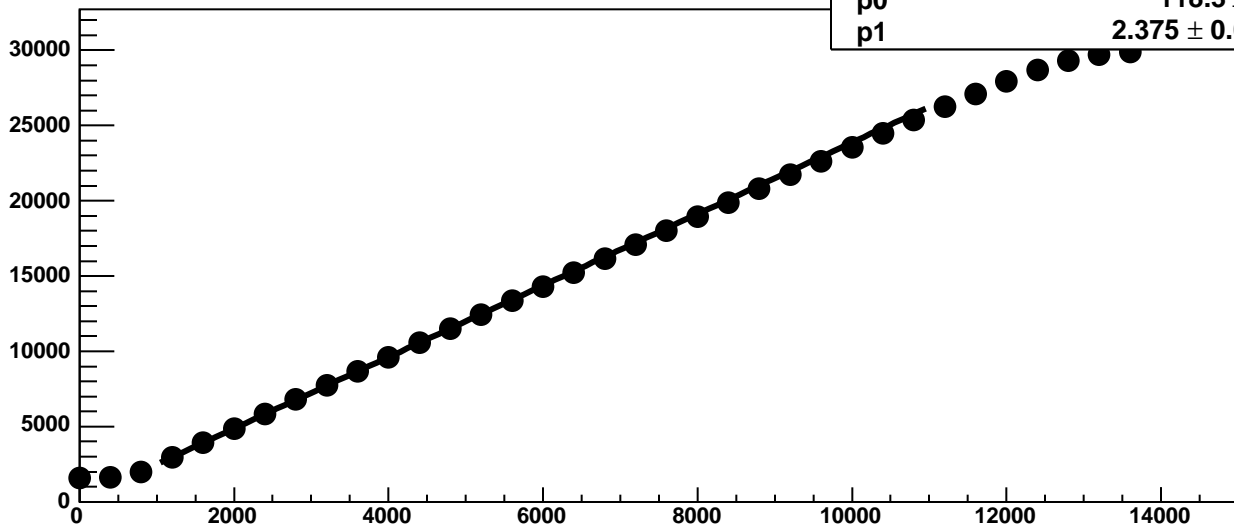
Chip 8, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



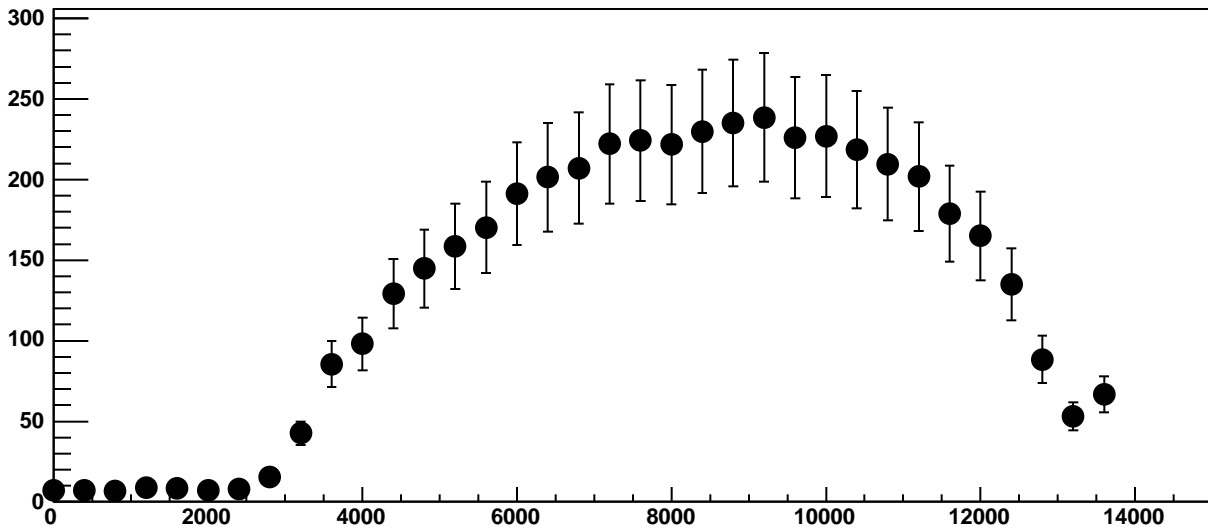
Chip 8, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



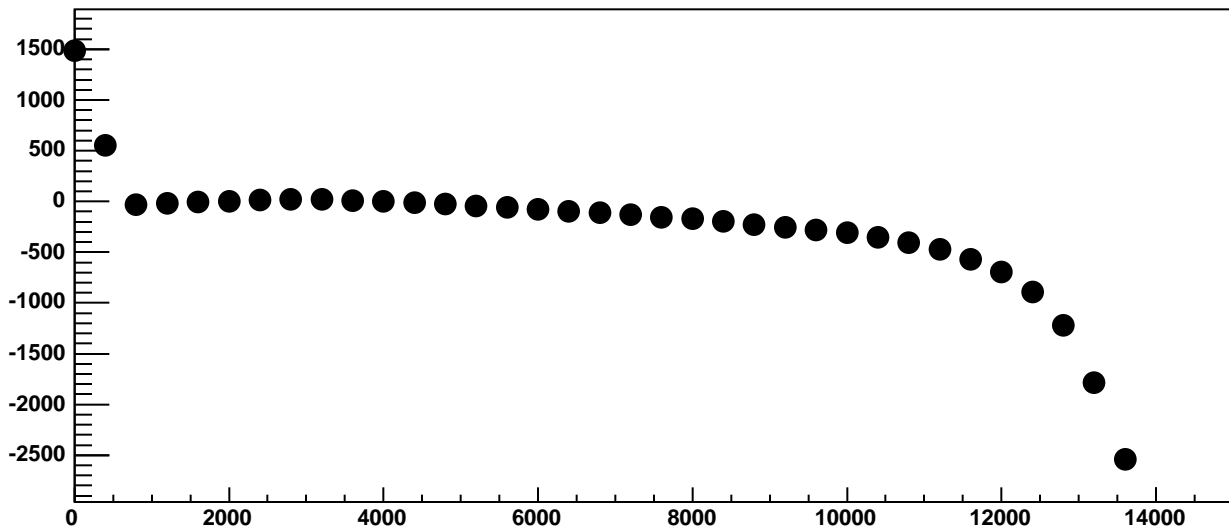
Chip 8, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC



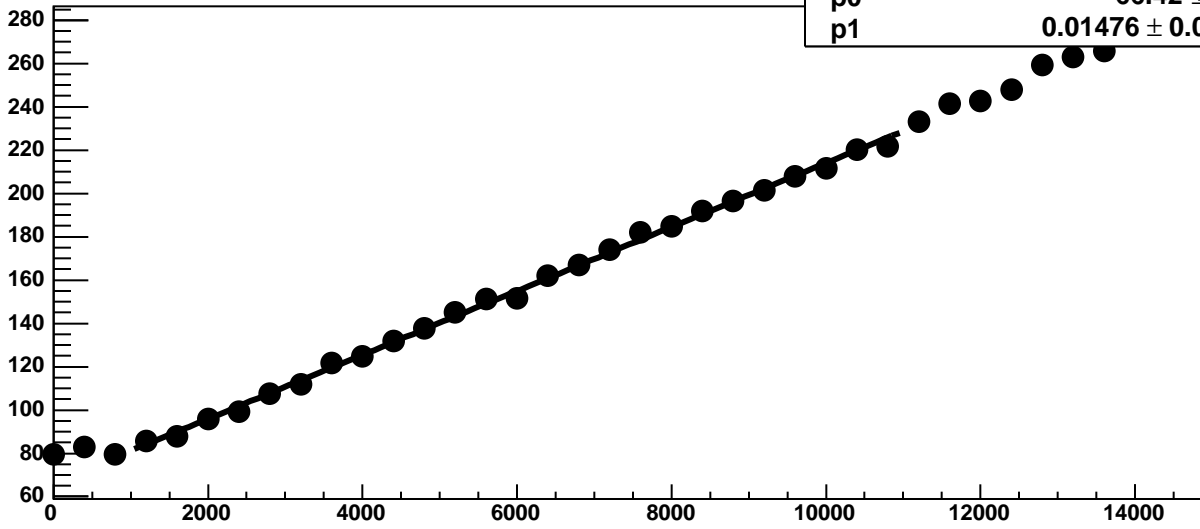
Chip 8, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC

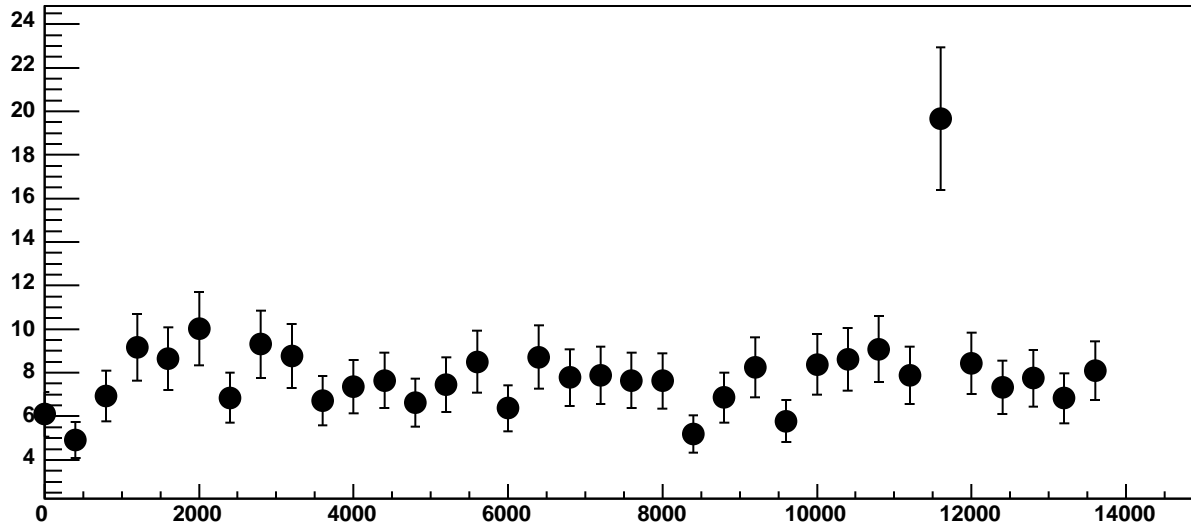


Chip 8, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

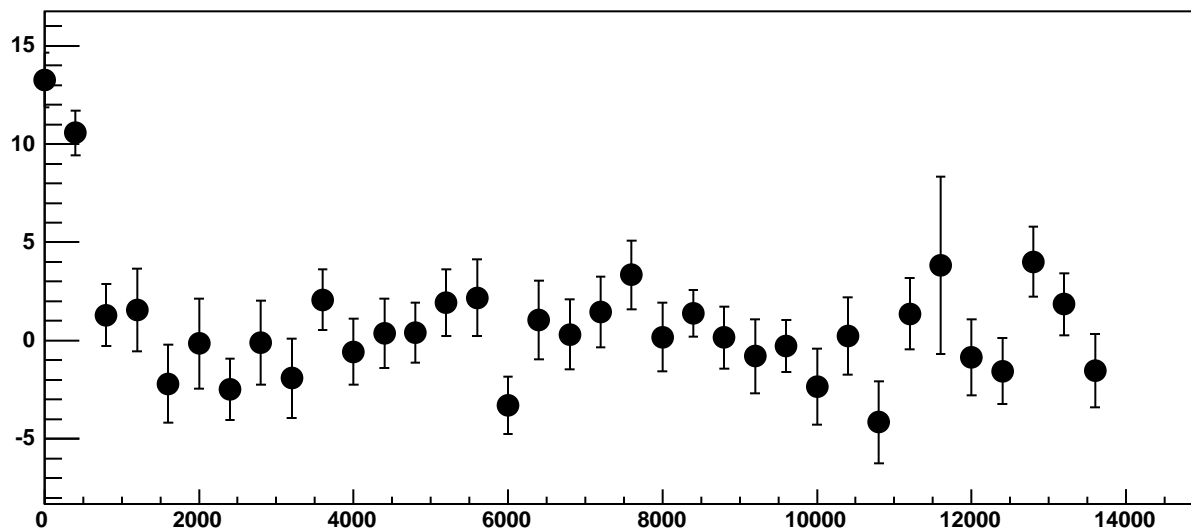


χ^2 / ndf 26.51 / 23
p0 66.42 ± 0.8616
p1 0.01476 ± 0.0001265

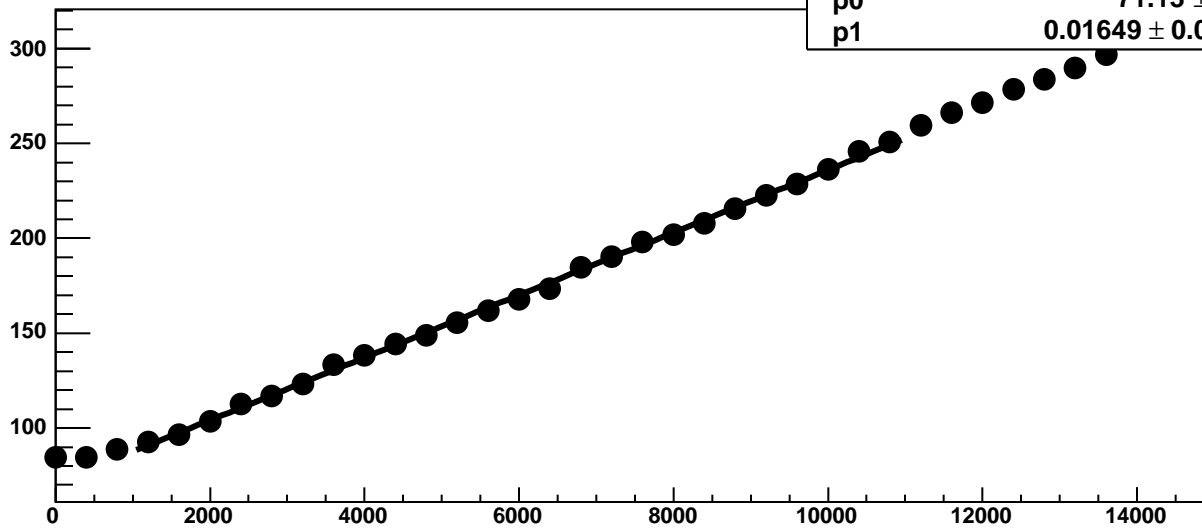
Chip 8, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

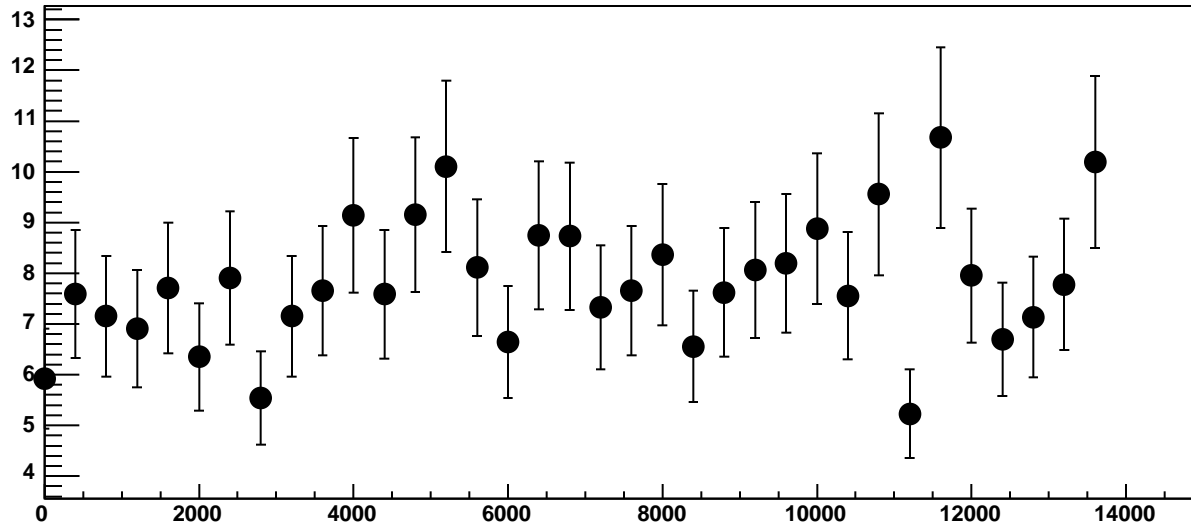


Chip 8, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

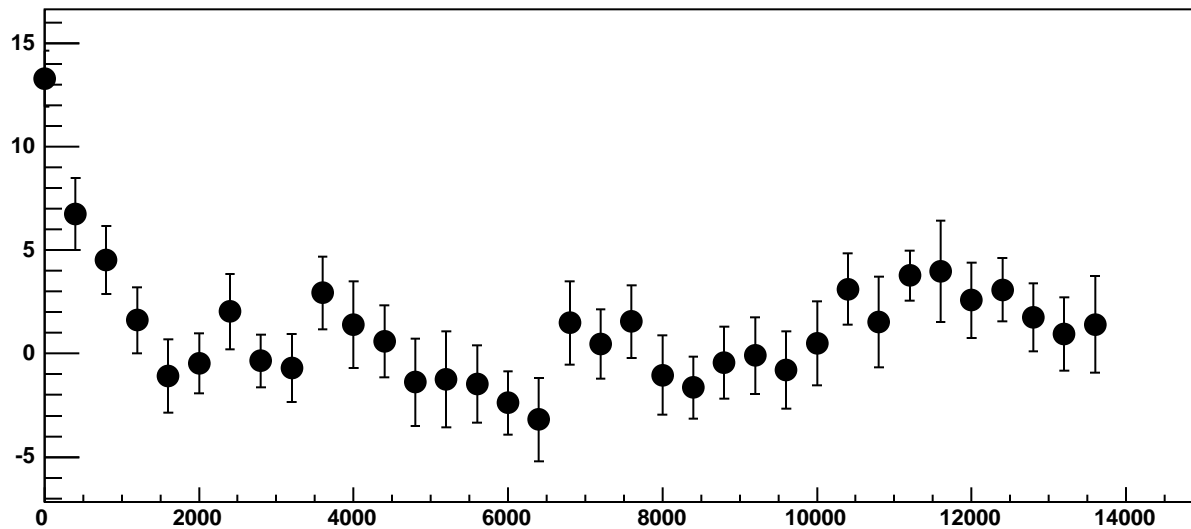


χ^2 / ndf 19.58 / 23
p0 71.13 ± 0.7709
p1 0.01649 ± 0.0001205

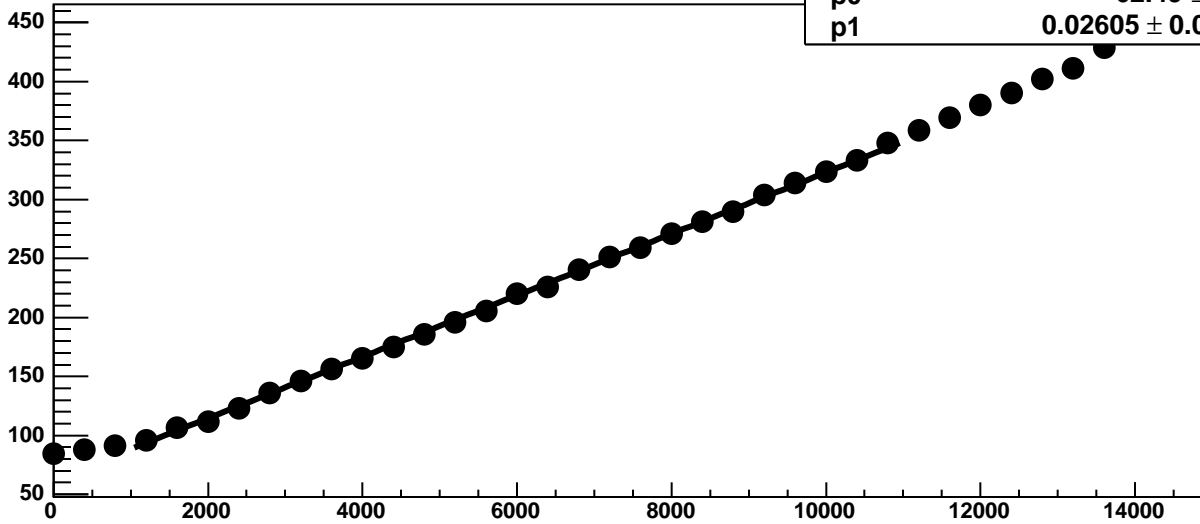
Chip 8, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

22.72 / 23

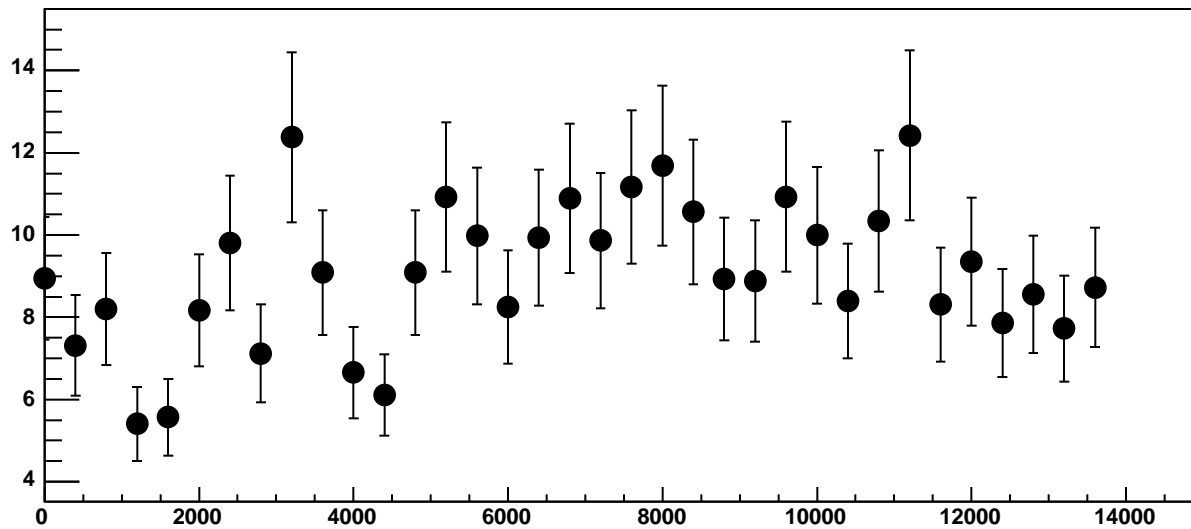
p0

62.49 ± 0.7693

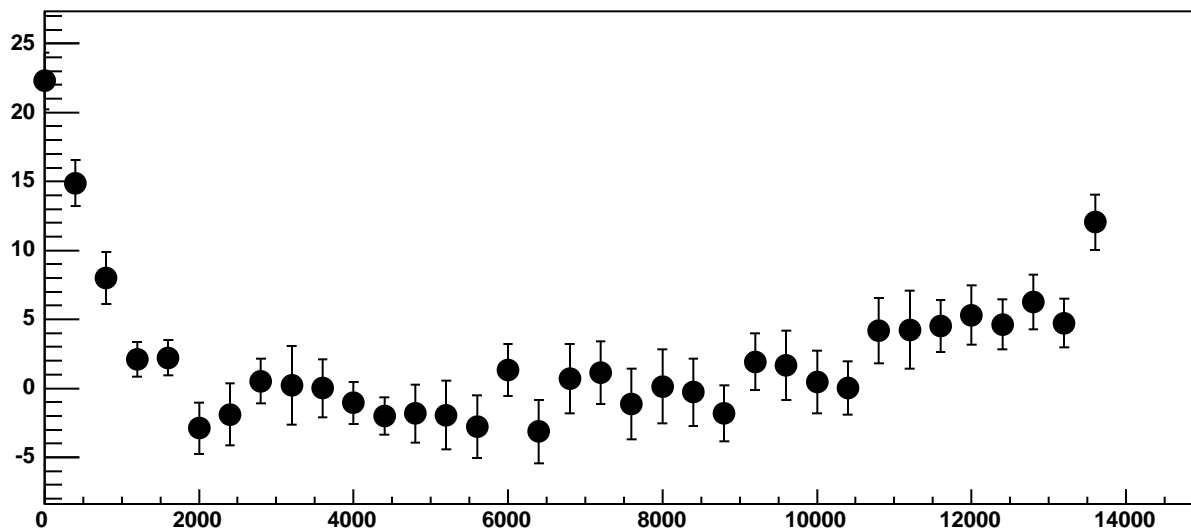
p1

0.02605 ± 0.0001295

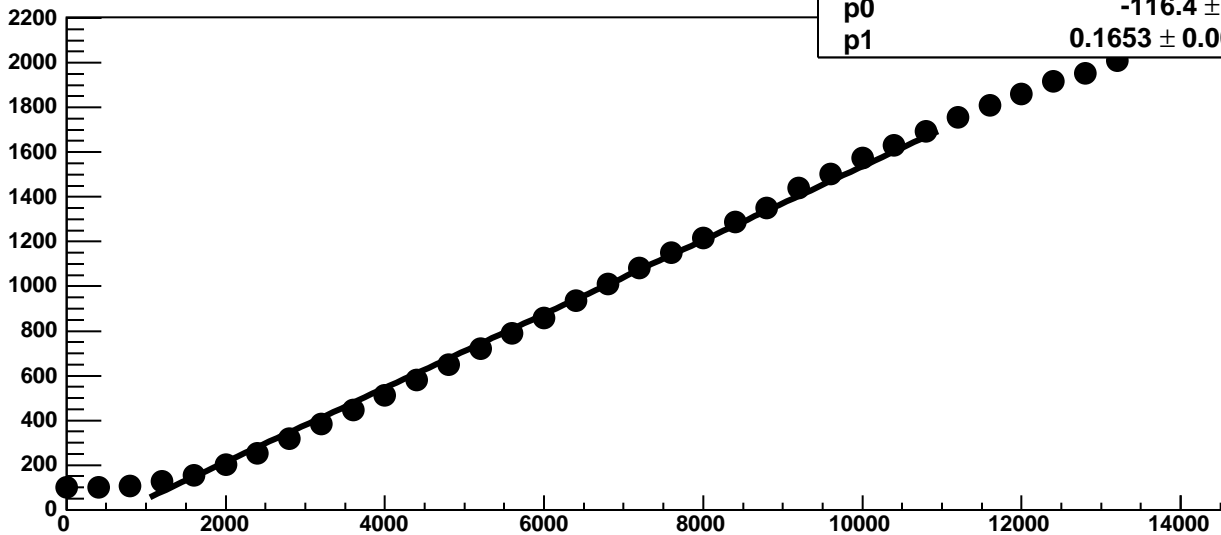
Chip 8, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



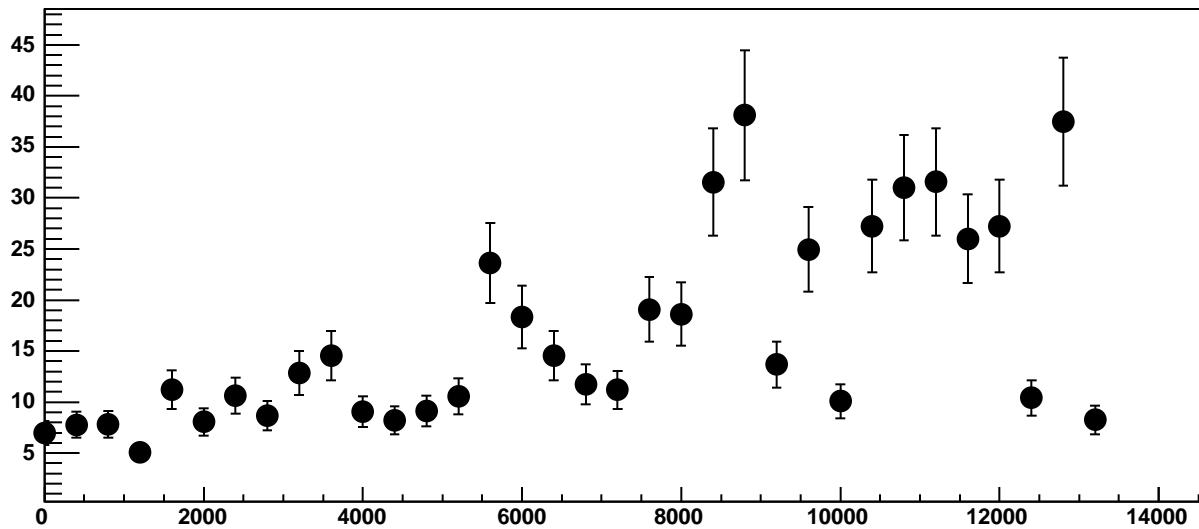
Chip 8, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC



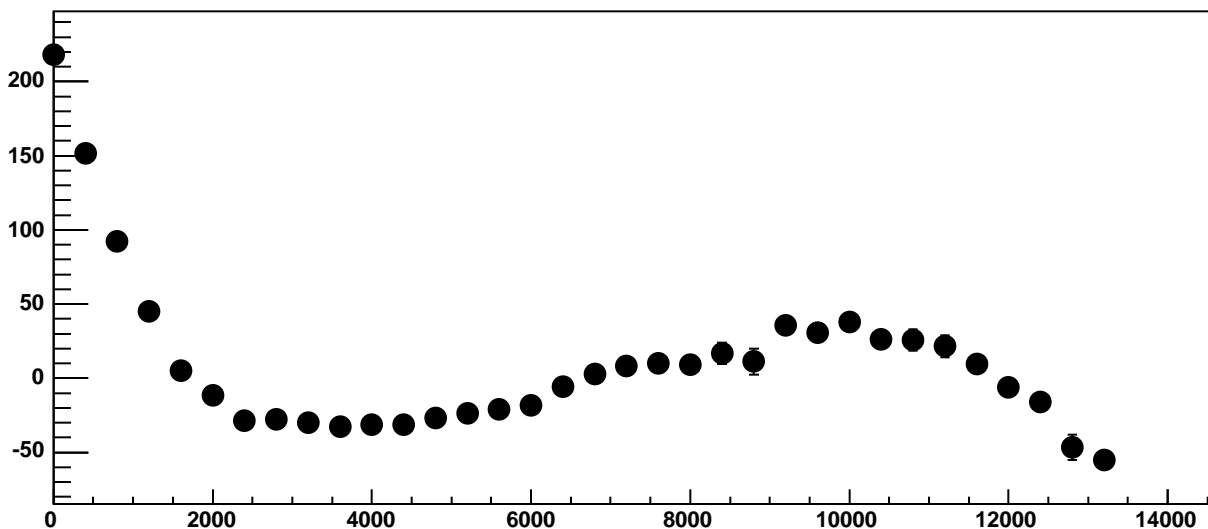
Chip 8, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



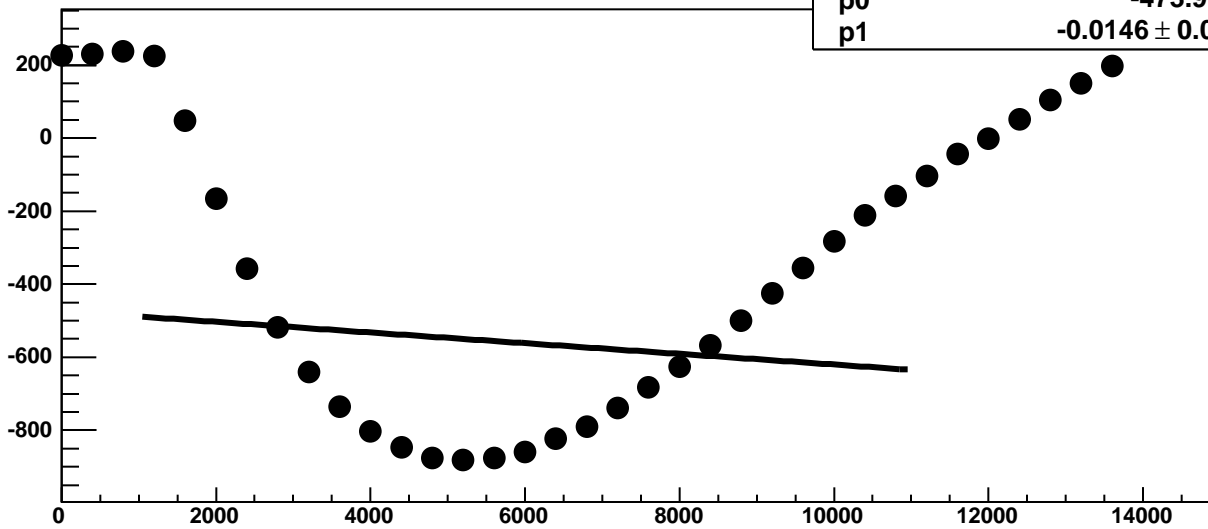
Chip 8, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

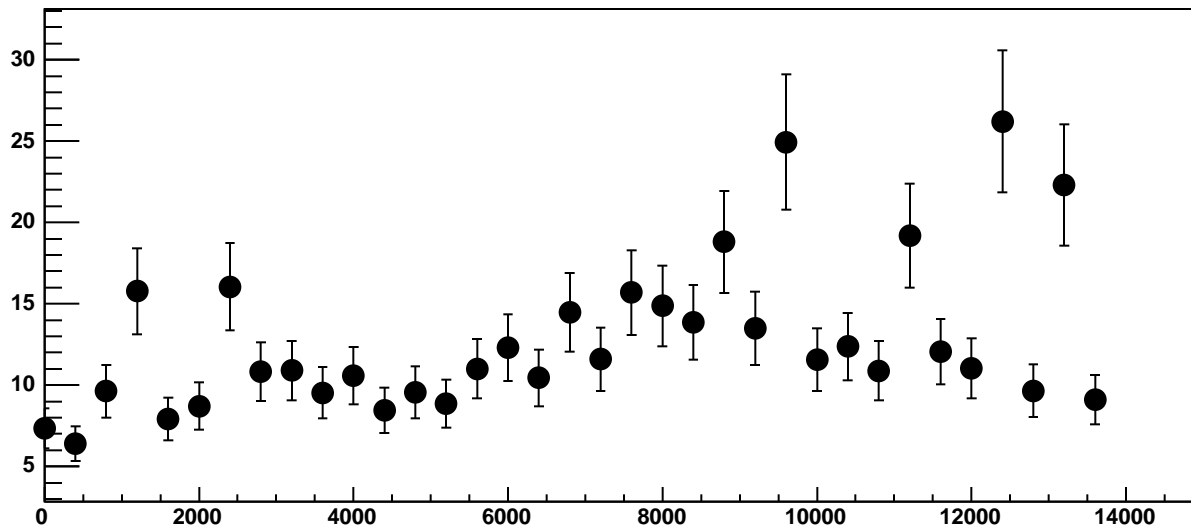


Chip 8, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

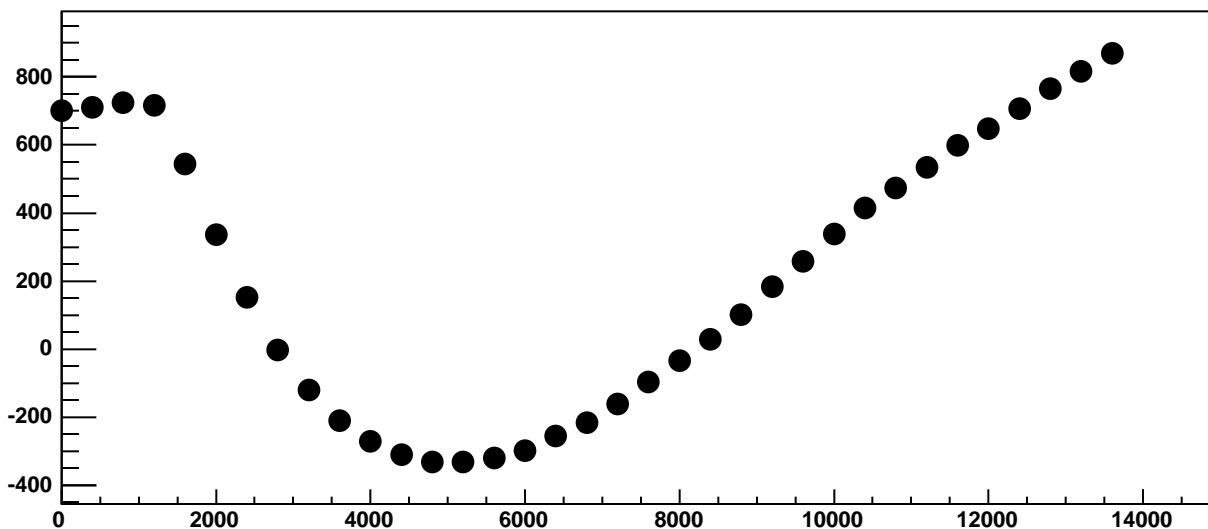


χ^2 / ndf 3.858e+05 / 23
p0 -473.9 ± 1.127
p1 -0.0146 ± 0.0001867

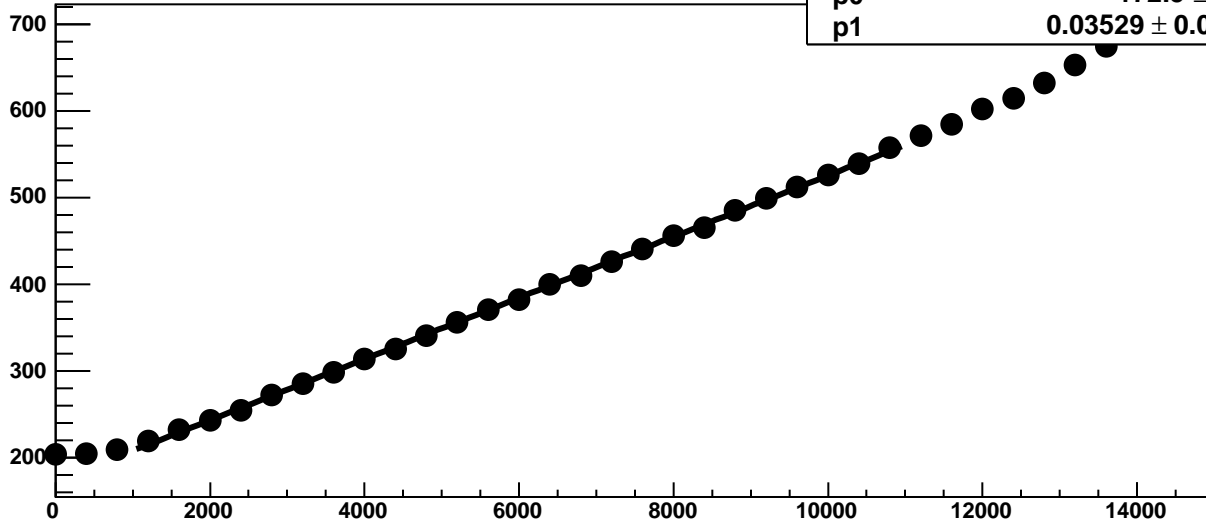
Chip 8, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

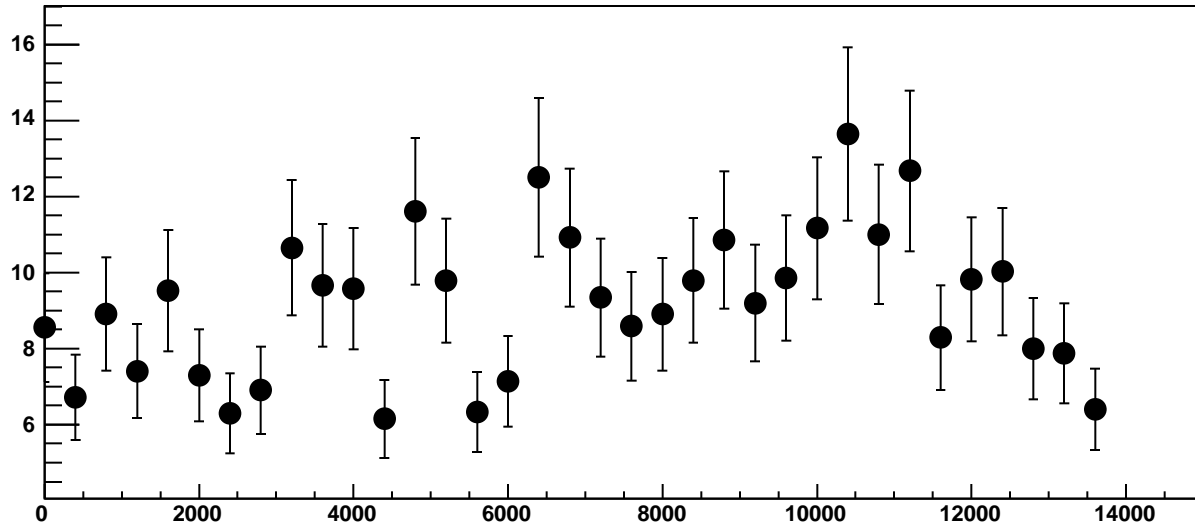


Chip 8, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

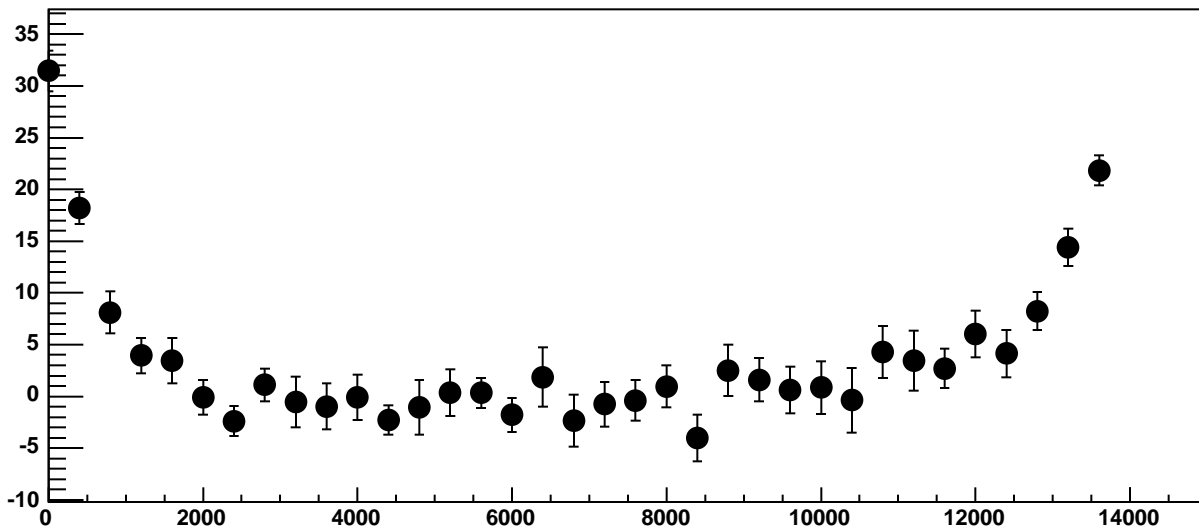


χ^2 / ndf 25.01 / 23
p0 172.5 ± 0.8699
p1 0.03529 ± 0.0001451

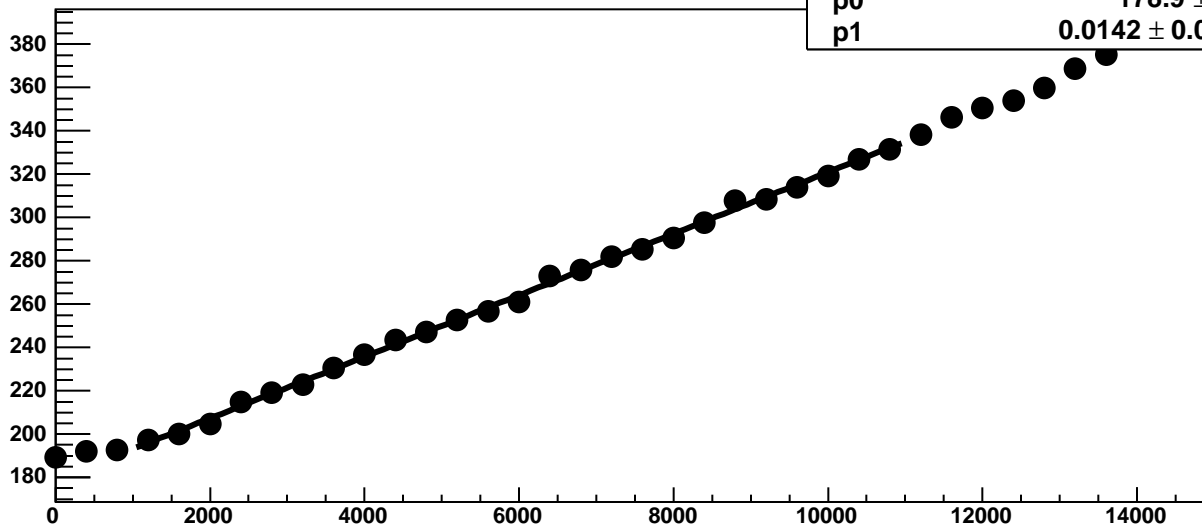
Chip 8, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC

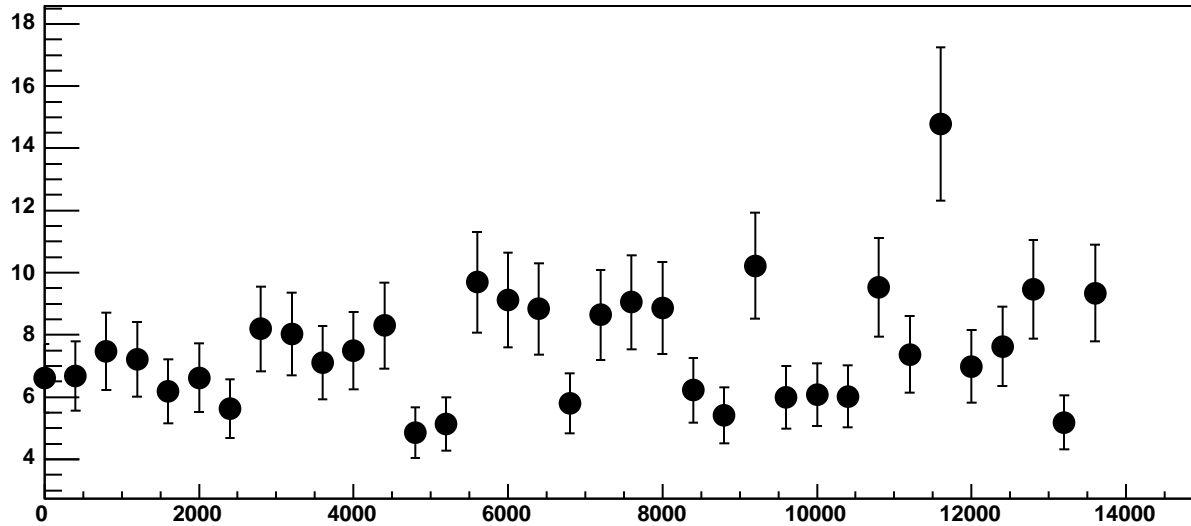


Chip 8, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

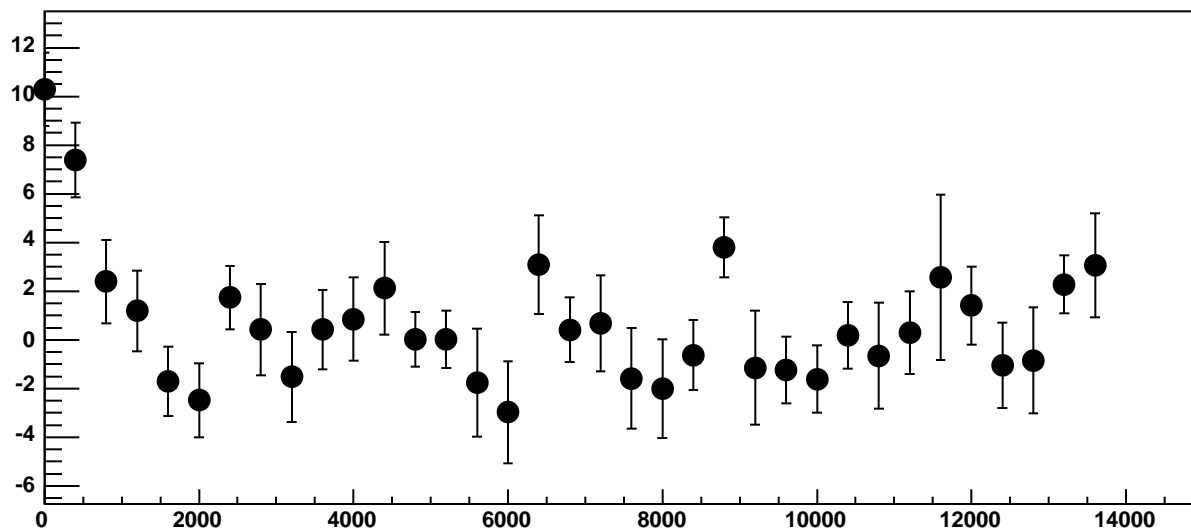


χ^2 / ndf 27.51 / 23
p0 178.9 ± 0.7137
p1 0.0142 ± 0.0001079

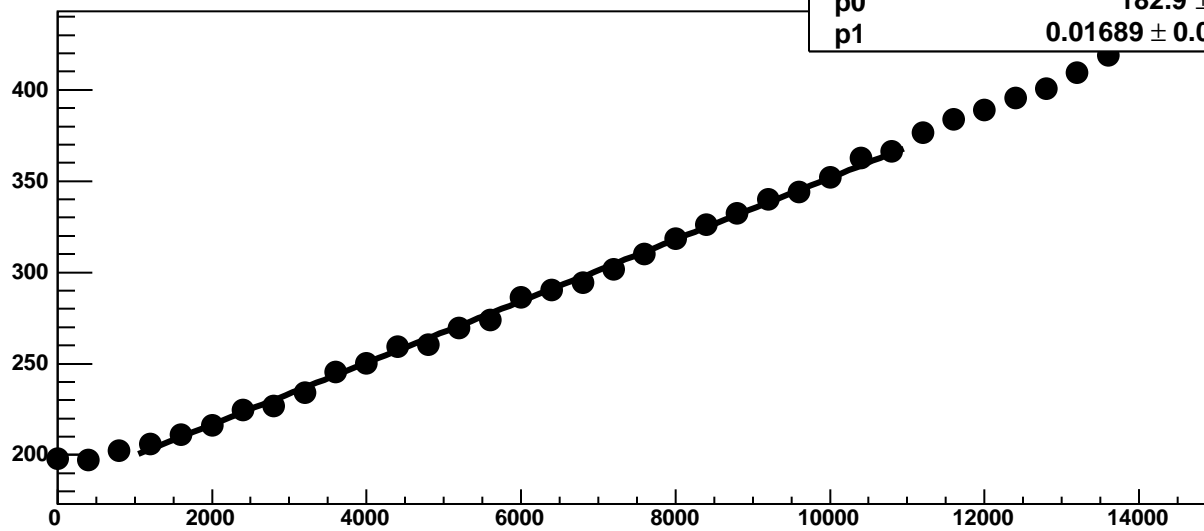
Chip 8, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

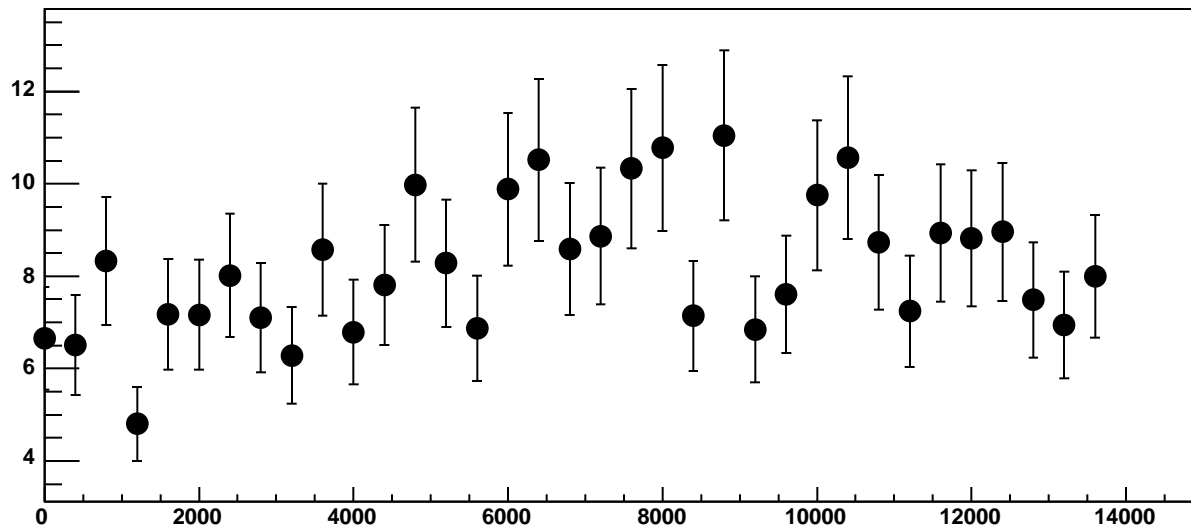


Chip 8, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

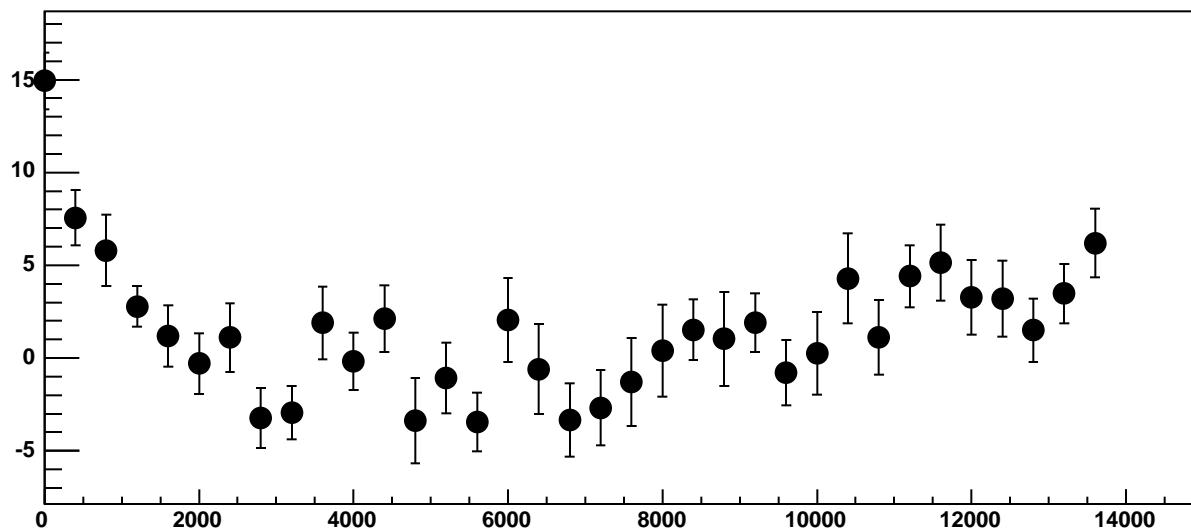


χ^2 / ndf 37 / 23
p0 182.9 ± 0.7288
p1 0.01689 ± 0.0001198

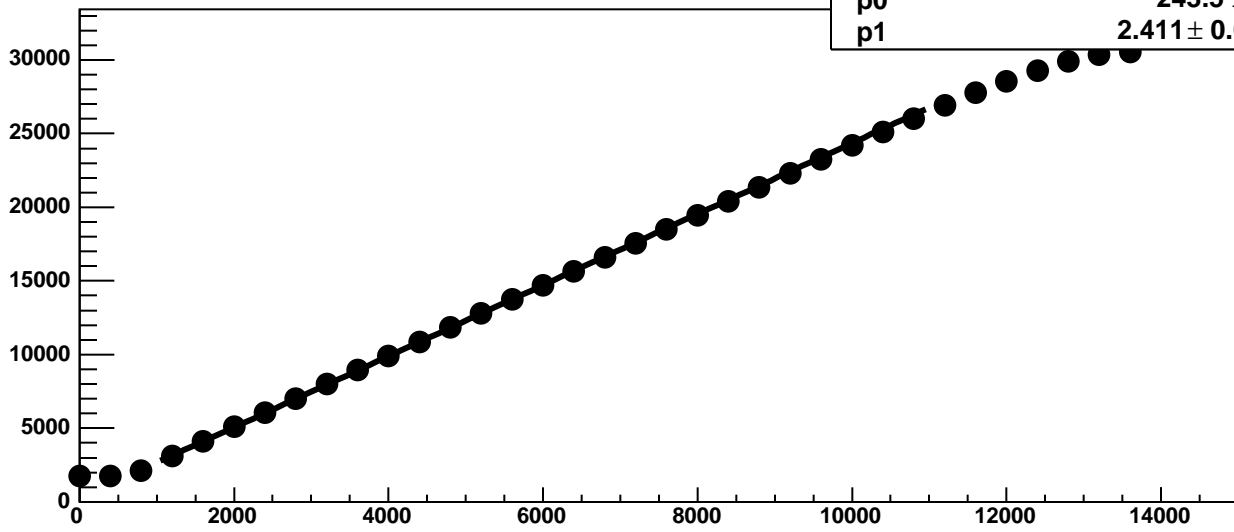
Chip 8, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



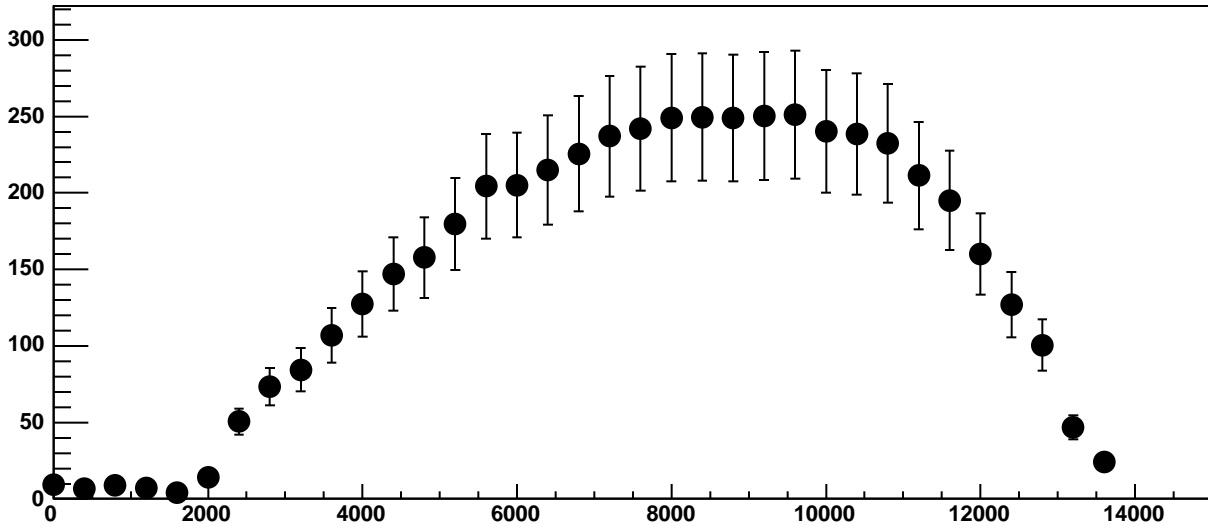
Chip 8, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



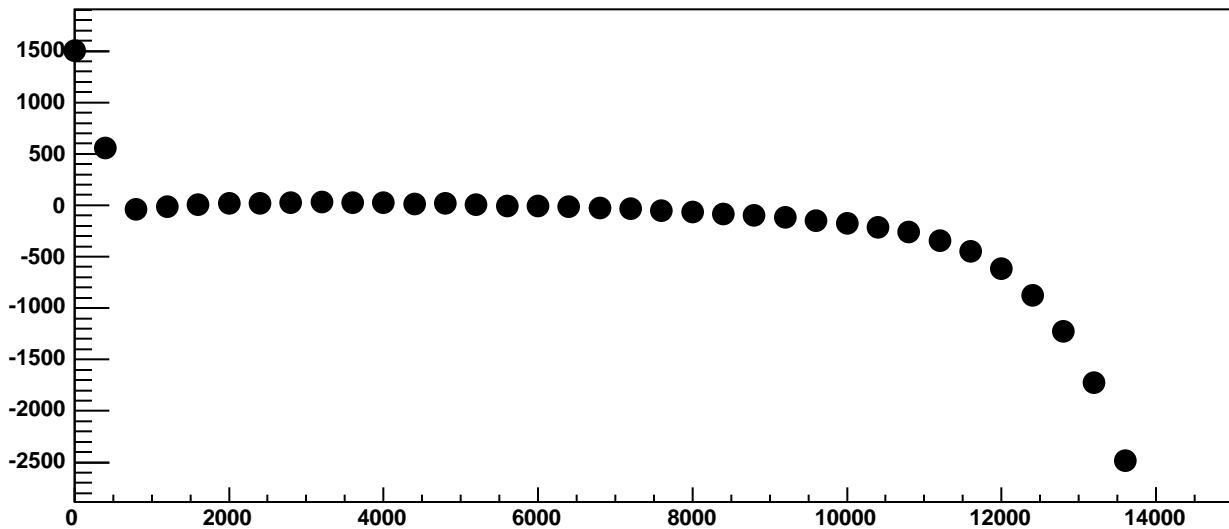
Chip 8, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC



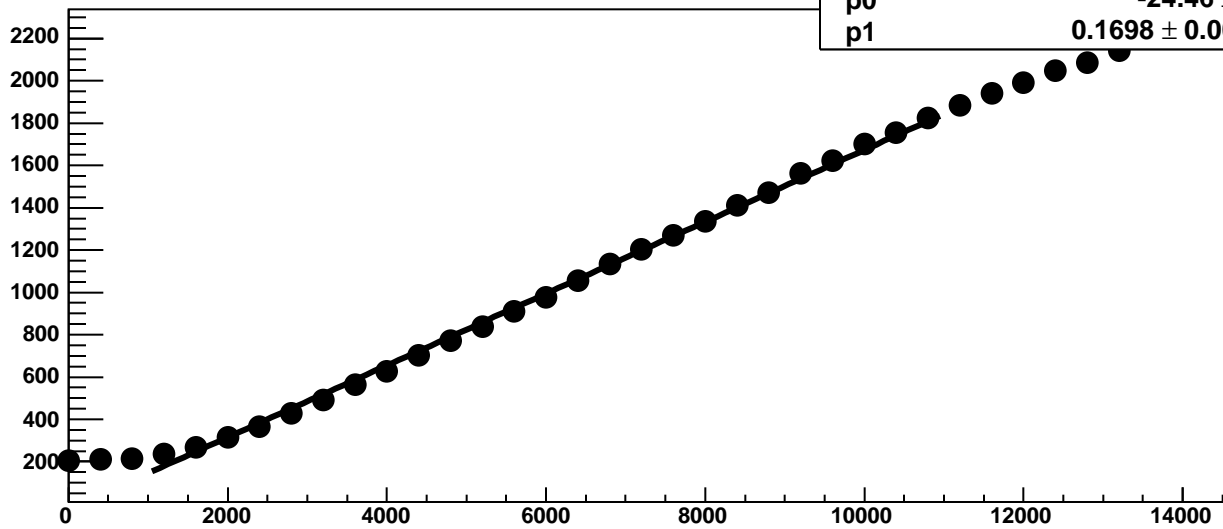
Chip 8, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC

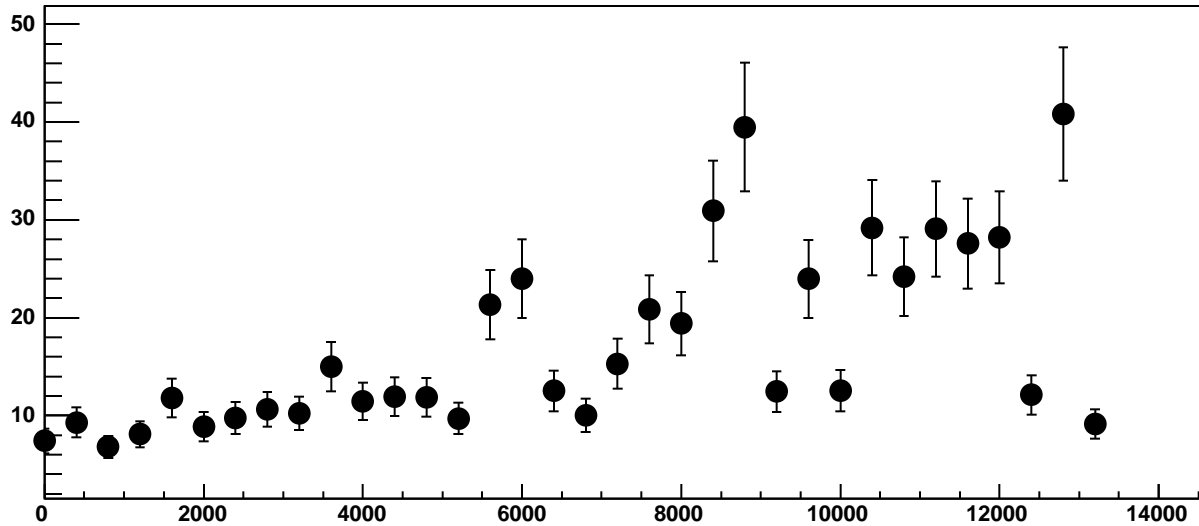


Chip 8, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

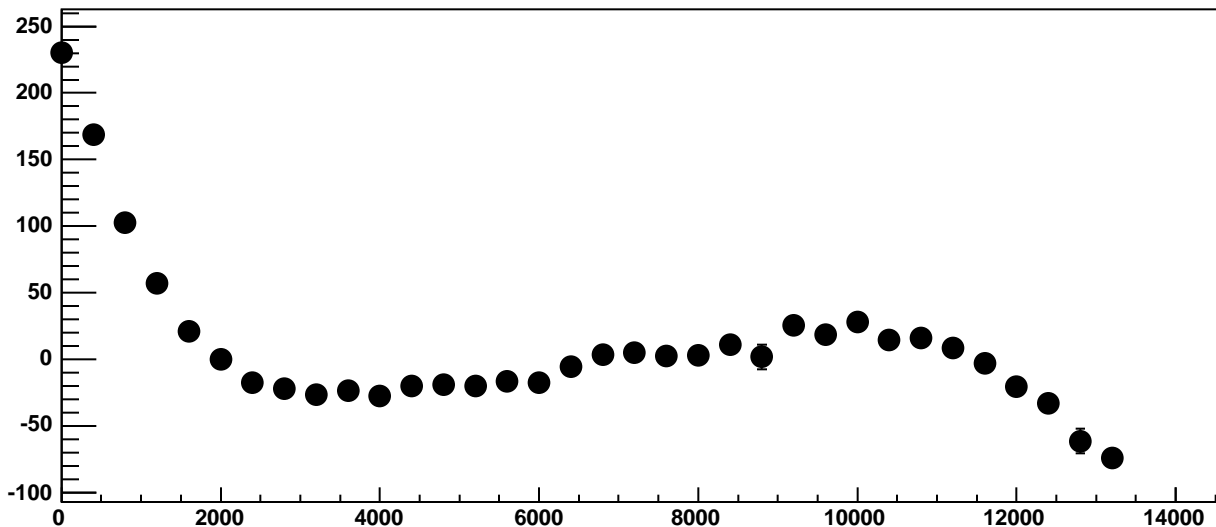


χ^2 / ndf 1834 / 23
p0 -24.46 ± 1.173
p1 0.1698 ± 0.0002199

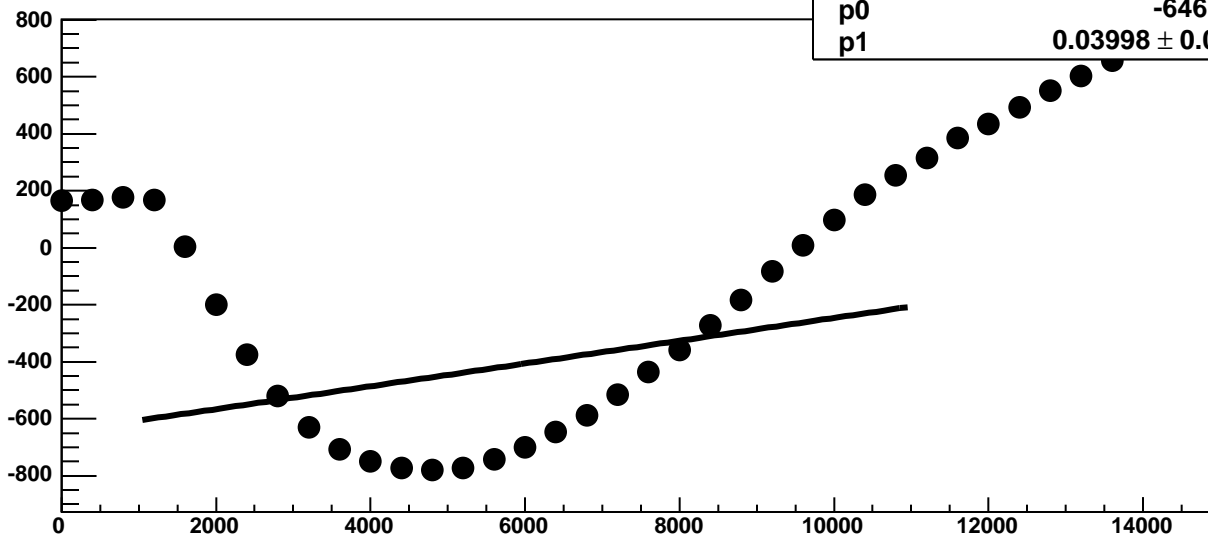
Chip 8, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

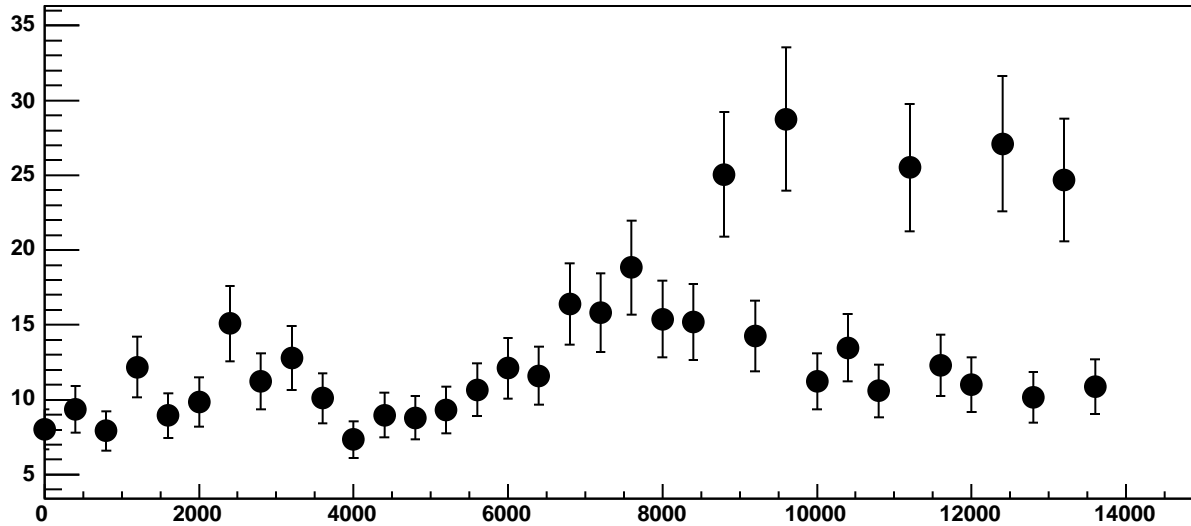


Chip 8, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

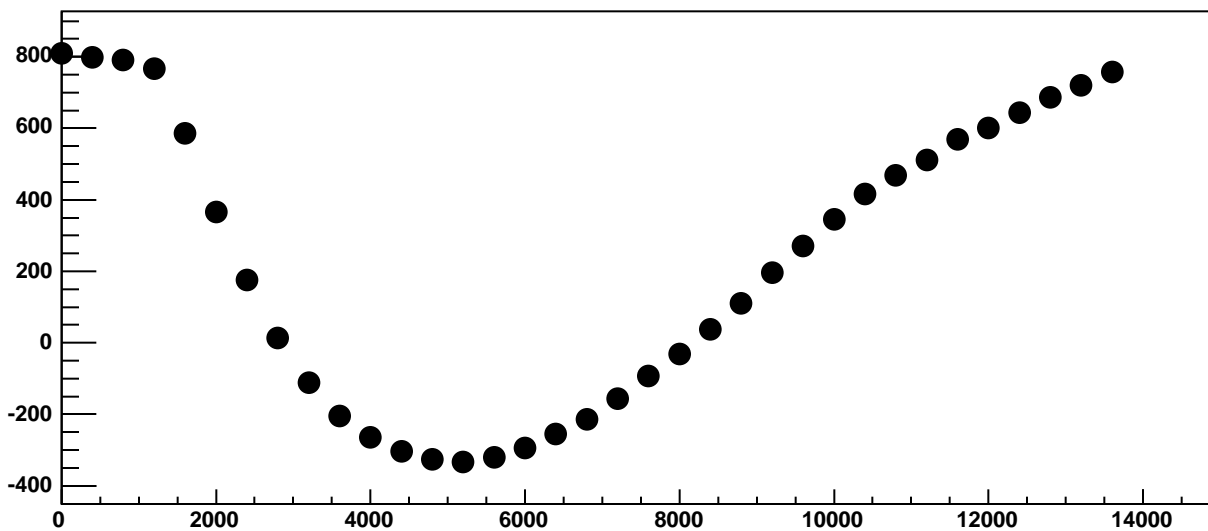


χ^2 / ndf 4.14e+05 / 23
p0 -646 ± 1.146
p1 0.03998 ± 0.0001931

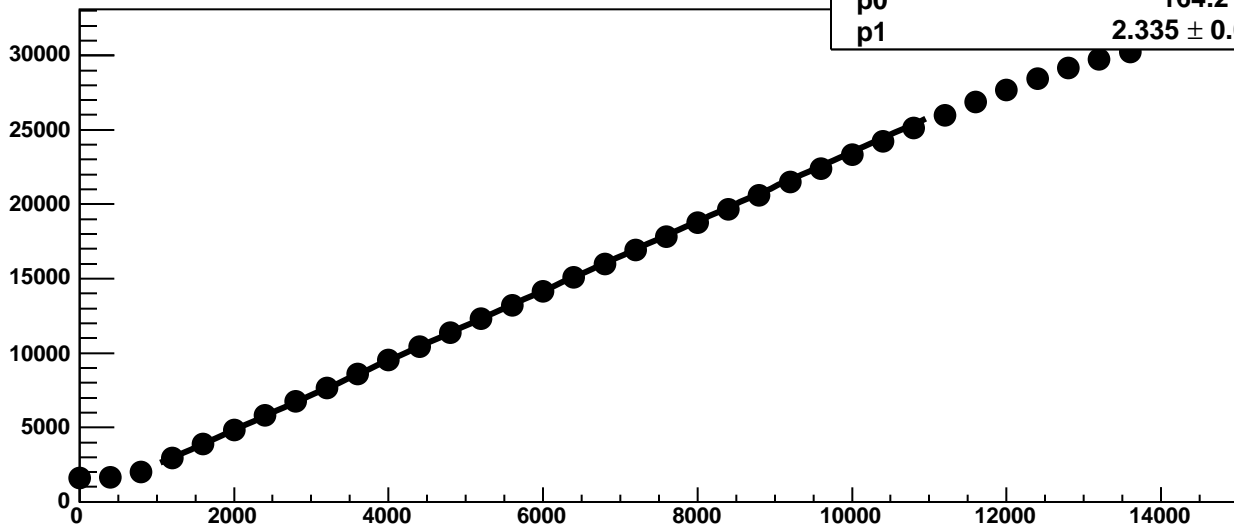
Chip 8, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

272.1 / 23

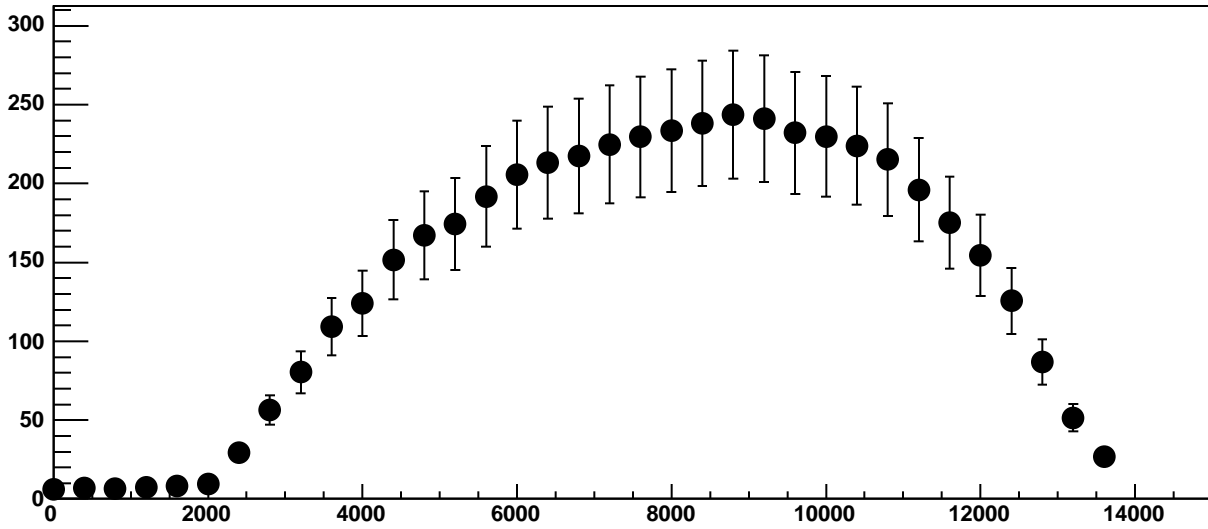
p0

164.2 ± 2.811

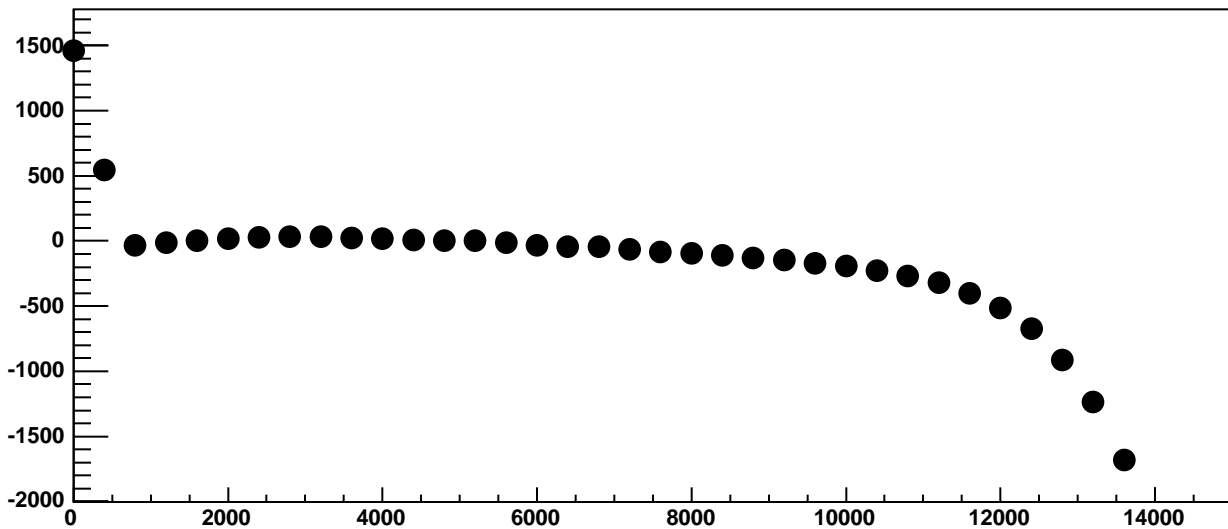
p1

2.335 ± 0.001596

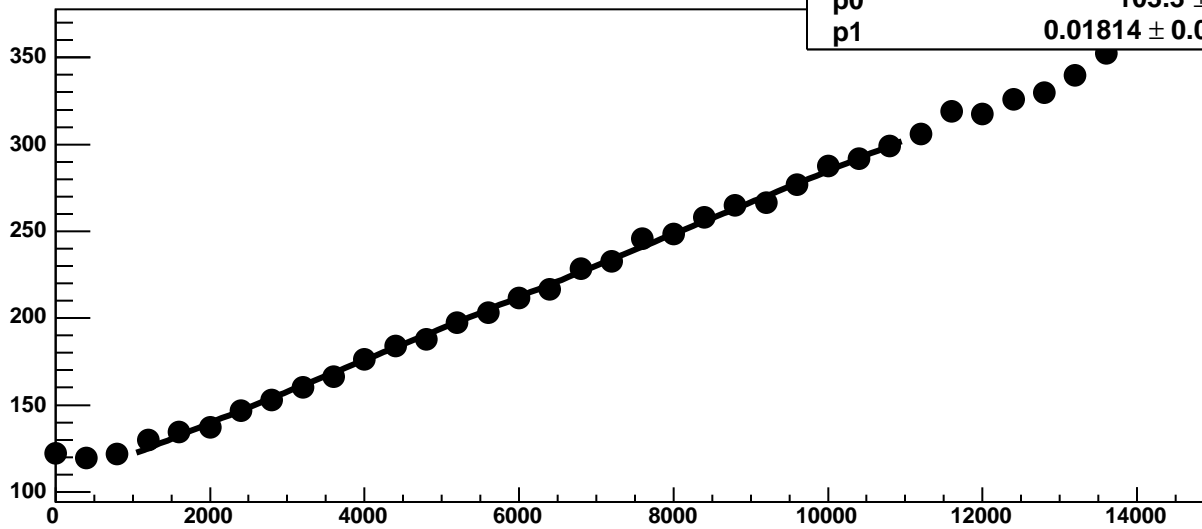
Chip 8, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC

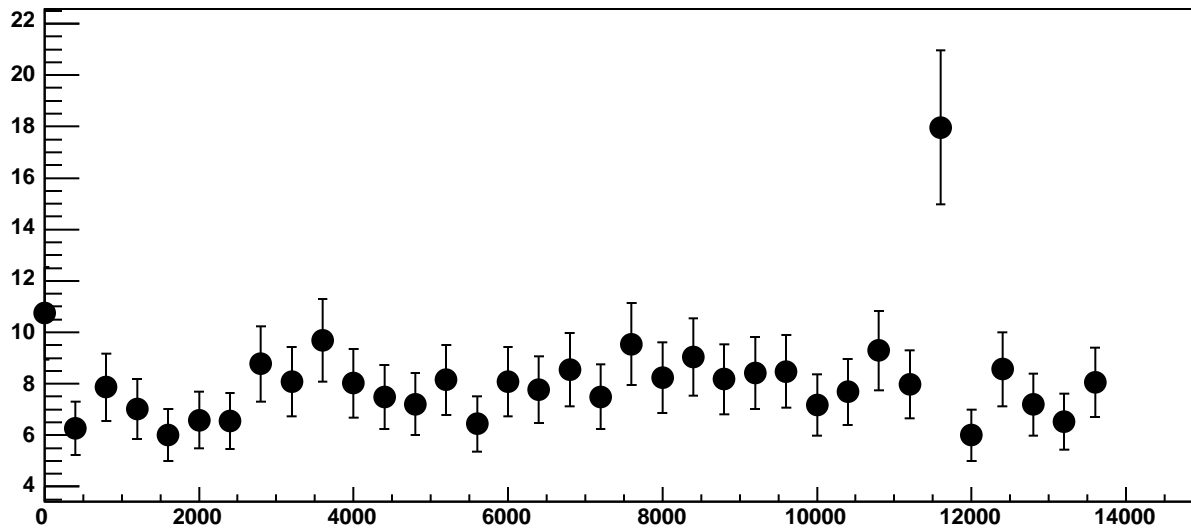


Chip 8, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

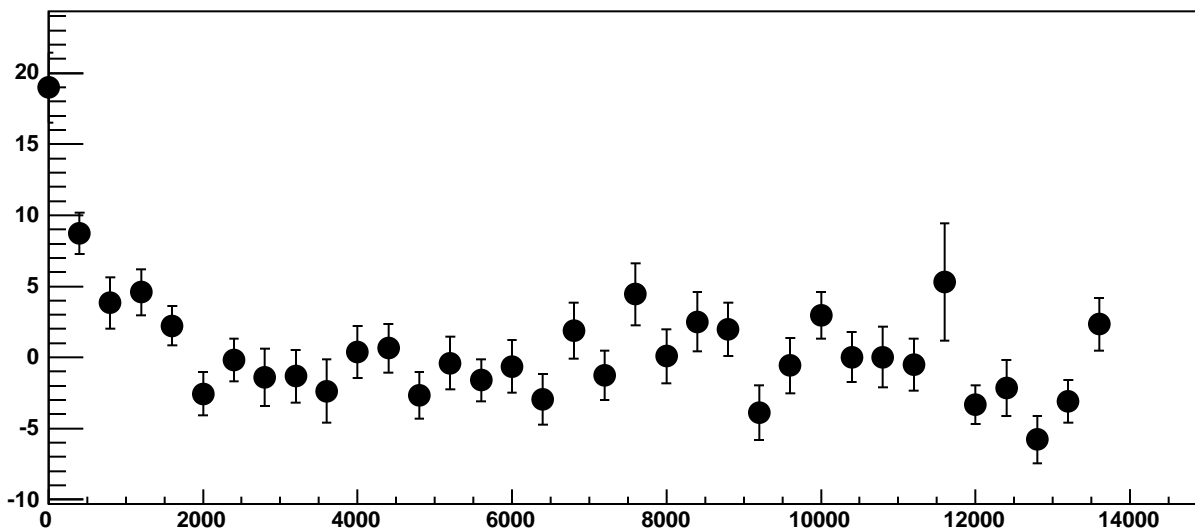


χ^2 / ndf 38.12 / 23
p0 103.3 ± 0.7669
p1 0.01814 ± 0.0001205

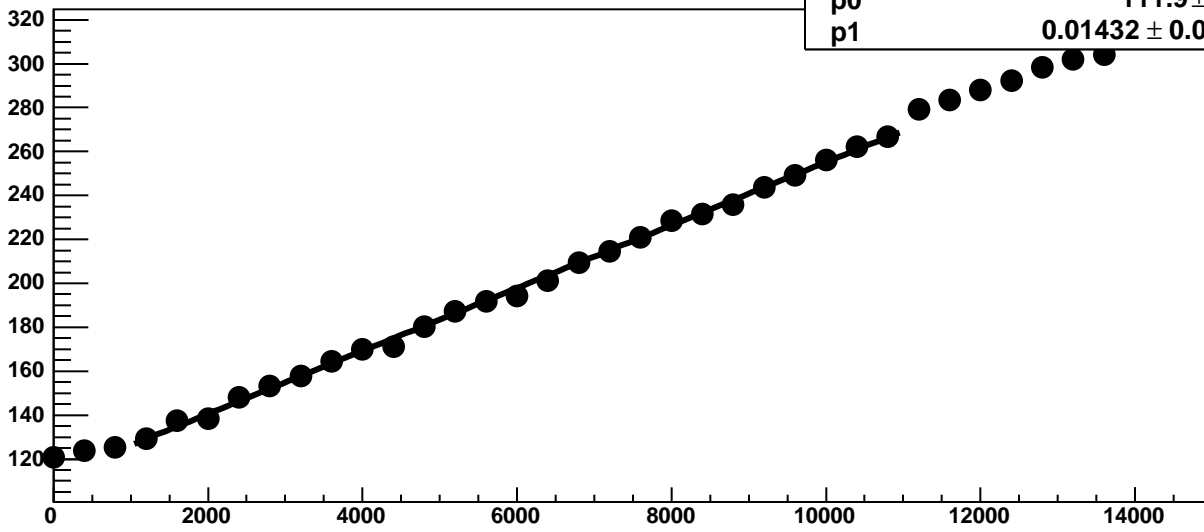
Chip 8, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

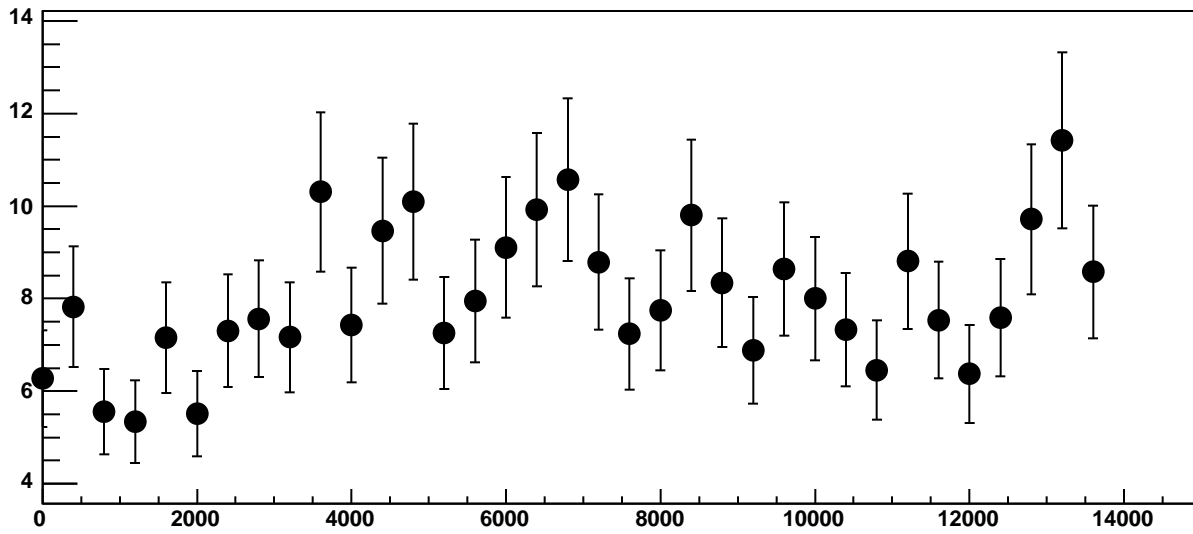


Chip 8, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

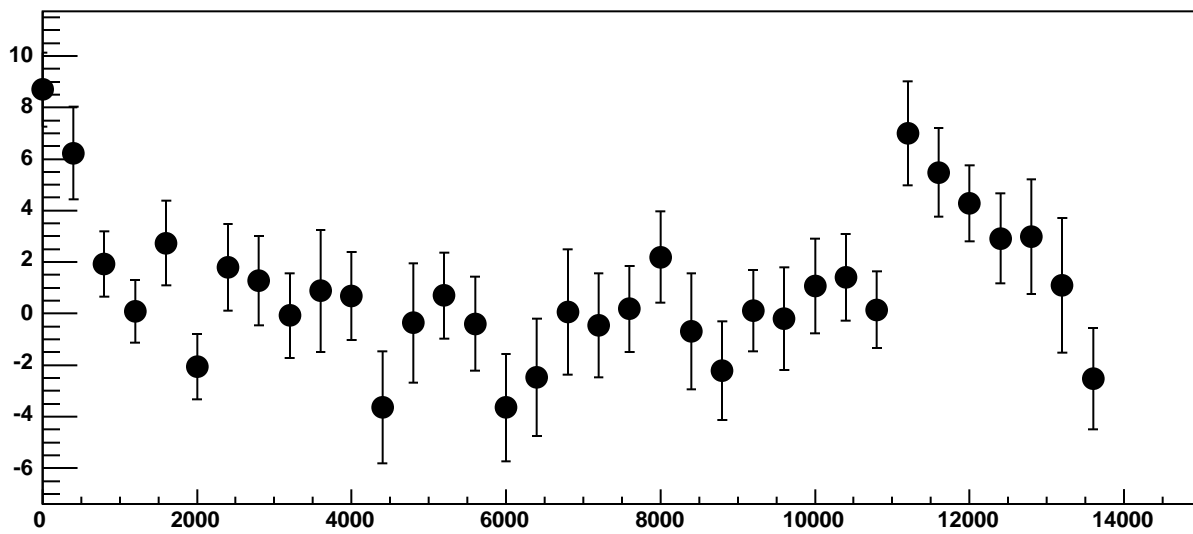


χ^2 / ndf 18.75 / 23
p0 111.9 ± 0.7195
p1 0.01432 ± 0.0001108

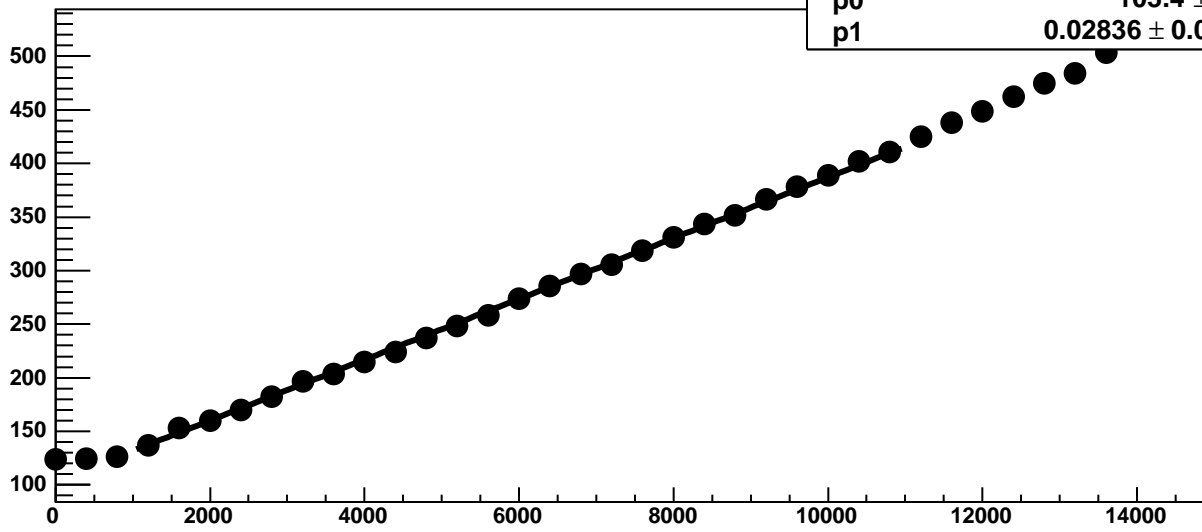
Chip 8, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

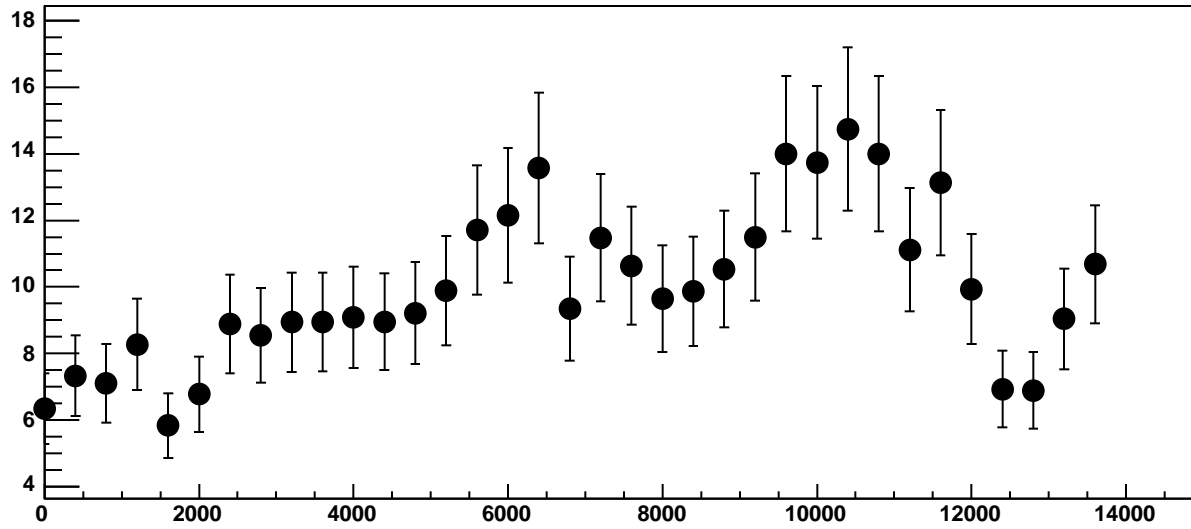


Chip 8, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

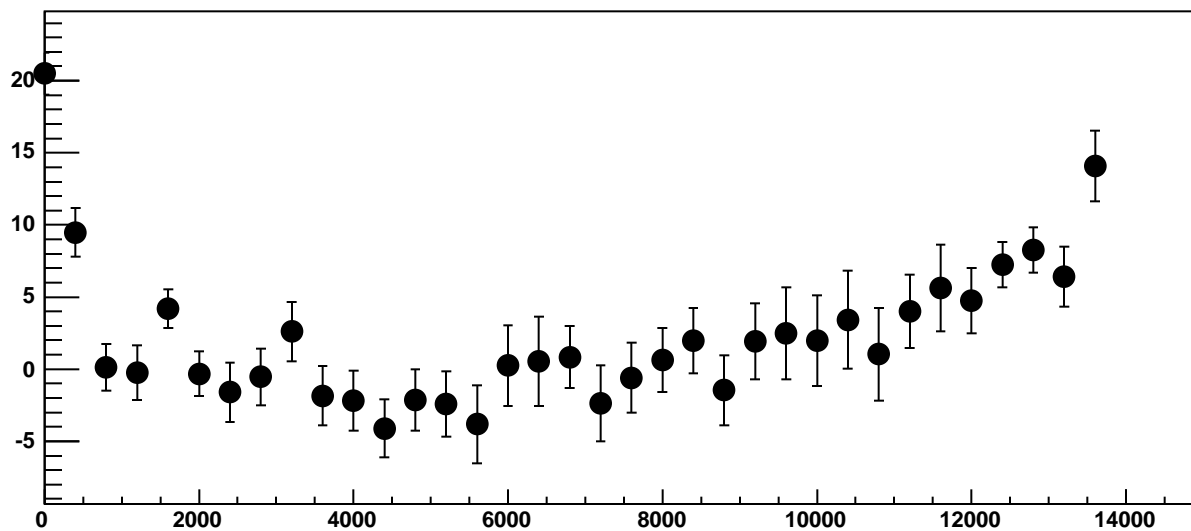


χ^2 / ndf 27.41 / 23
p0 103.4 ± 0.8834
p1 0.02836 ± 0.0001562

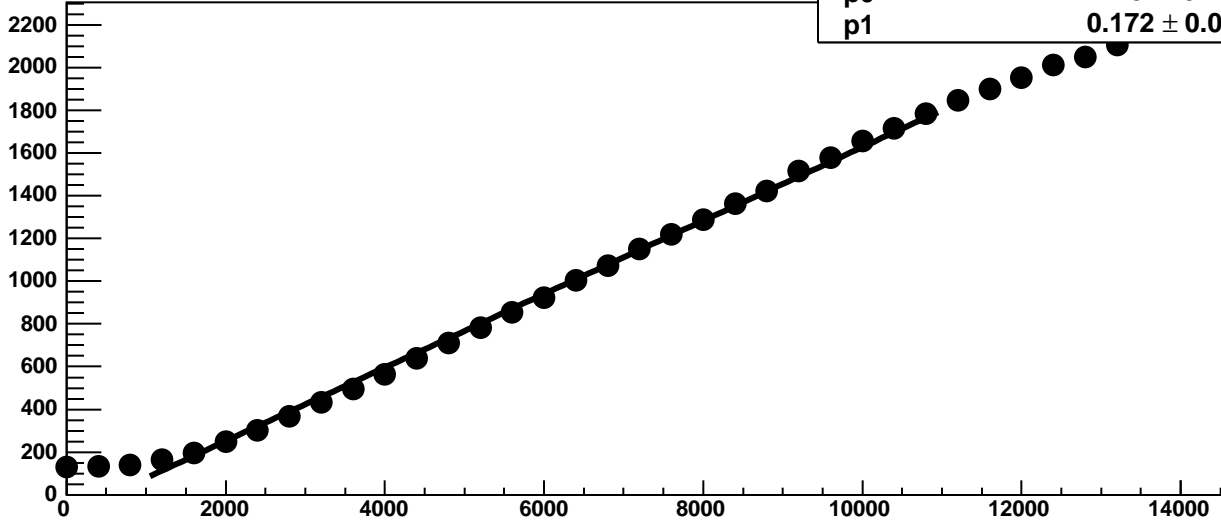
Chip 8, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



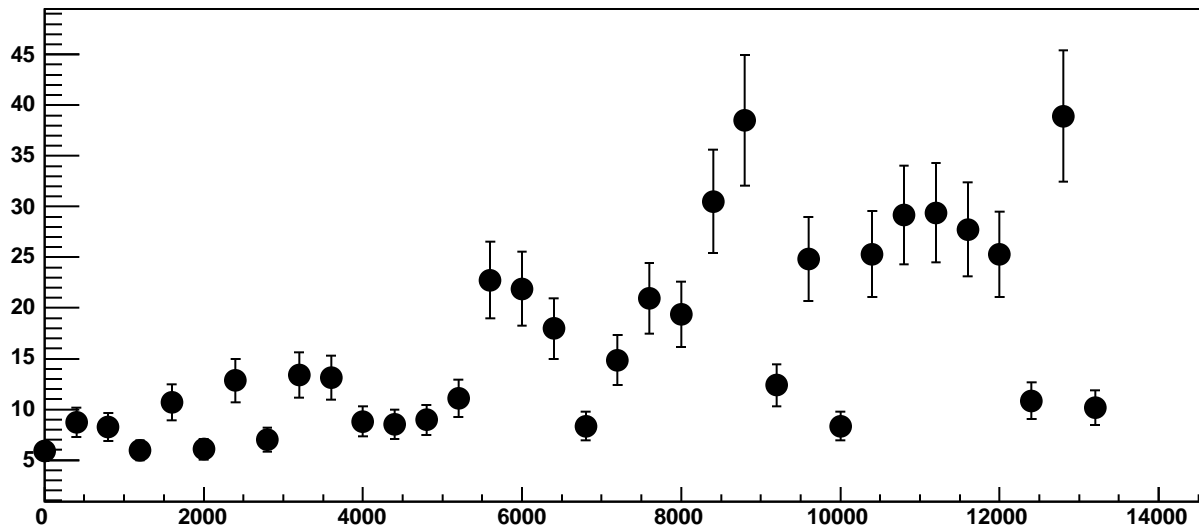
Chip 8, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



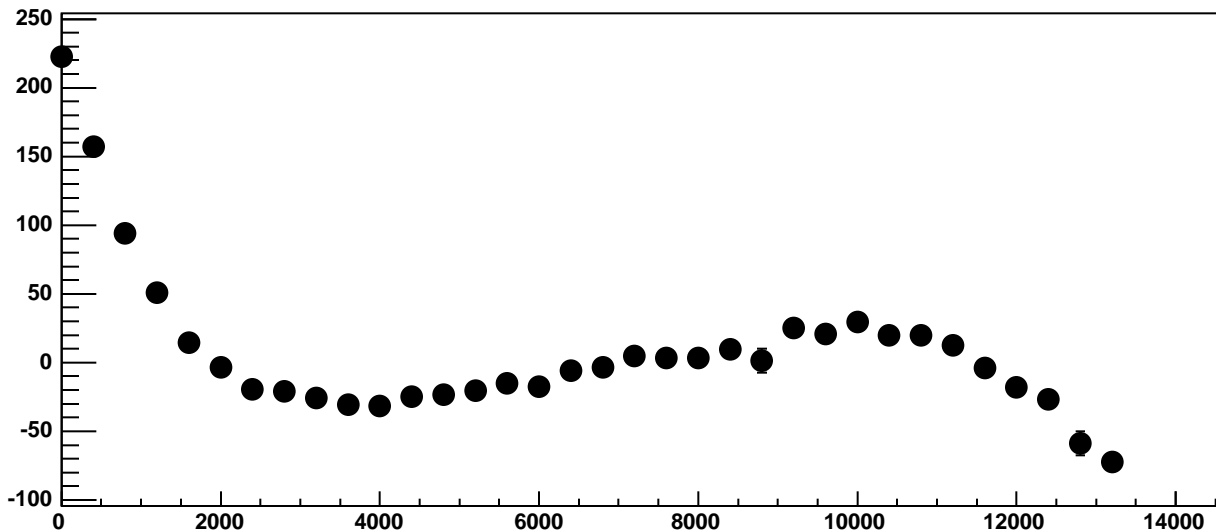
Chip 8, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



Chip 8, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

χ^2 / ndf

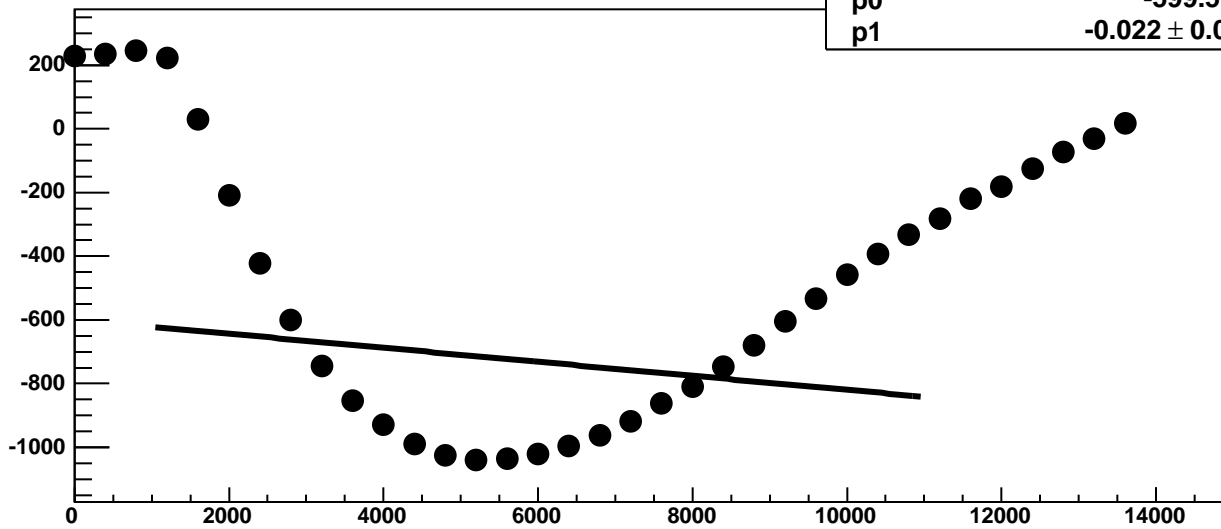
4.571e+05 / 23

p0

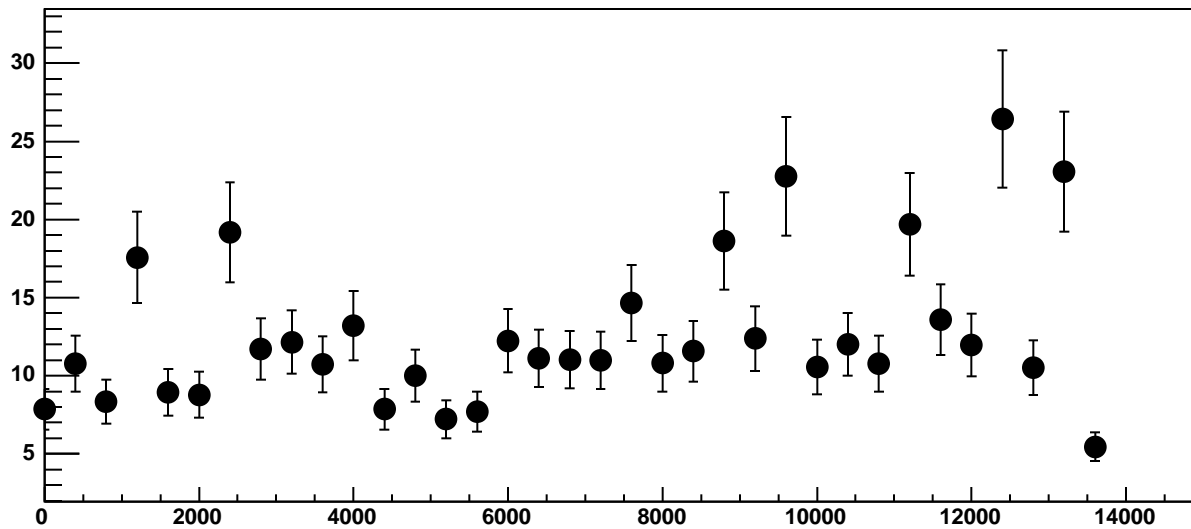
-599.5 ± 1.178

p1

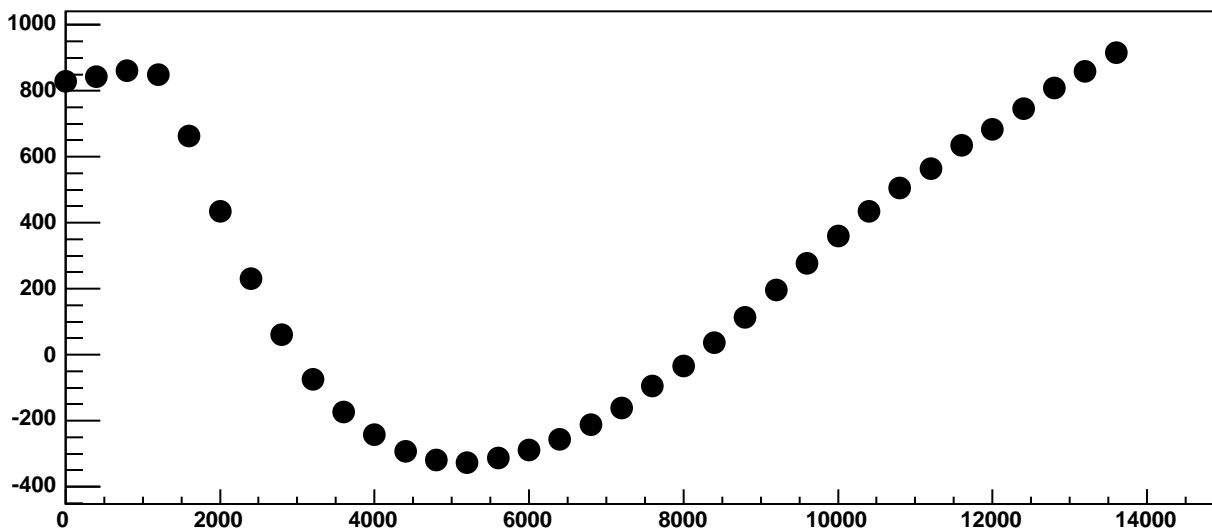
-0.022 ± 0.0001867



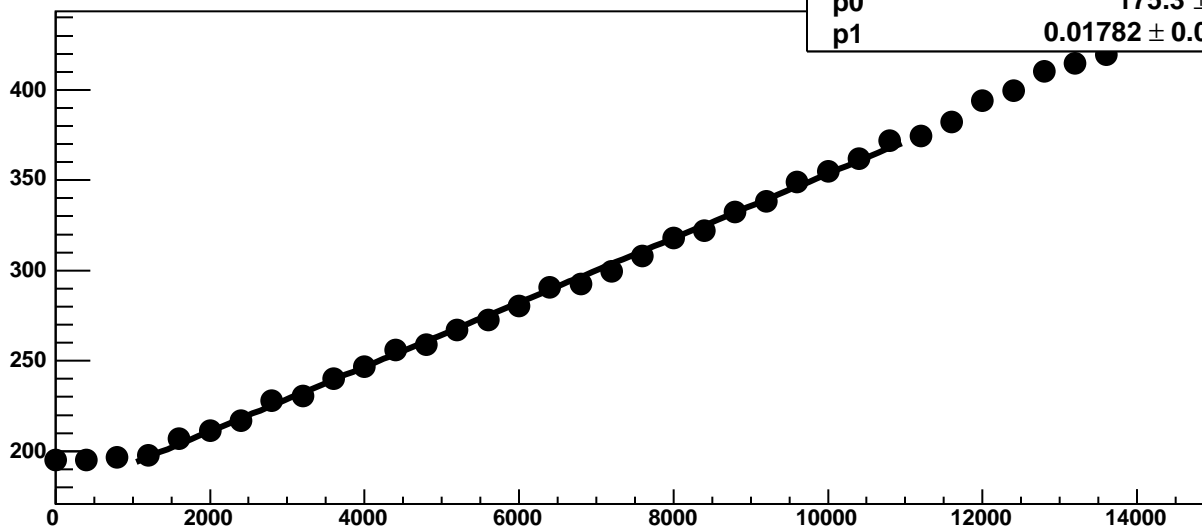
Chip 8, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

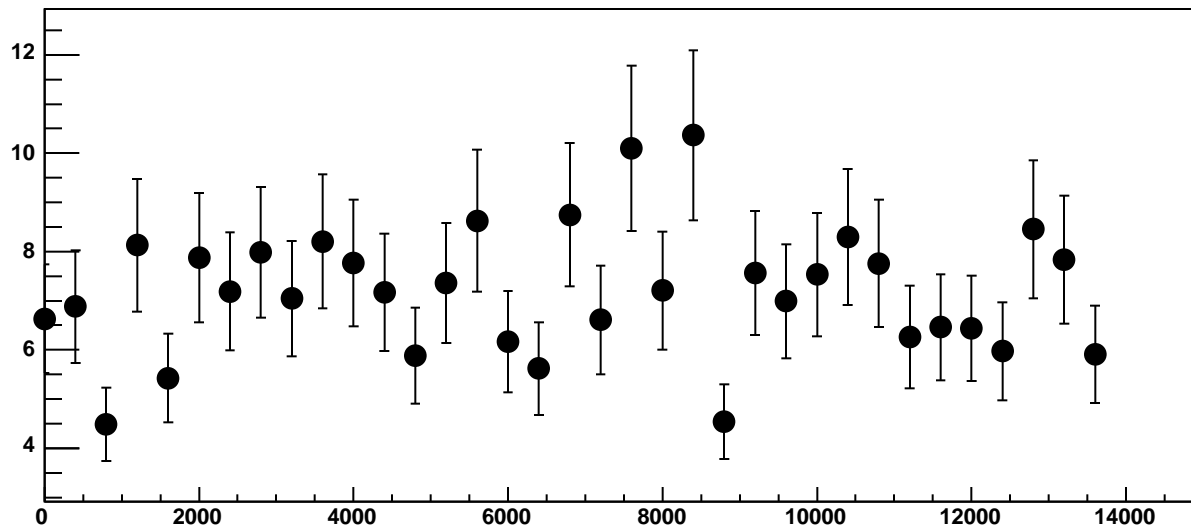


Chip 8, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

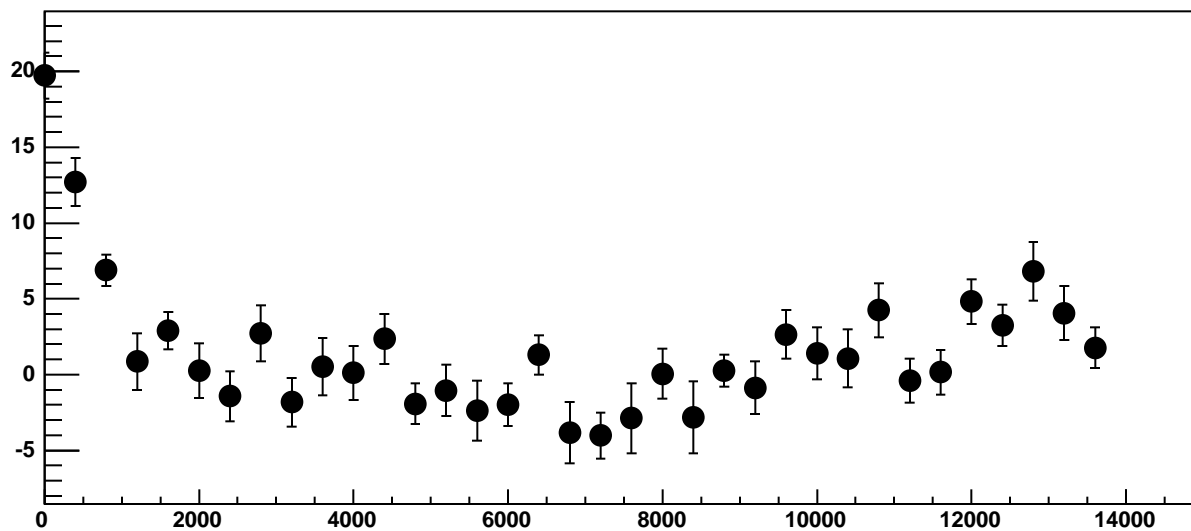


χ^2 / ndf 42.19 / 23
p0 175.3 ± 0.7537
p1 0.01782 ± 0.0001138

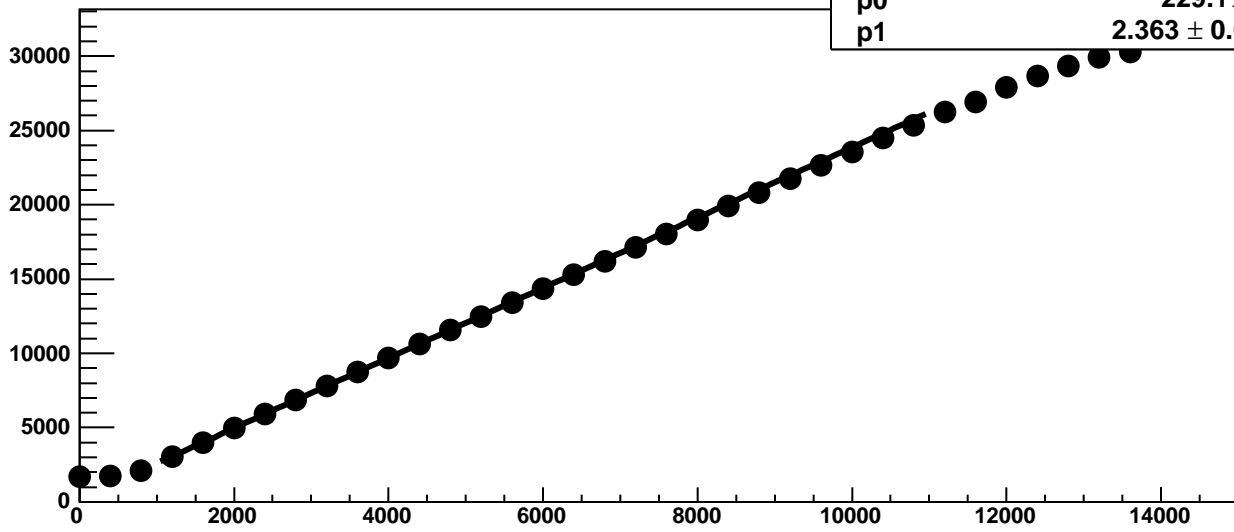
Chip 8, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



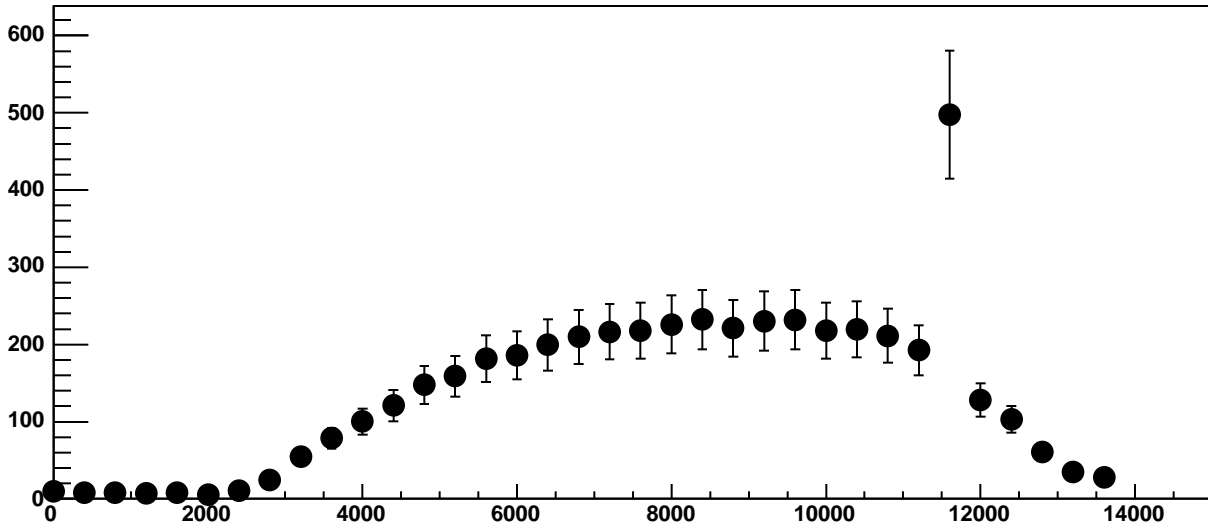
Chip 8, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



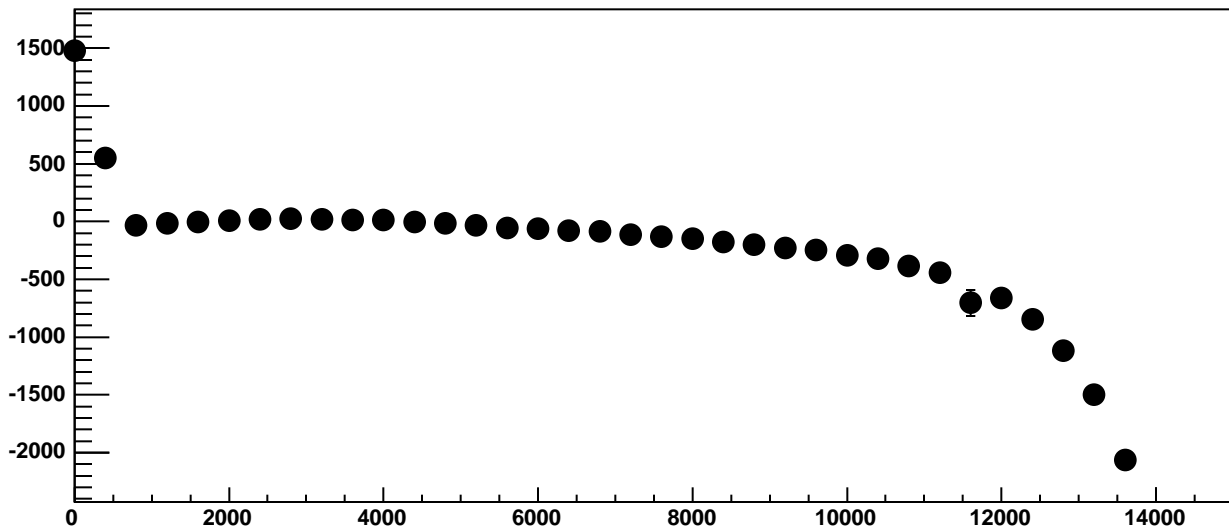
Chip 8, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



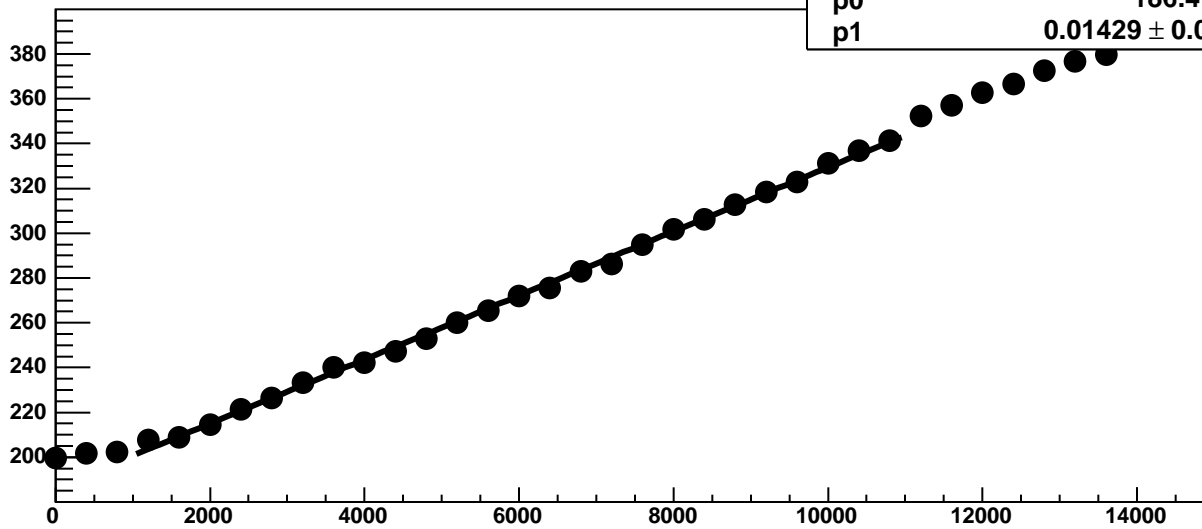
Chip 8, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC

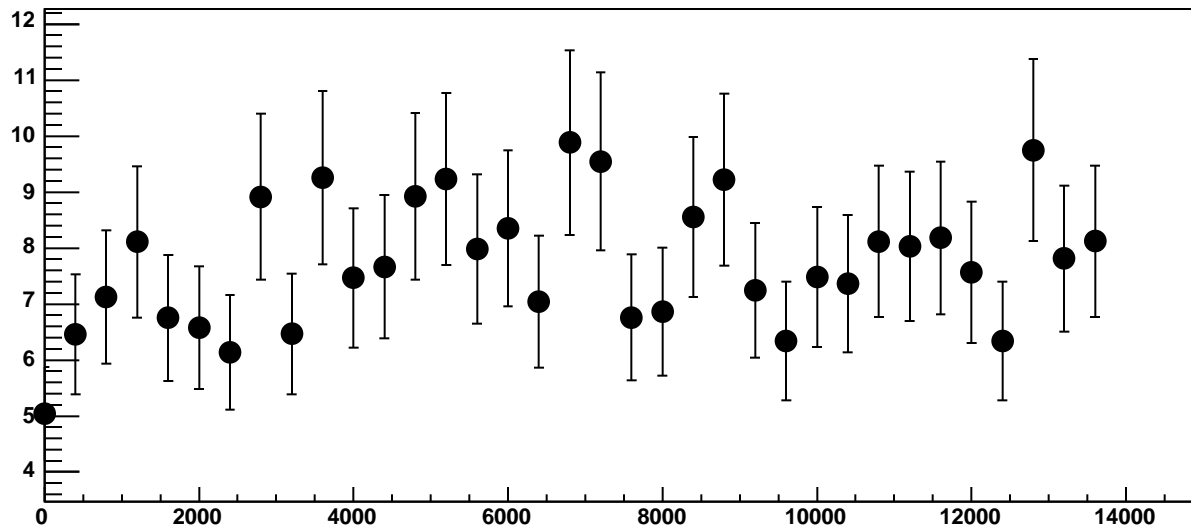


Chip 8, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

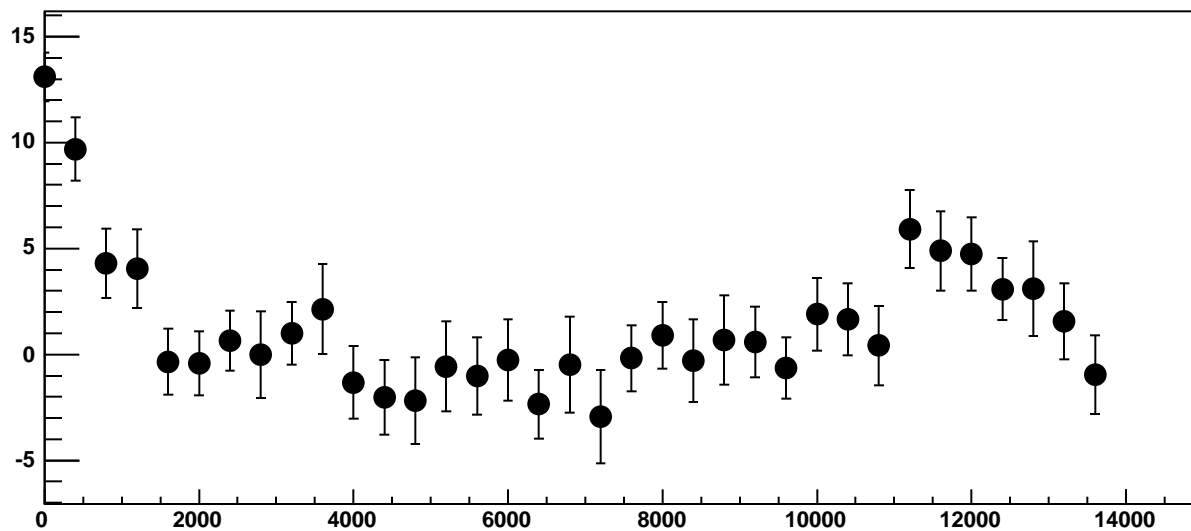


χ^2 / ndf 17 / 23
p0 186.4 ± 0.774
p1 0.01429 ± 0.0001168

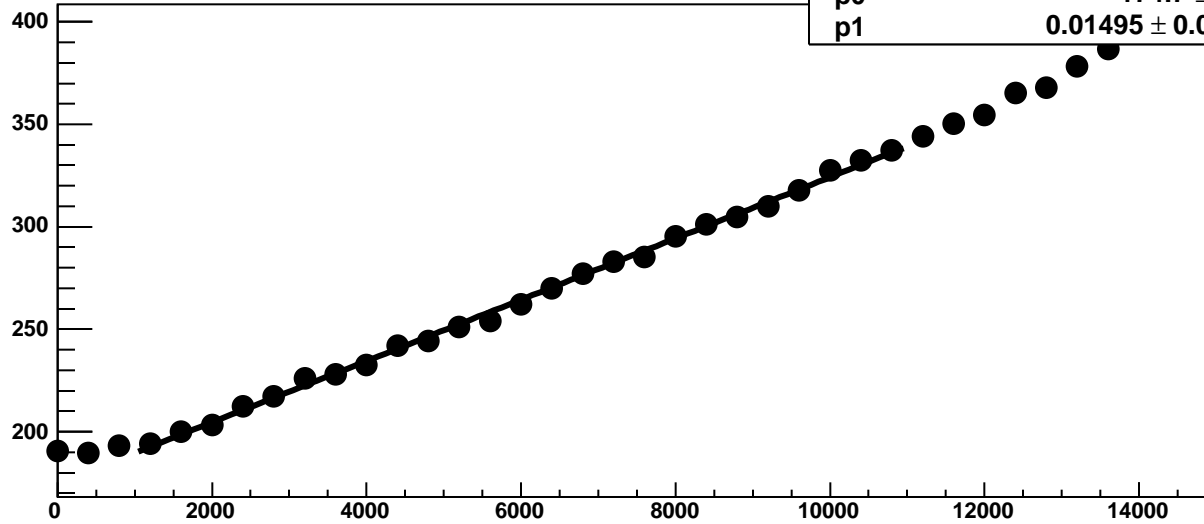
Chip 8, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

34.05 / 23

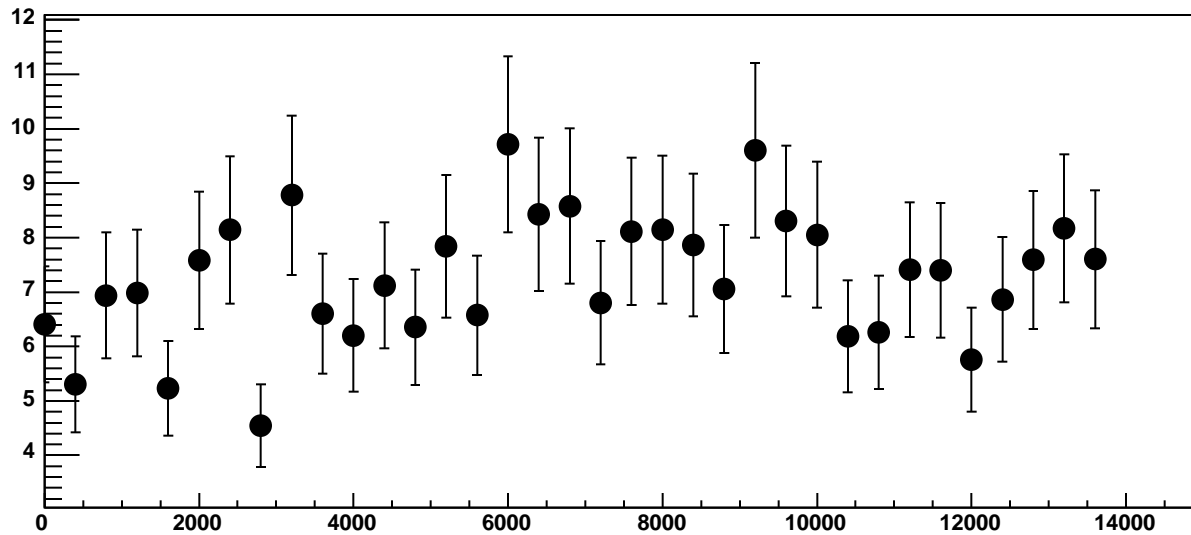
p0

174.7 ± 0.6858

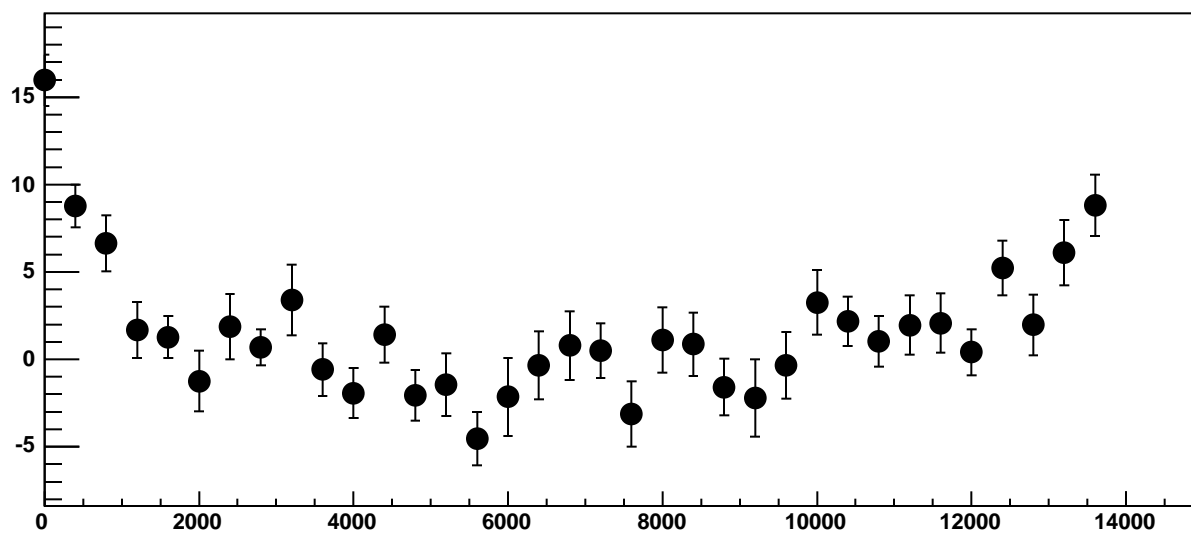
p1

0.01495 ± 0.0001075

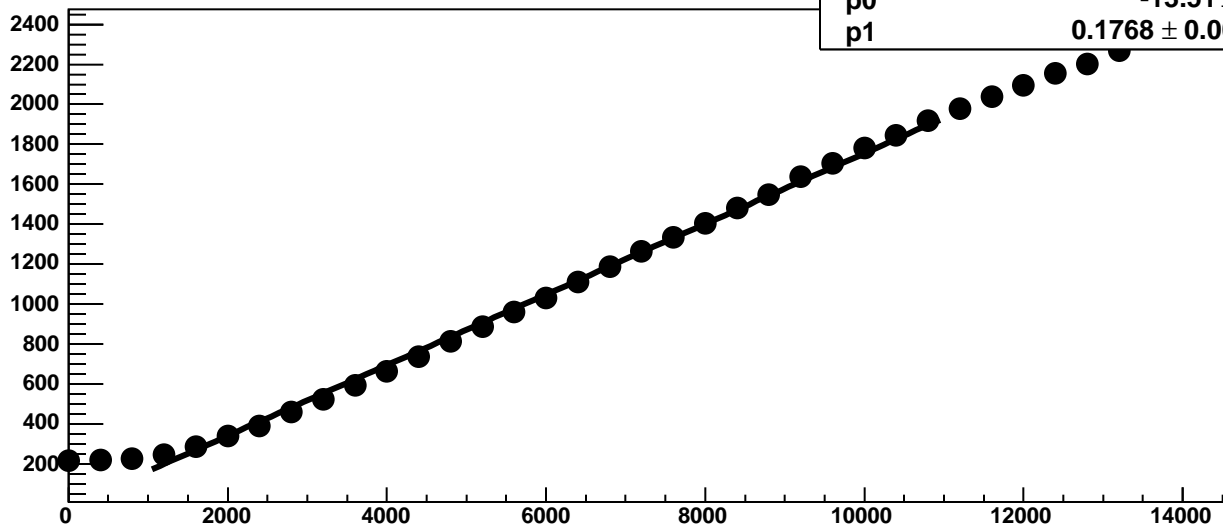
Chip 8, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



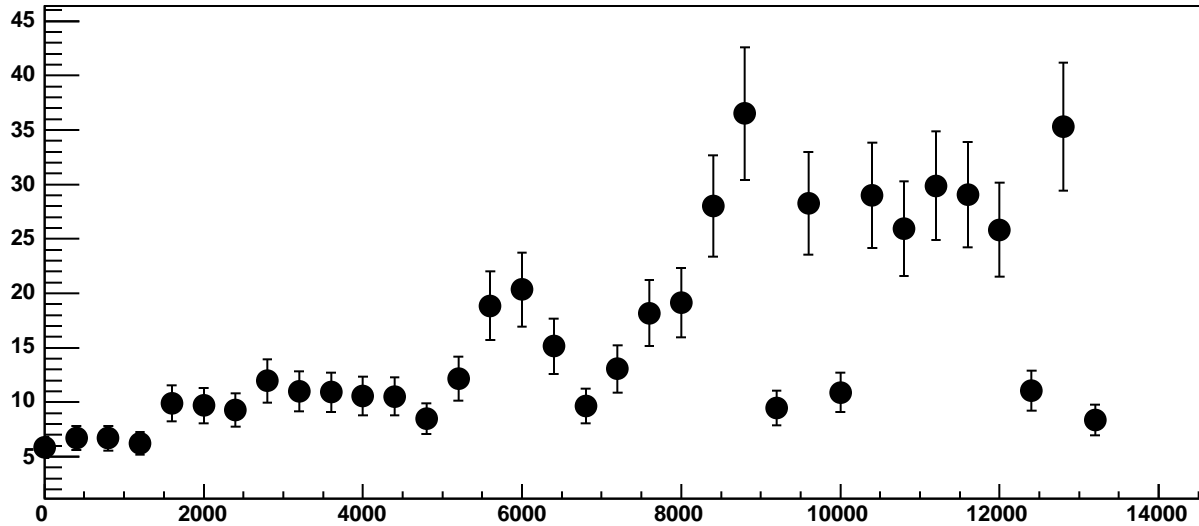
Chip 8, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



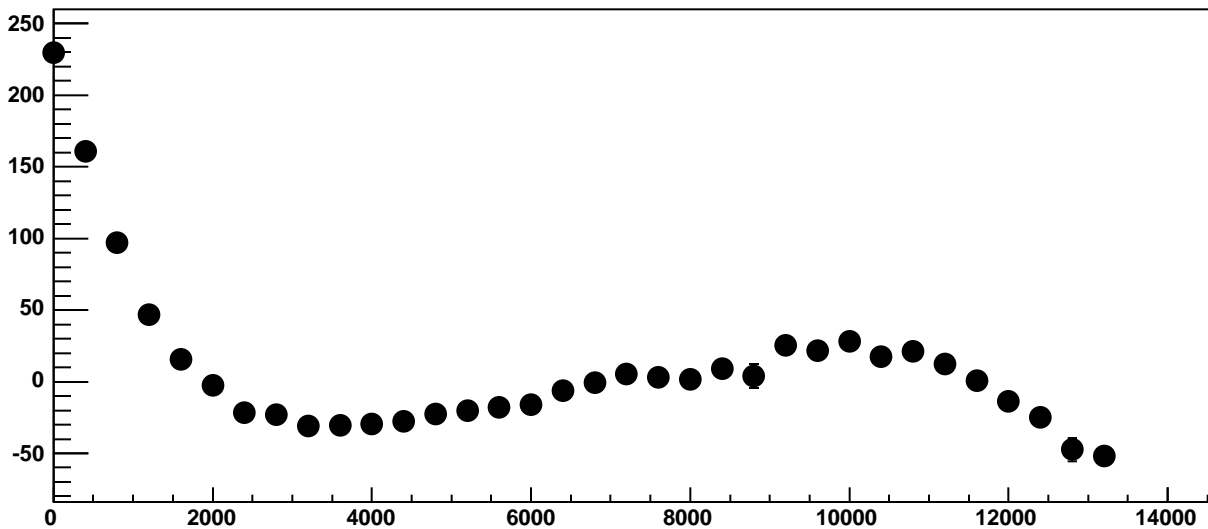
Chip 8, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC



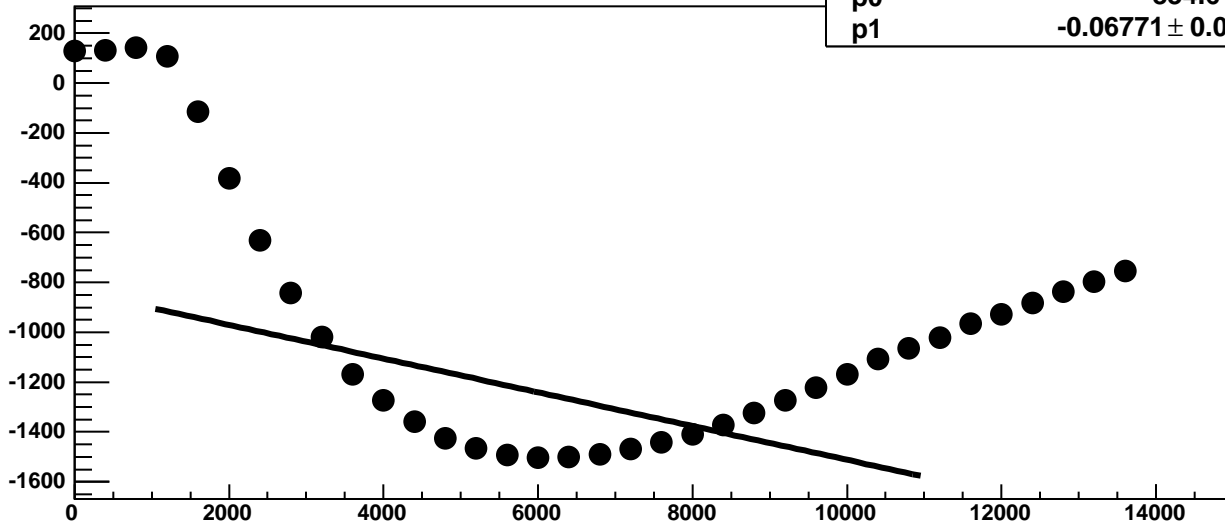
Chip 8, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC

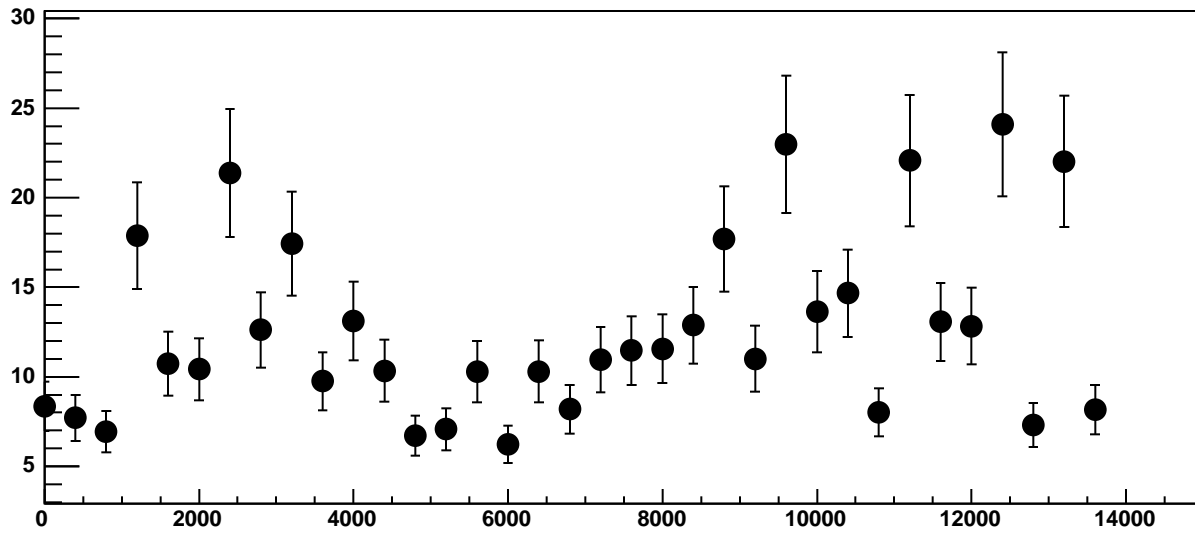


Chip 8, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

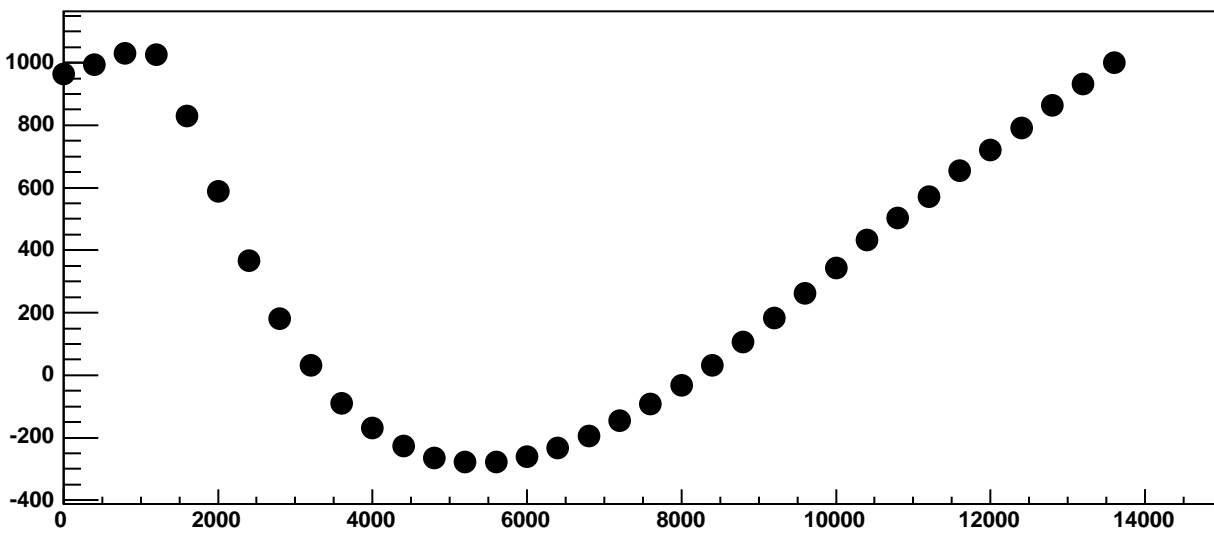


χ^2 / ndf 5.03e+05 / 23
p0 -834.6 ± 1.246
p1 -0.06771 ± 0.0001915

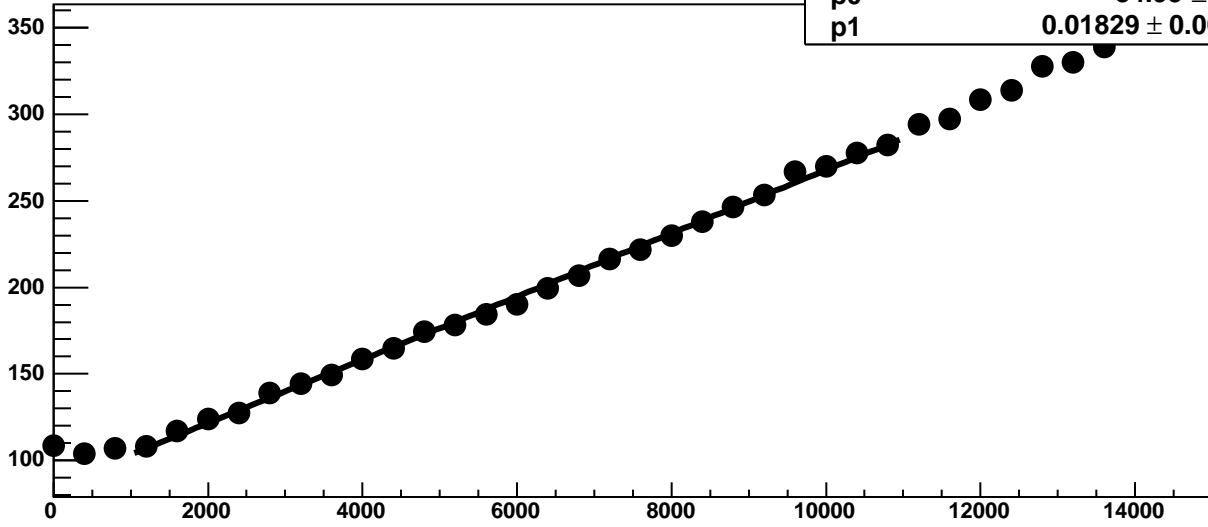
Chip 8, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

46.25 / 23

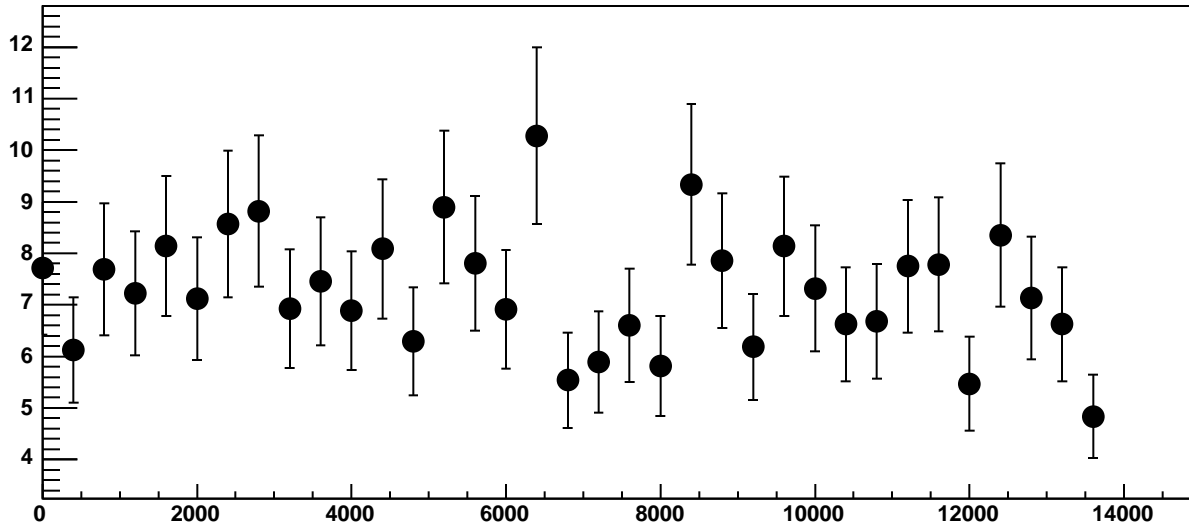
p0

84.95 ± 0.7925

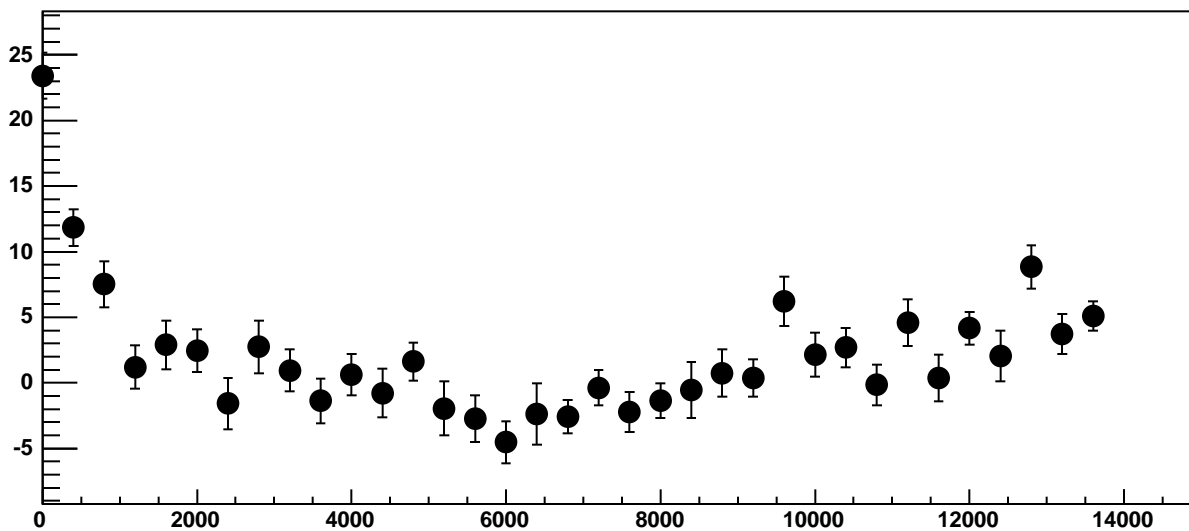
p1

0.01829 ± 0.0001158

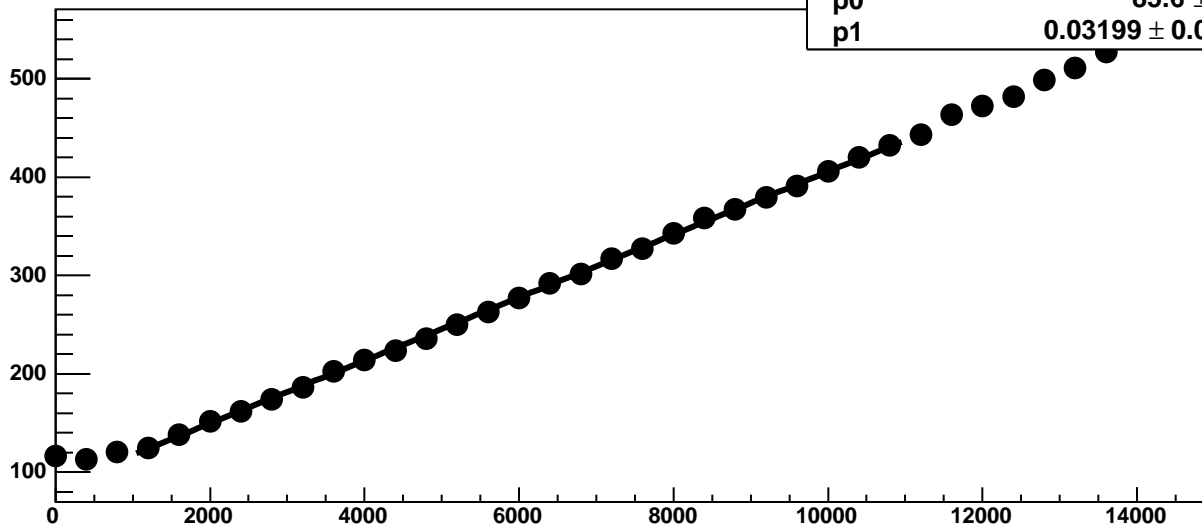
Chip 8, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

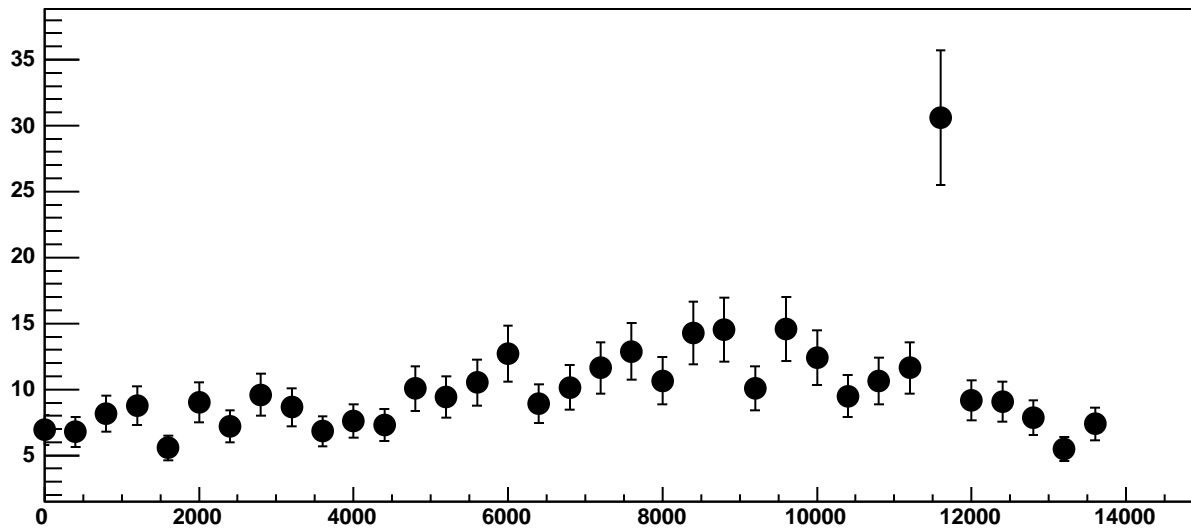


Chip 8, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC

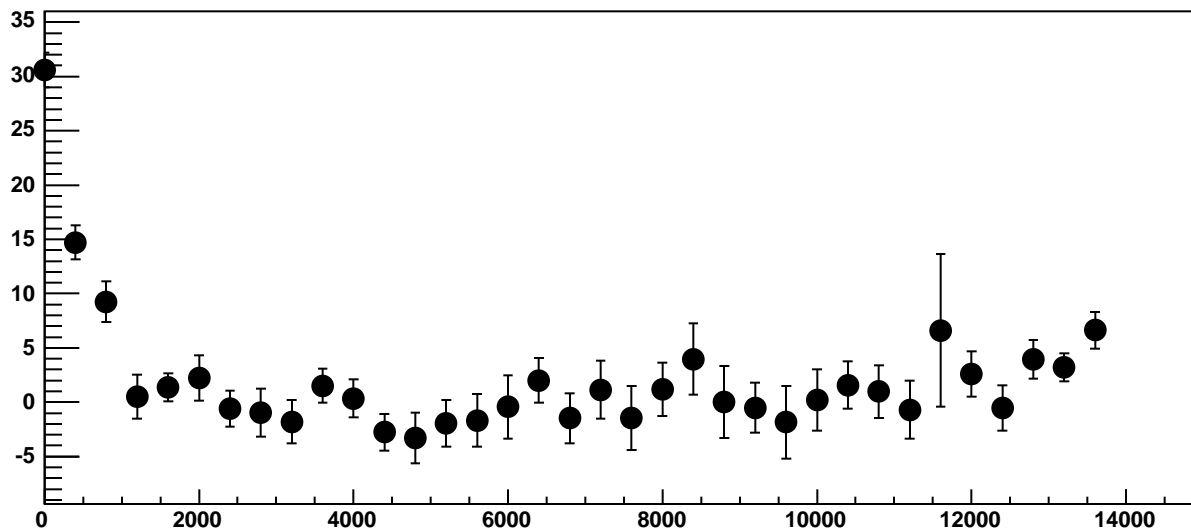


χ^2 / ndf 15.05 / 23
p0 85.6 ± 0.8569
p1 0.03199 ± 0.0001492

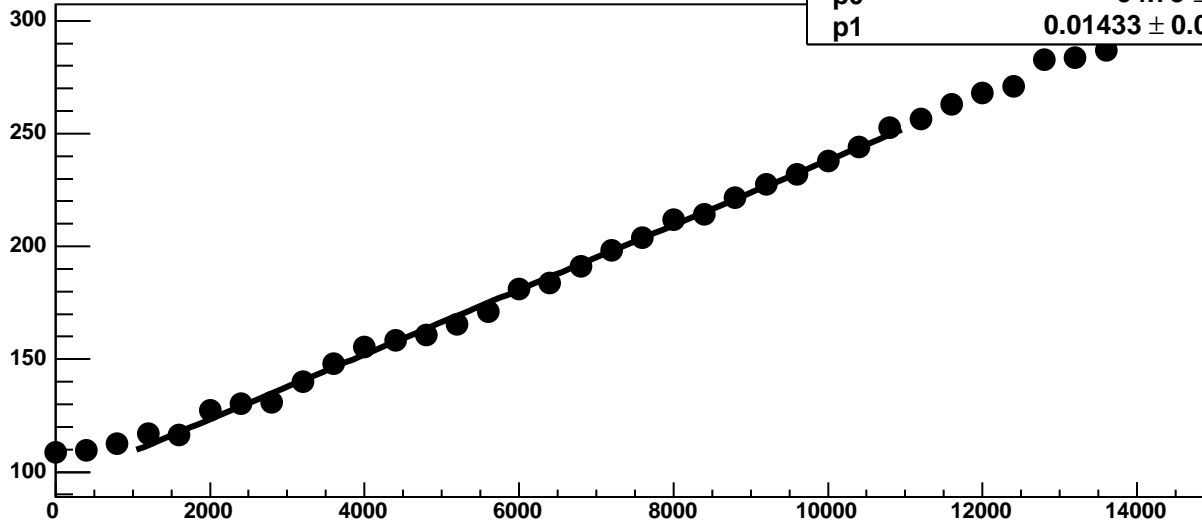
Chip 8, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

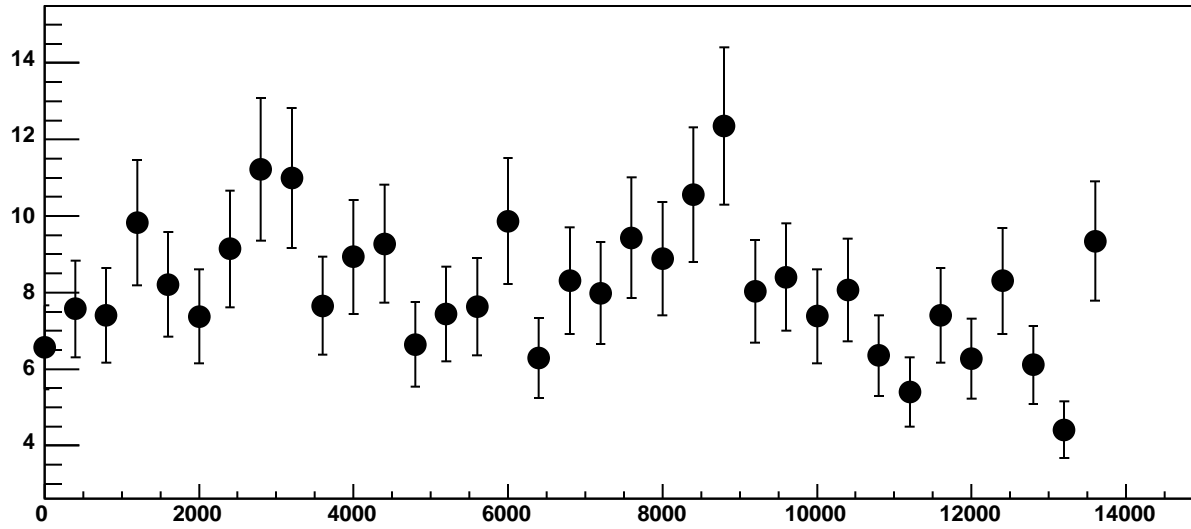


Chip 8, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

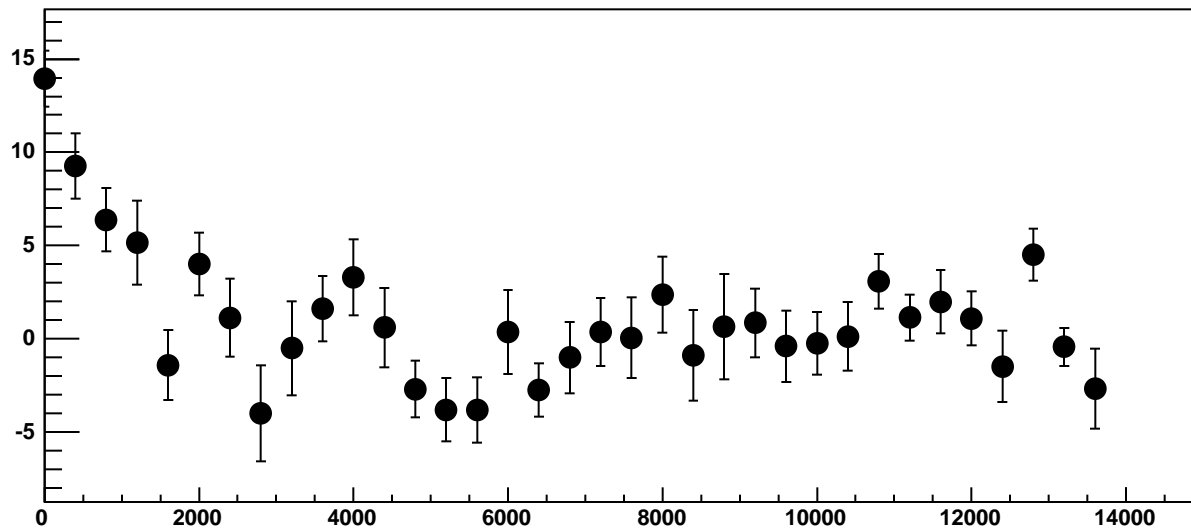


χ^2 / ndf 40.73 / 23
p0 94.73 ± 0.8976
p1 0.01433 ± 0.0001313

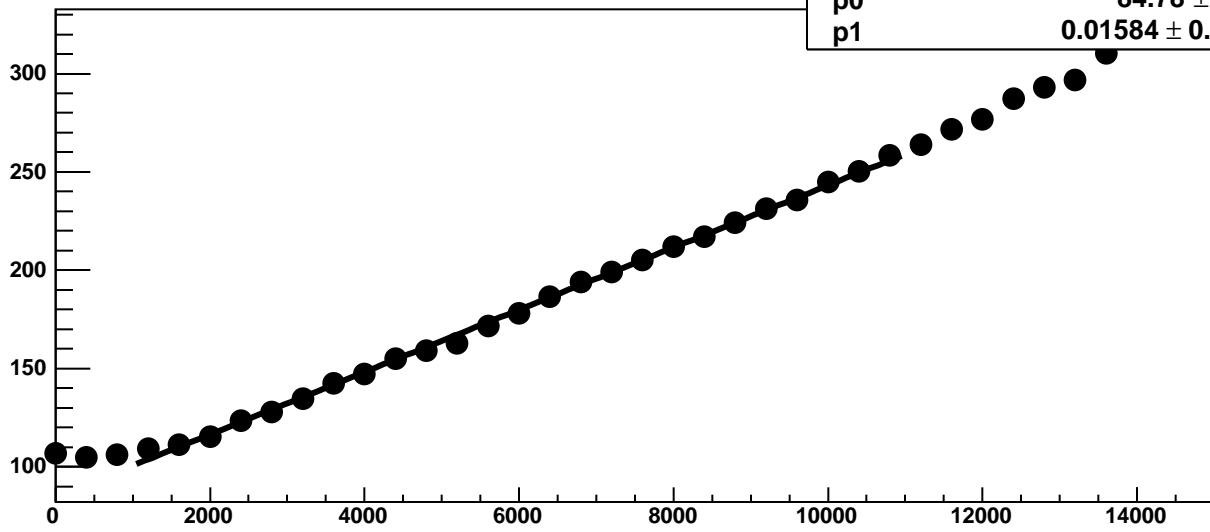
Chip 8, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

29.19 / 23

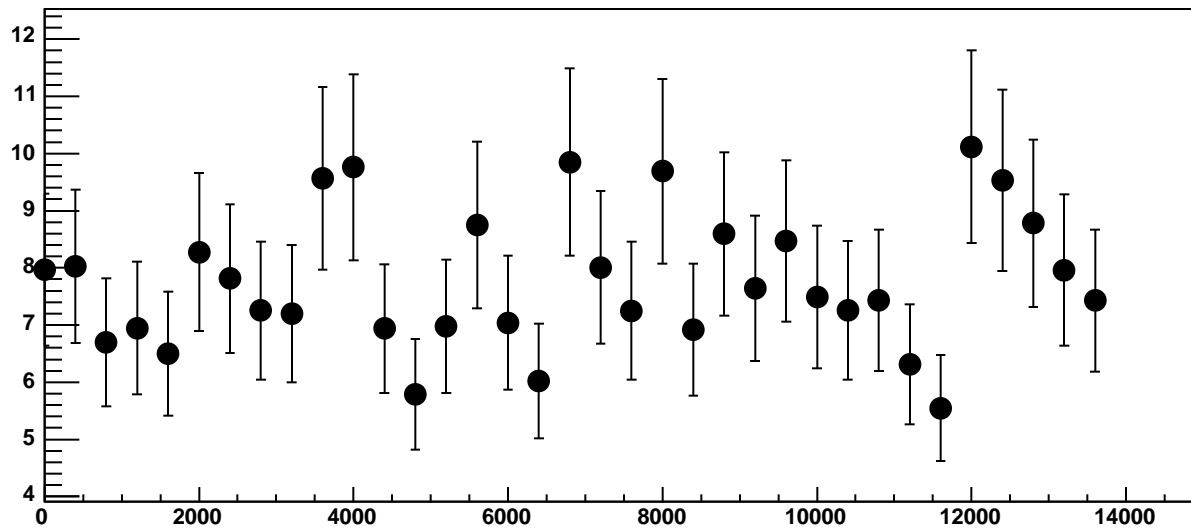
p0

84.78 ± 0.7803

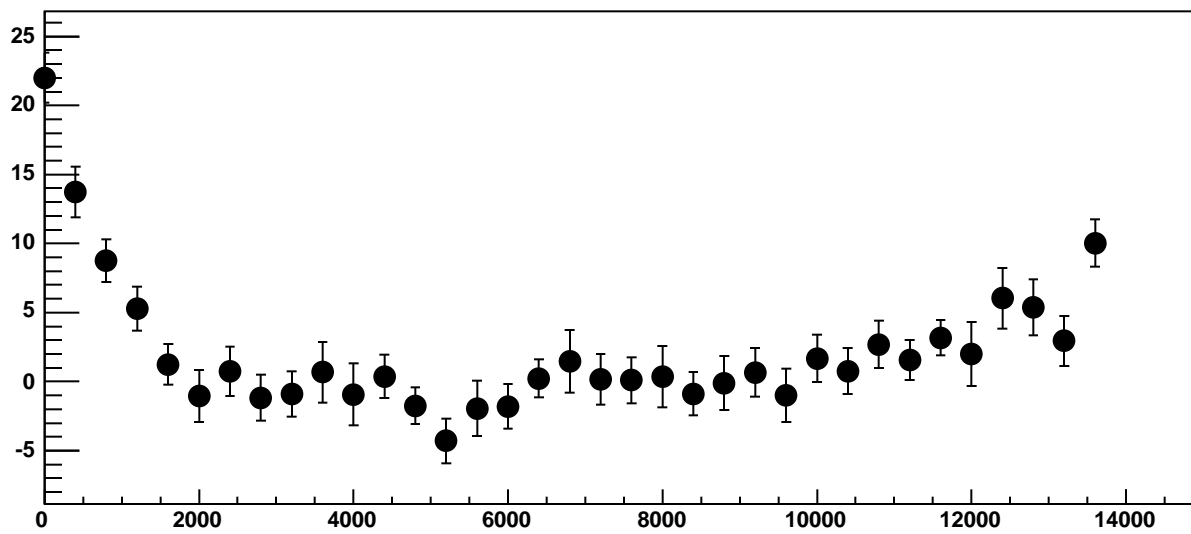
p1

0.01584 ± 0.000119

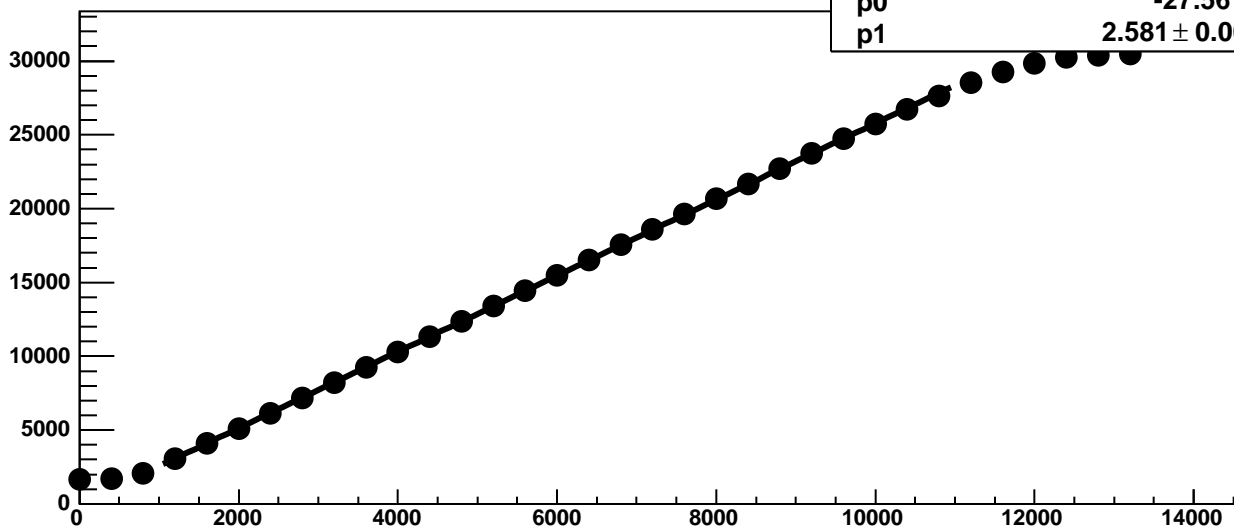
Chip 8, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

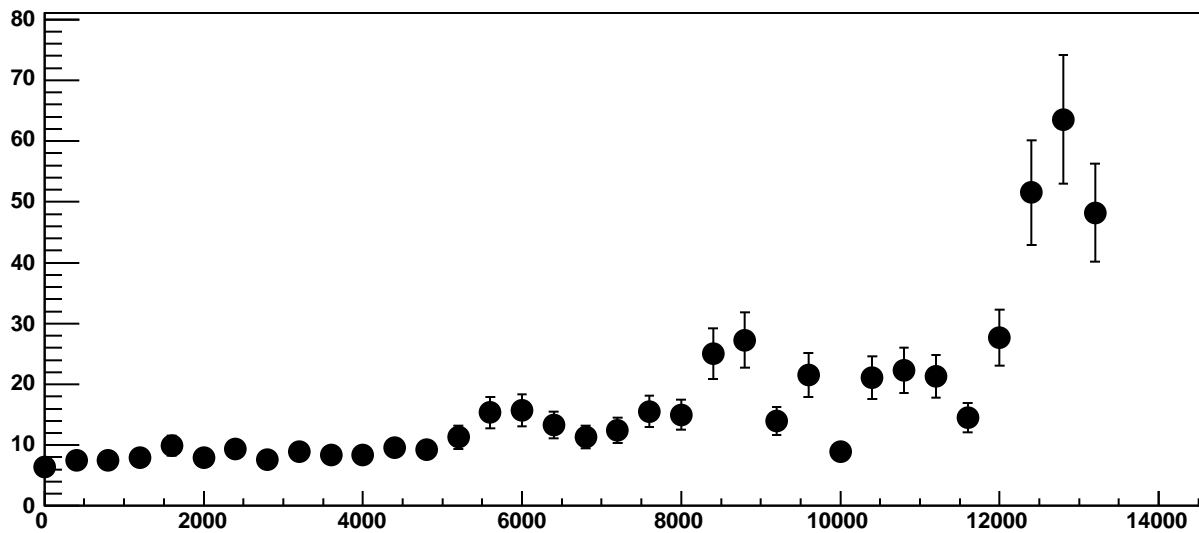


Chip 8, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC

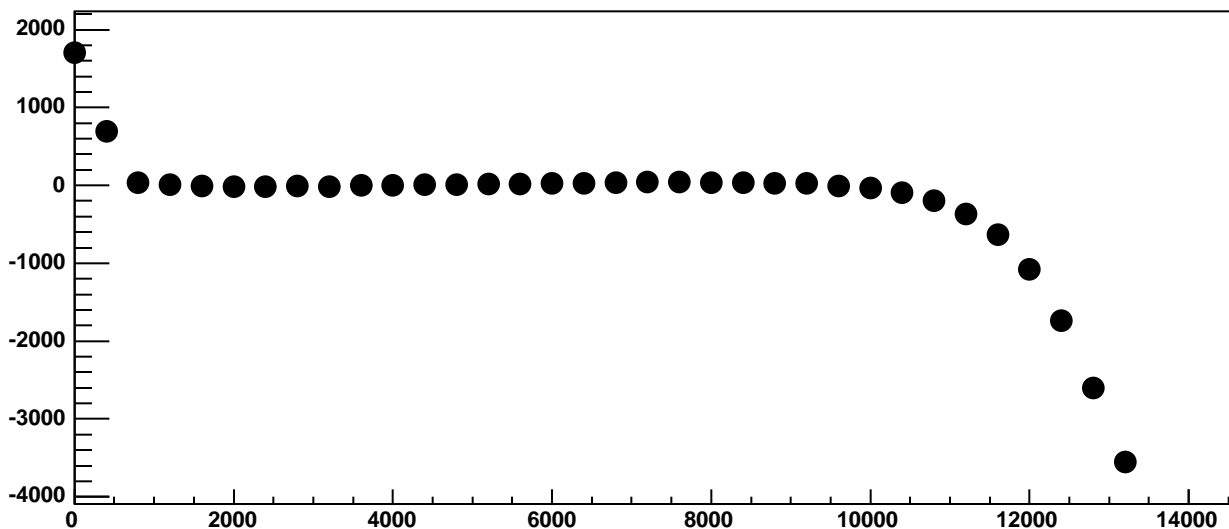


χ^2 / ndf 3455 / 23
p0 -27.56 ± 1.011
p1 2.581 ± 0.0001896

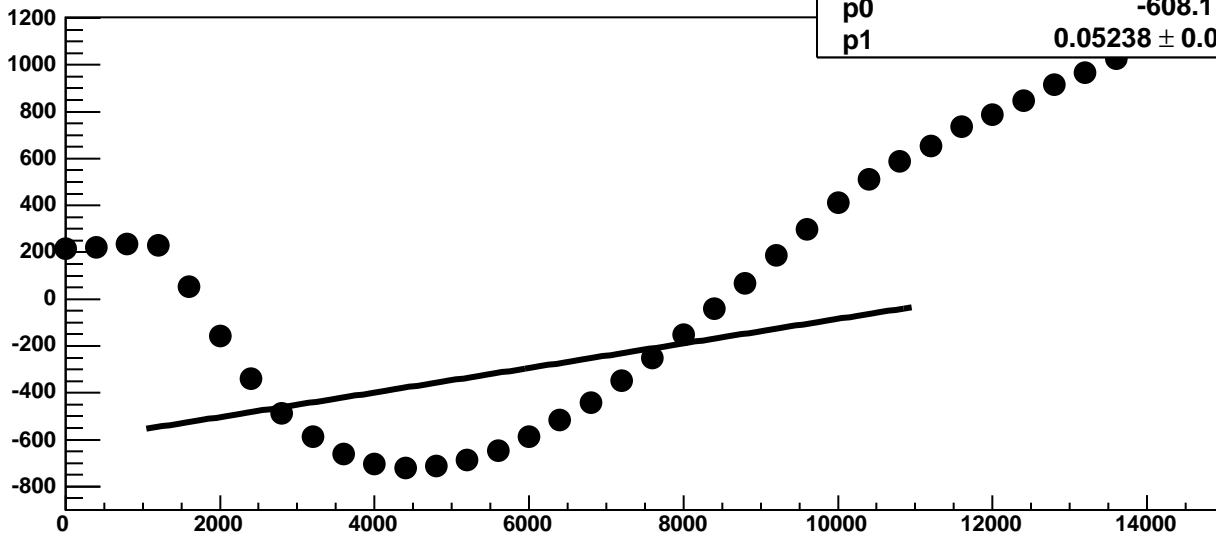
Chip 8, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC

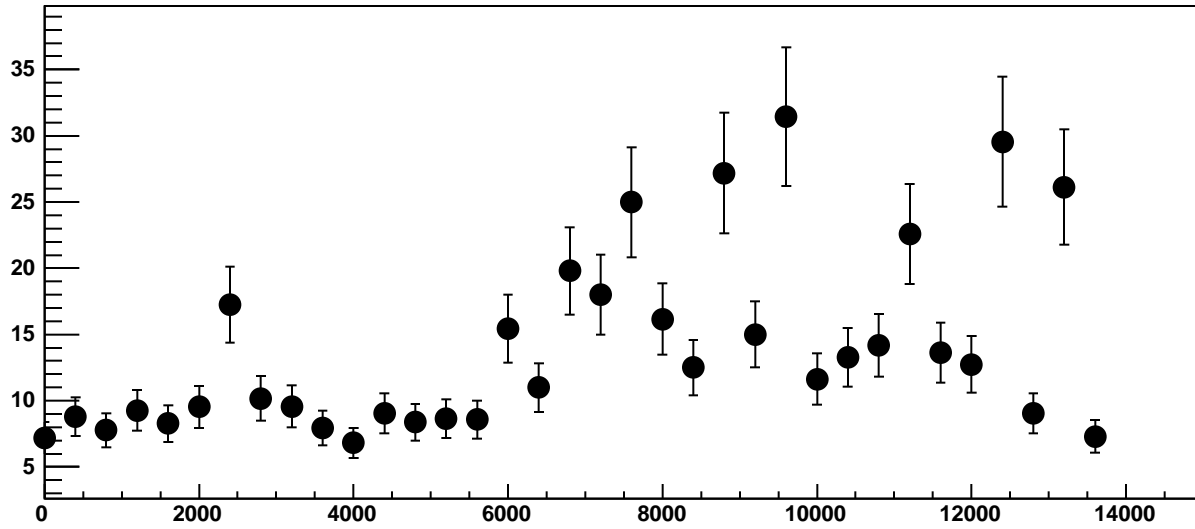


Chip 8, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

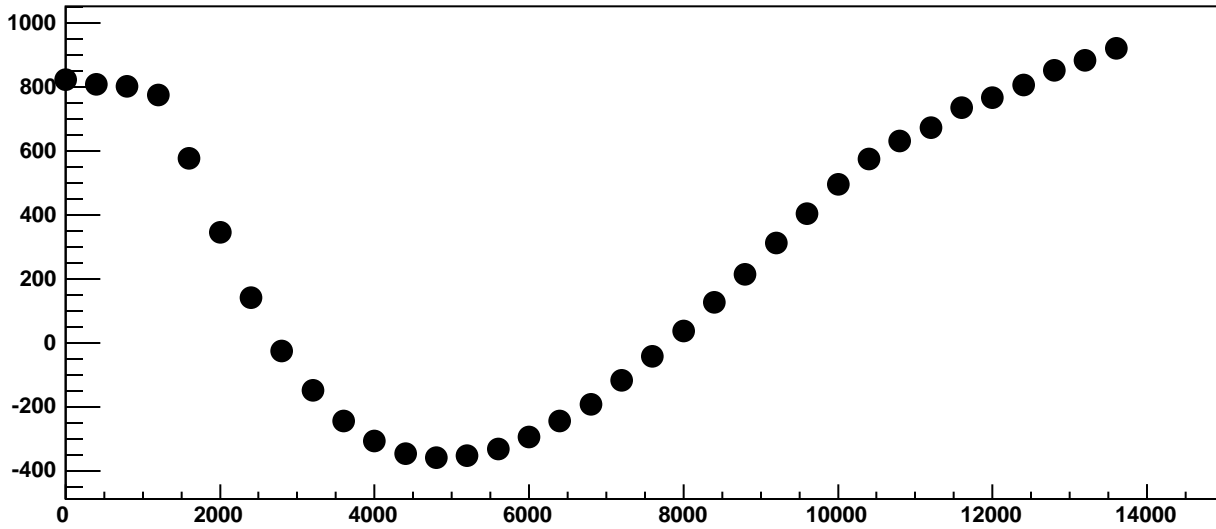


χ^2 / ndf 5.754e+05 / 23
p0 -608.1 ± 1.068
p1 0.05238 ± 0.0001939

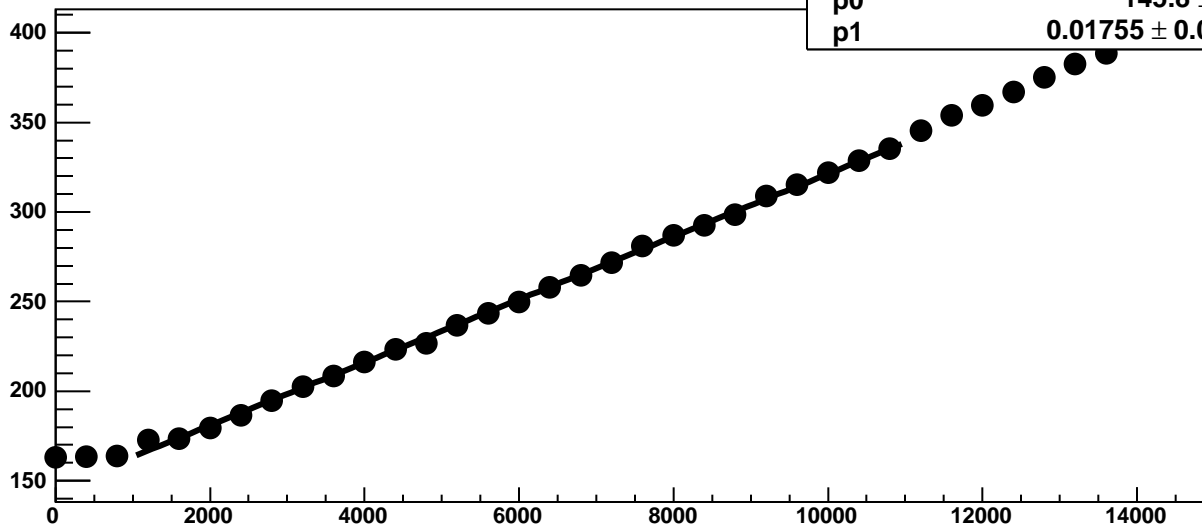
Chip 8, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

23.96 / 23

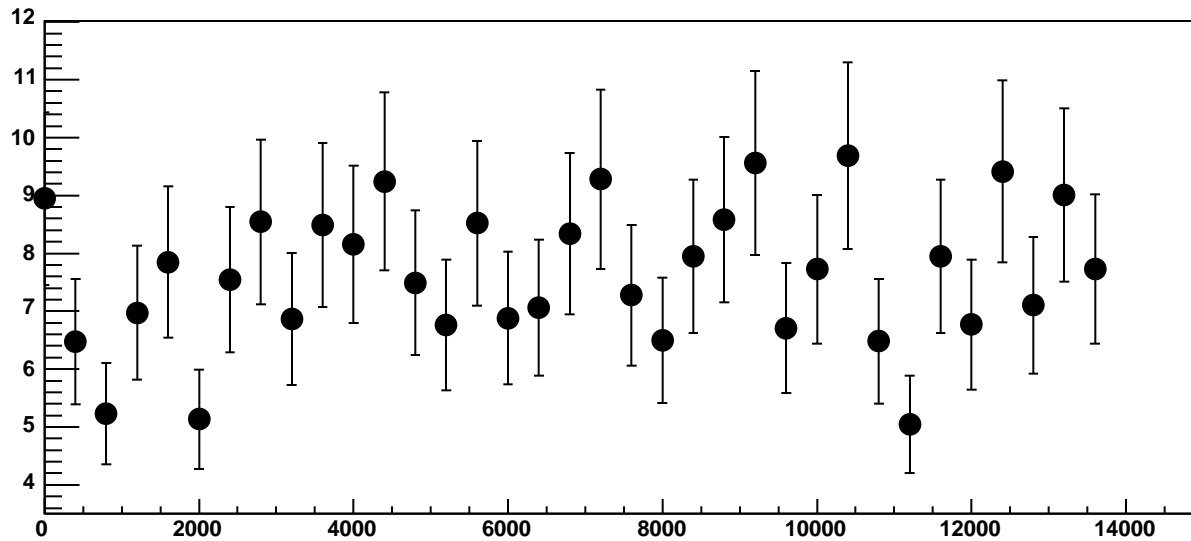
p0

145.8 ± 0.7561

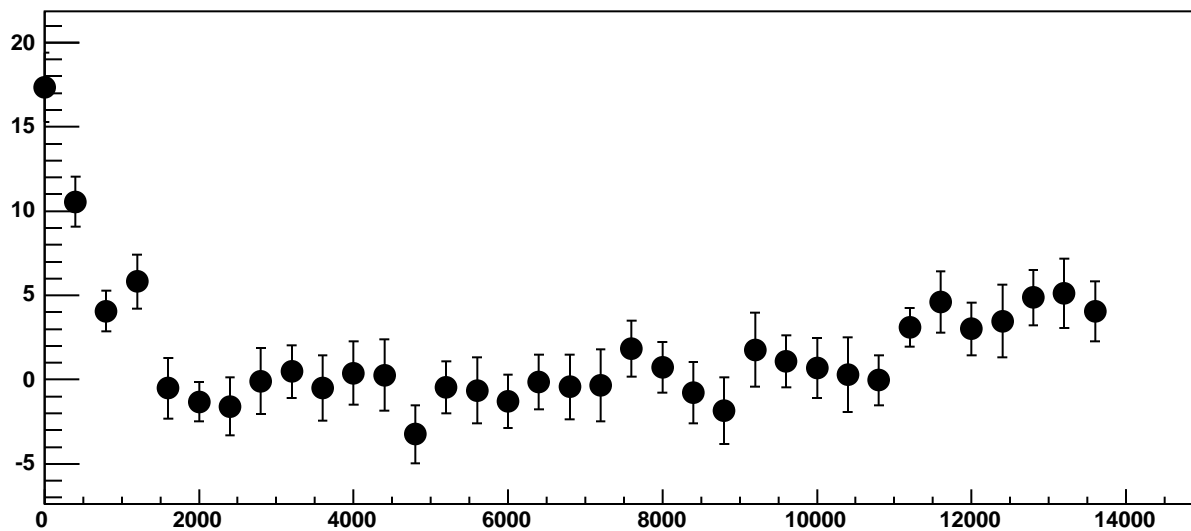
p1

0.01755 ± 0.0001159

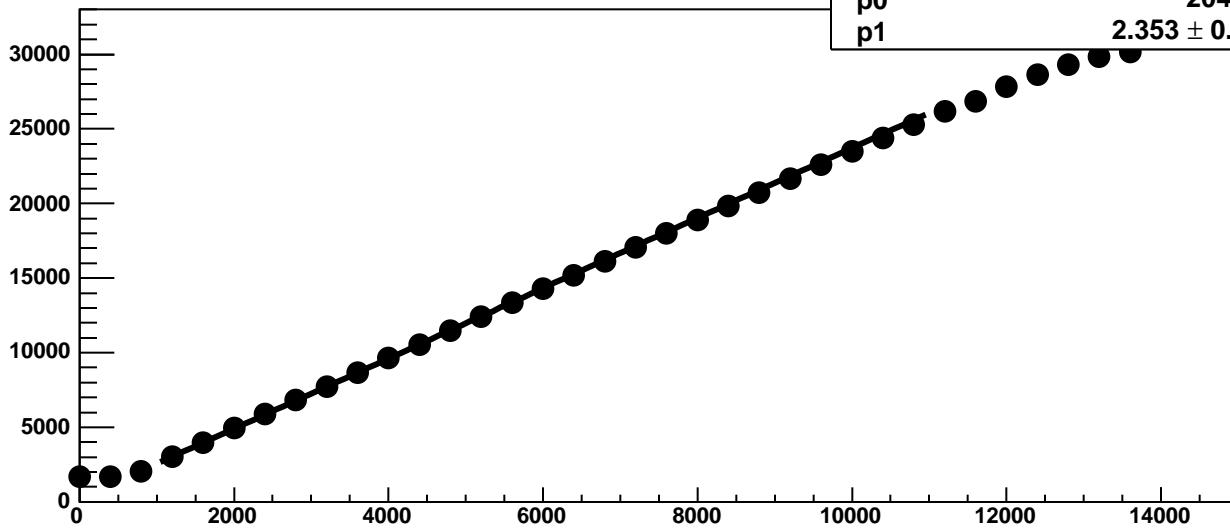
Chip 8, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

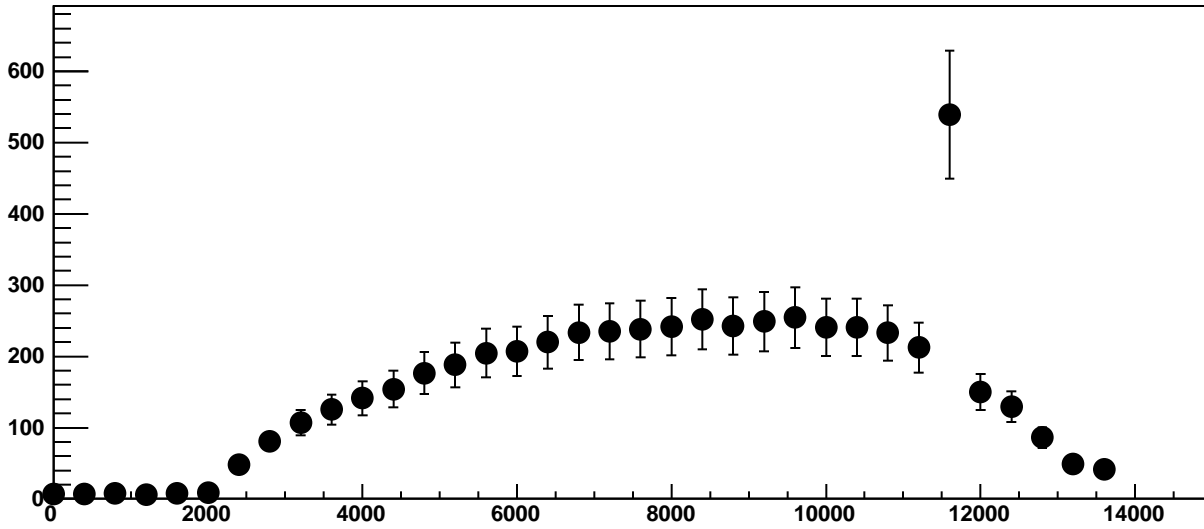


Chip 8, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC

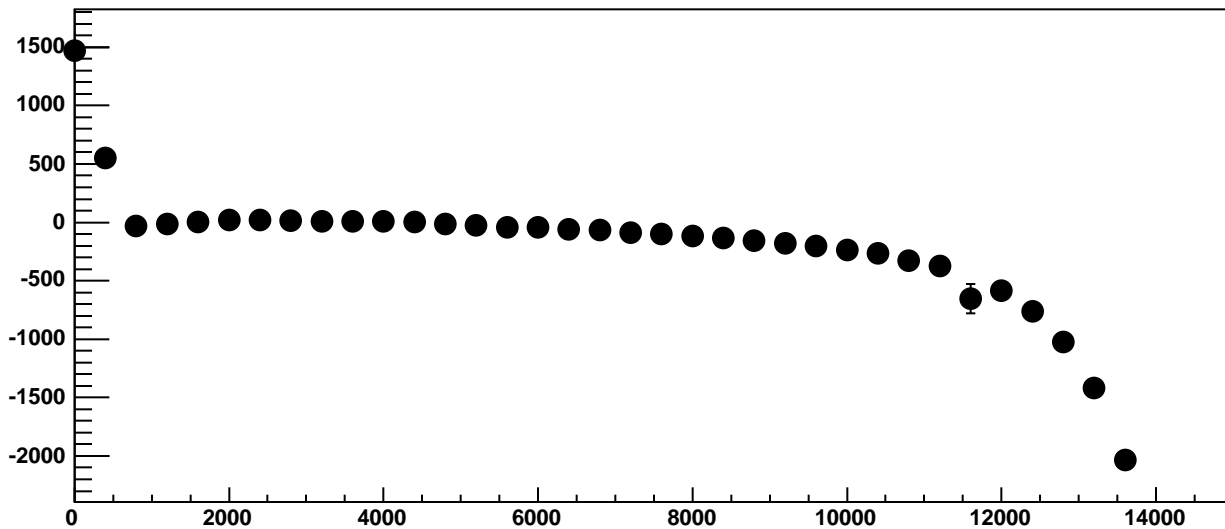


χ^2 / ndf 335.7 / 23
p0 204 ± 2.67
p1 2.353 ± 0.001612

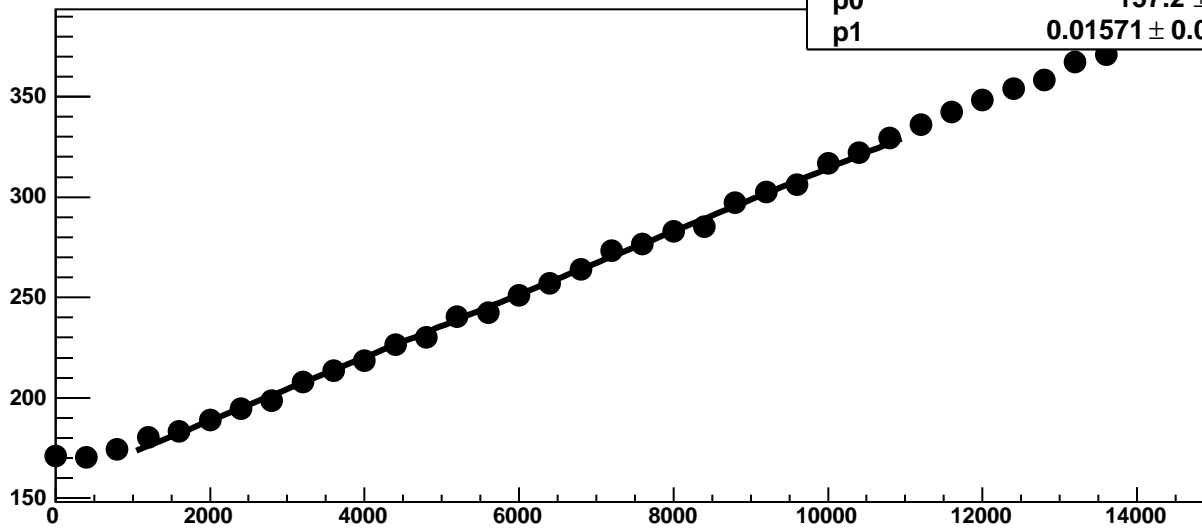
Chip 8, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC

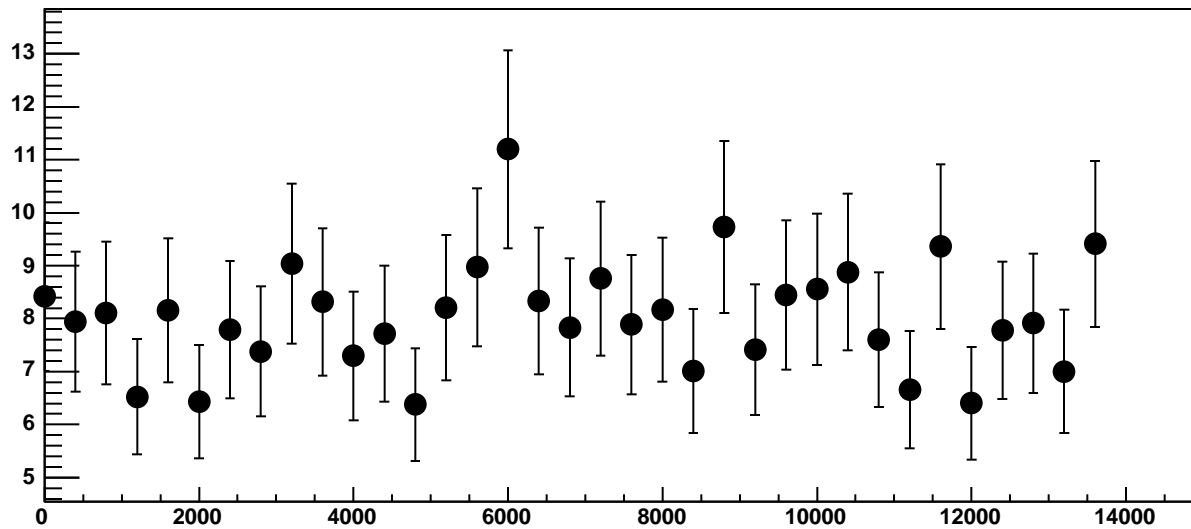


Chip 8, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

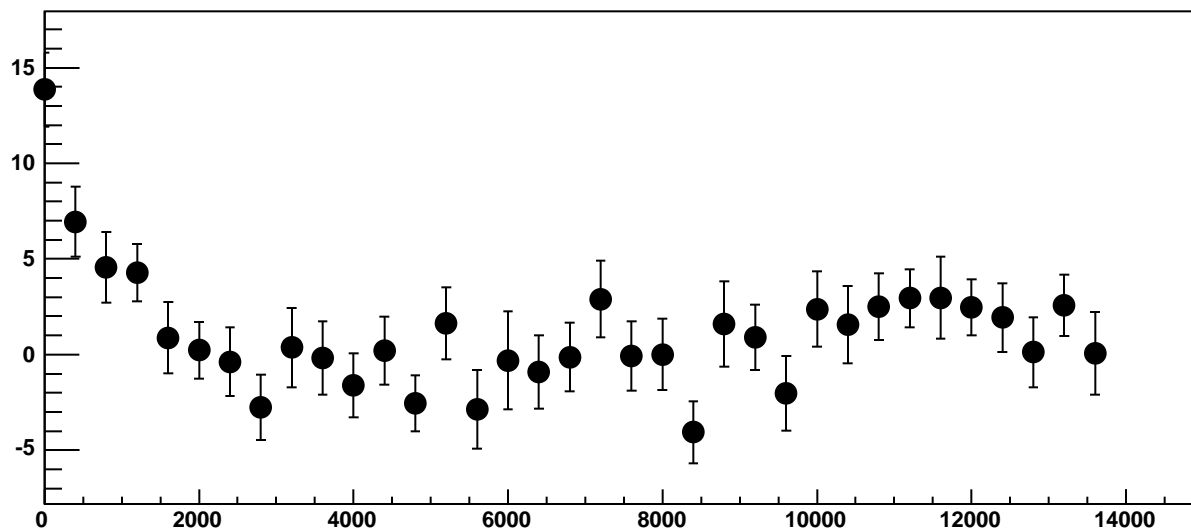


χ^2 / ndf 32.49 / 23
p0 157.2 ± 0.7918
p1 0.01571 ± 0.0001226

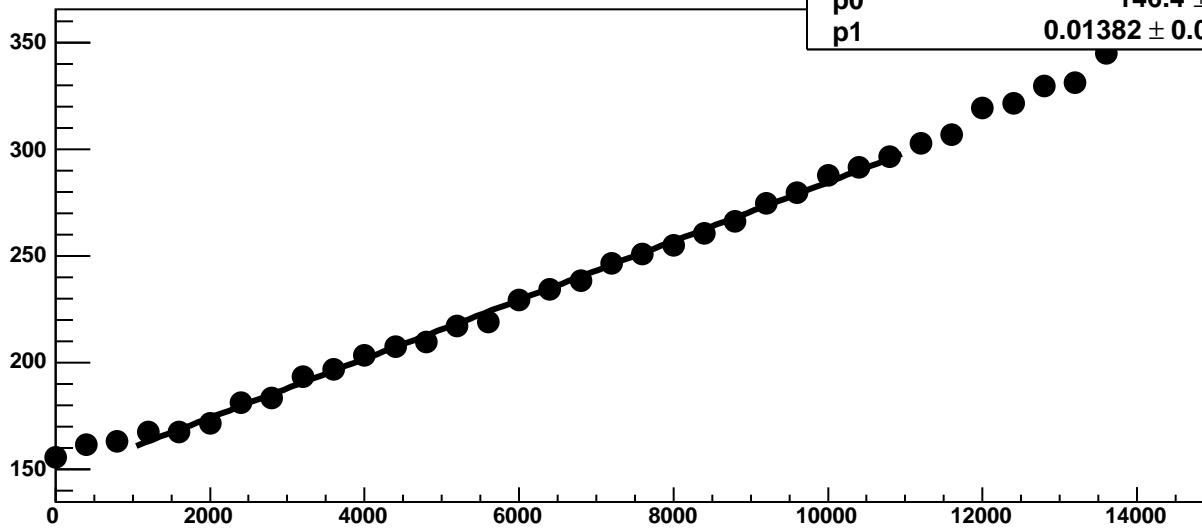
Chip 8, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

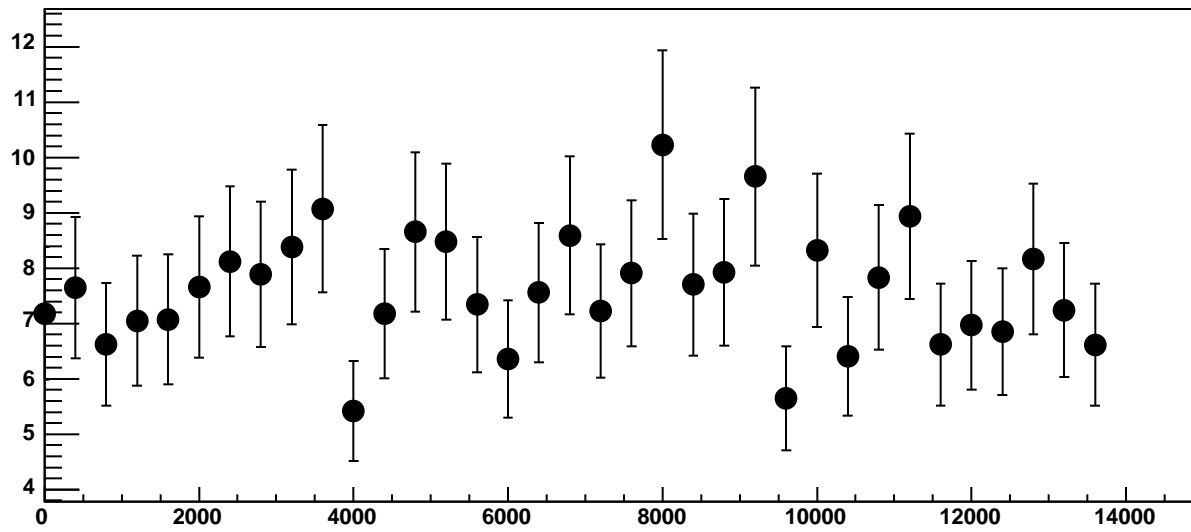


Chip 8, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

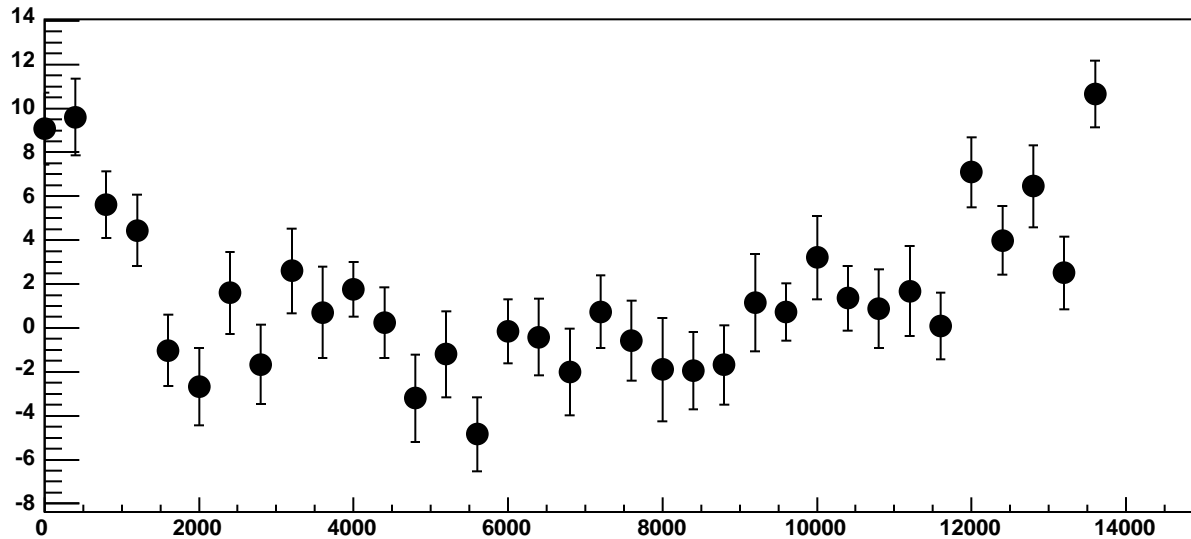


χ^2 / ndf 35.62 / 23
p0 146.4 ± 0.7832
p1 0.01382 ± 0.0001174

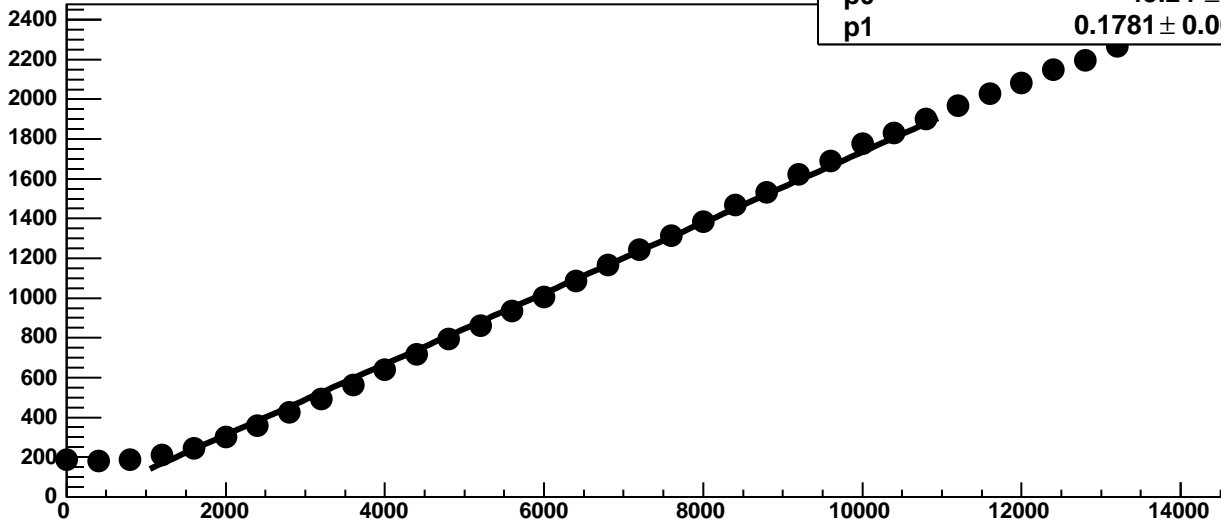
Chip 8, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



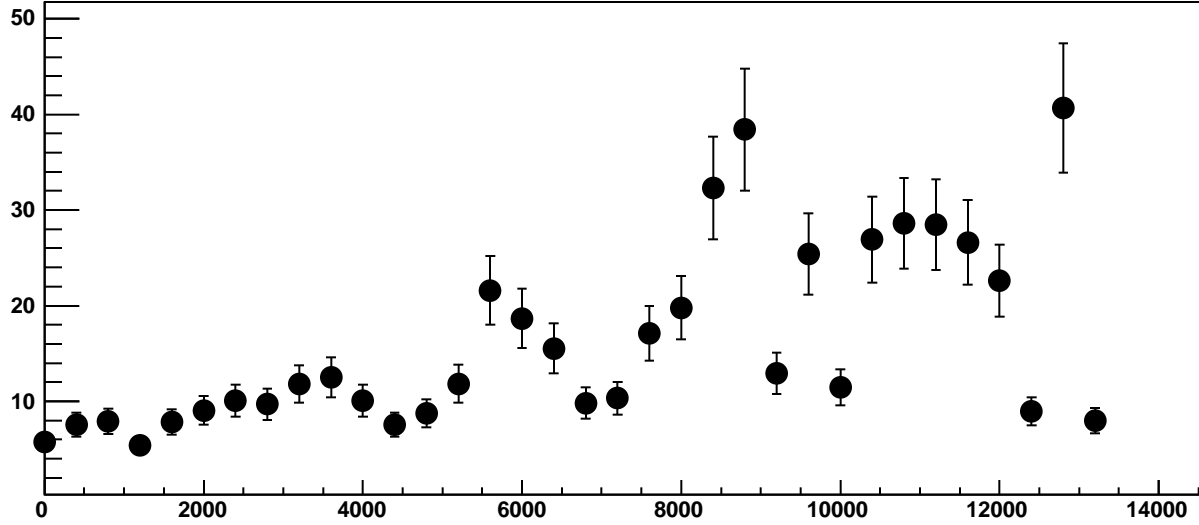
Chip 8, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



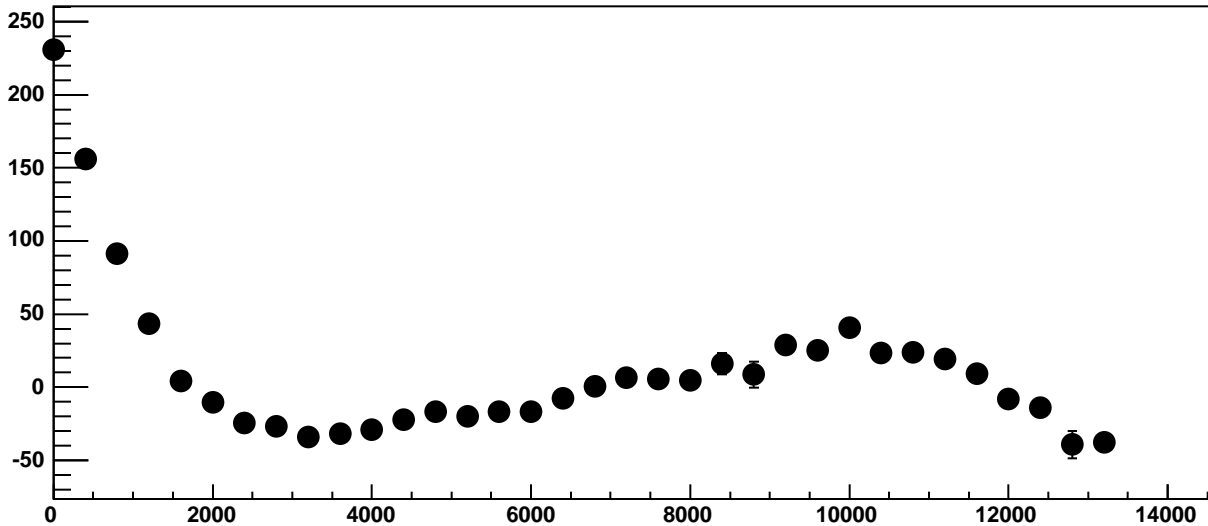
Chip 8, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC



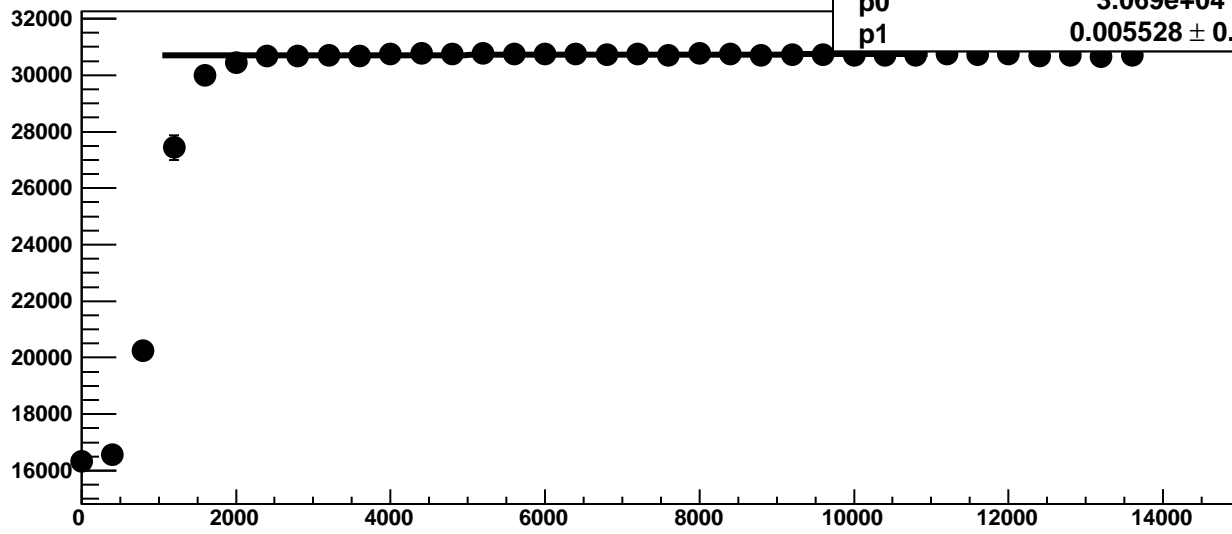
Chip 8, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

252.4 / 23

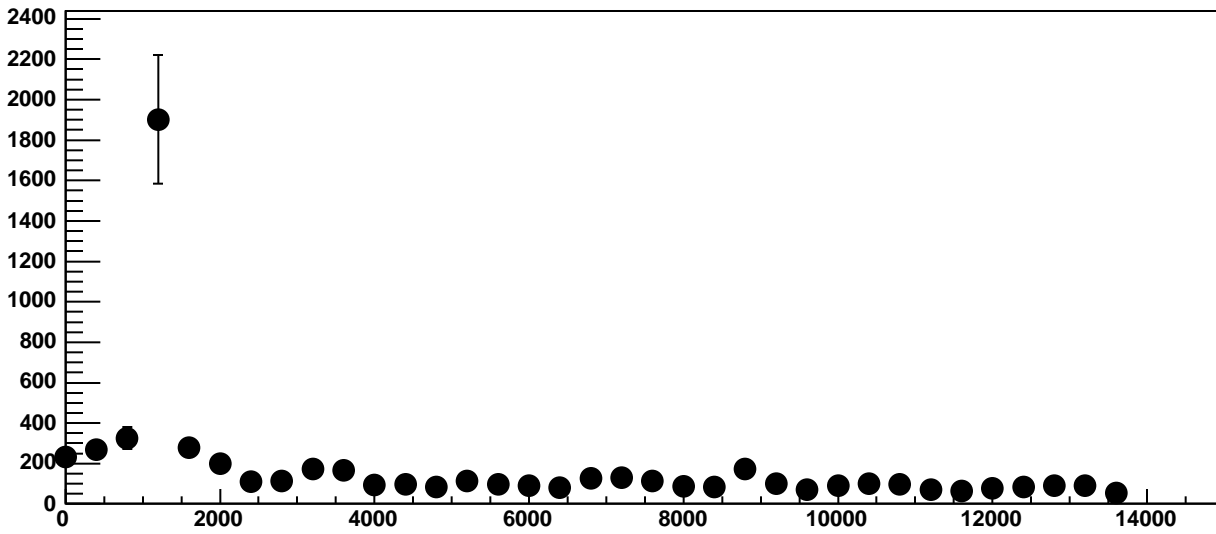
p0

$3.069\text{e}+04 \pm 14.14$

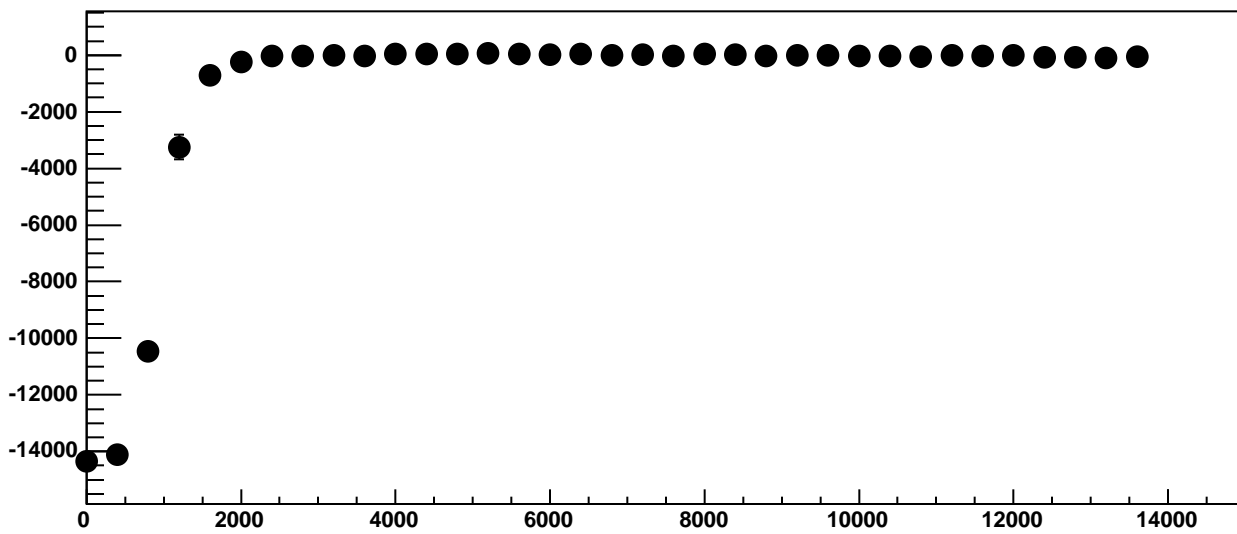
p1

0.005528 ± 0.001935

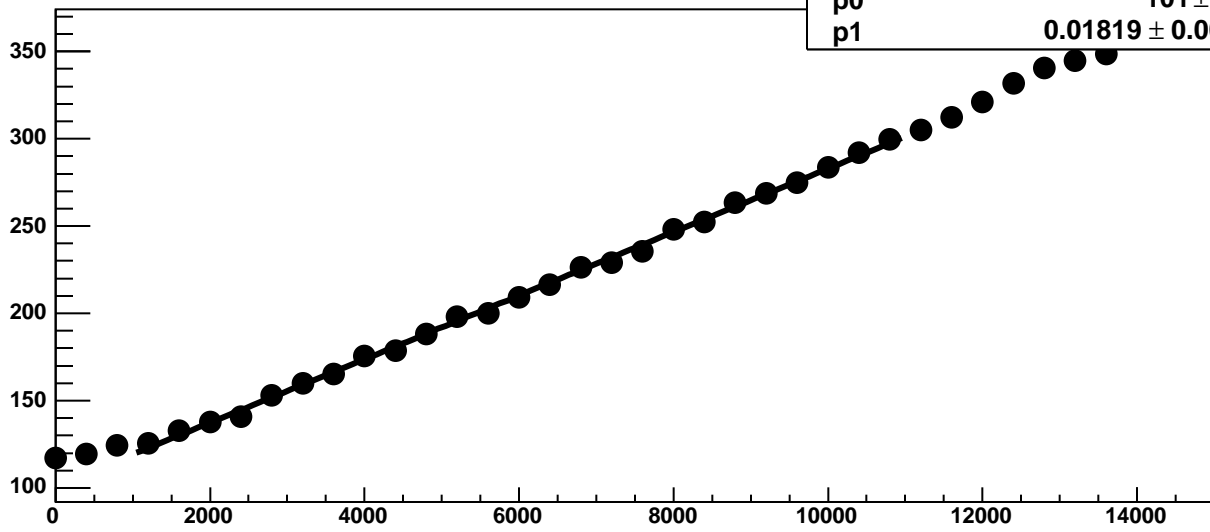
Chip 8, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC

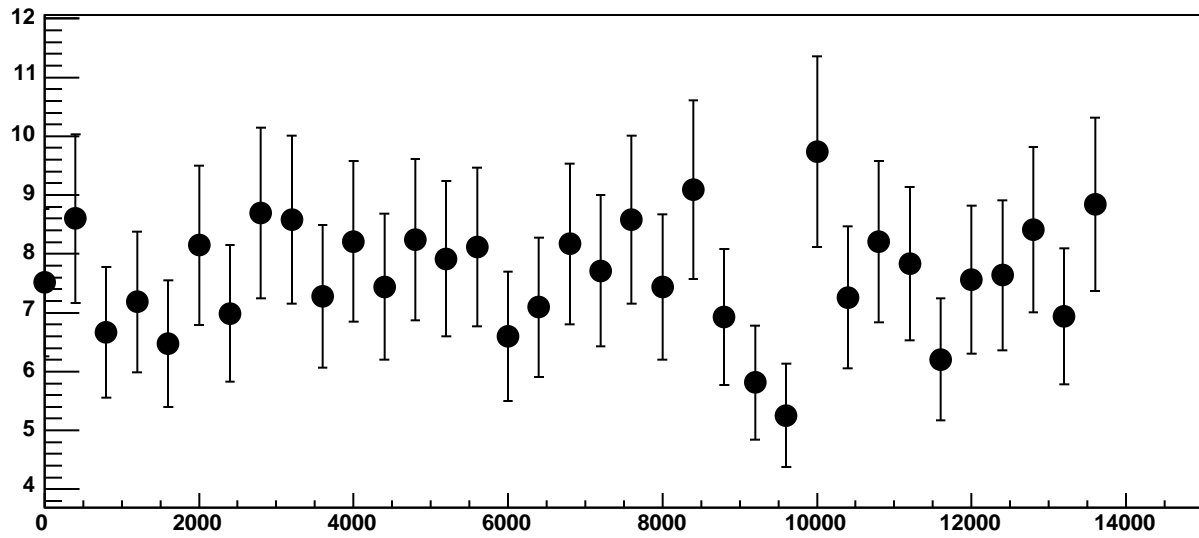


Chip 8, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

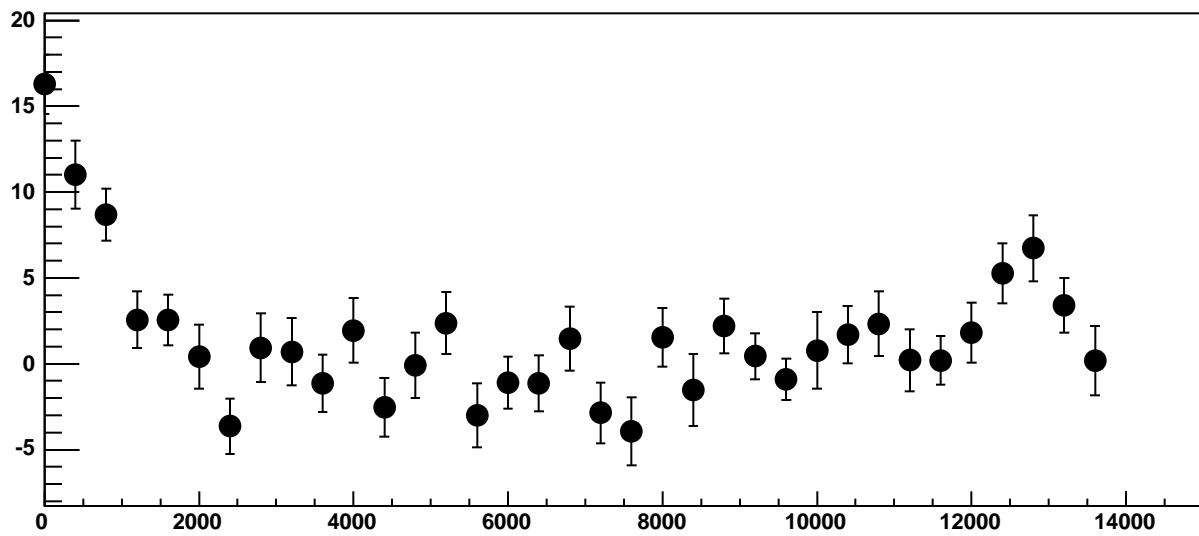


χ^2 / ndf 33.82 / 23
p0 101 ± 0.7864
p1 0.01819 ± 0.0001158

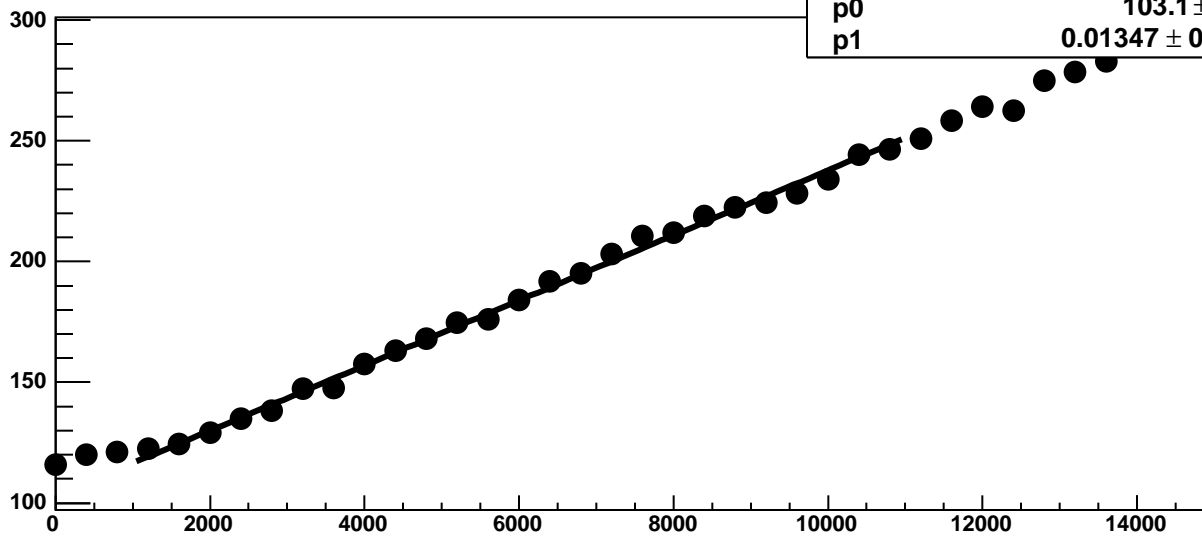
Chip 8, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

52.8 / 23

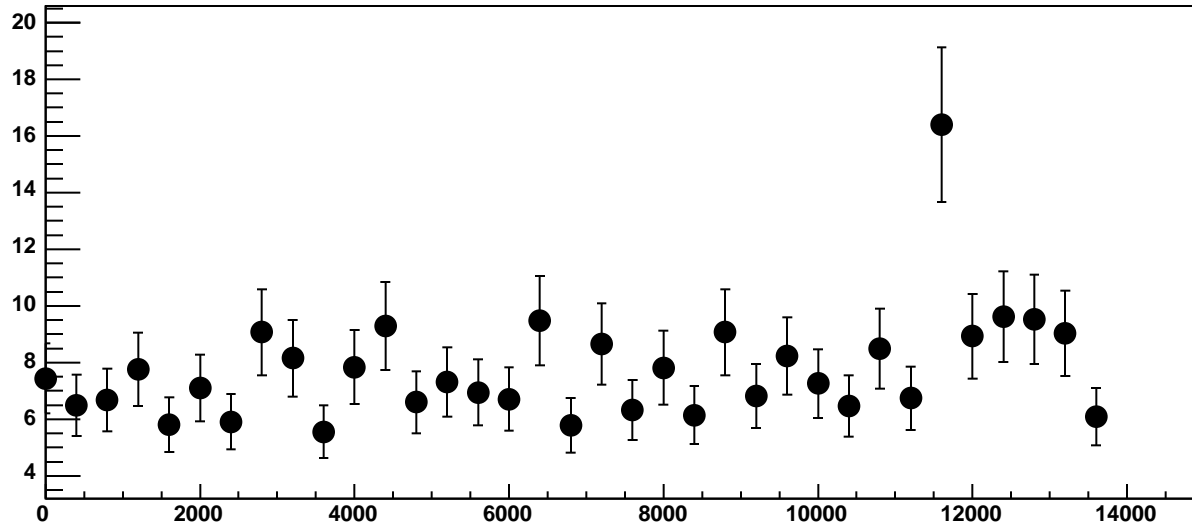
p0

103.1 ± 0.7385

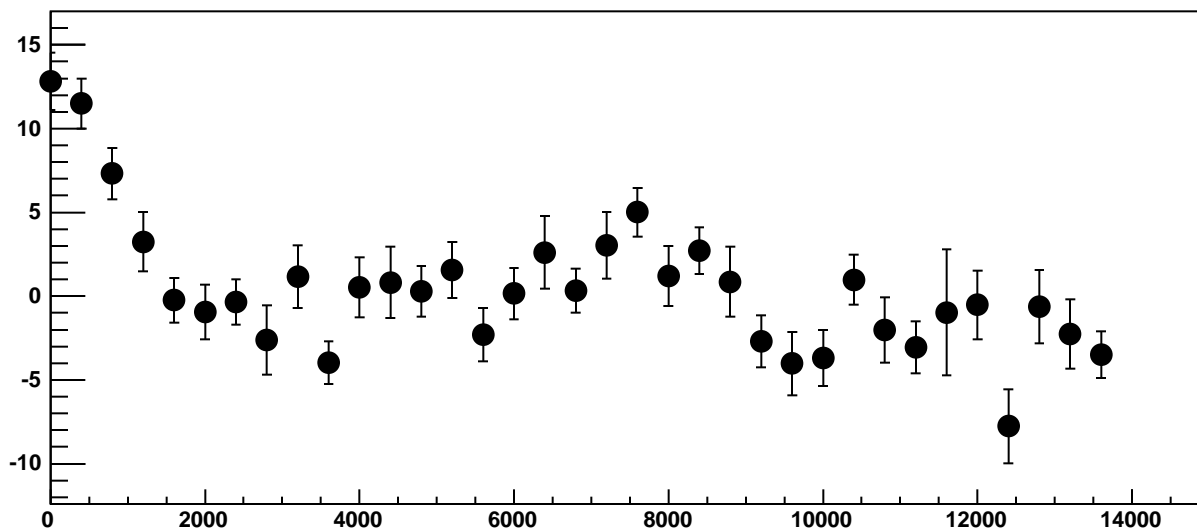
p1

0.01347 ± 0.000113

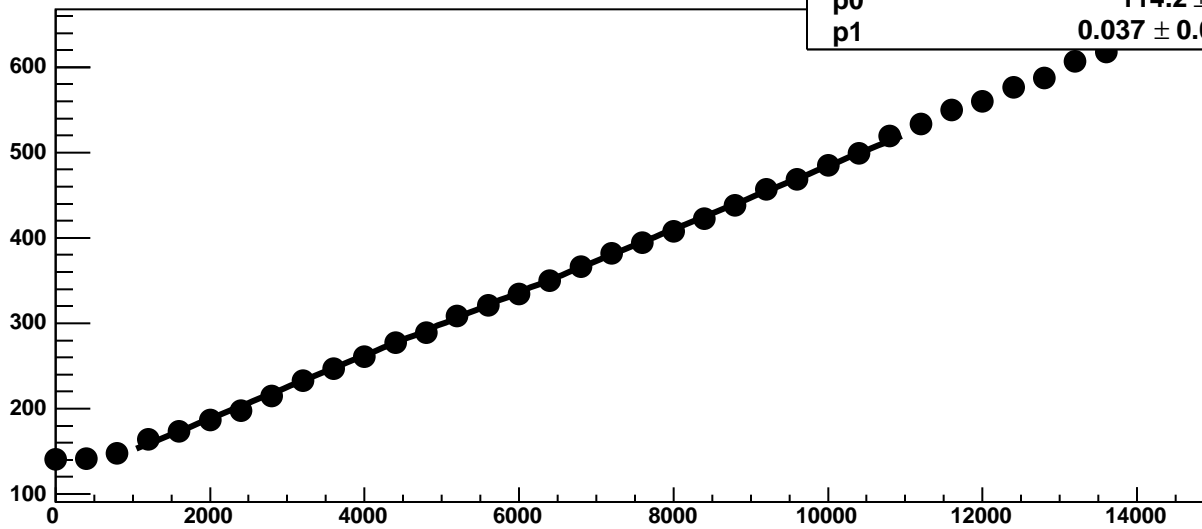
Chip 8, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

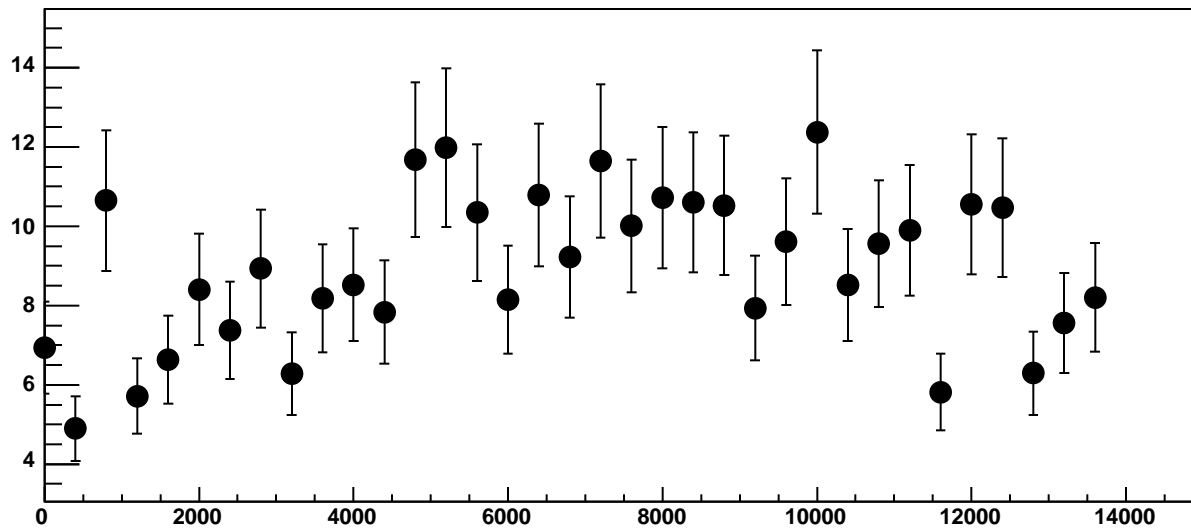


Chip 8, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

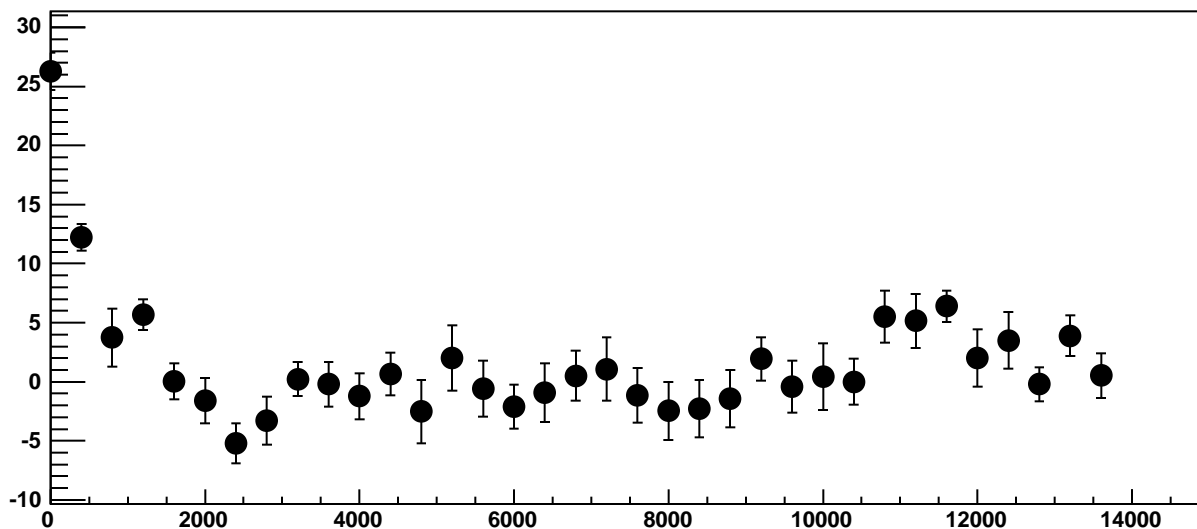


χ^2 / ndf 45.1 / 23
p0 114.2 ± 0.7923
p1 0.037 ± 0.0001311

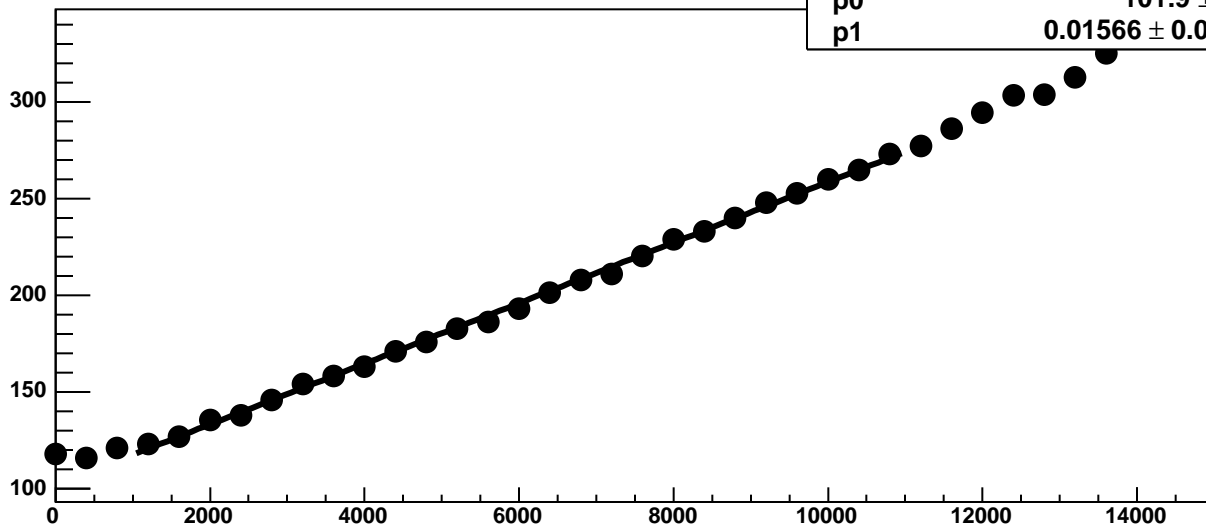
Chip 8, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

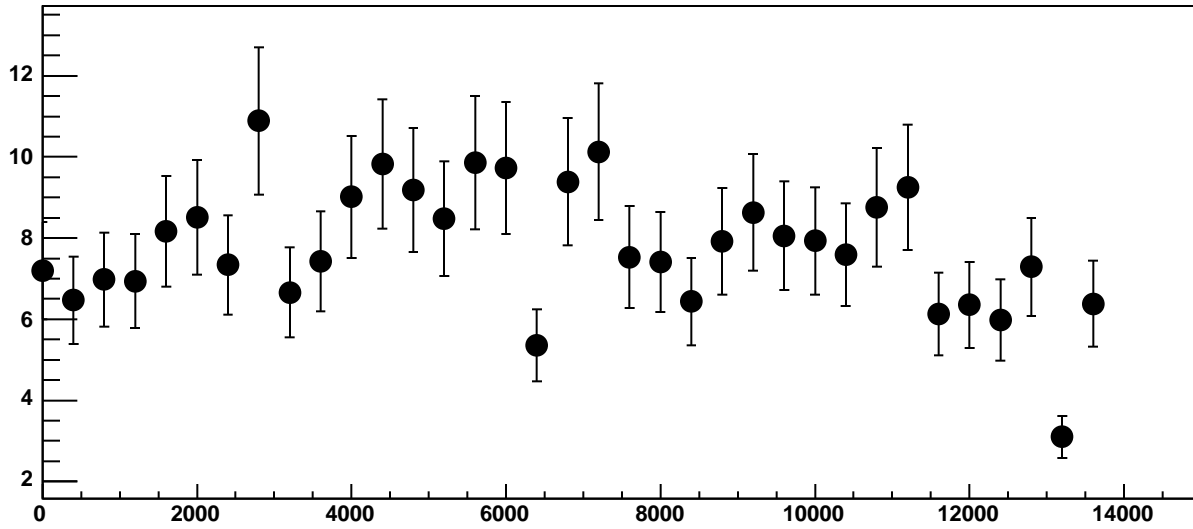


Chip 8, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

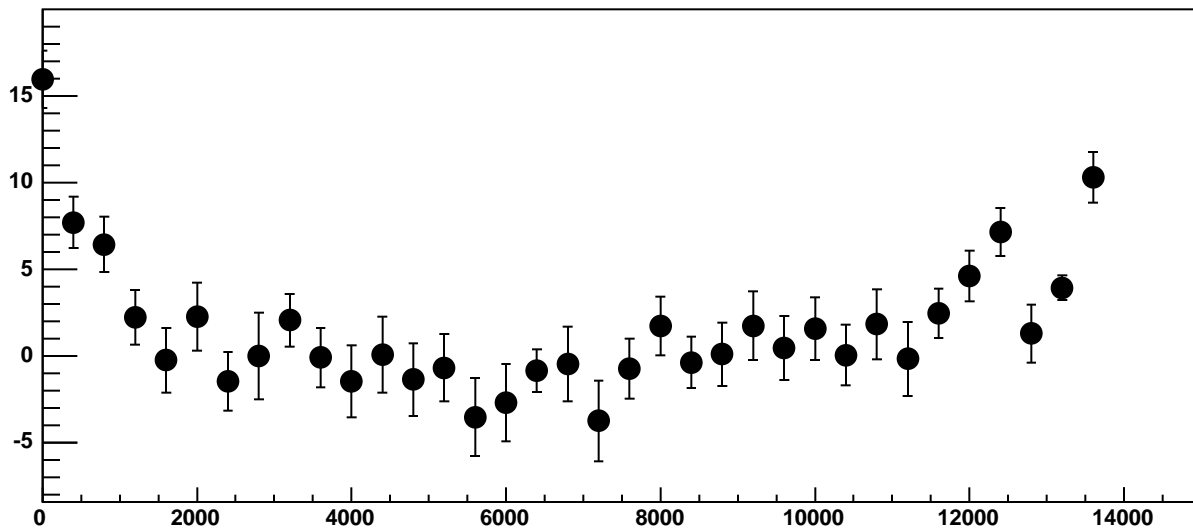


χ^2 / ndf 17.62 / 23
p0 101.9 ± 0.8401
p1 0.01566 ± 0.0001256

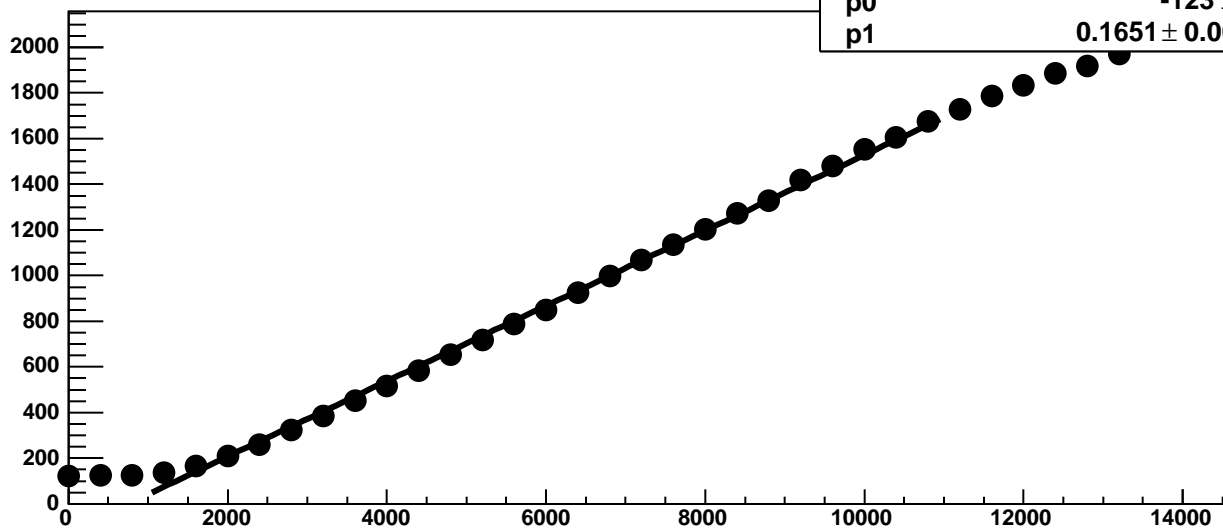
Chip 8, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

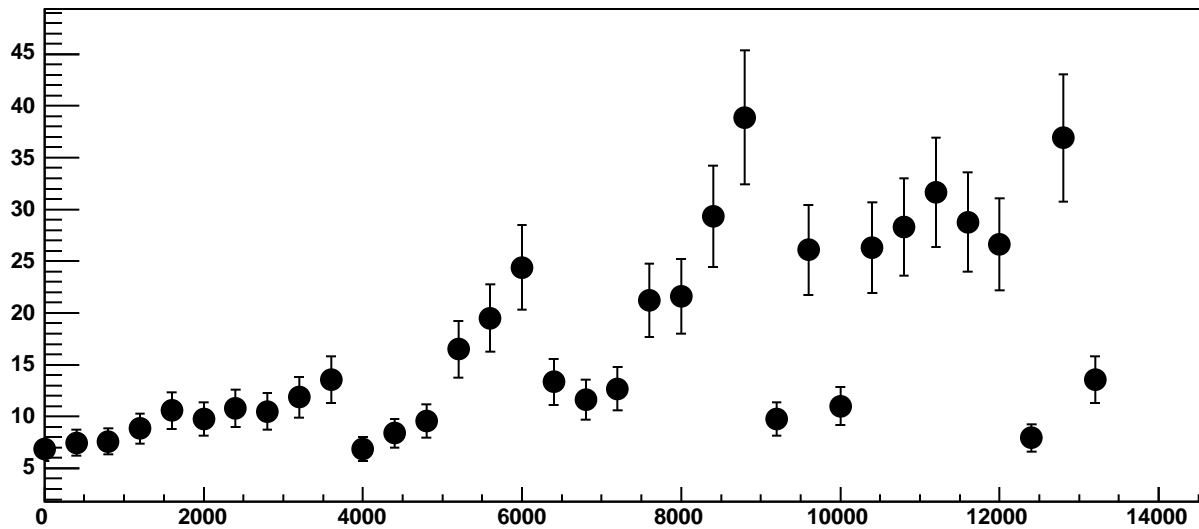


Chip 8, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

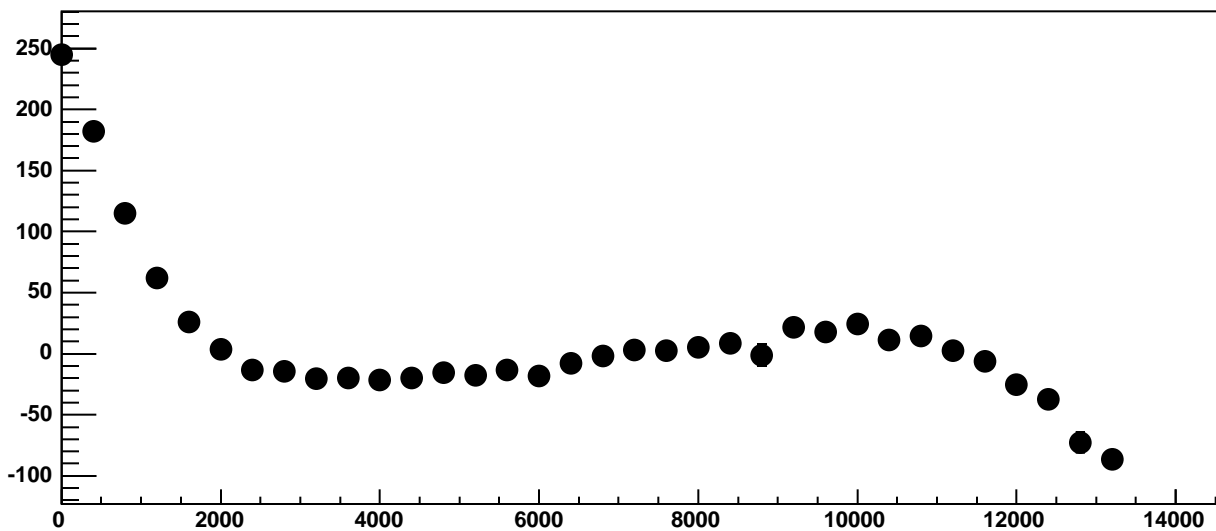


χ^2 / ndf 1815 / 23
p0 -123 ± 1.157
p1 0.1651 ± 0.0002123

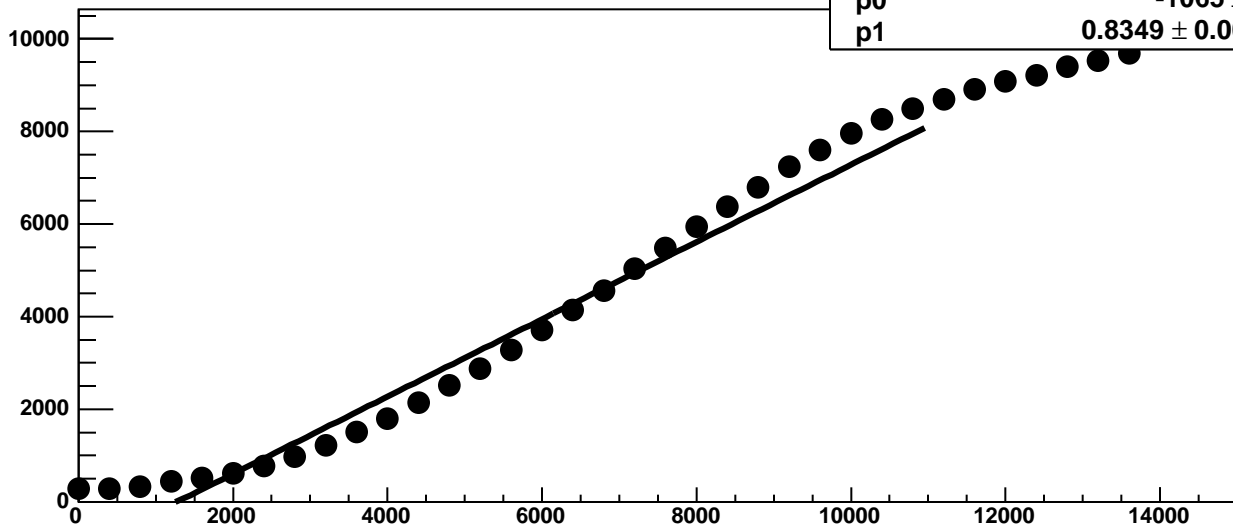
Chip 8, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



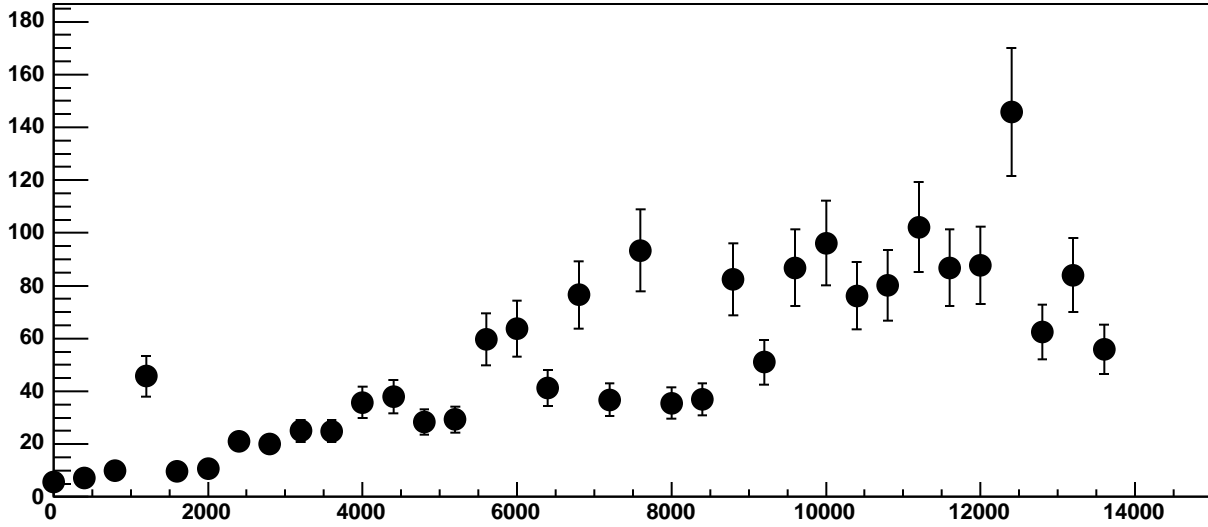
Chip 8, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



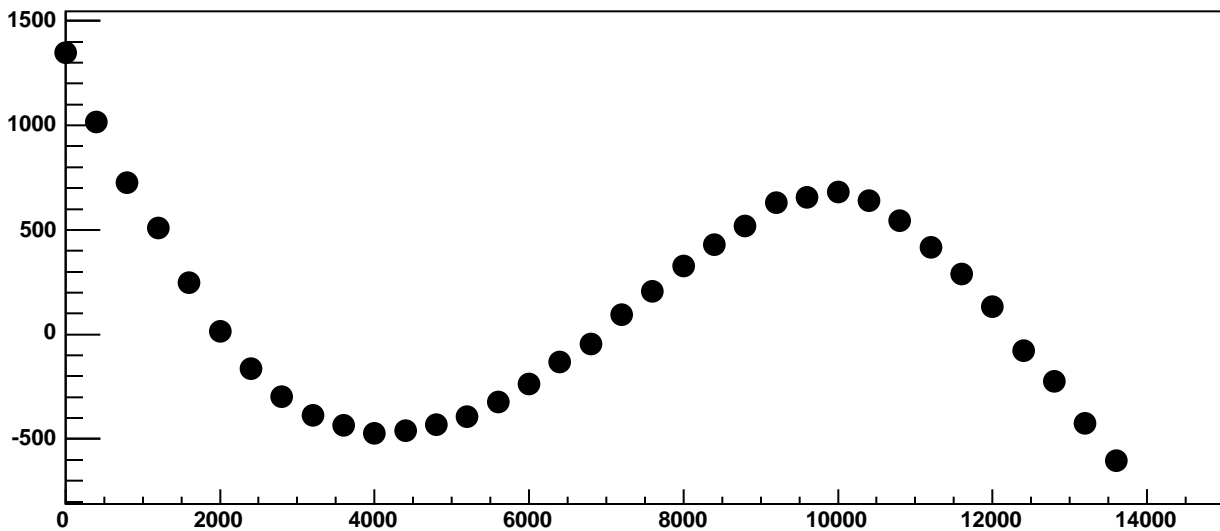
Chip 8, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC



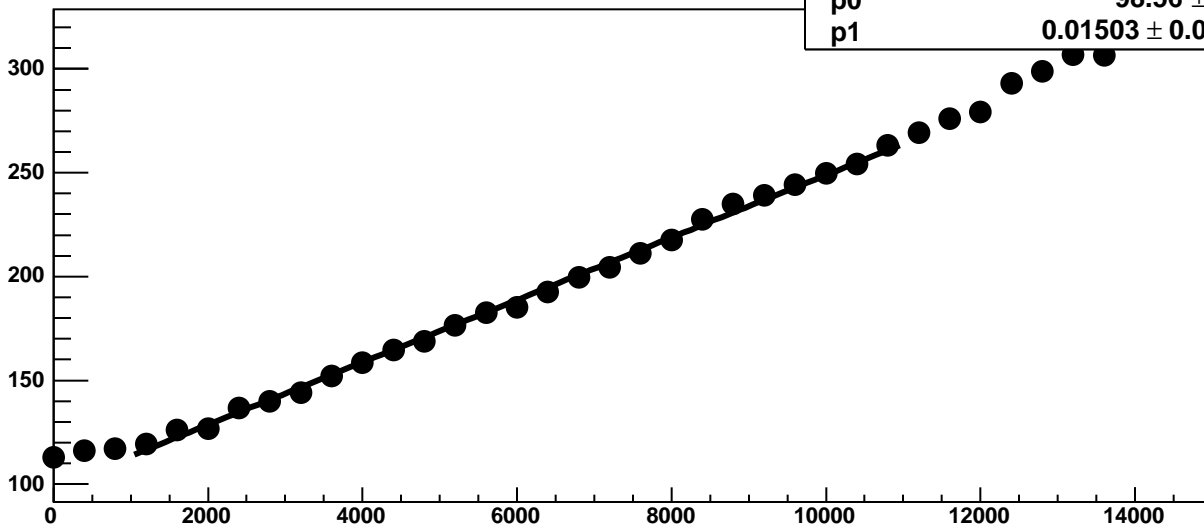
Chip 8, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC

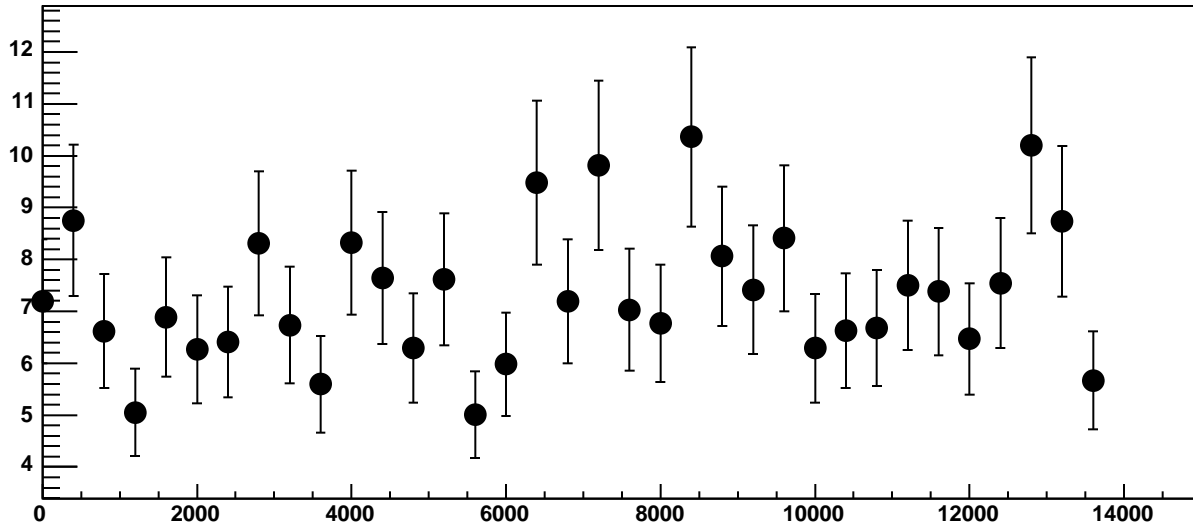


Chip 8, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

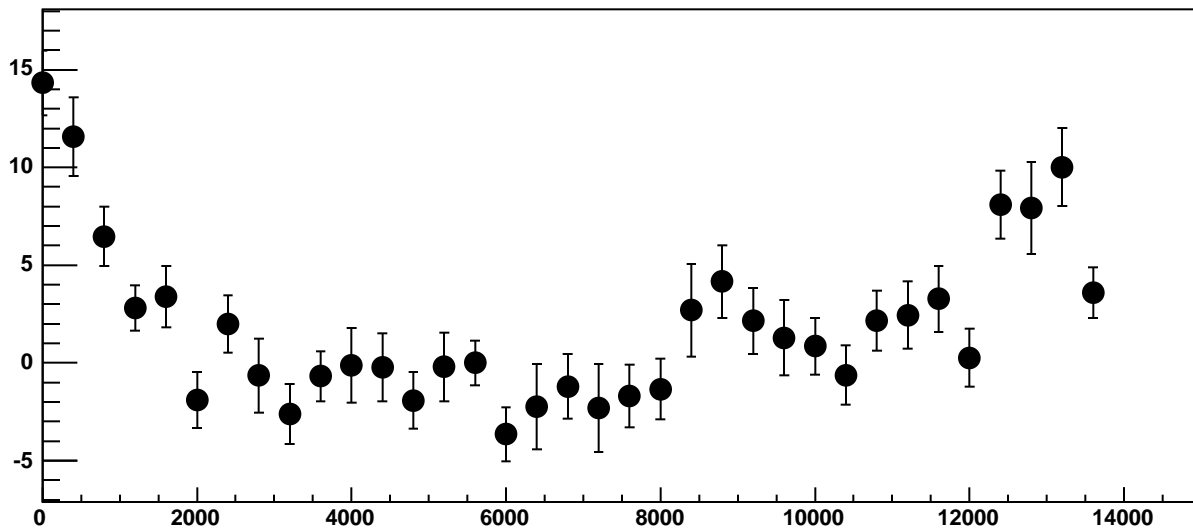


χ^2 / ndf 41.58 / 23
p0 98.56 ± 0.6765
p1 0.01503 ± 0.0001057

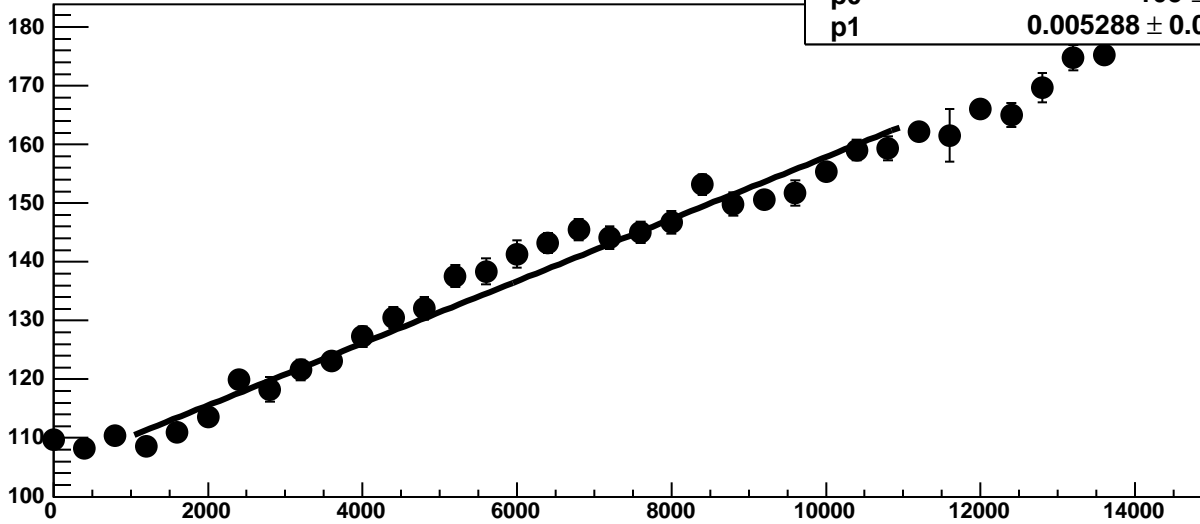
Chip 8, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

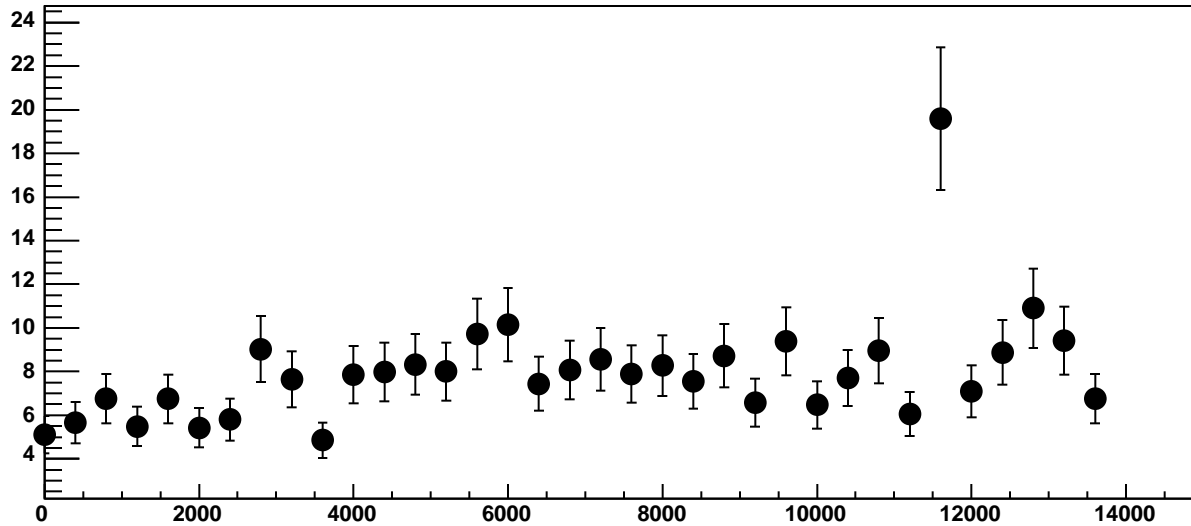


Chip 8, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

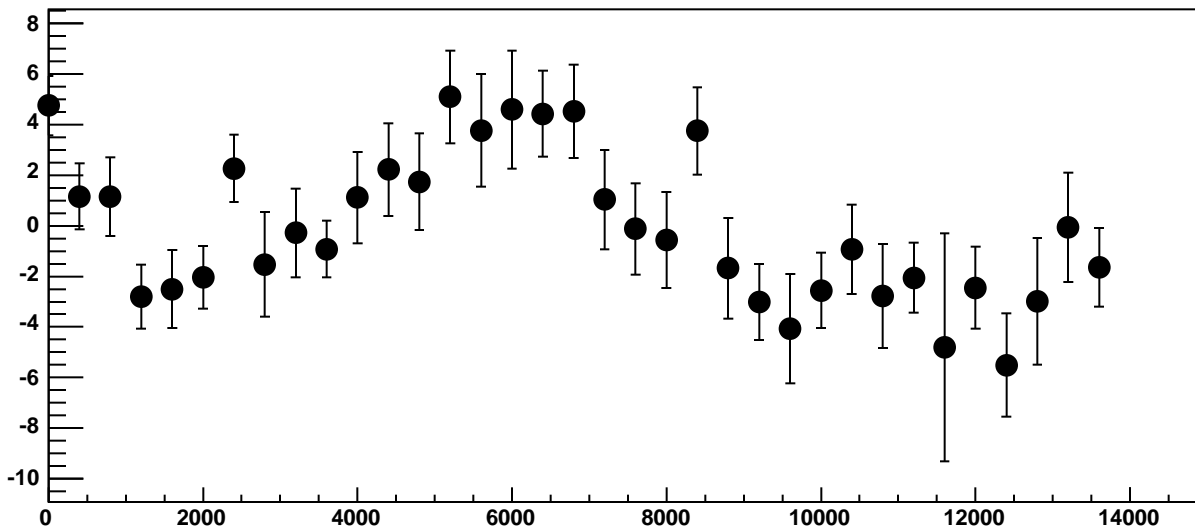


χ^2 / ndf 62.67 / 23
p0 105 ± 0.6868
p1 0.005288 ± 0.0001103

Chip 8, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

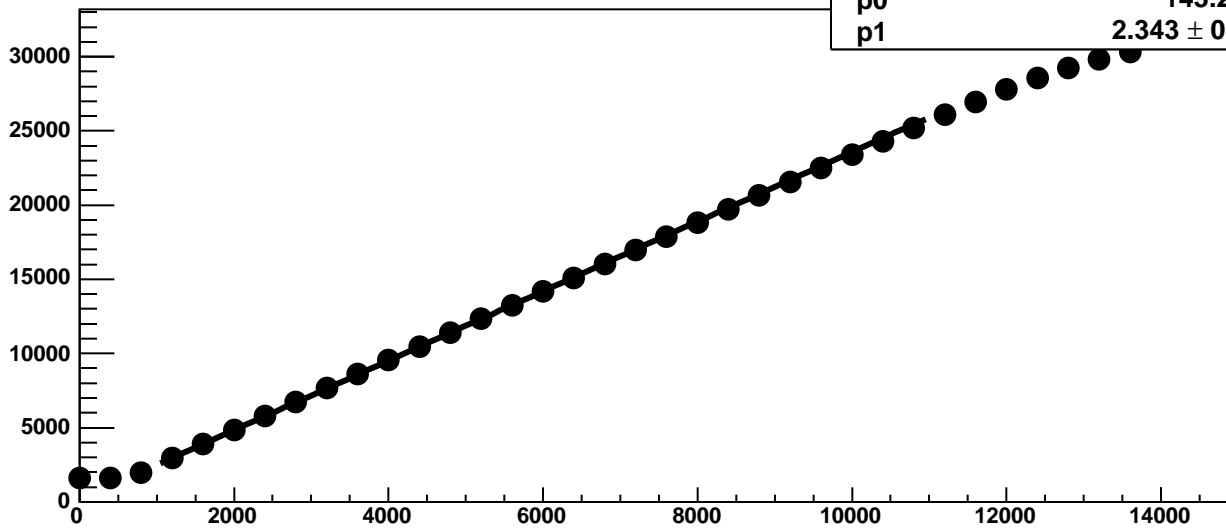


Chip 8, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

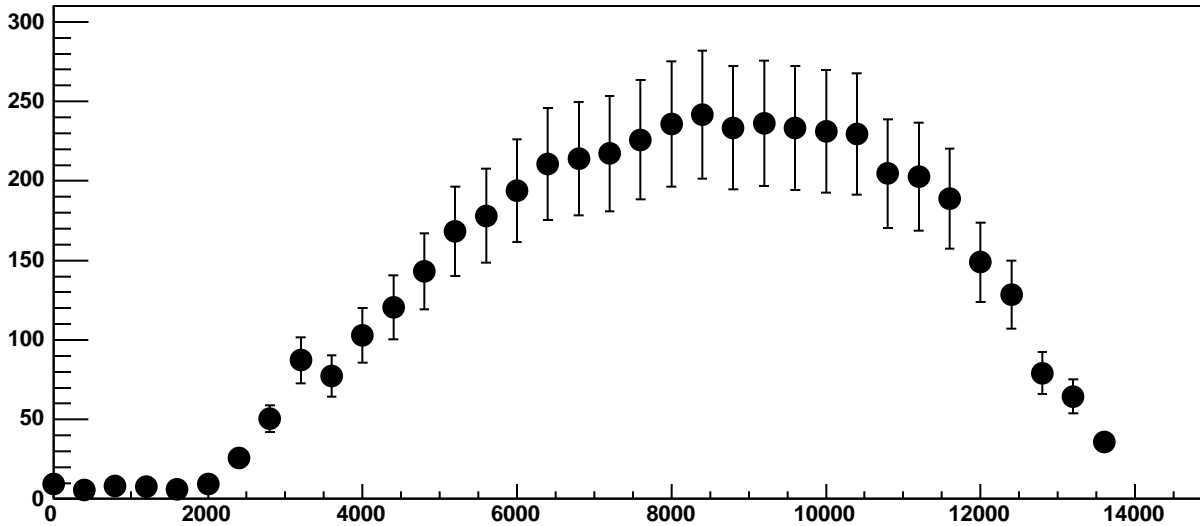


Chip 8, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC

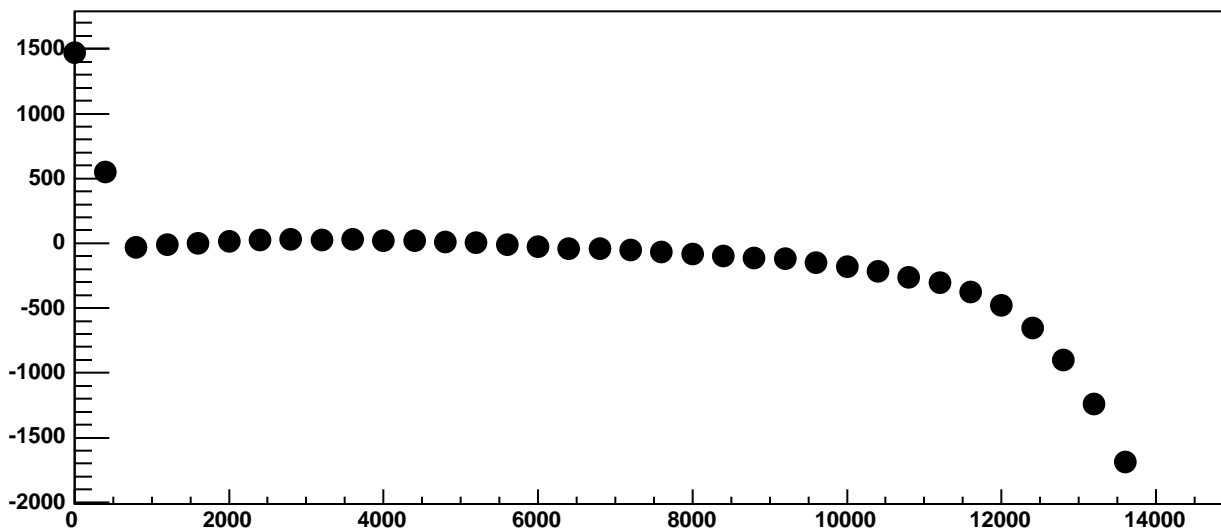
χ^2 / ndf 218.5 / 23
p0 143.2 ± 2.74
p1 2.343 ± 0.001553



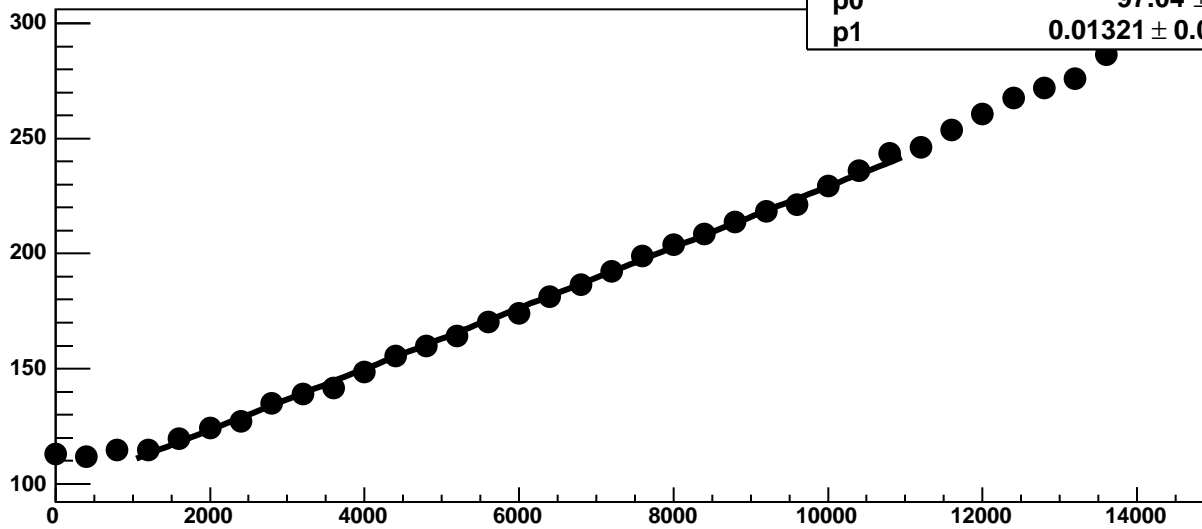
Chip 8, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC

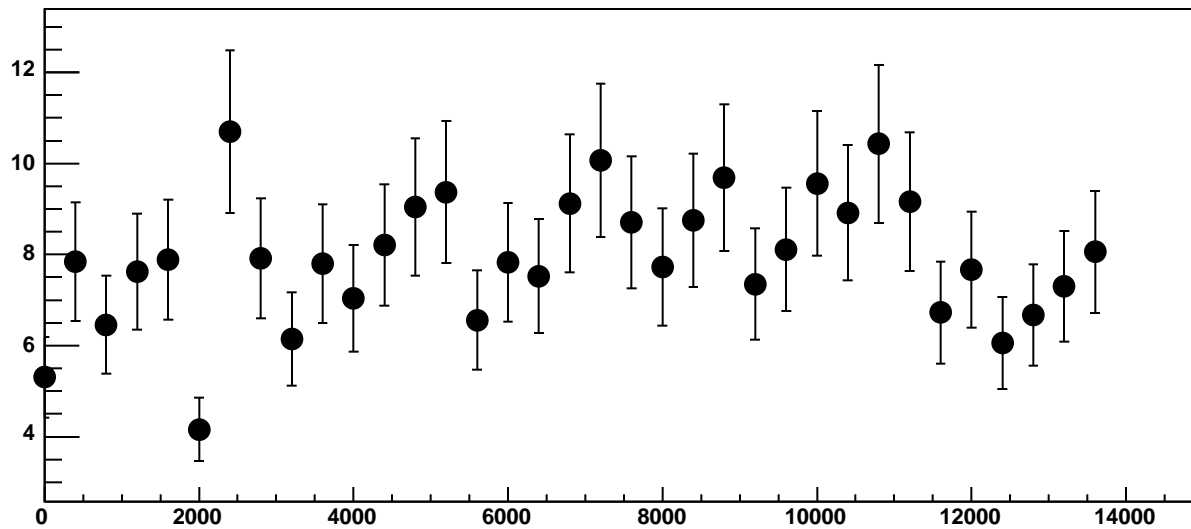


Chip 8, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

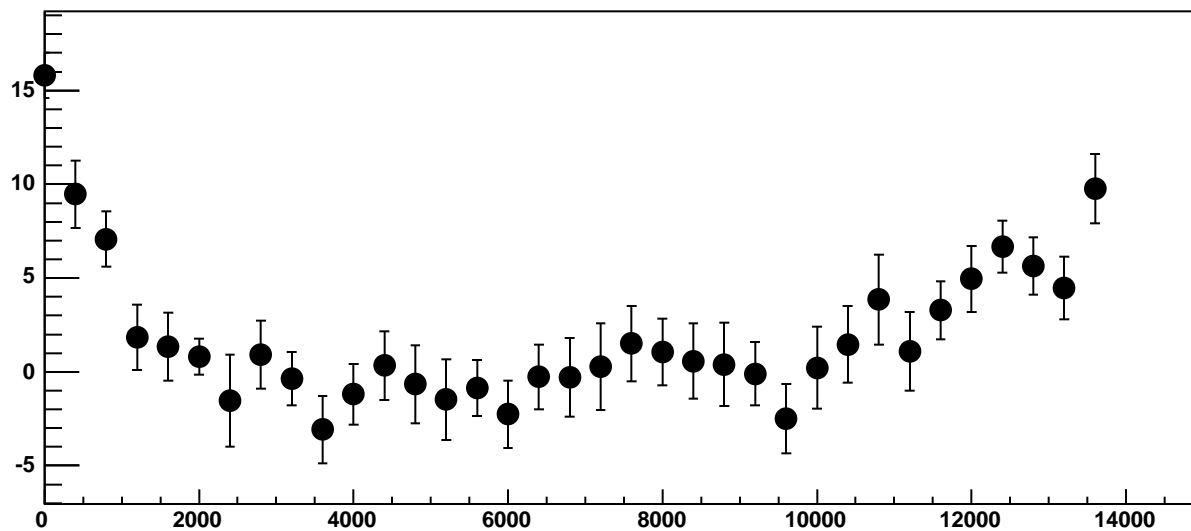


χ^2 / ndf 15.11 / 23
p0 97.04 ± 0.7464
p1 0.01321 ± 0.0001231

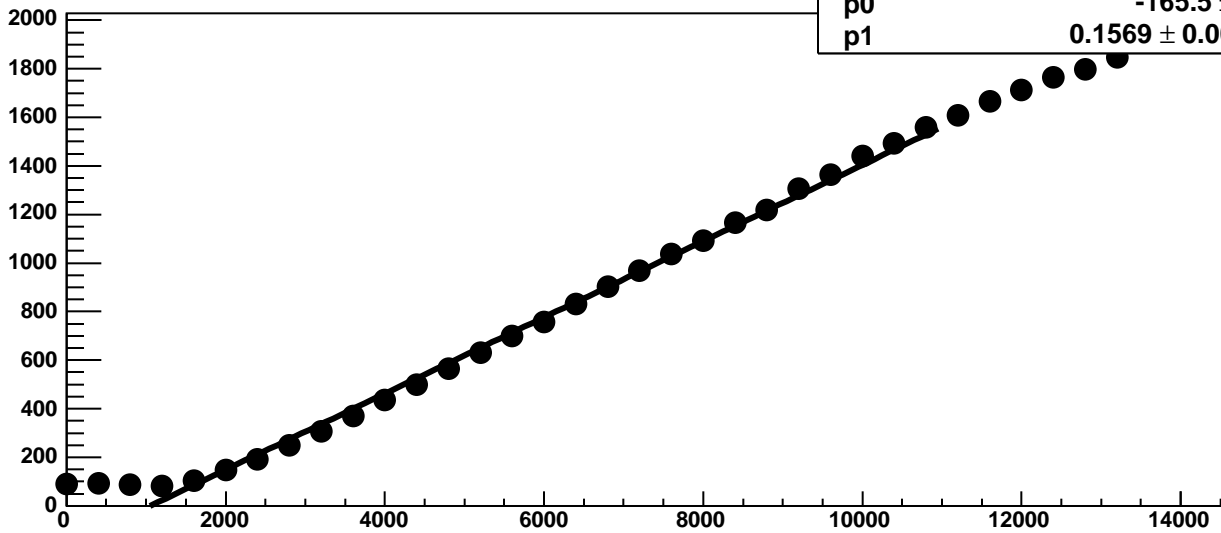
Chip 8, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



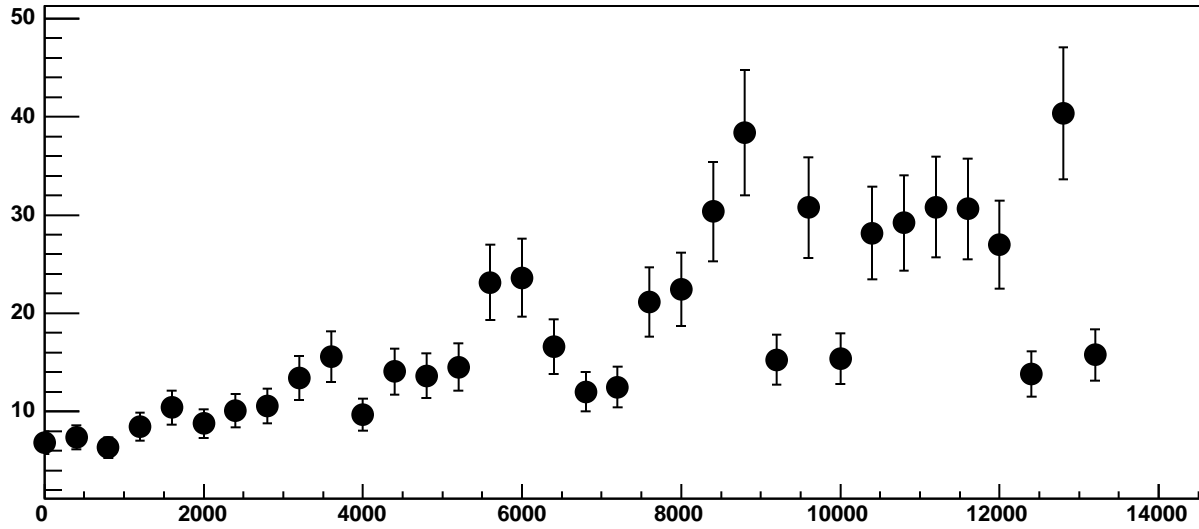
Chip 8, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



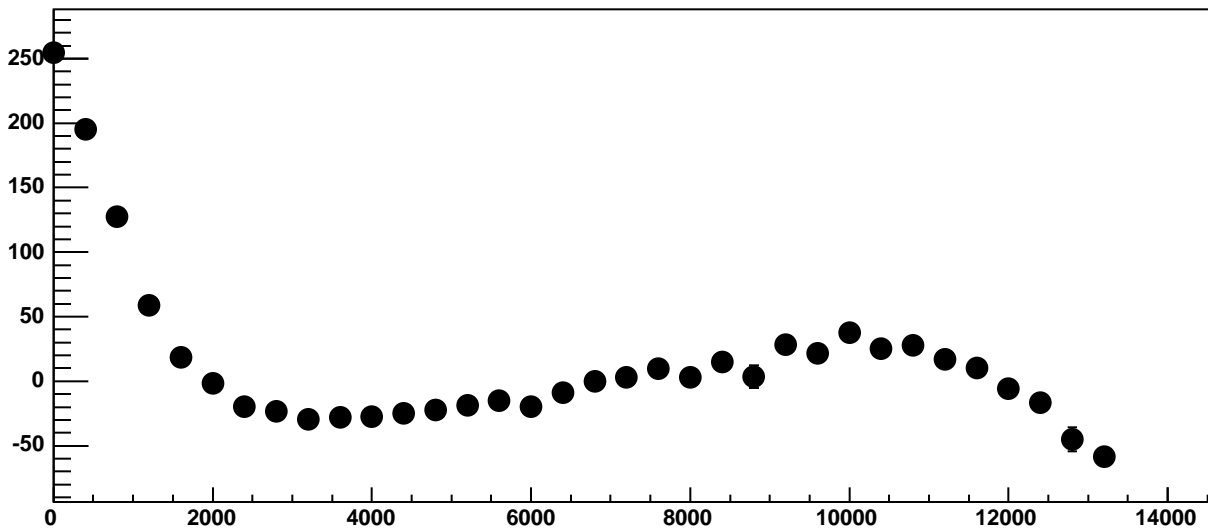
Chip 8, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



Chip 8, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

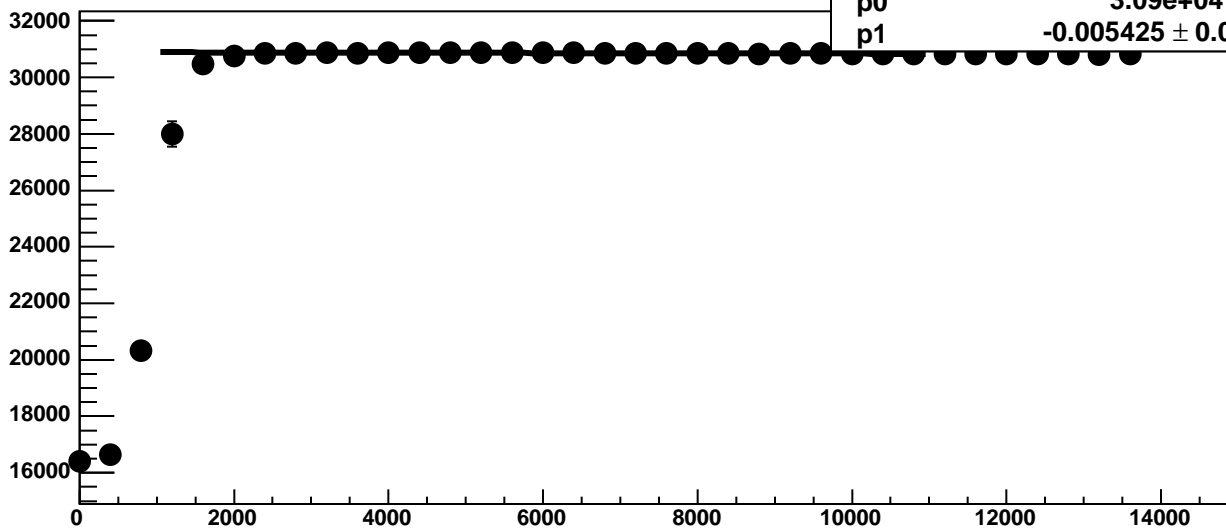


Chip 8, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

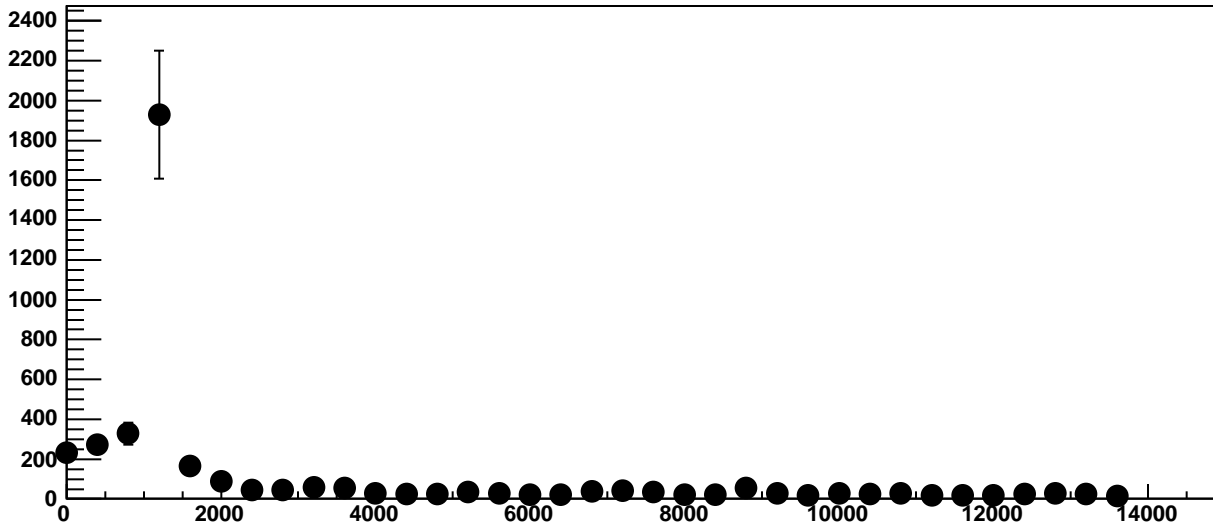


Chip 8, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC

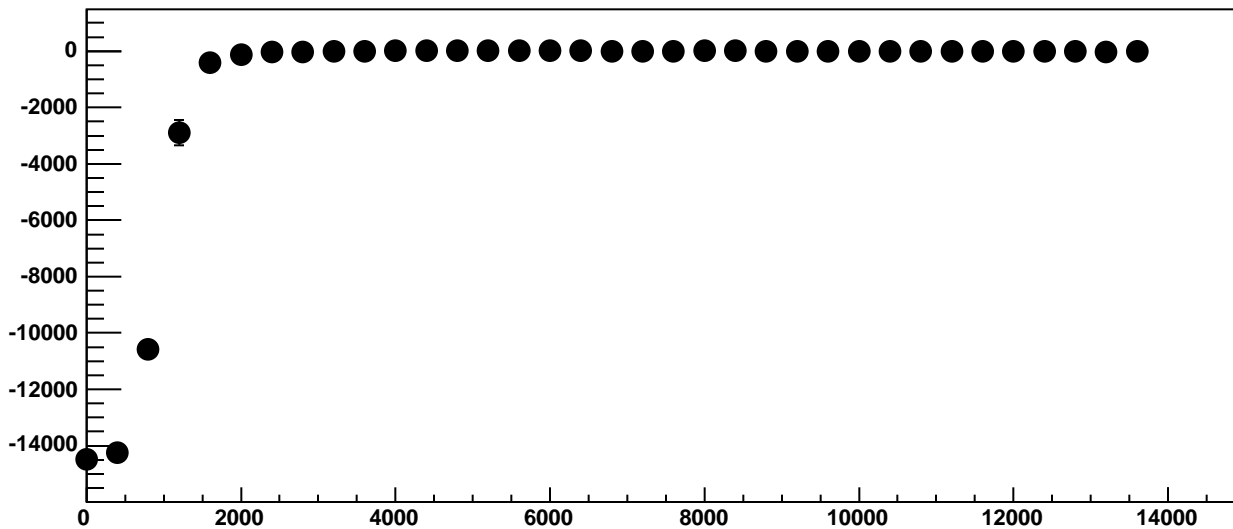
χ^2 / ndf 242.5 / 23
p0 $3.09\text{e}+04 \pm 4.415$
p1 -0.005425 ± 0.0005825



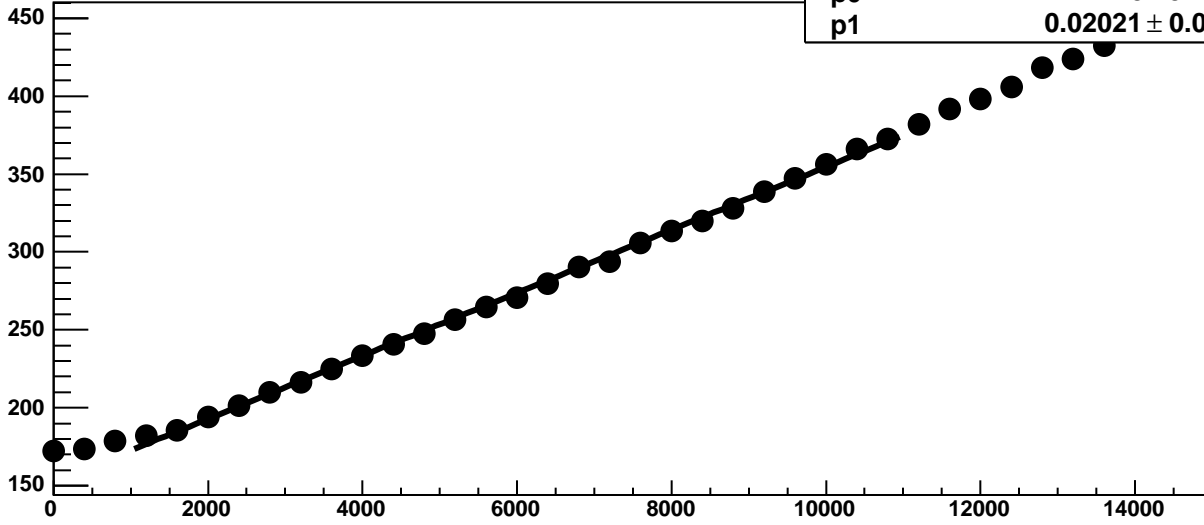
Chip 8, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC

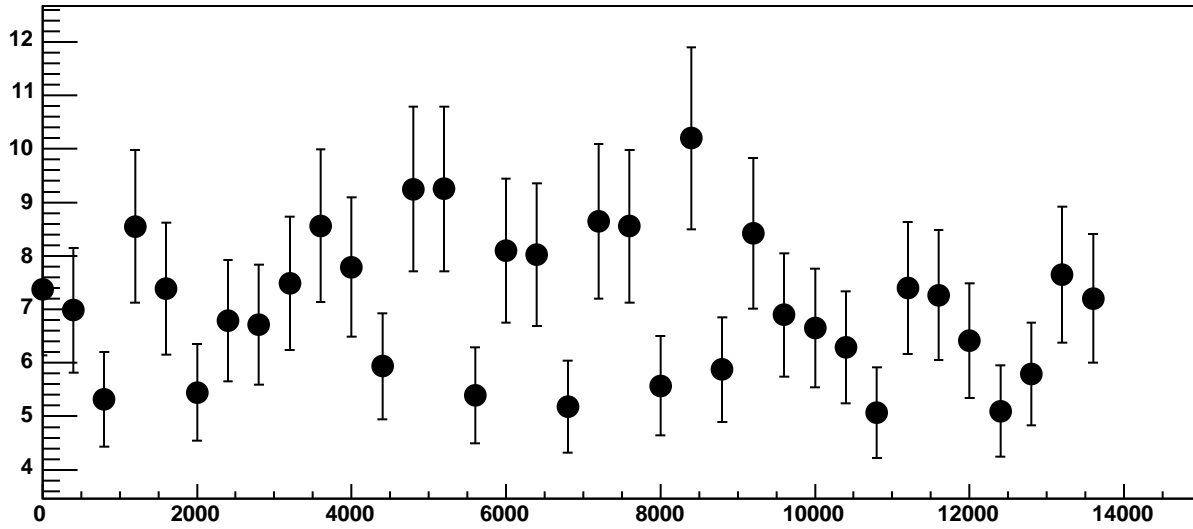


Chip 8, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

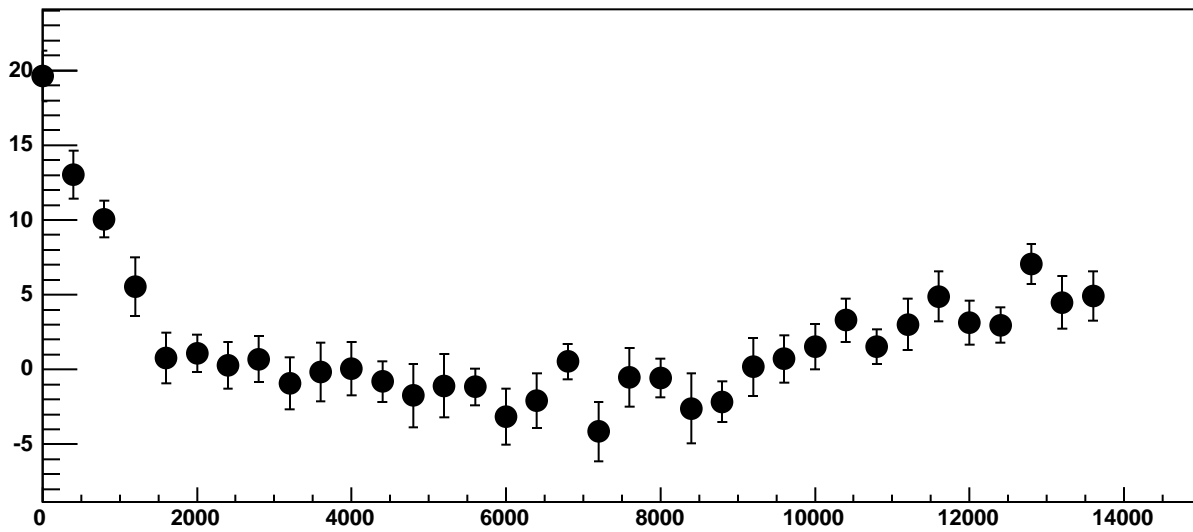


χ^2 / ndf 32.57 / 23
p0 152.5 ± 0.7287
p1 0.02021 ± 0.0001055

Chip 8, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

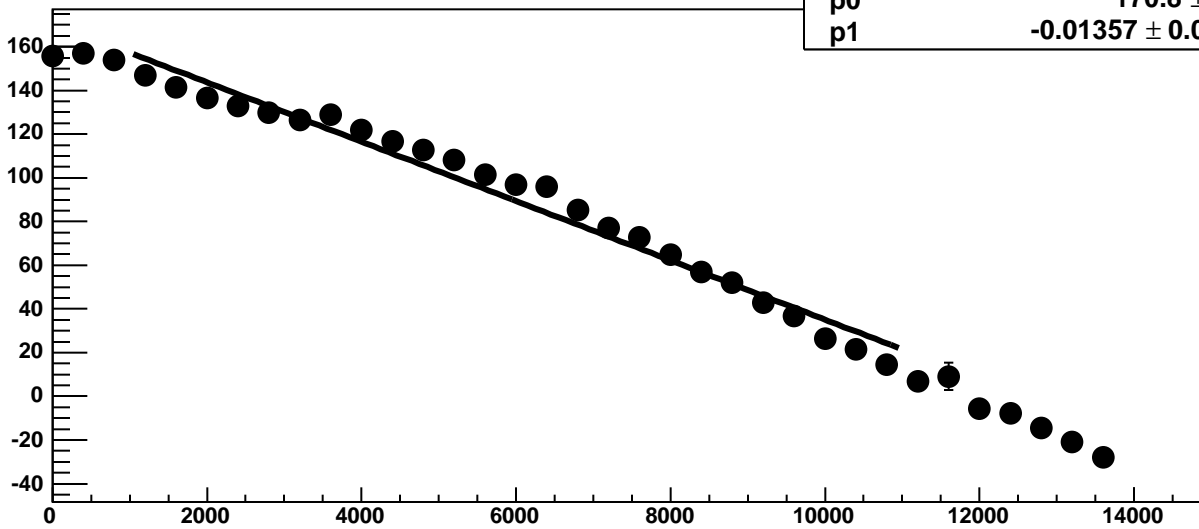


Chip 8, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

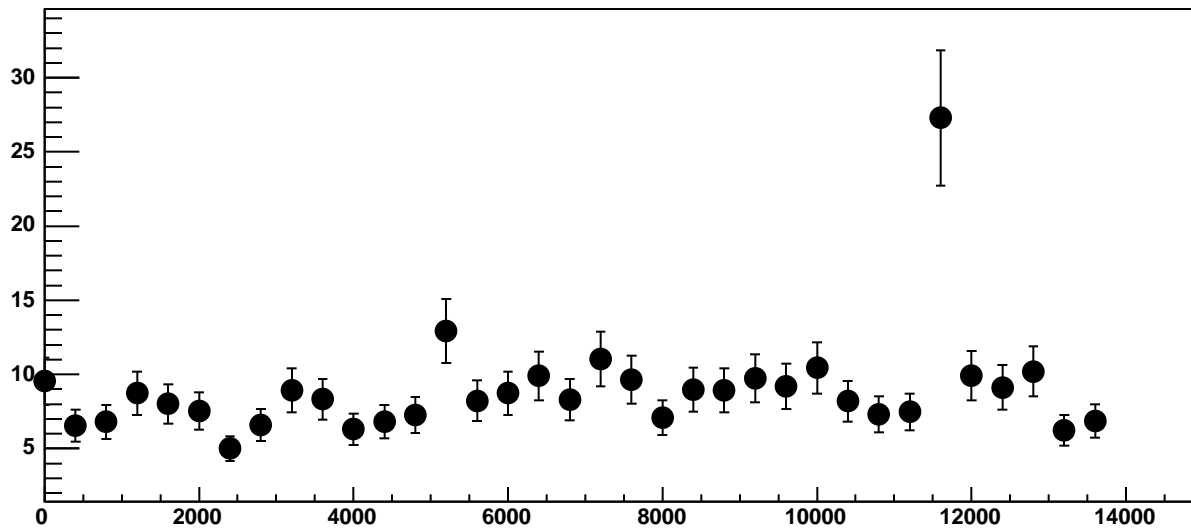


Chip 8, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

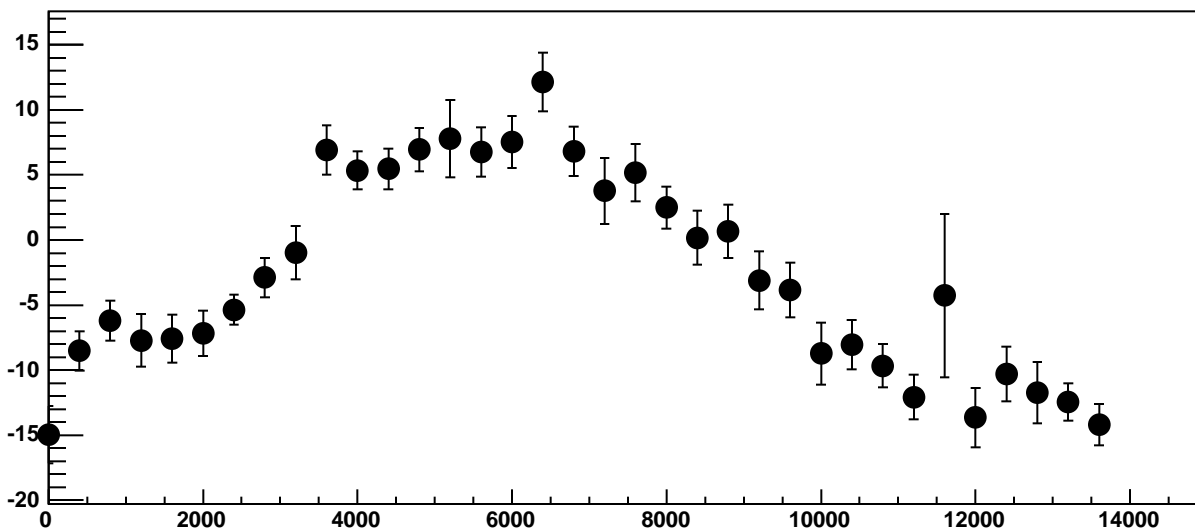
χ^2 / ndf 286.9 / 23
p0 170.8 ± 0.7825
p1 -0.01357 ± 0.0001254



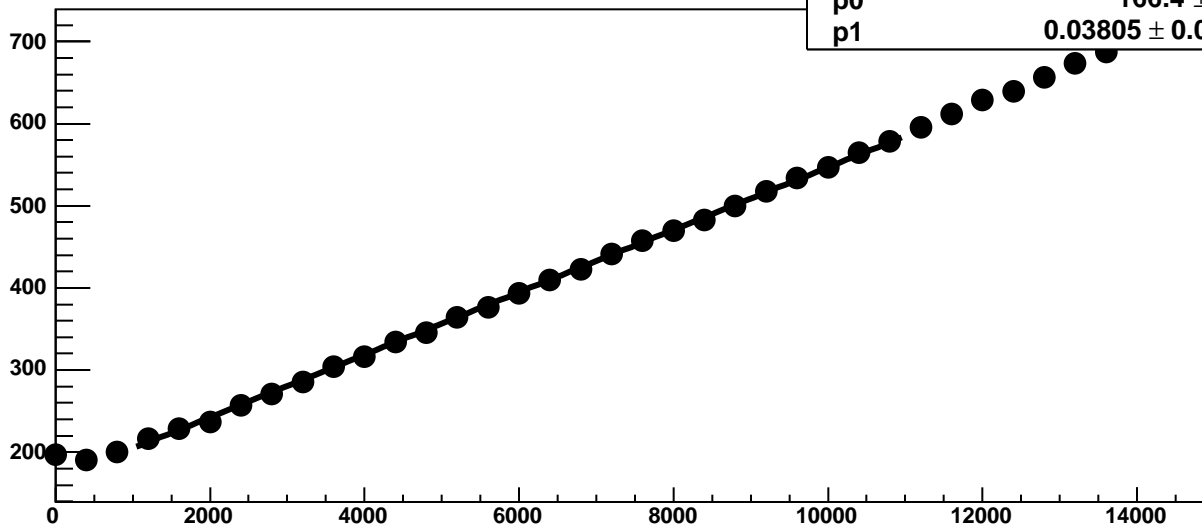
Chip 8, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

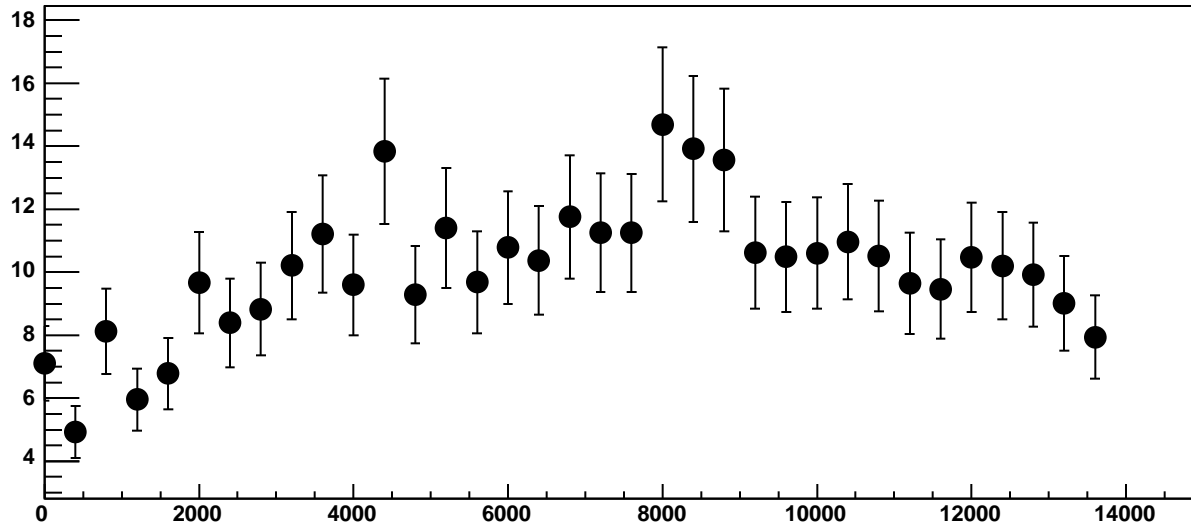


Chip 8, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

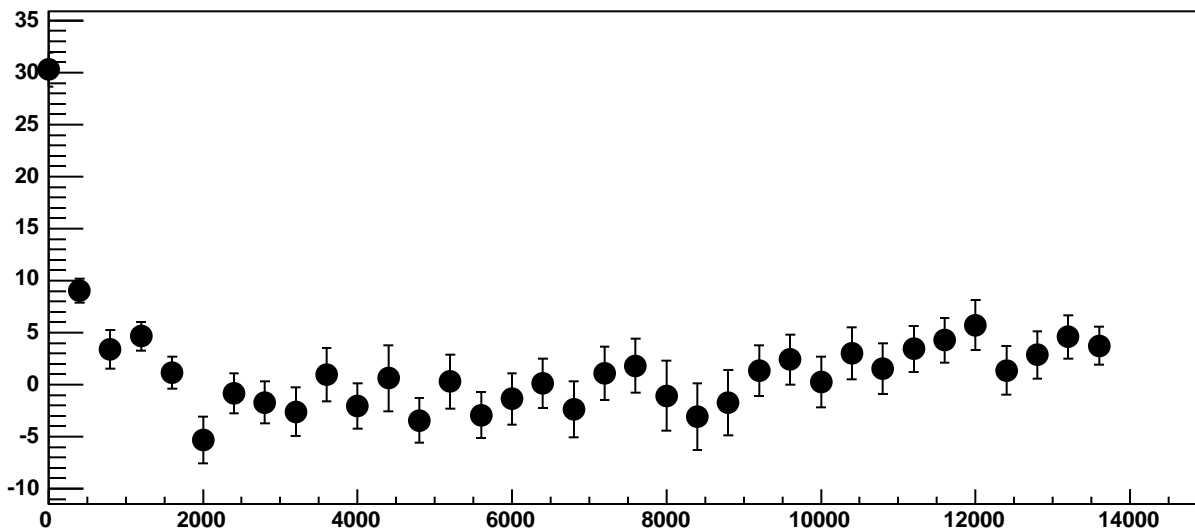


χ^2 / ndf 31.68 / 23
p0 166.4 ± 0.8839
p1 0.03805 ± 0.0001477

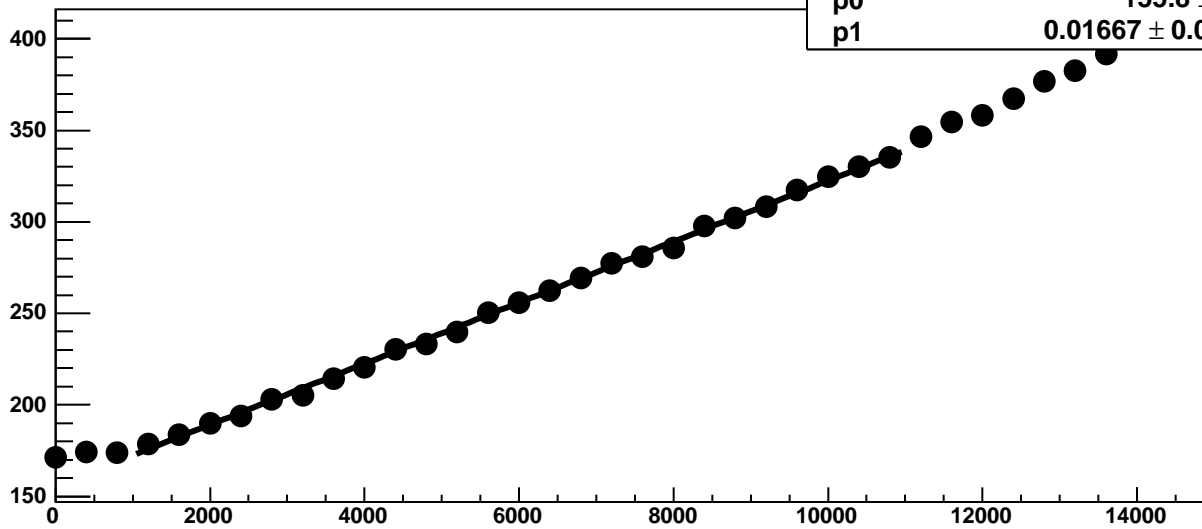
Chip 8, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

25 / 23

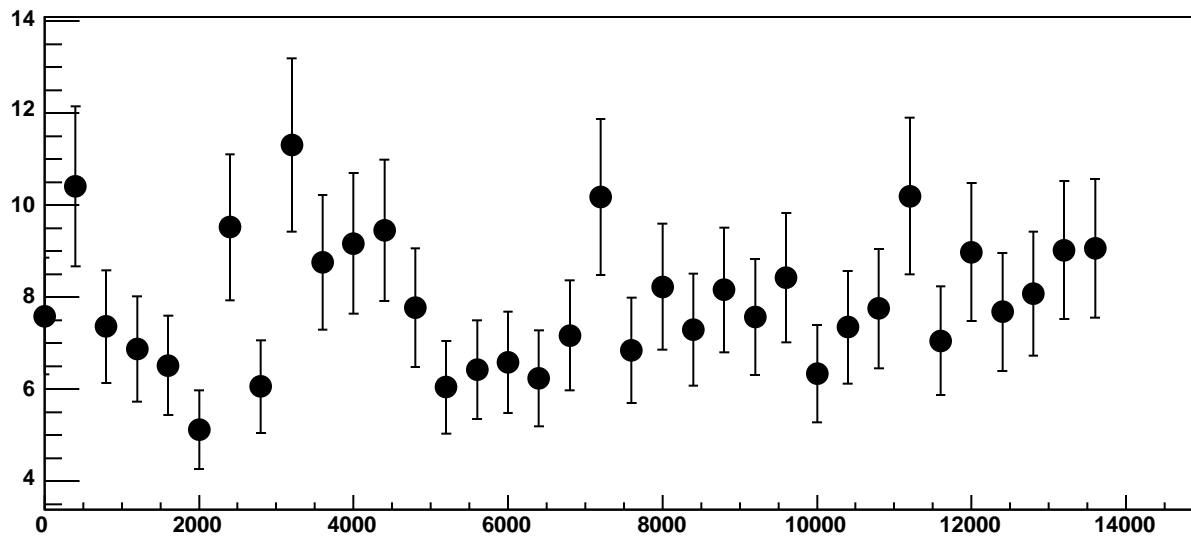
p0

155.8 ± 0.7411

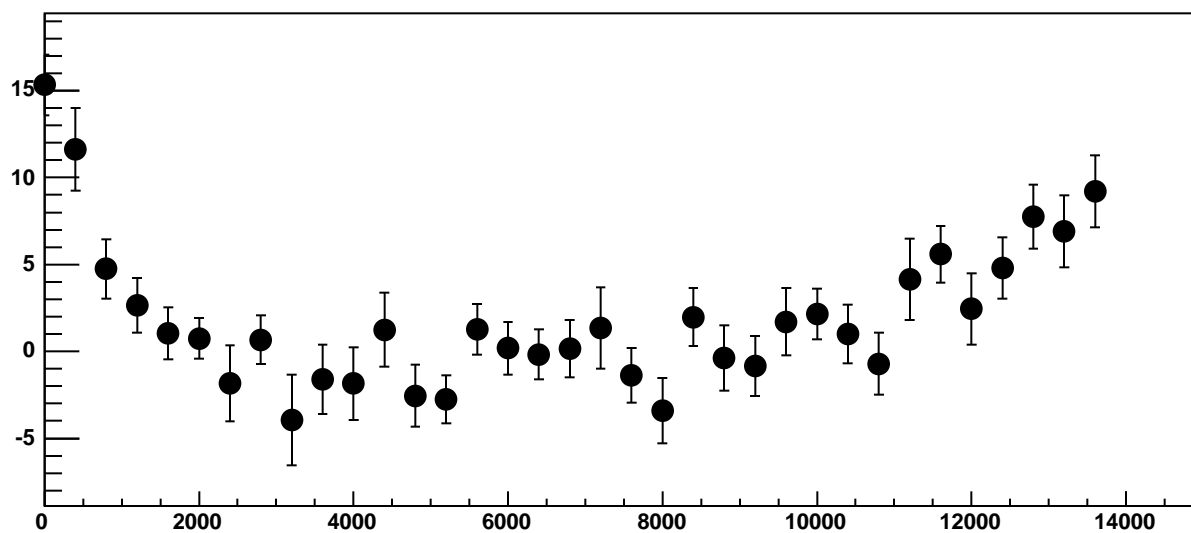
p1

0.01667 ± 0.0001134

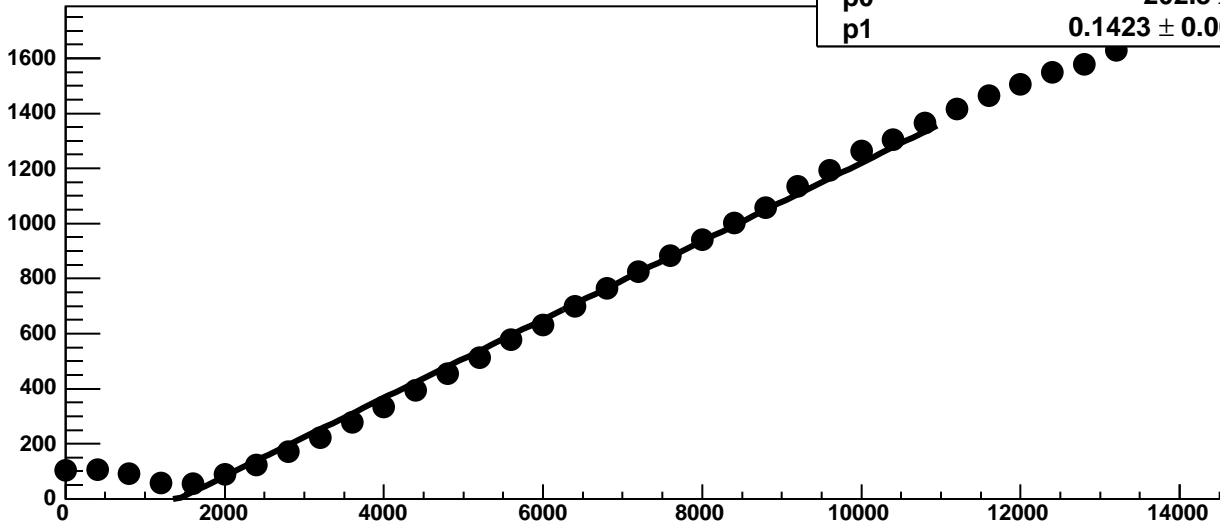
Chip 8, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



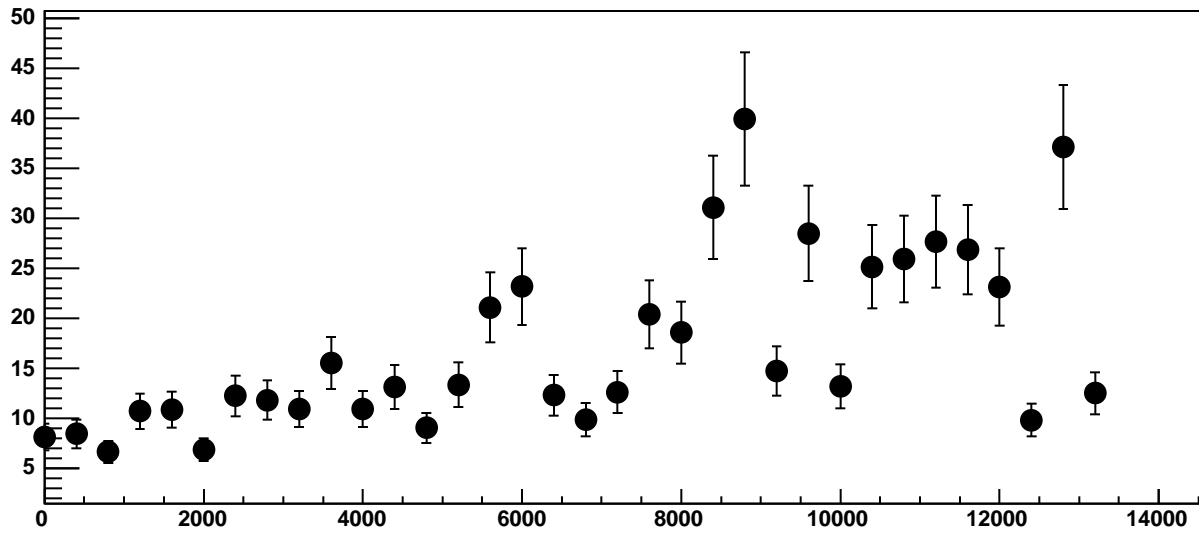
Chip 8, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



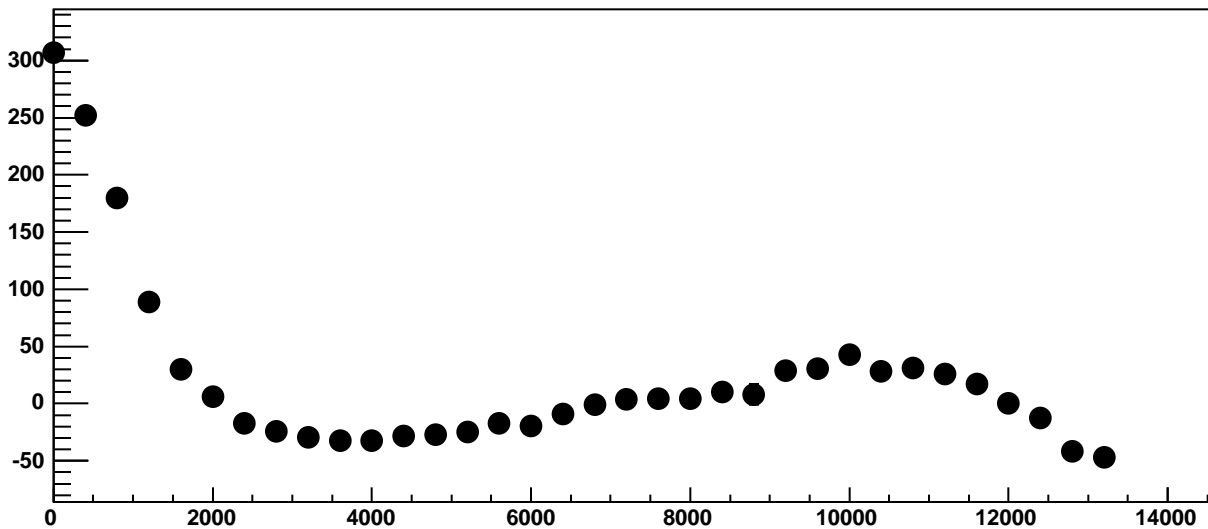
Chip 8, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



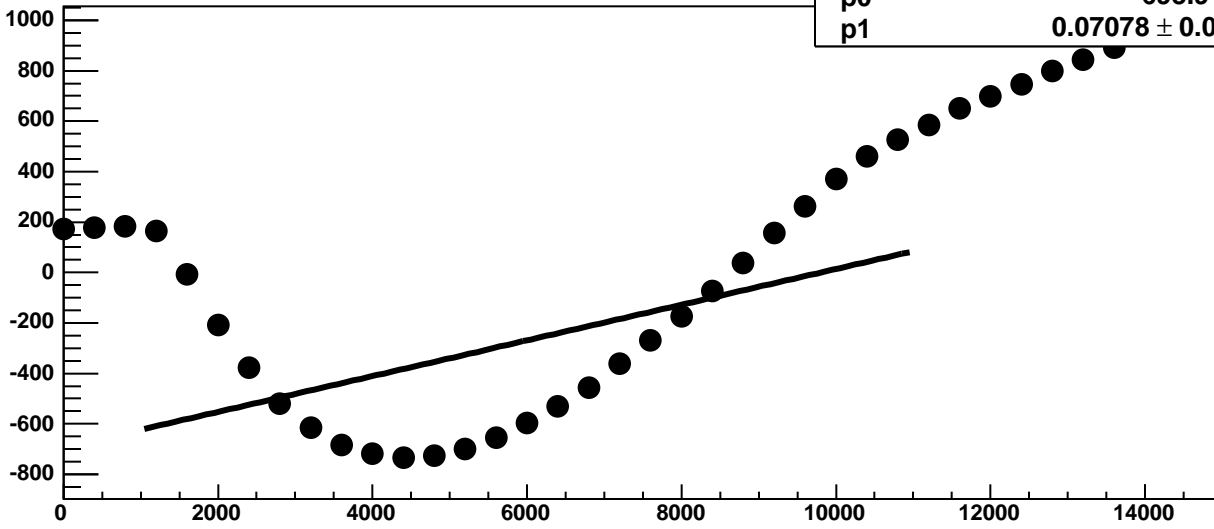
Chip 8, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

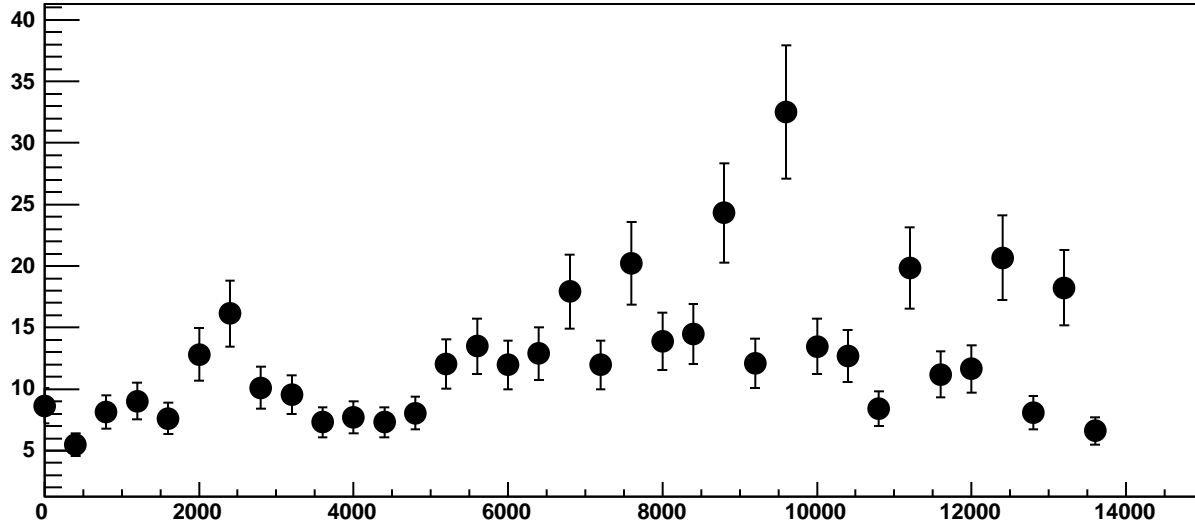


Chip 8, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

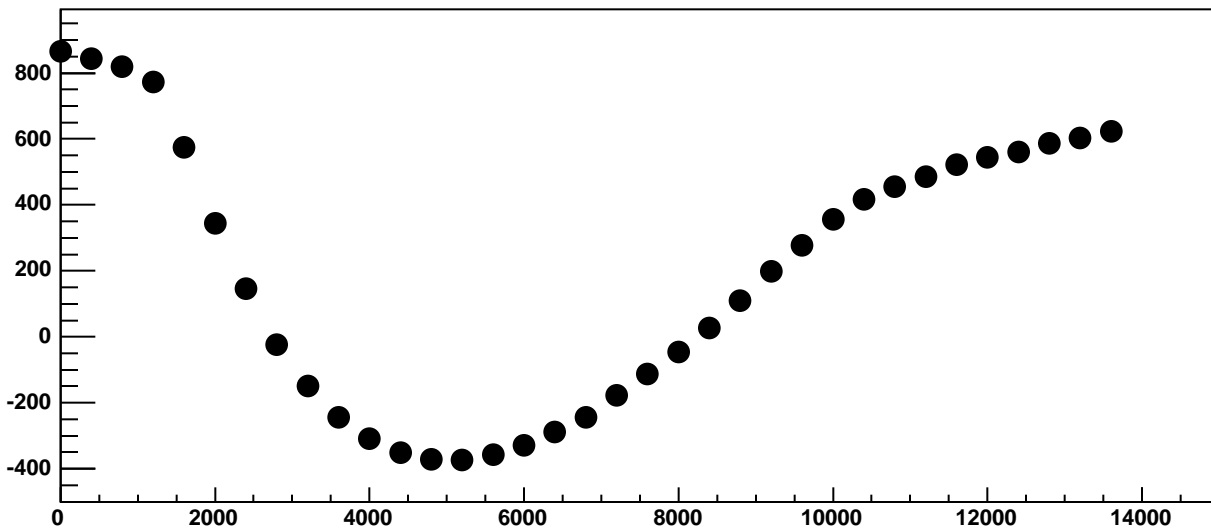


χ^2 / ndf 5.64e+05 / 23
p0 -693.9 ± 1.023
p1 0.07078 ± 0.0001739

Chip 8, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

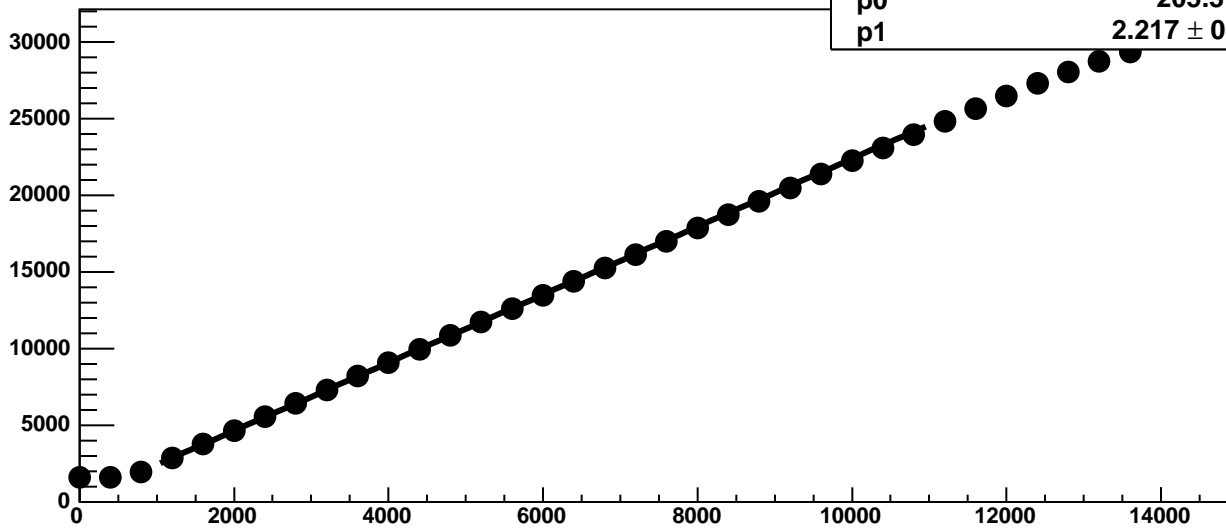


Chip 8, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

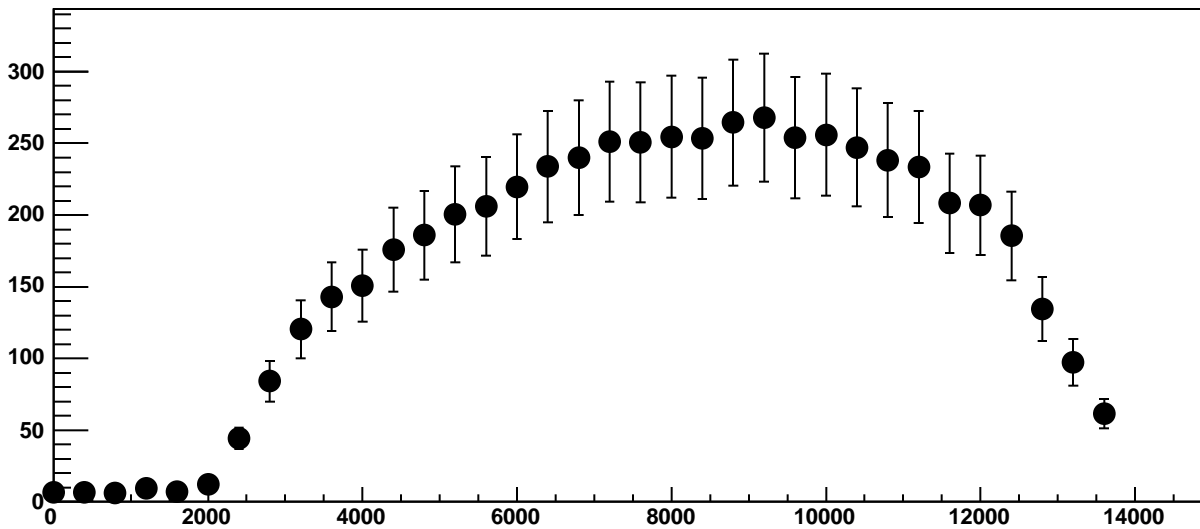


Chip 8, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC

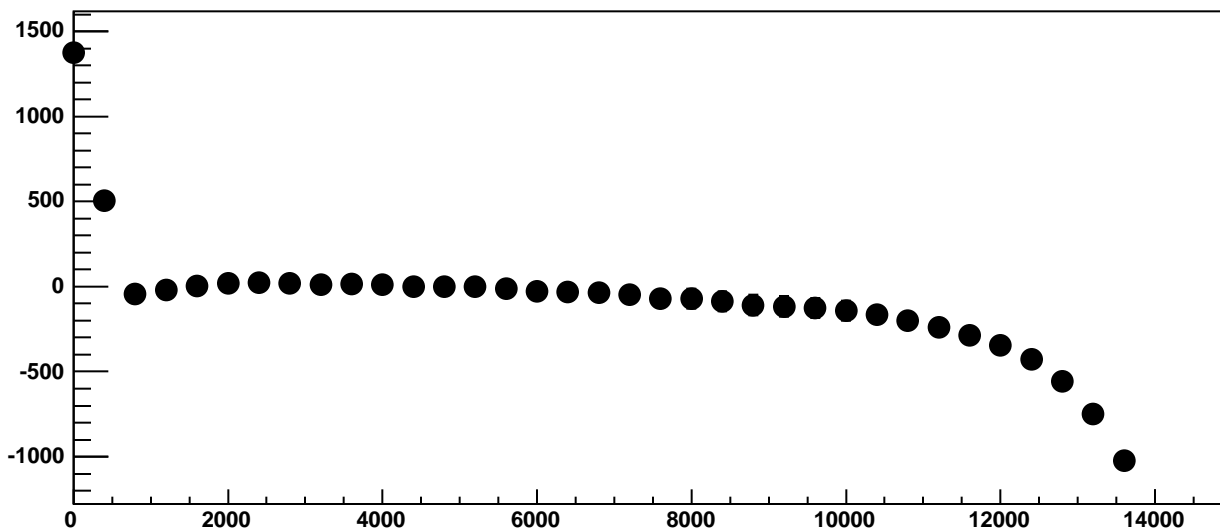
χ^2 / ndf 180.9 / 23
p0 205.3 ± 3.223
p1 2.217 ± 0.001853



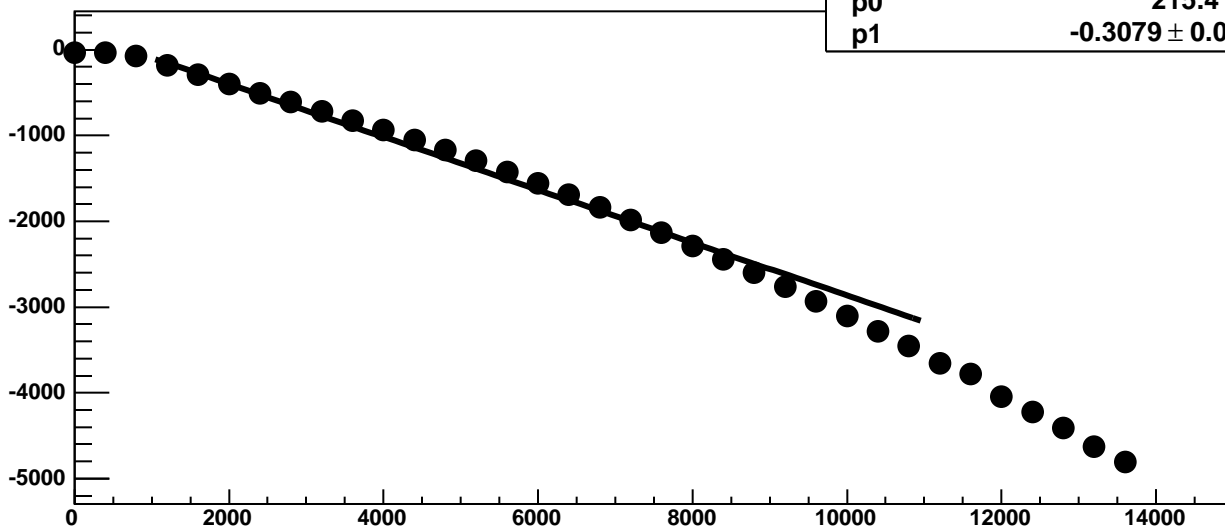
Chip 8, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

3734 / 23

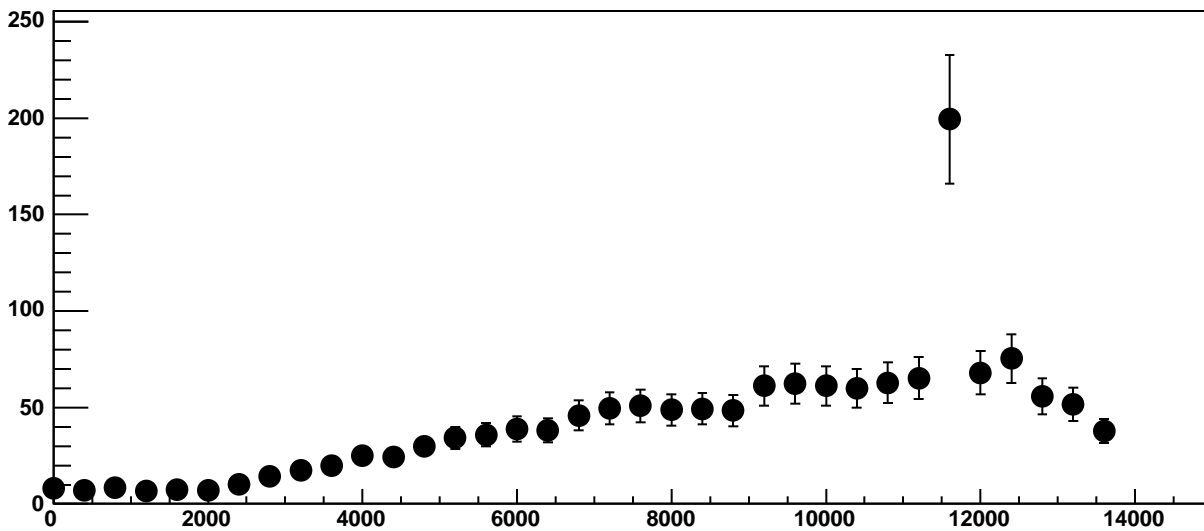
p0

215.4 ± 1.322

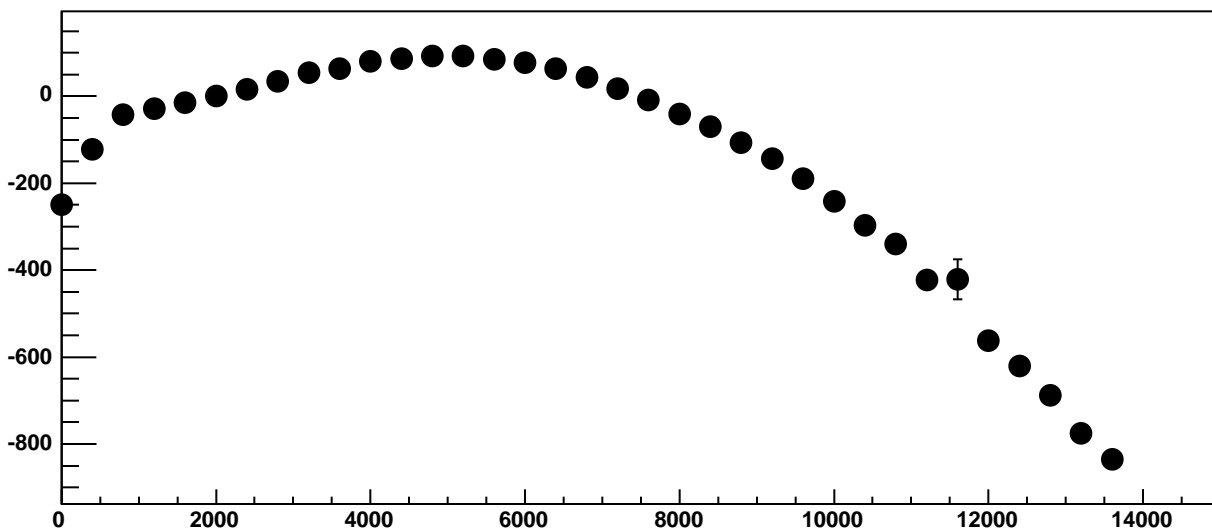
p1

-0.3079 ± 0.0004527

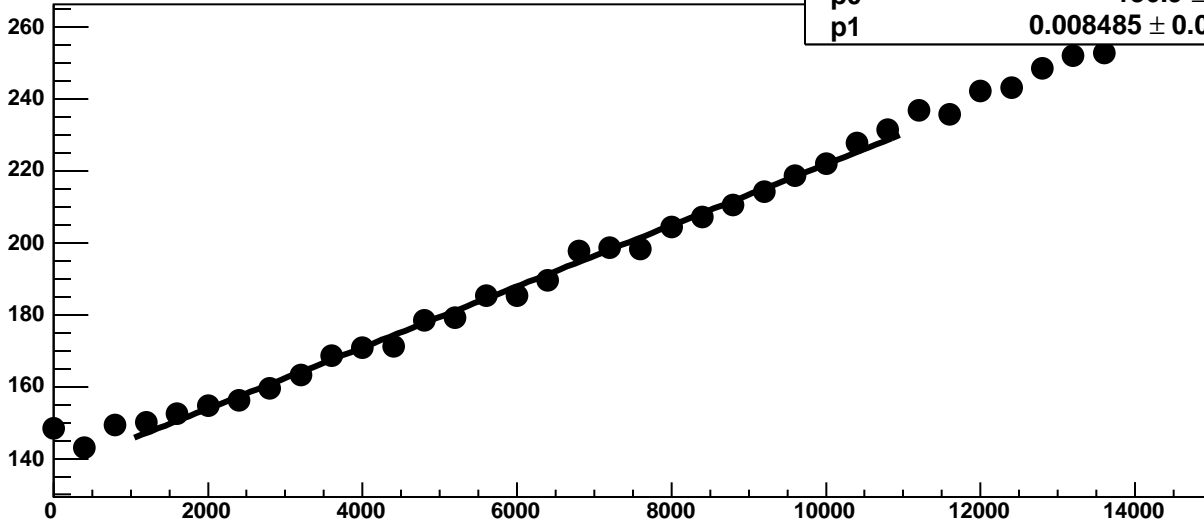
Chip 8, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

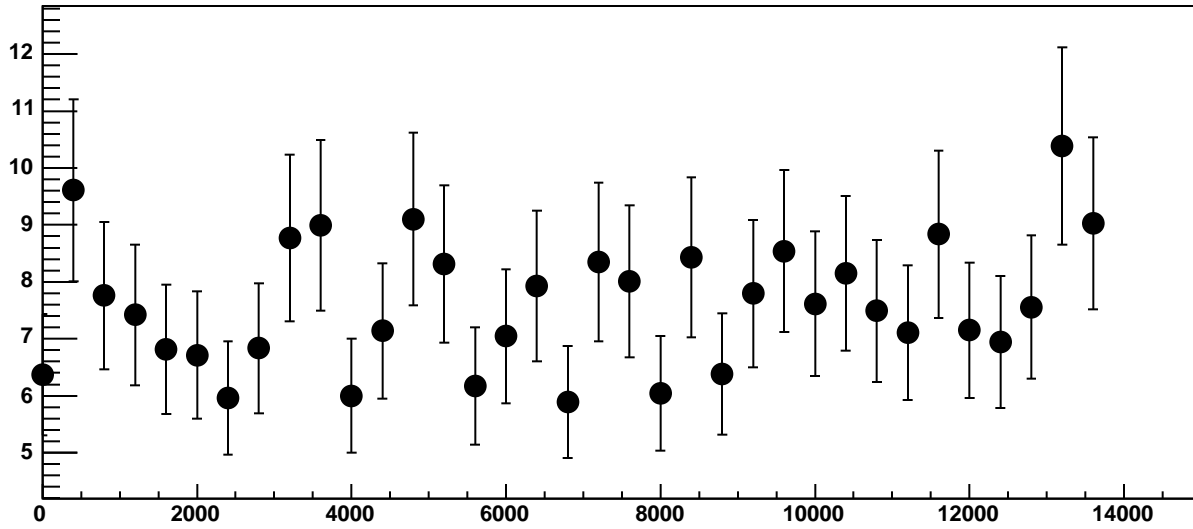


Chip 8, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

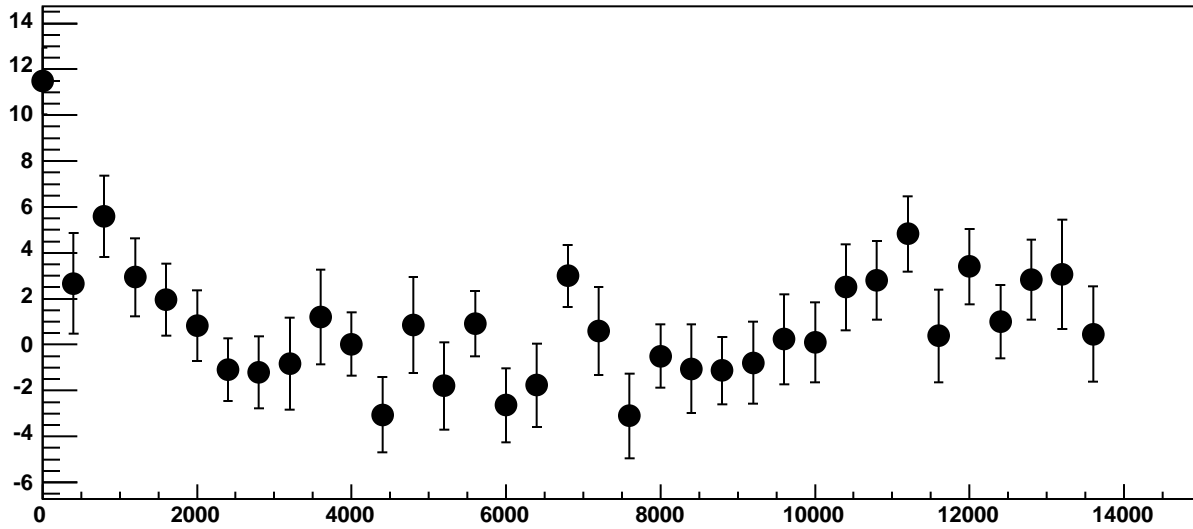


χ^2 / ndf 28.71 / 23
p0 136.9 ± 0.7536
p1 0.008485 ± 0.0001159

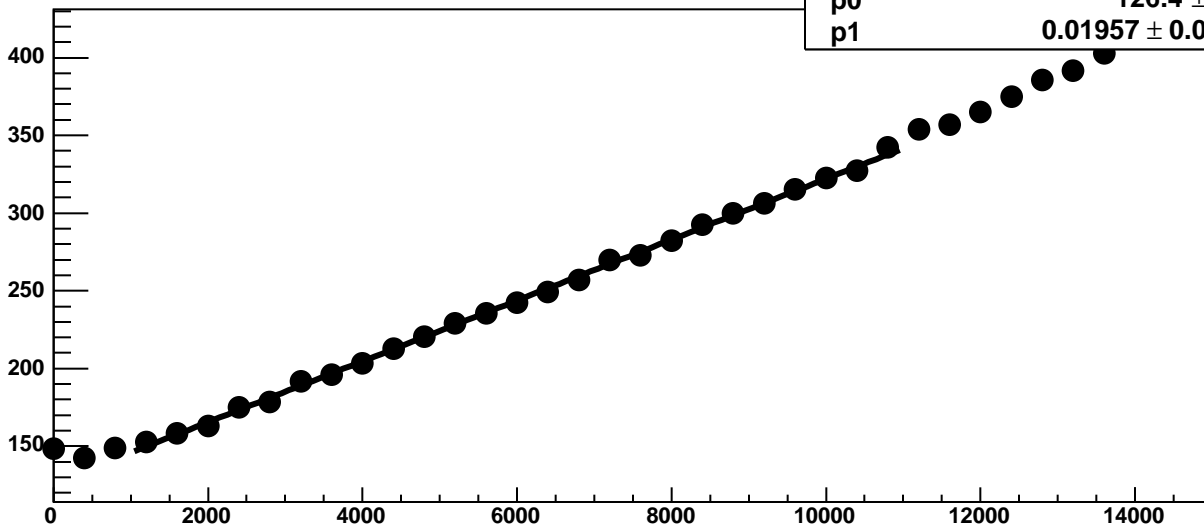
Chip 8, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

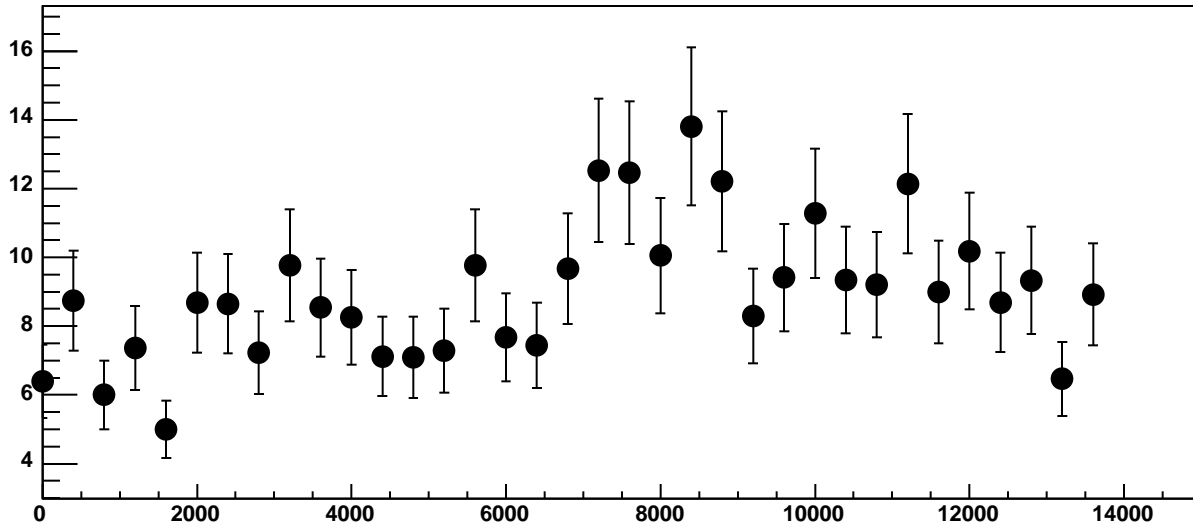


Chip 8, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC

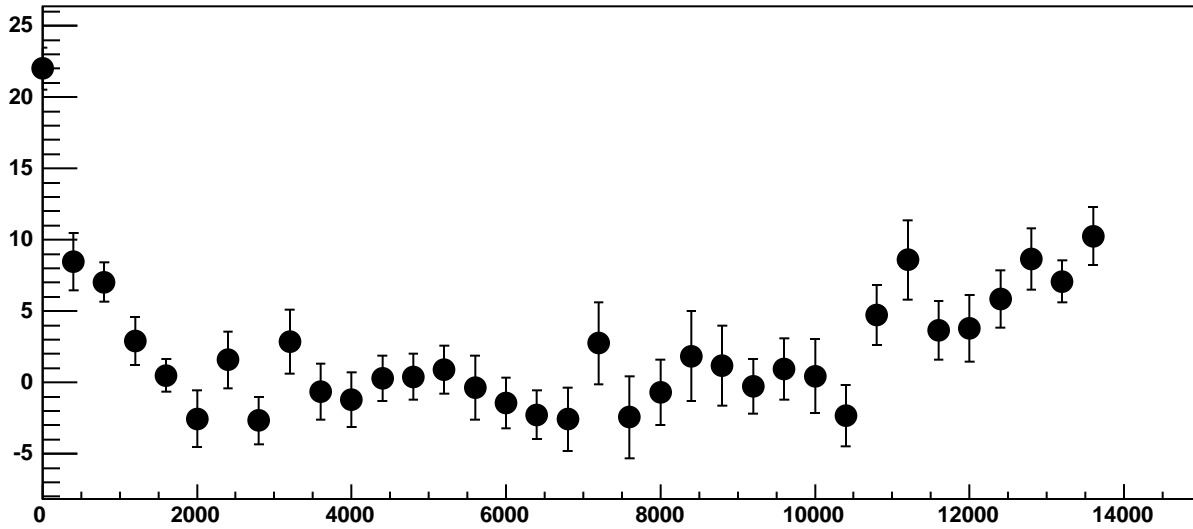


χ^2 / ndf 23.05 / 23
p0 126.4 ± 0.7955
p1 0.01957 ± 0.0001339

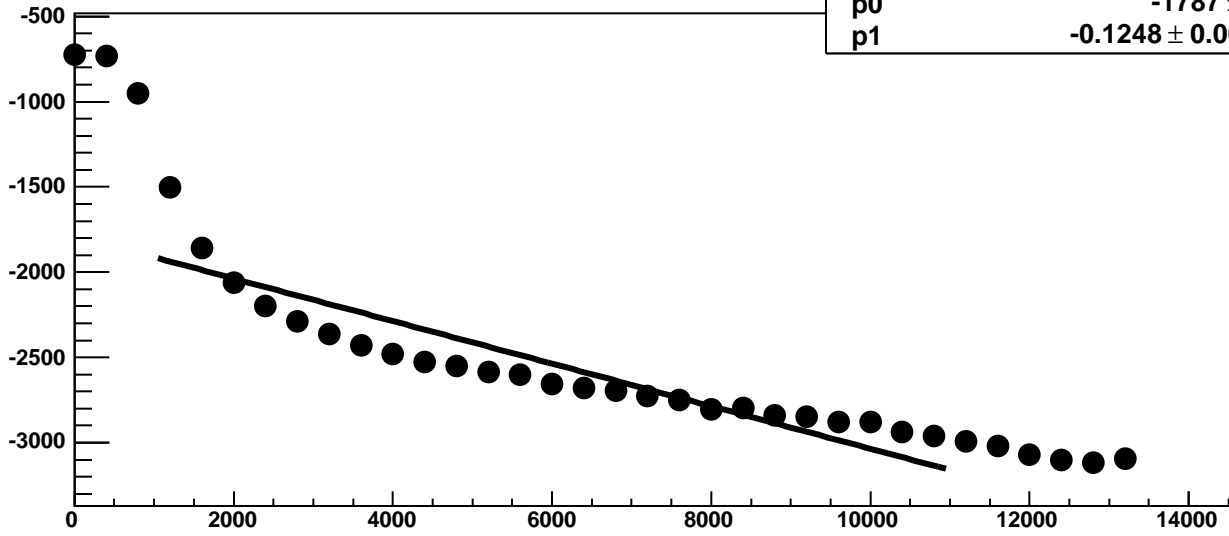
Chip 8, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC

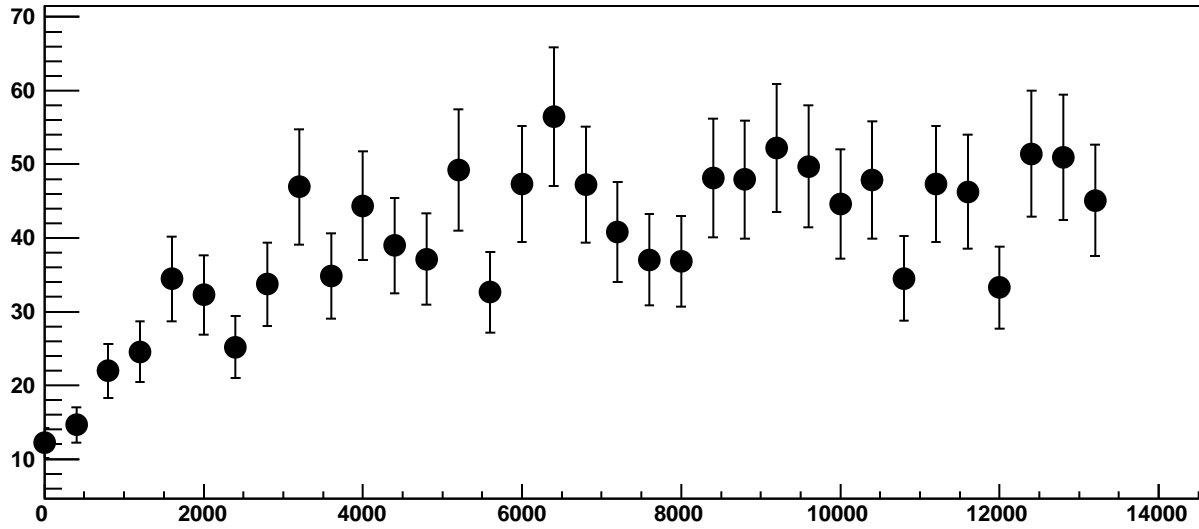


Chip 8, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

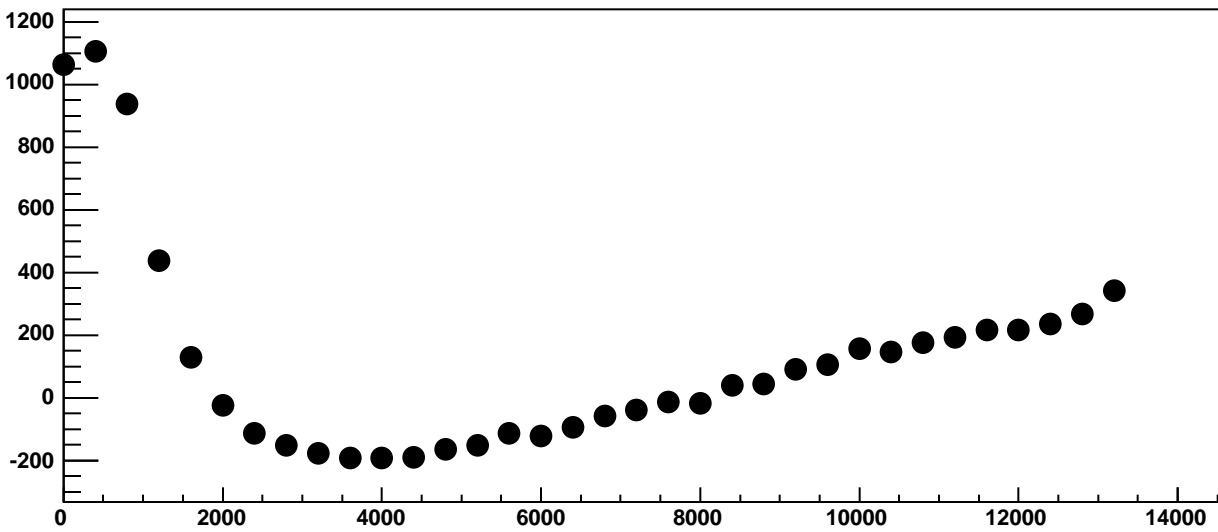


χ^2 / ndf 1.079e+04 / 23
p0 -1787 ± 3.463
p1 -0.1248 ± 0.0005776

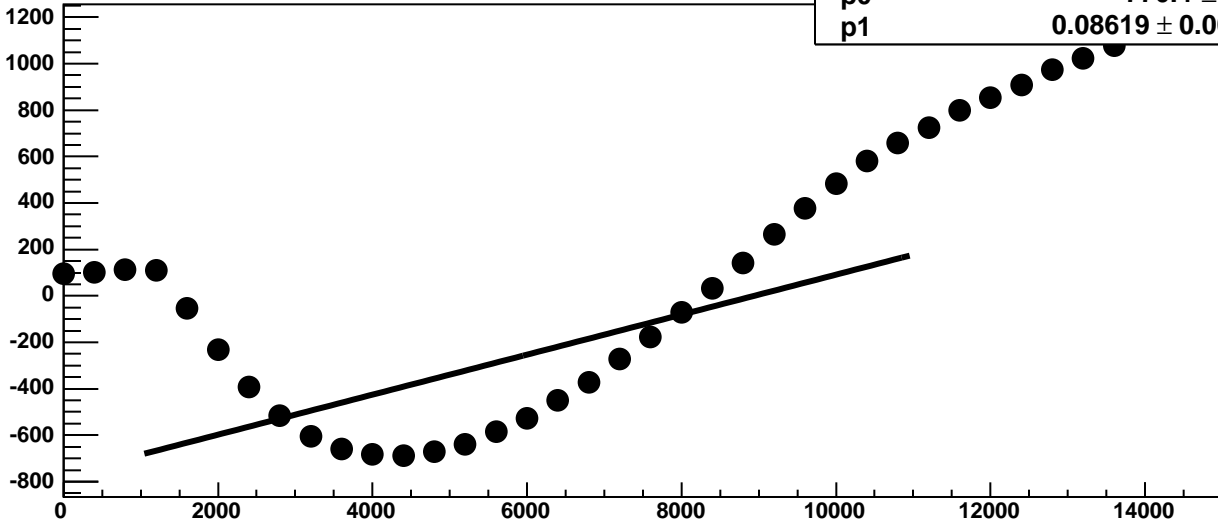
Chip 8, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

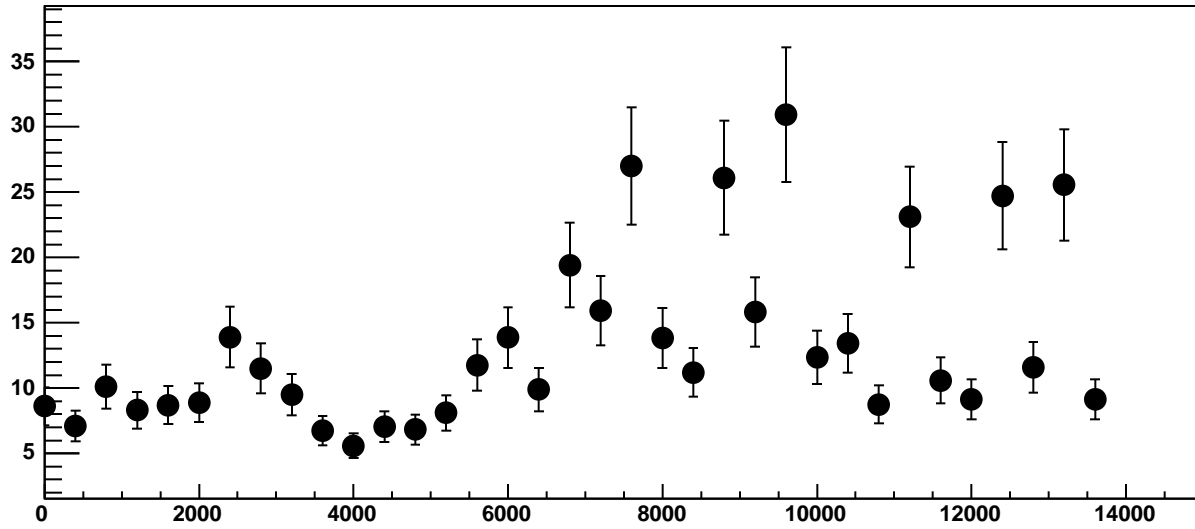


Chip 8, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

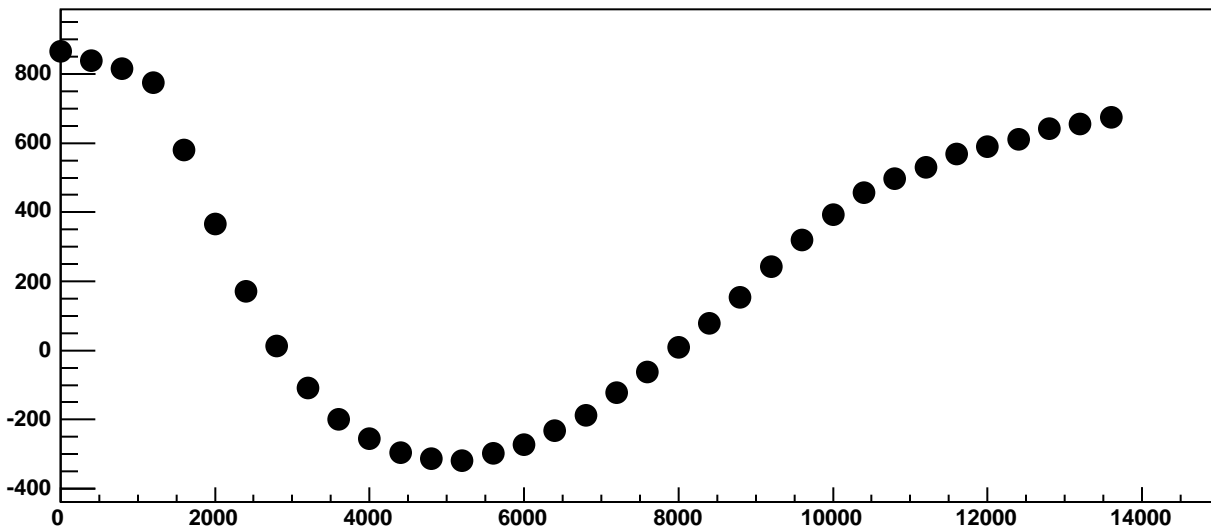


χ^2 / ndf 5.911e+05 / 23
p0 -770.4 ± 0.9717
p1 0.08619 ± 0.0001734

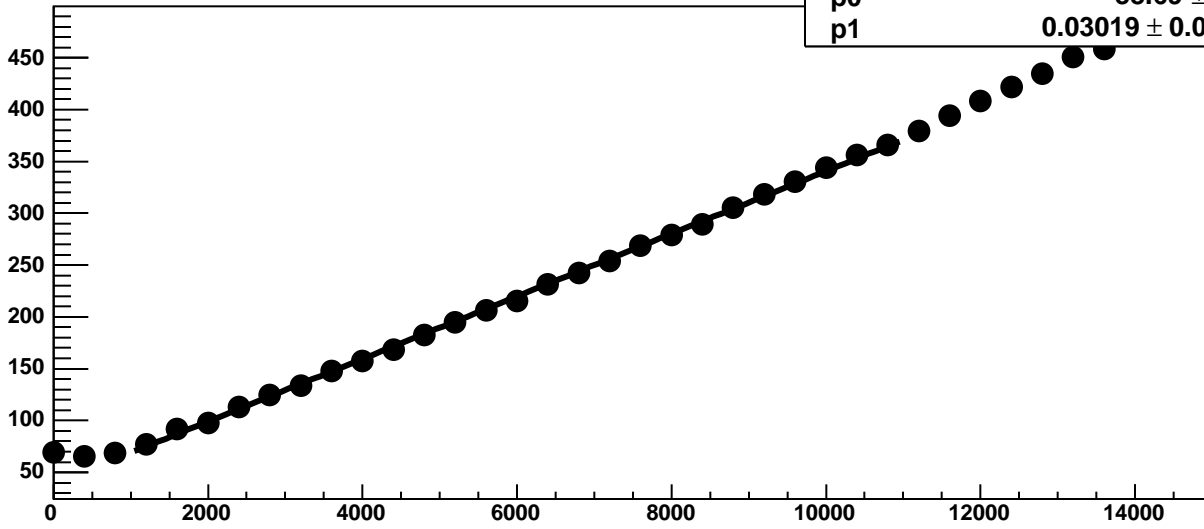
Chip 8, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

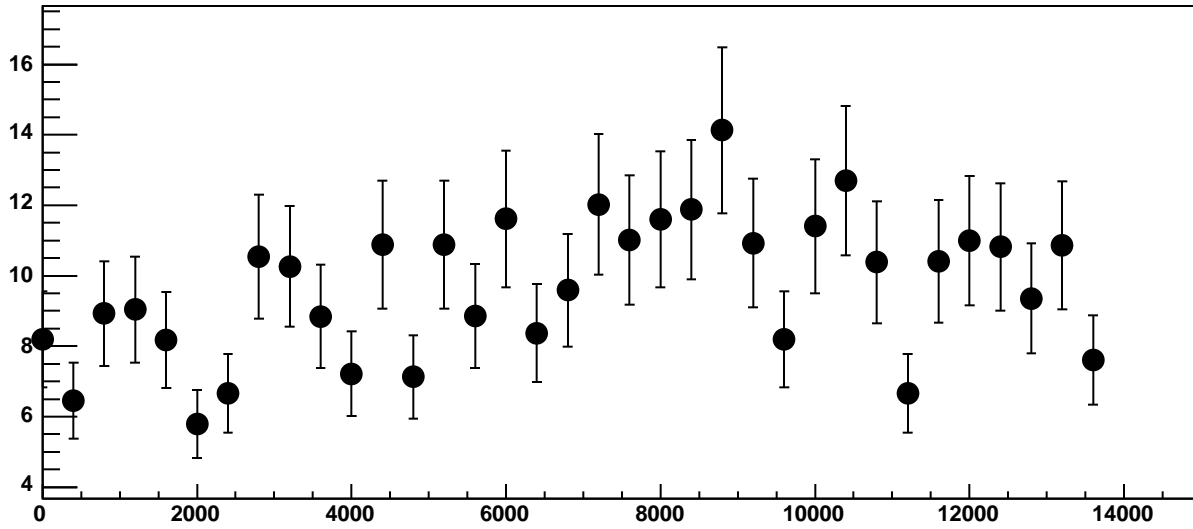


Chip 8, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC

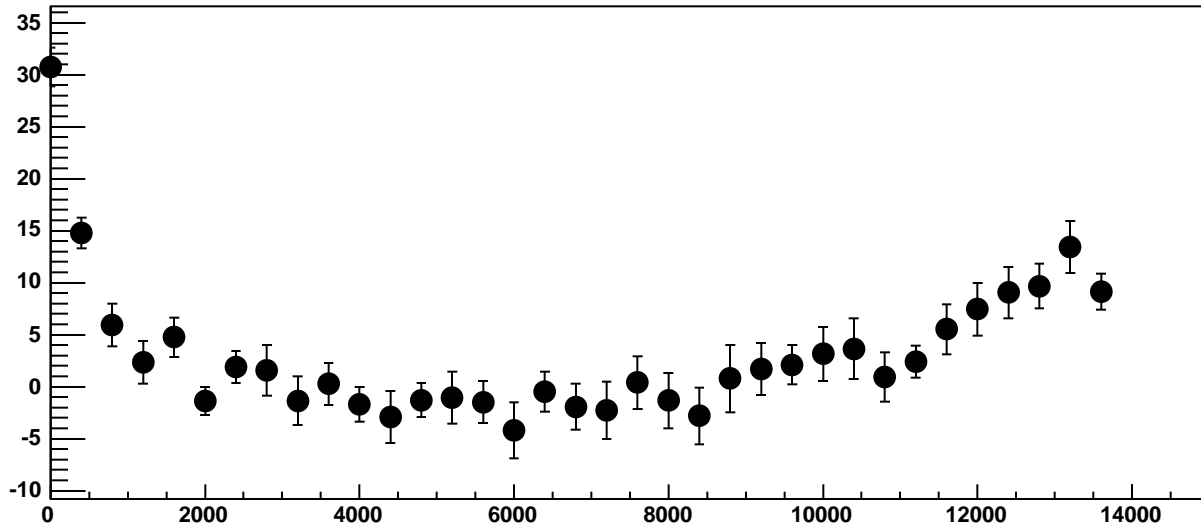


χ^2 / ndf 25.13 / 23
p0 38.69 ± 0.8773
p1 0.03019 ± 0.0001476

Chip 8, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC

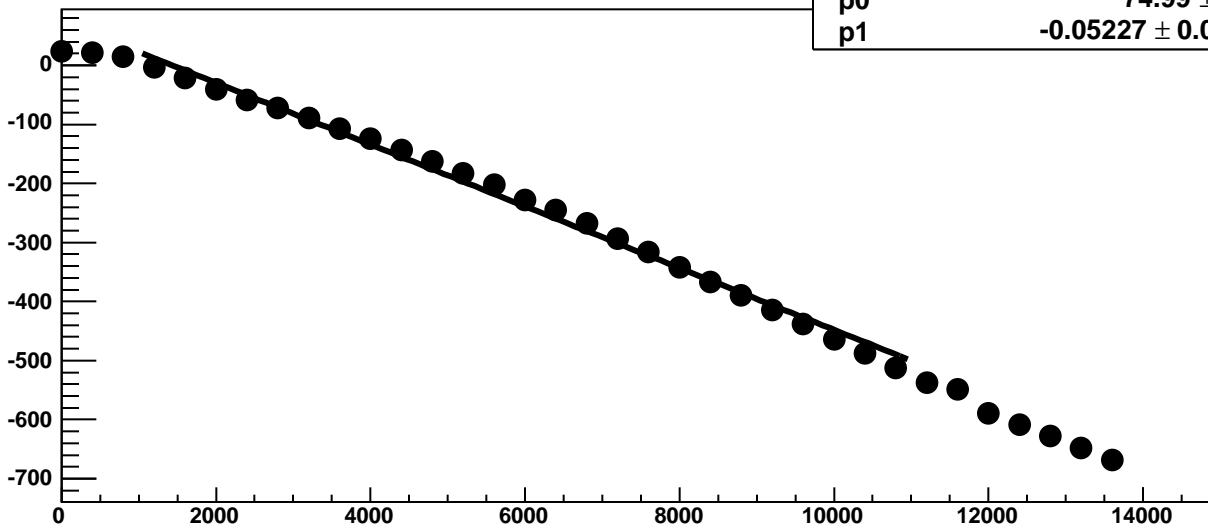


Chip 8, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC

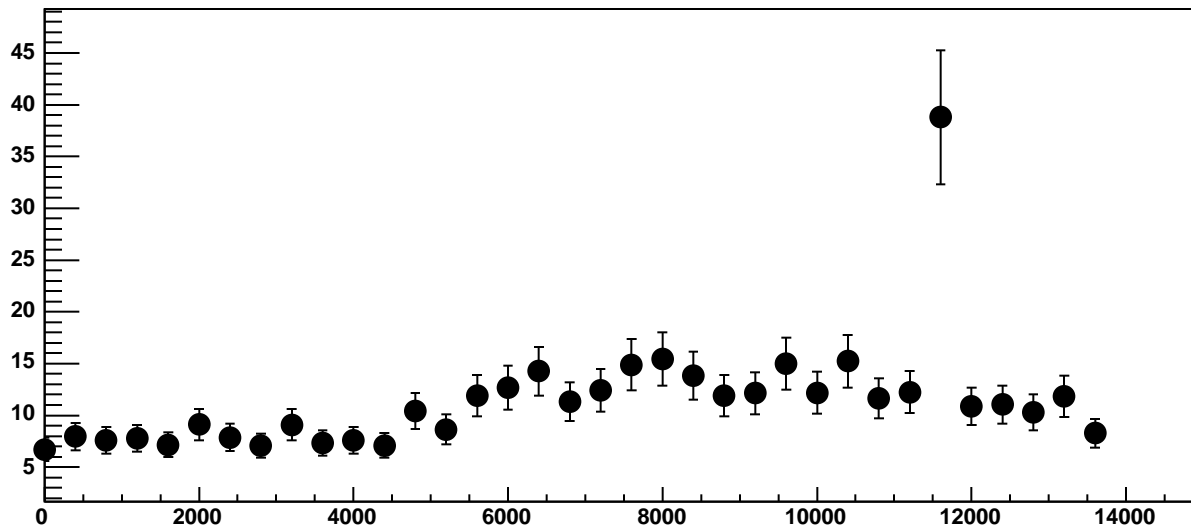


Chip 8, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

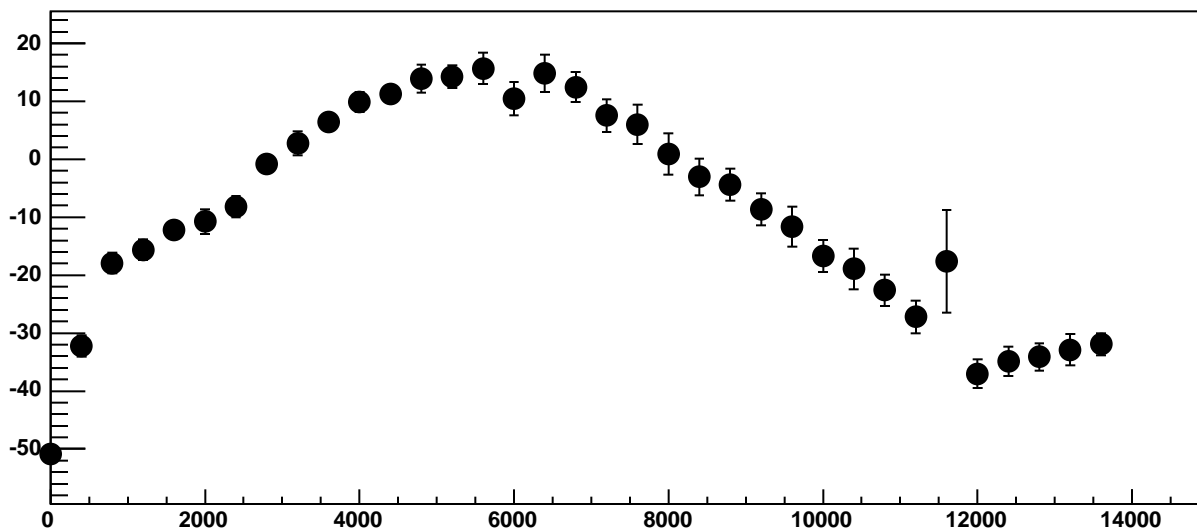
χ^2 / ndf 621.6 / 23
p0 74.99 ± 0.9059
p1 -0.05227 ± 0.0001653



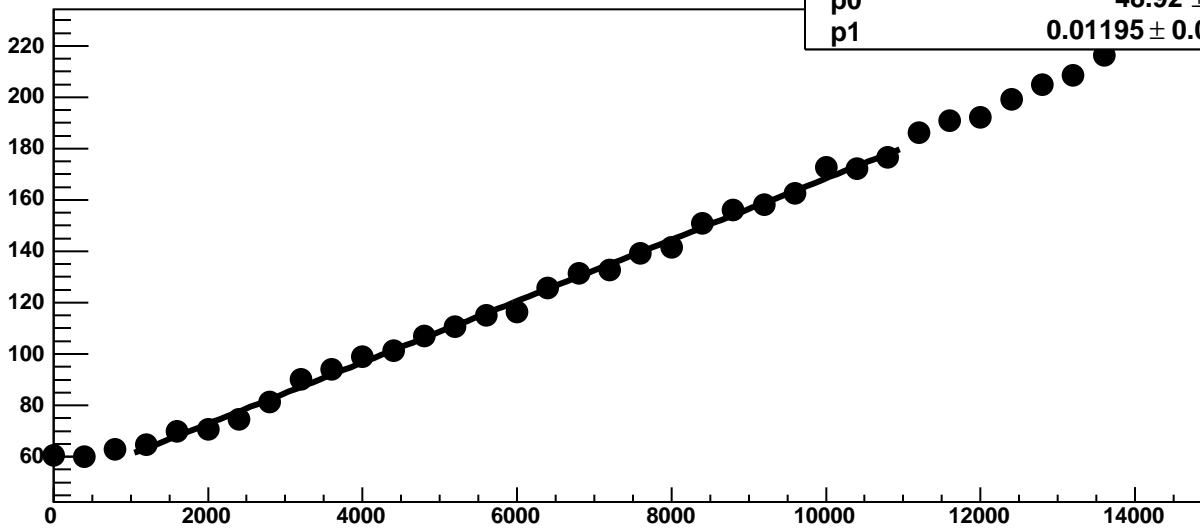
Chip 8, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

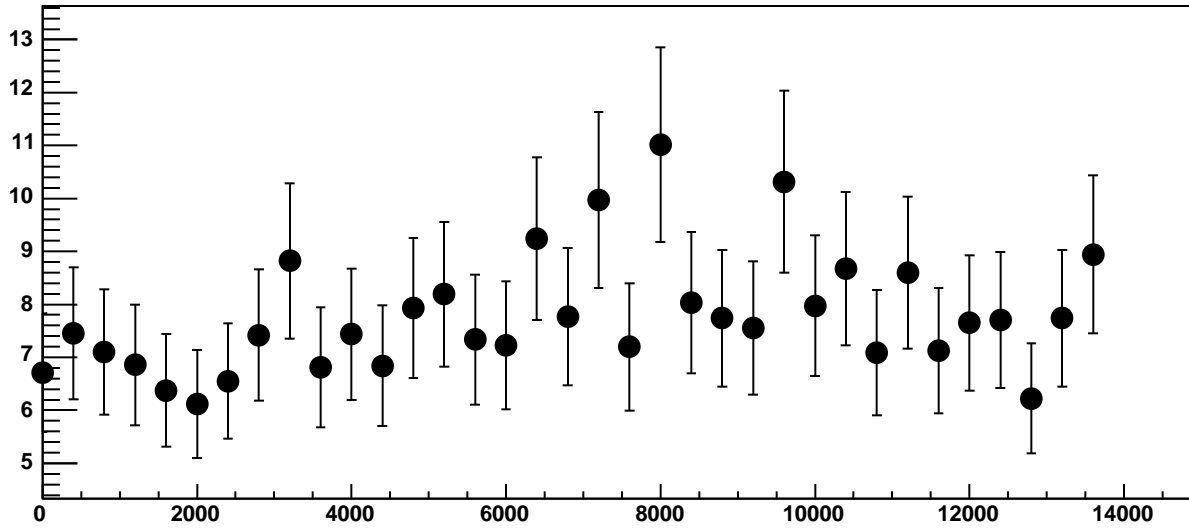


Chip 8, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

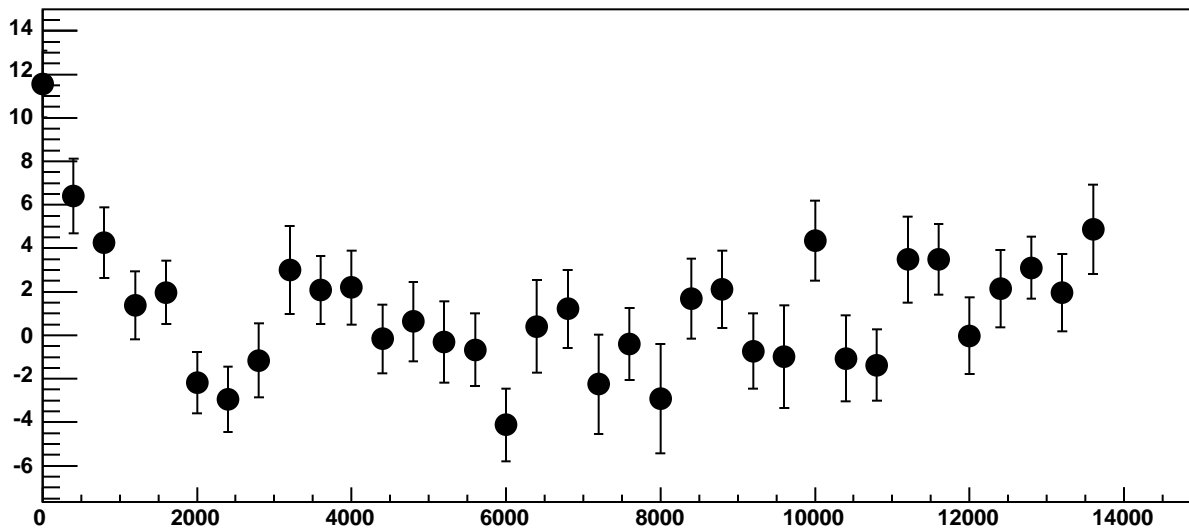


χ^2 / ndf 33.53 / 23
p0 48.92 ± 0.7435
p1 0.01195 ± 0.0001179

Chip 8, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

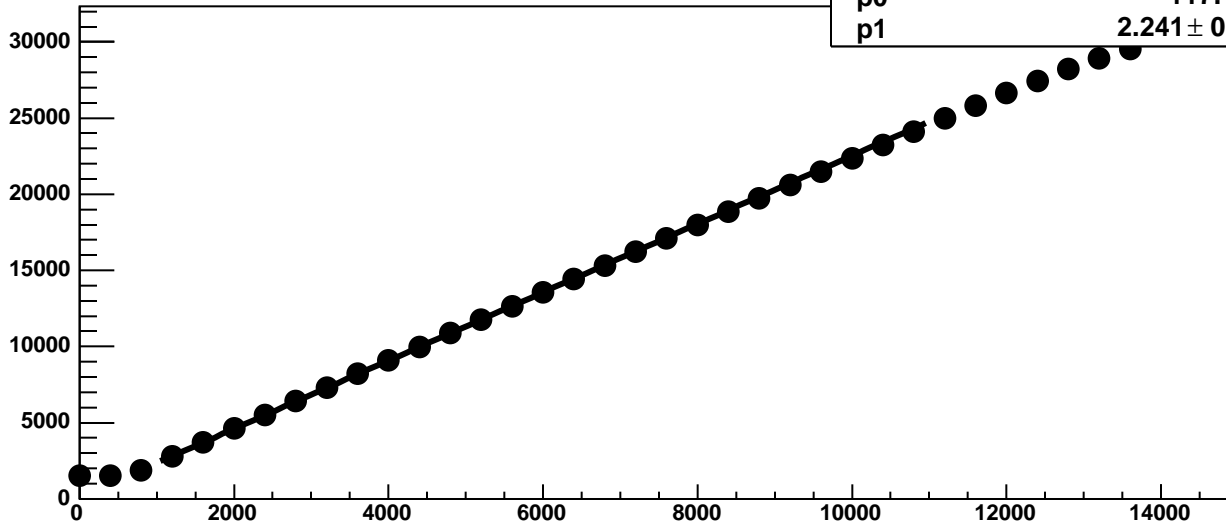


Chip 8, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

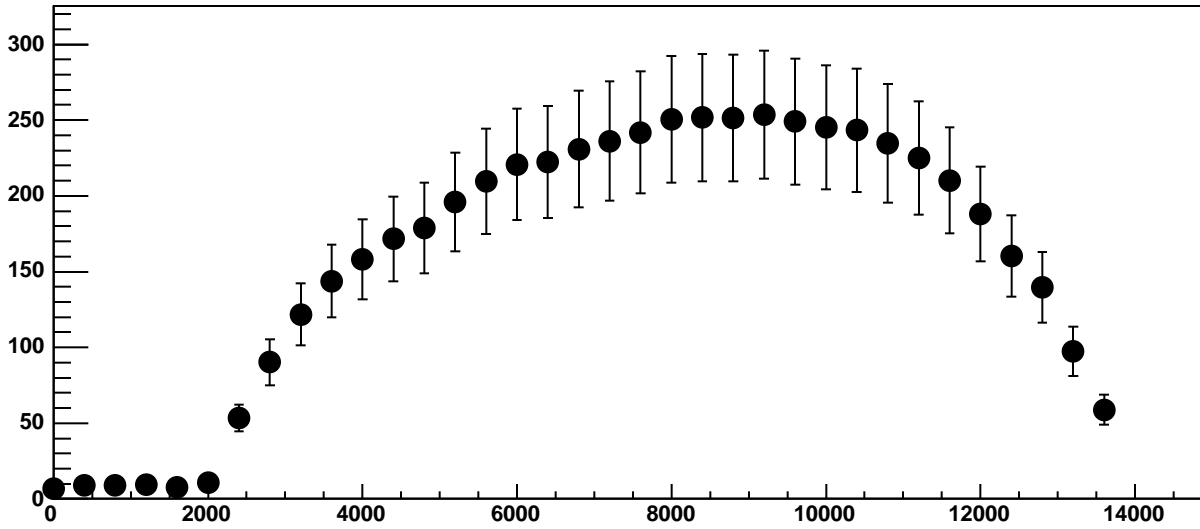


Chip 8, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

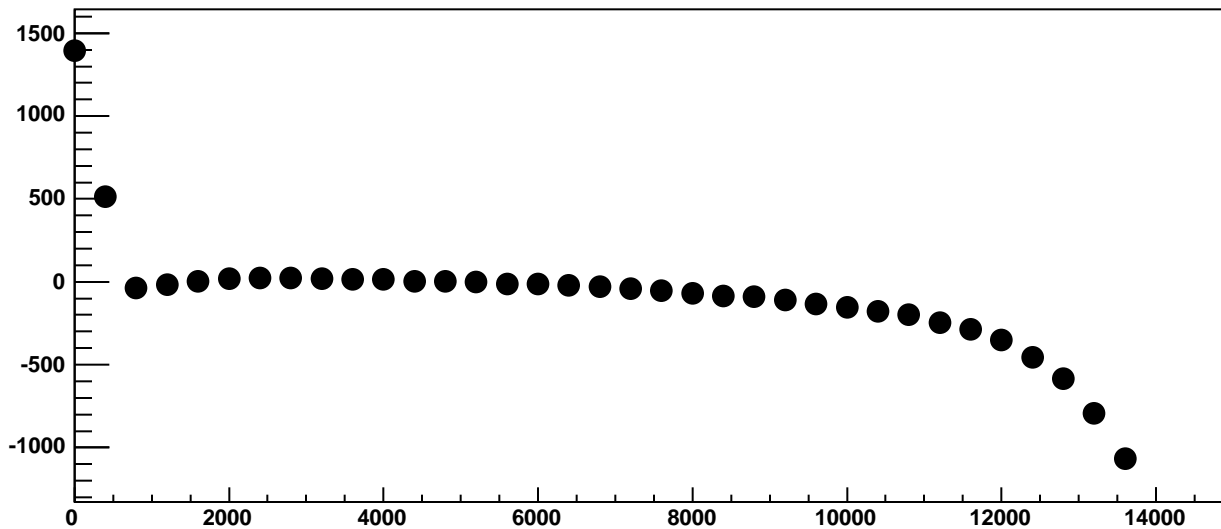
χ^2 / ndf 171.8 / 23
p0 117.1 ± 3.18
p1 2.241 ± 0.001807



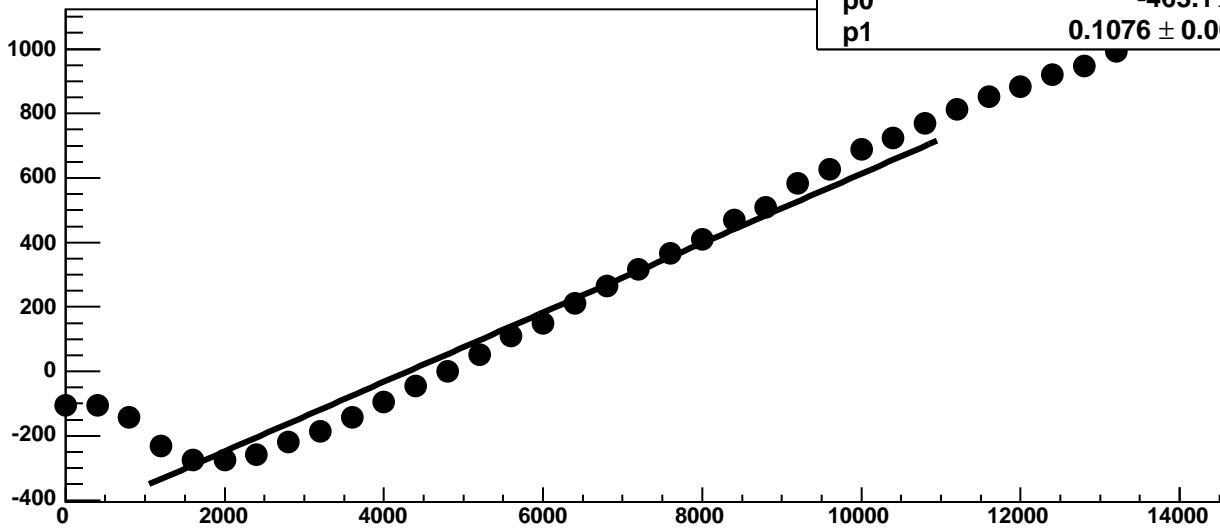
Chip 8, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC



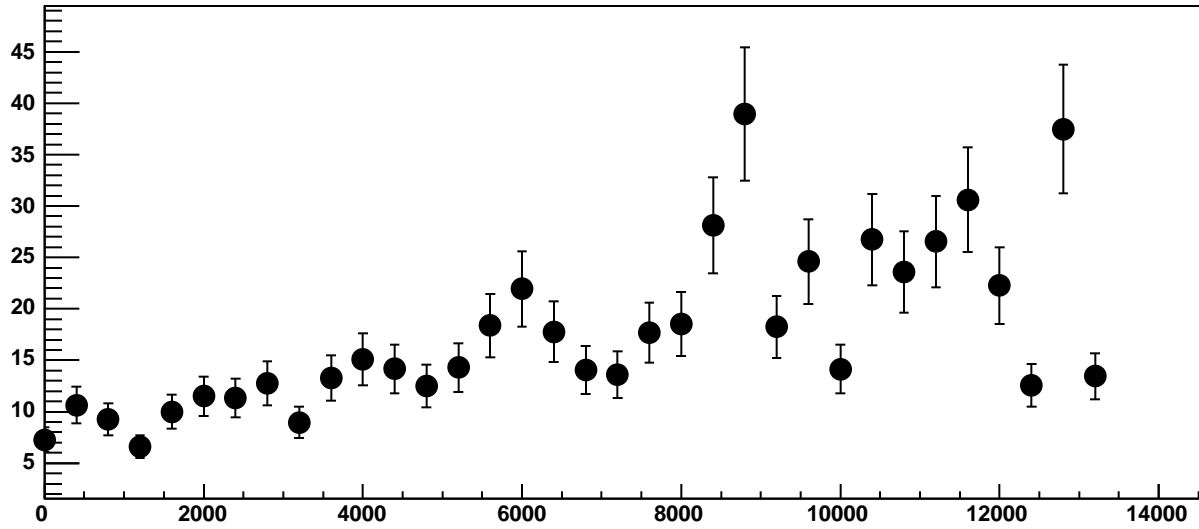
Chip 8, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC



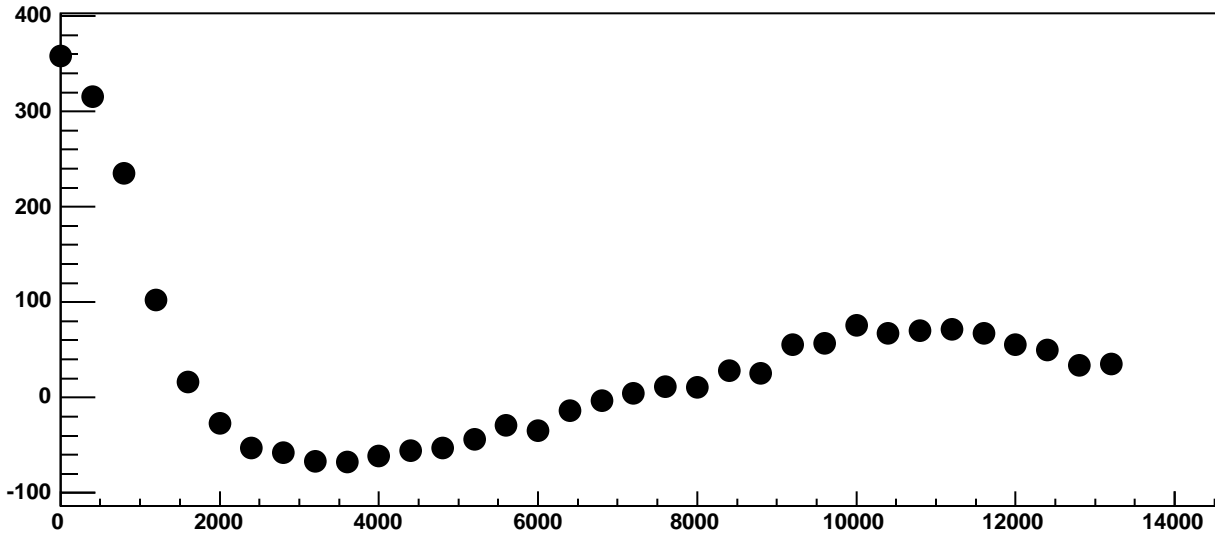
Chip 8, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



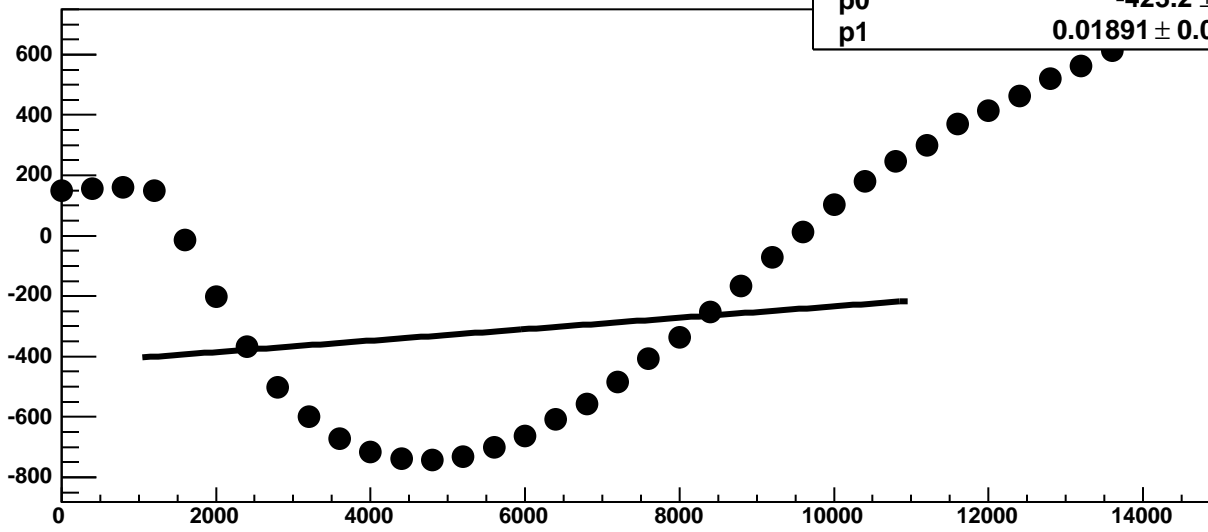
Chip 8, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



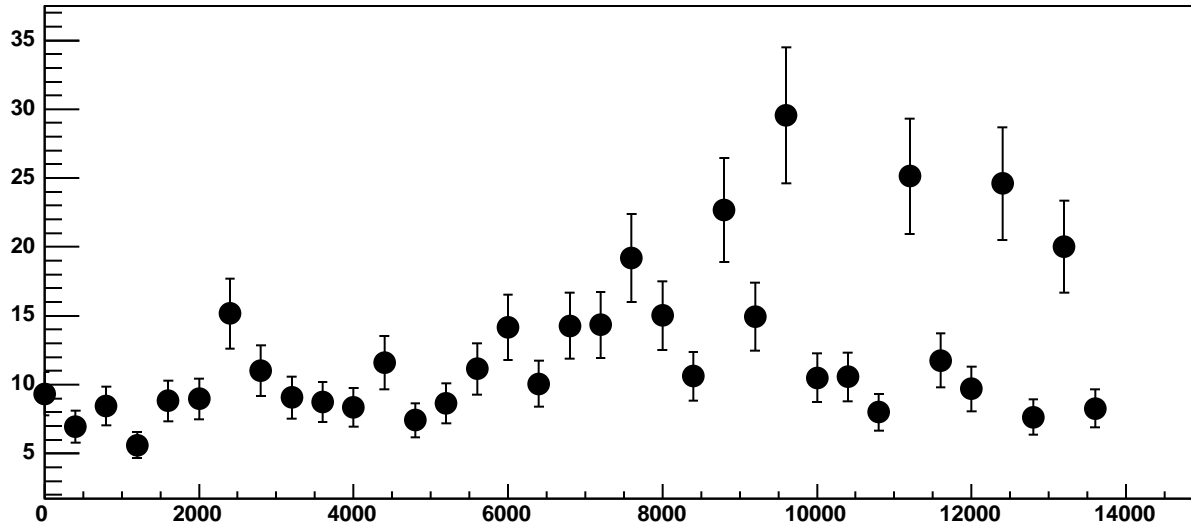
Chip 8, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



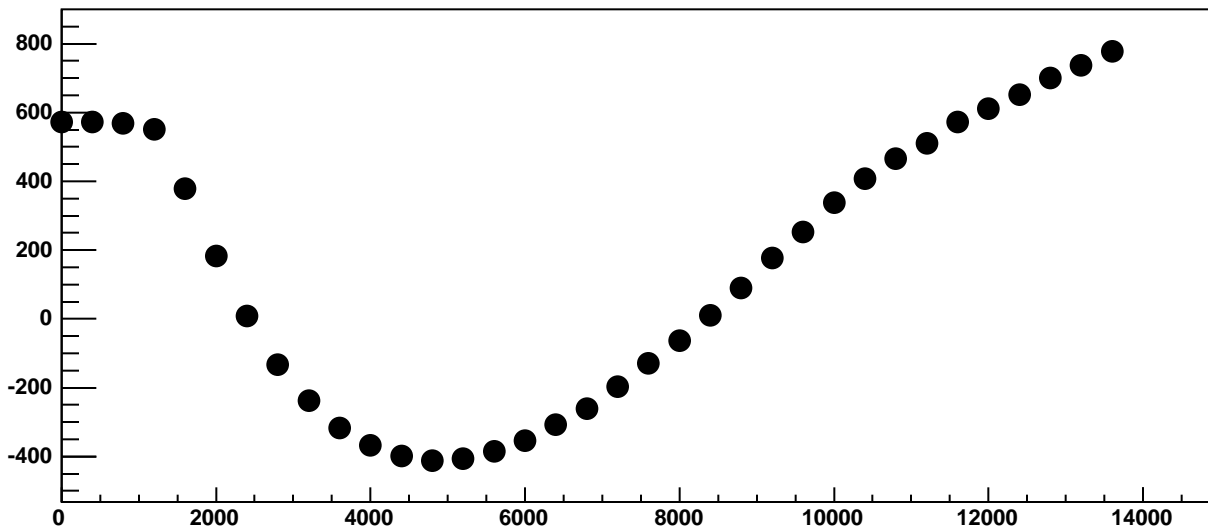
Chip 8, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



Chip 8, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

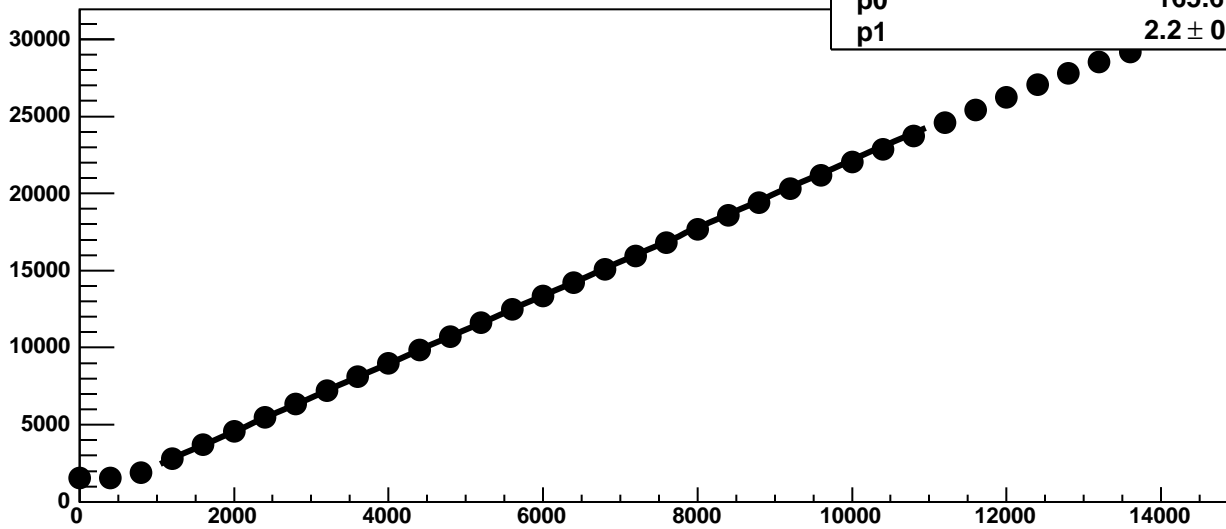


Chip 8, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

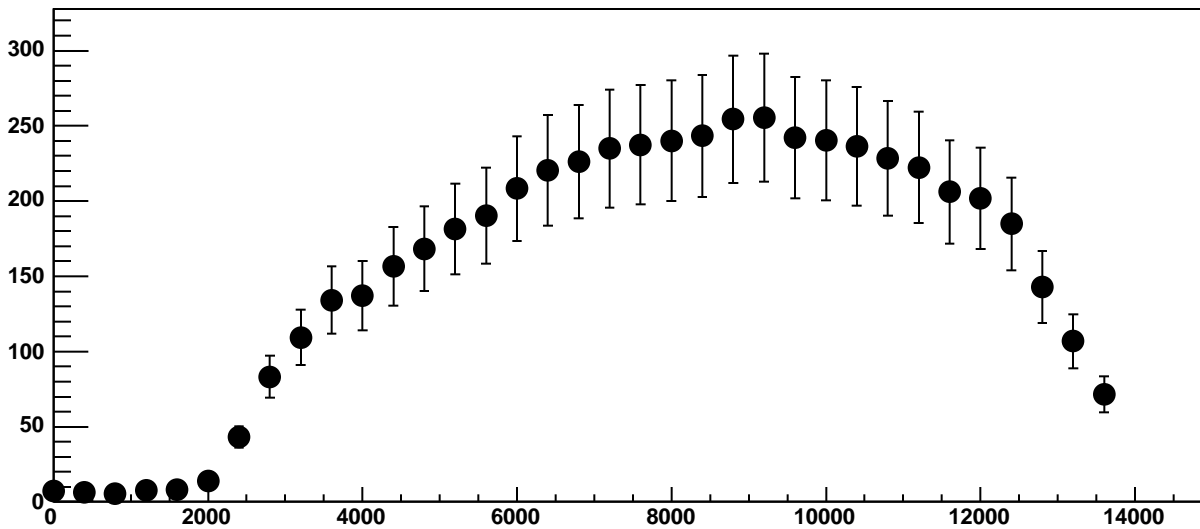


Chip 8, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC

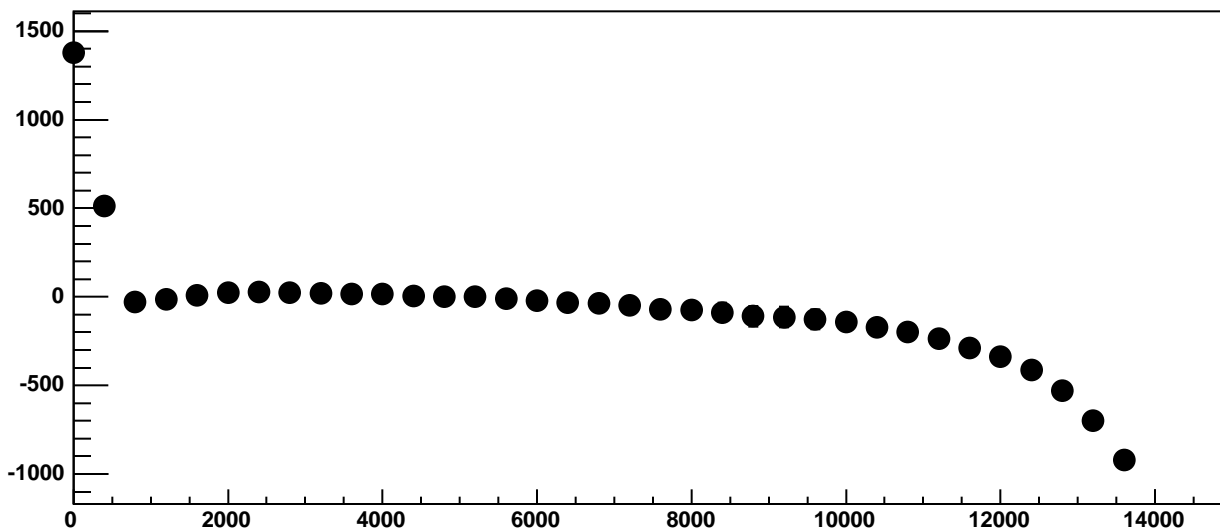
χ^2 / ndf 195.3 / 23
p0 165.6 ± 2.926
p1 2.2 ± 0.001735



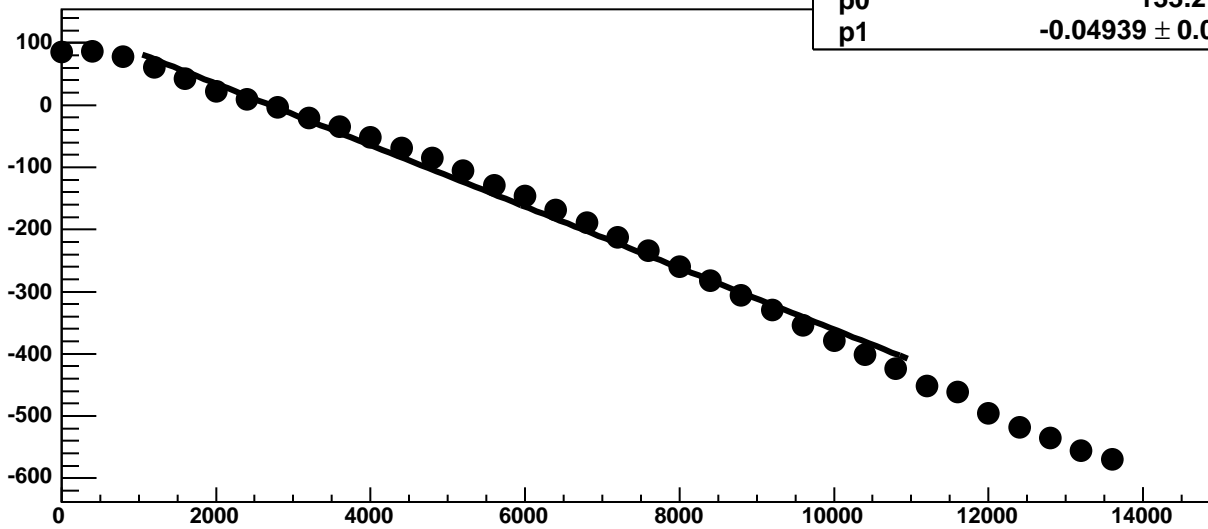
Chip 8, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

584.5 / 23

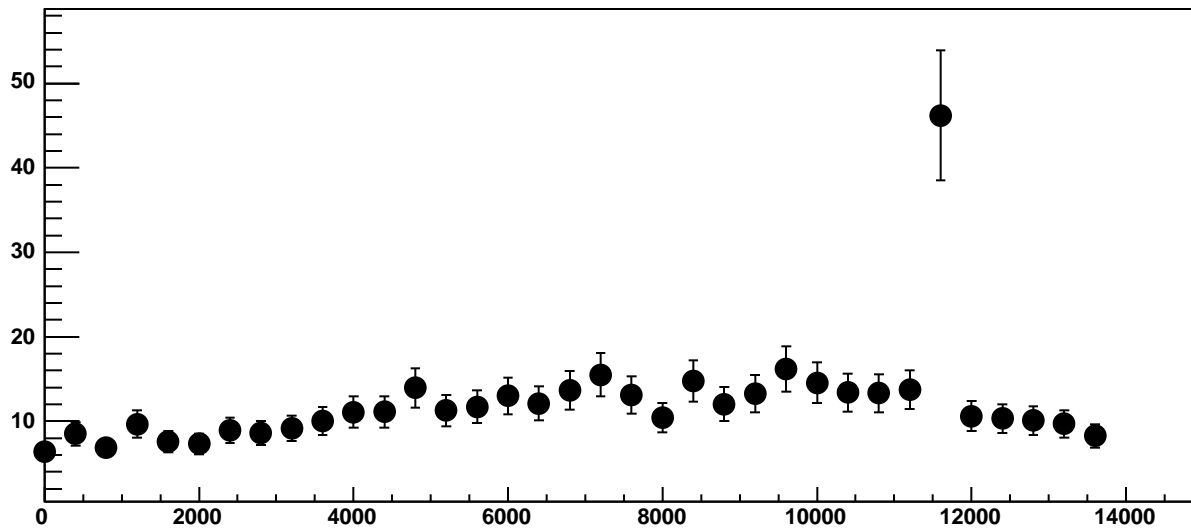
p0

133.2 ± 0.998

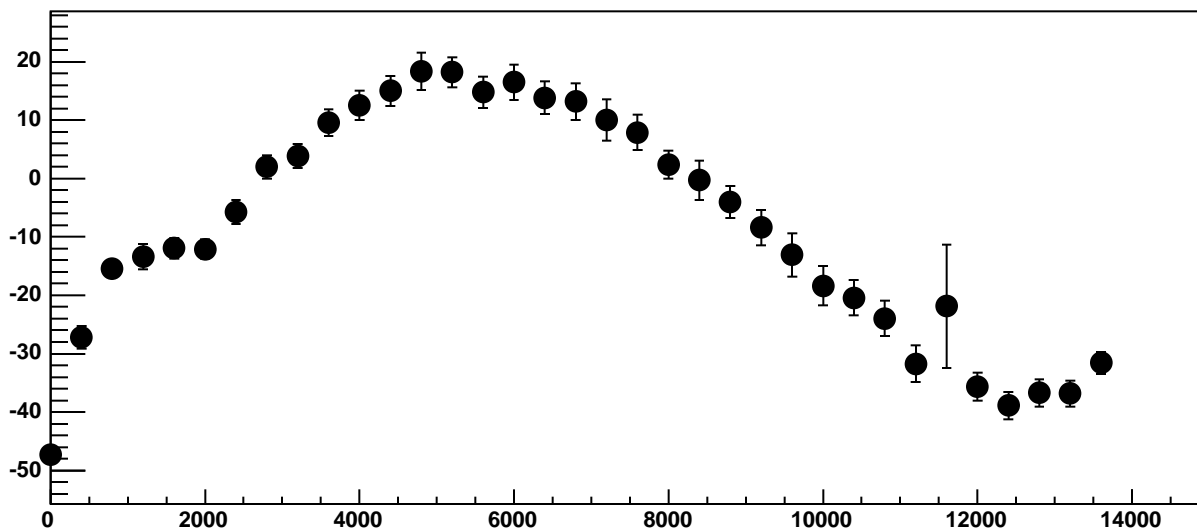
p1

-0.04939 ± 0.0001745

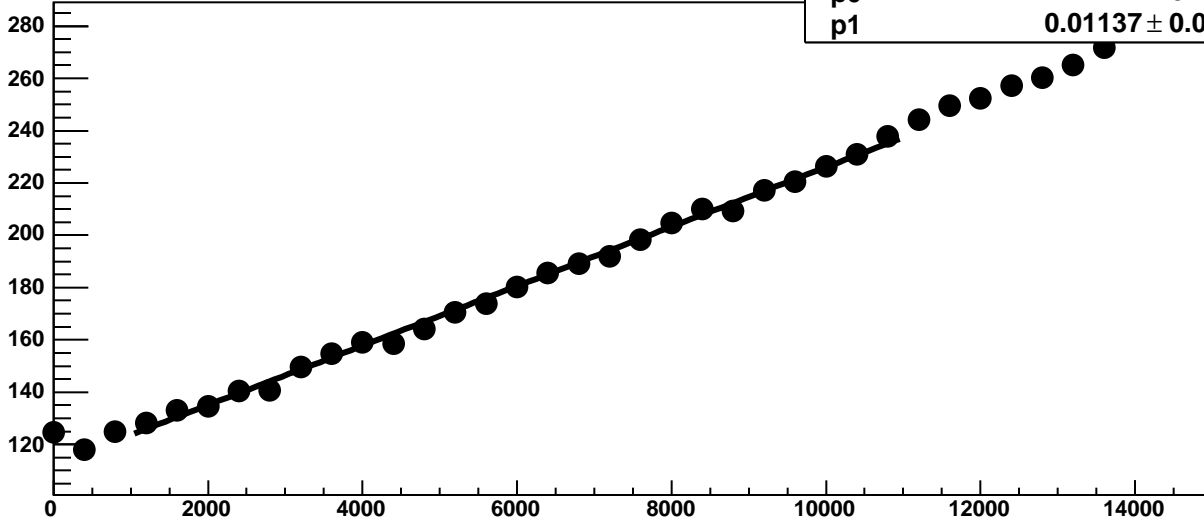
Chip 8, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

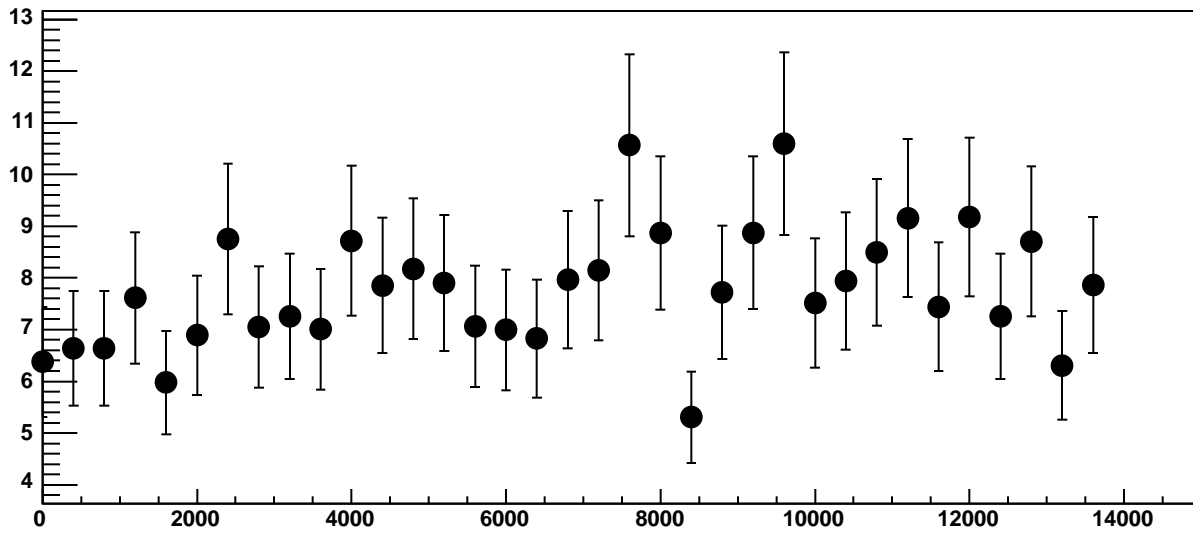


Chip 8, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

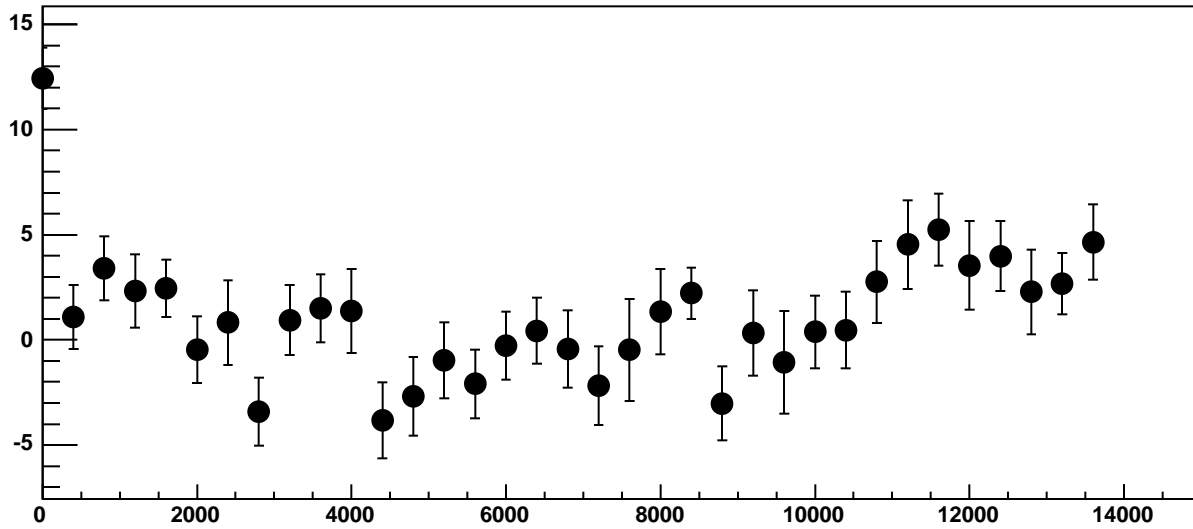


χ^2 / ndf 30.35 / 23
p0 112.3 ± 0.7767
p1 0.01137 ± 0.0001205

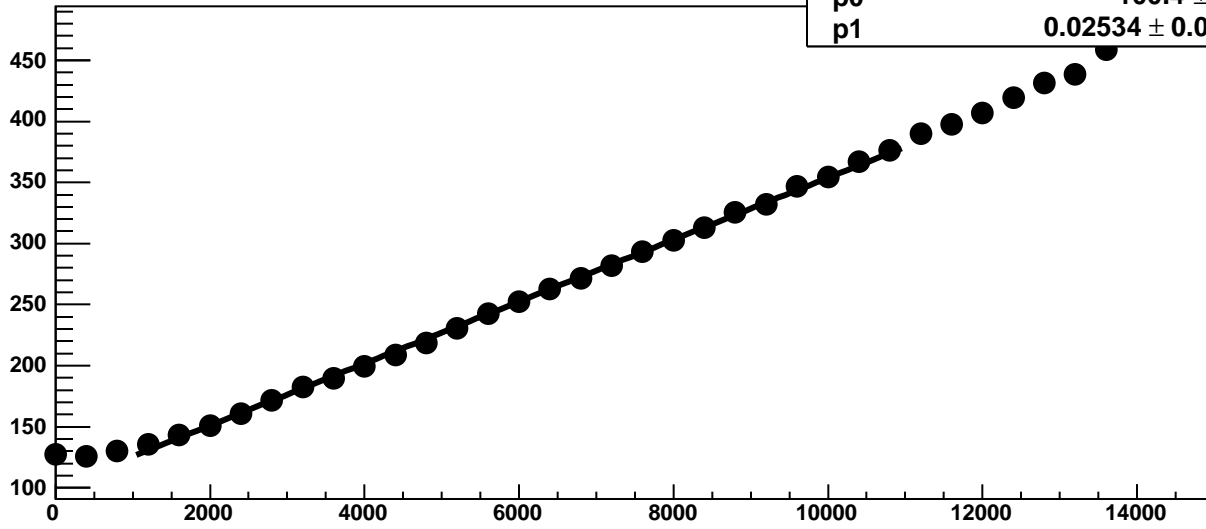
Chip 8, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

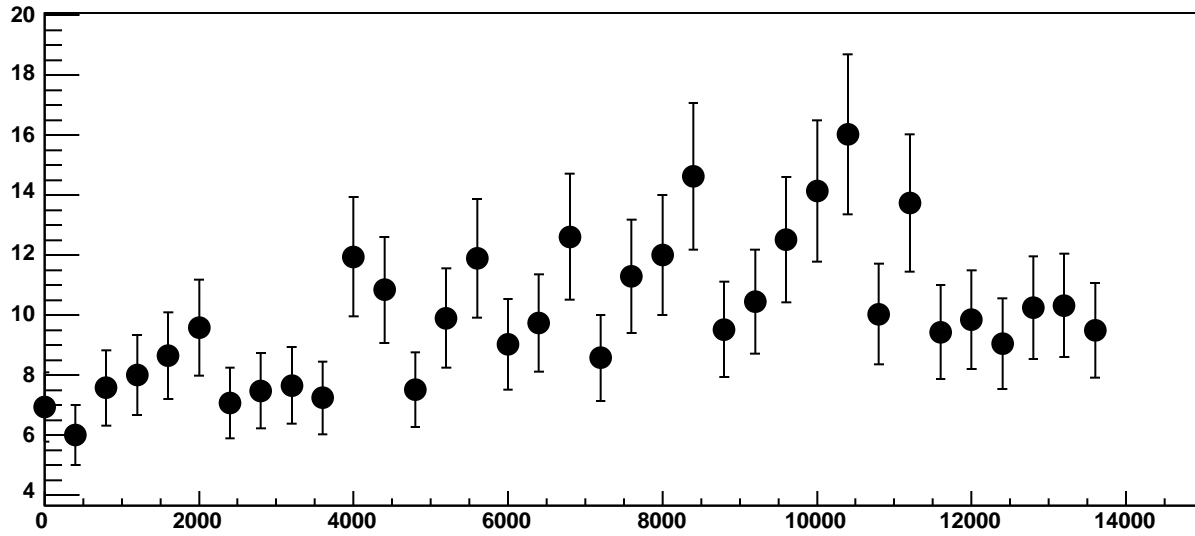


Chip 8, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

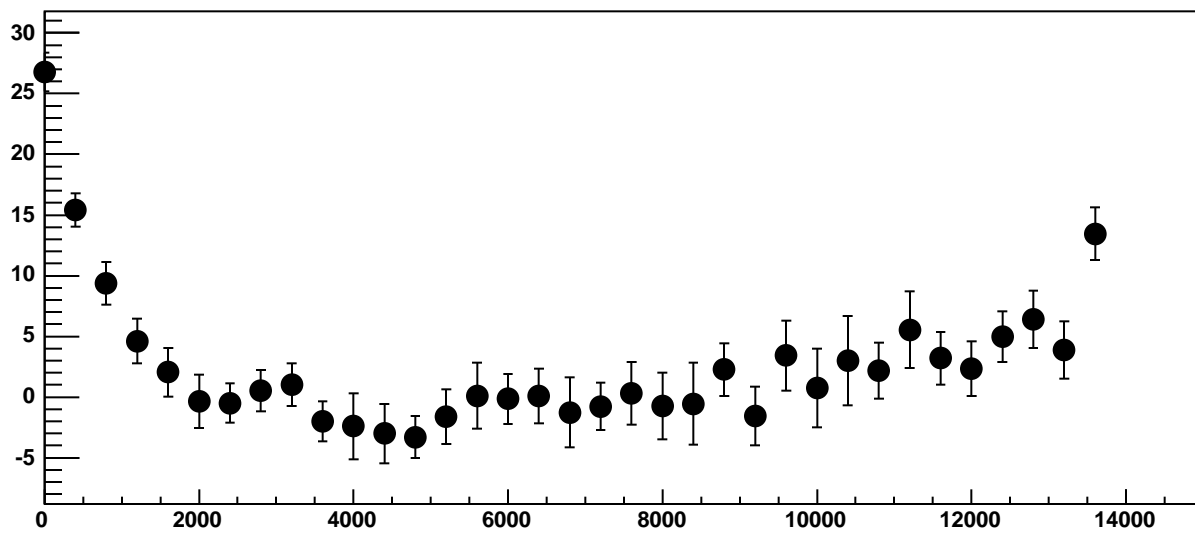


χ^2 / ndf 20.82 / 23
p0 100.4 ± 0.9249
p1 0.02534 ± 0.0001577

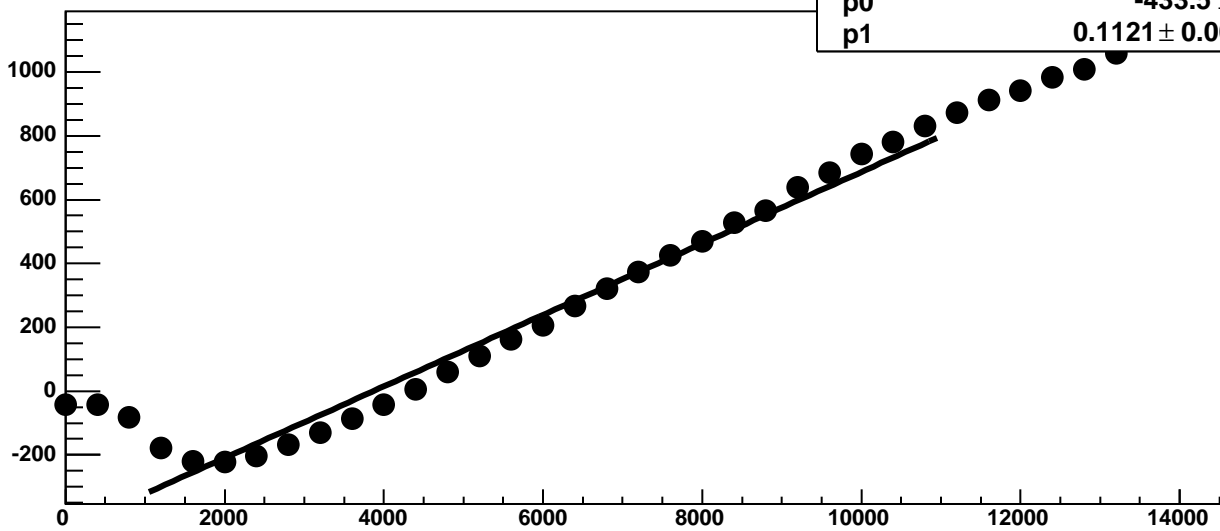
Chip 8, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC

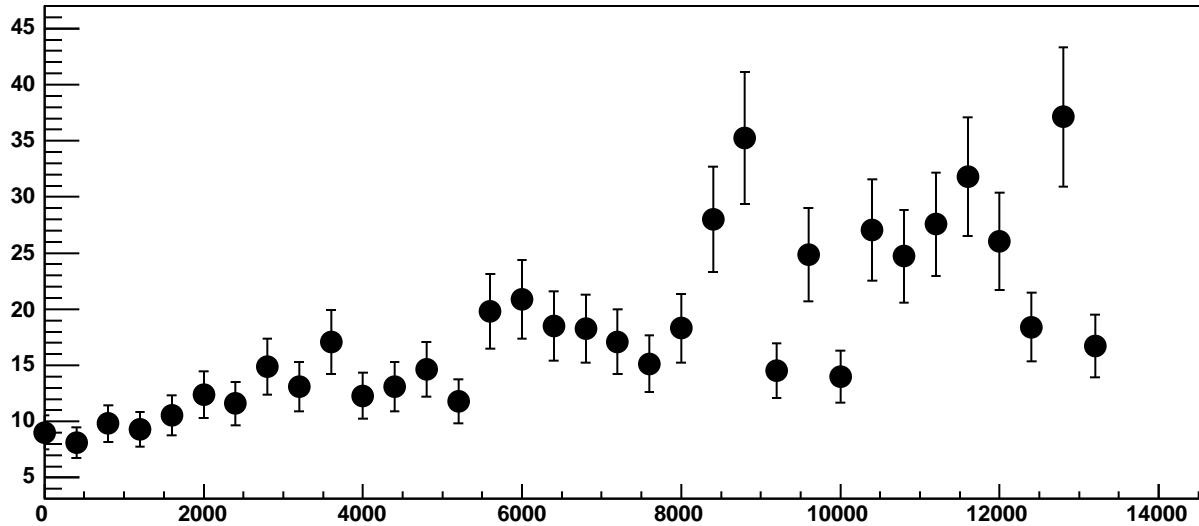


Chip 8, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

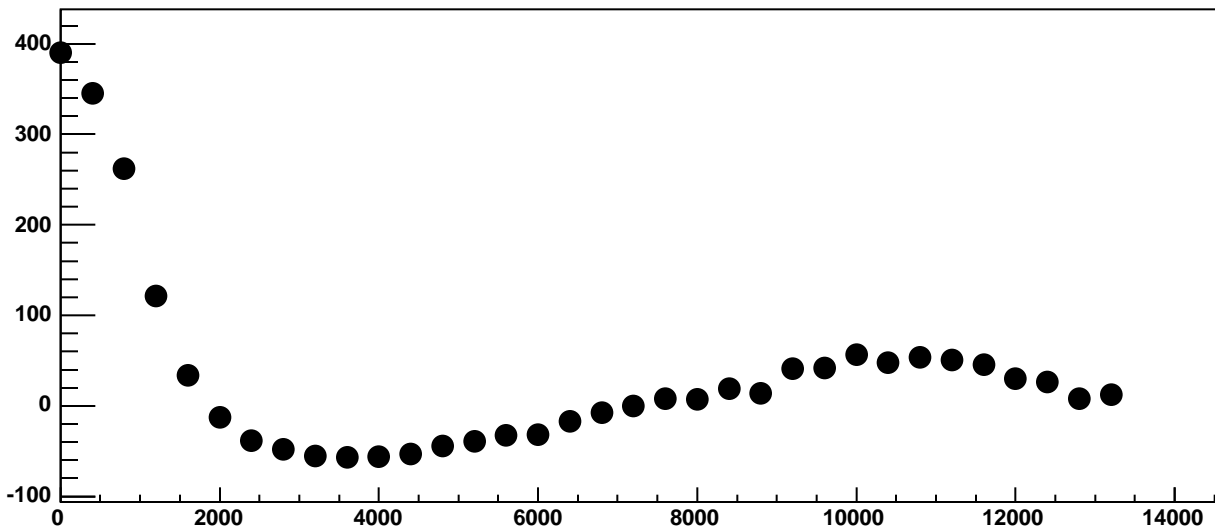


χ^2 / ndf 6283 / 23
p0 -433.5 ± 1.344
p1 0.1121 ± 0.0002437

Chip 8, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

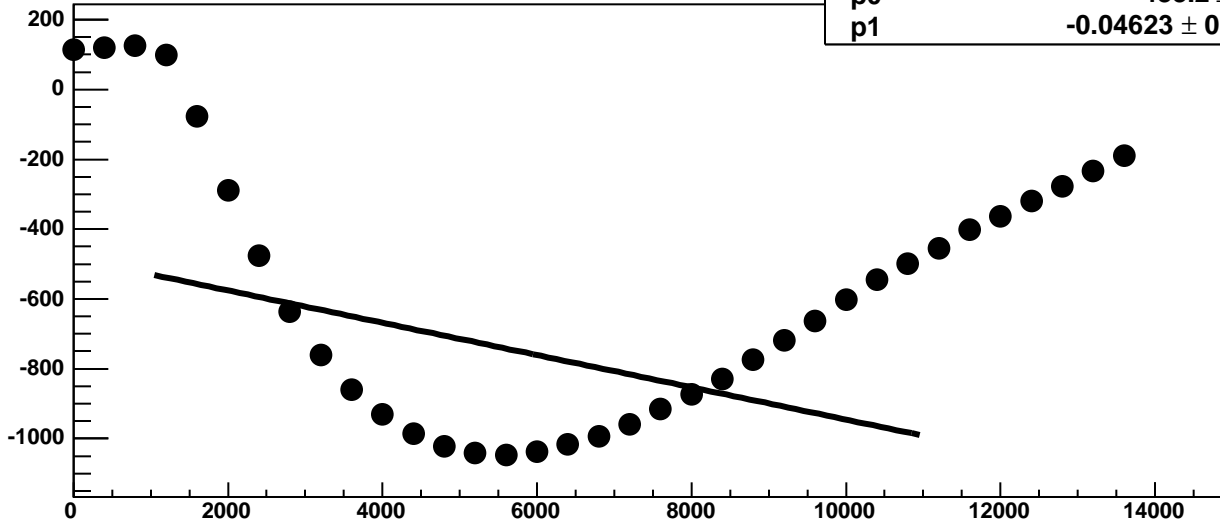


Chip 8, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

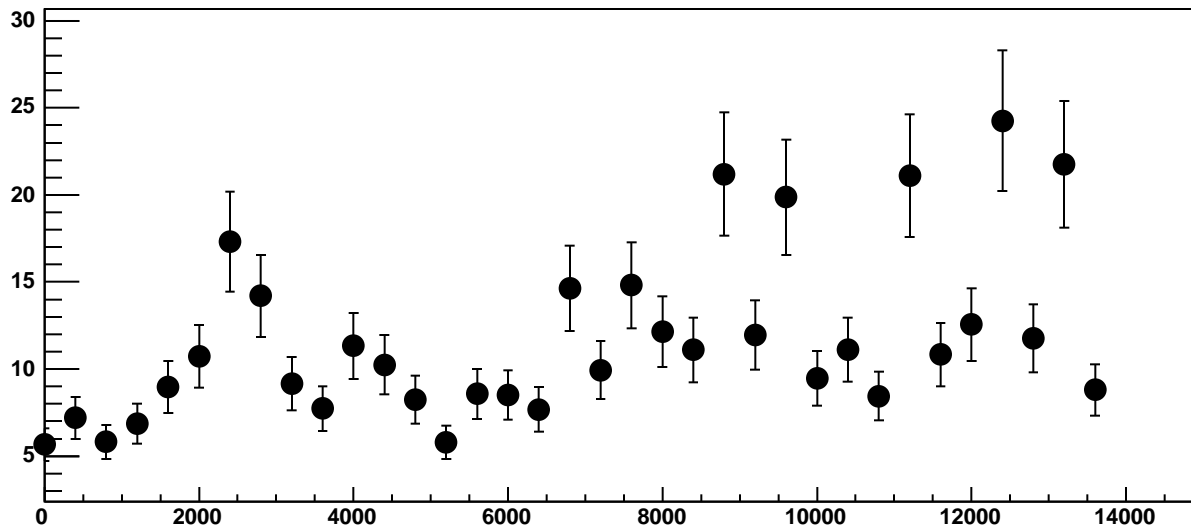


Chip 8, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

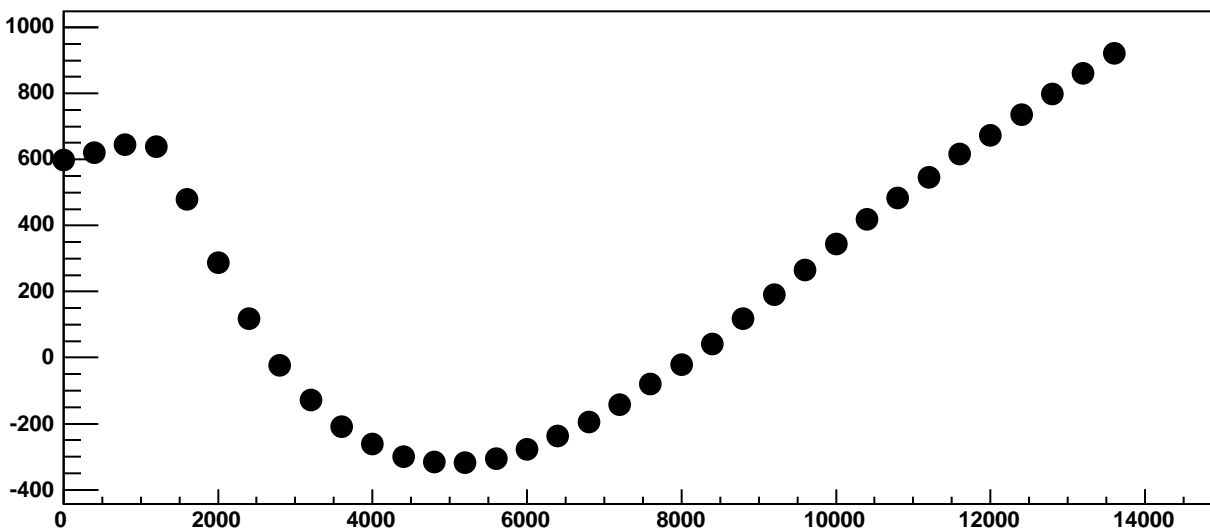
χ^2 / ndf 5.567e+05 / 23
p0 -483.2 ± 0.9871
p1 -0.04623 ± 0.000159



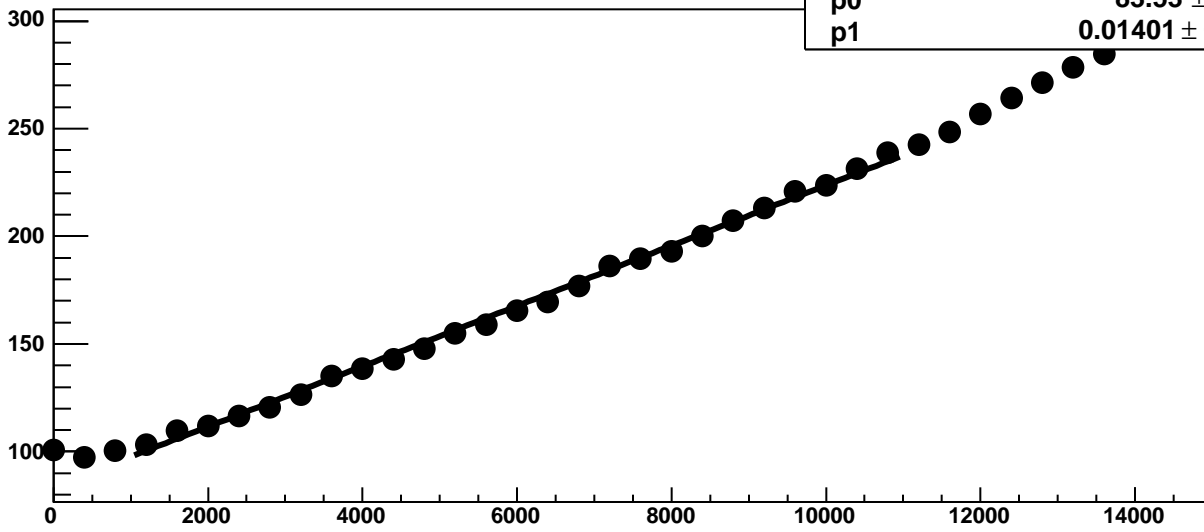
Chip 8, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



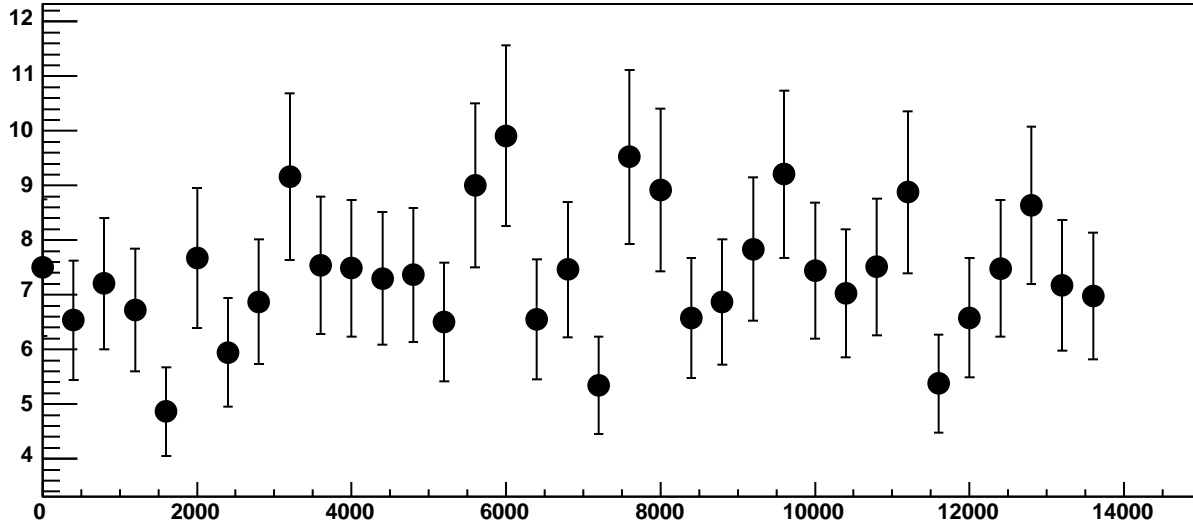
Chip 8, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



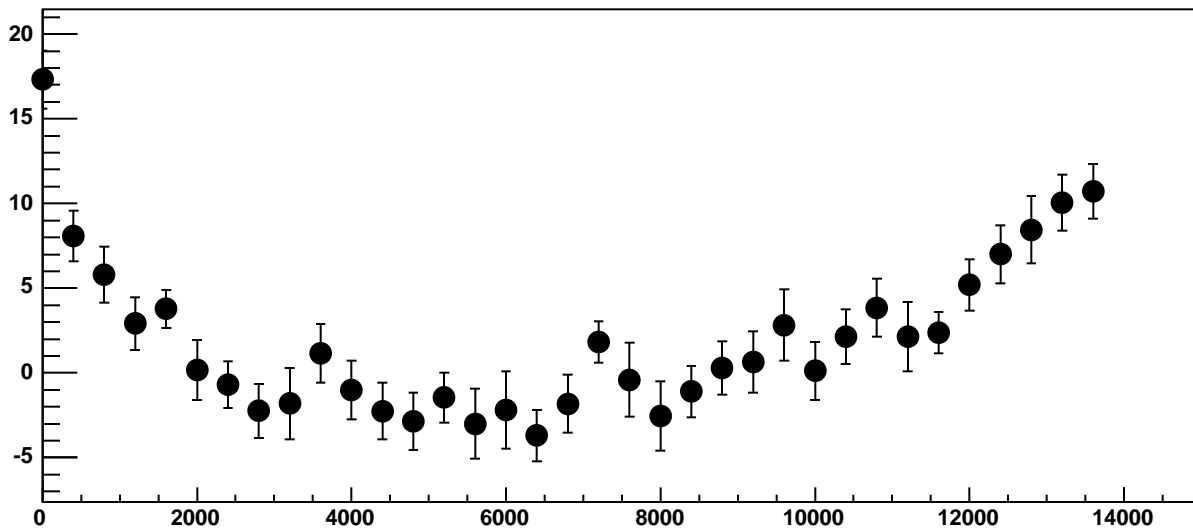
Chip 8, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



Chip 8, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

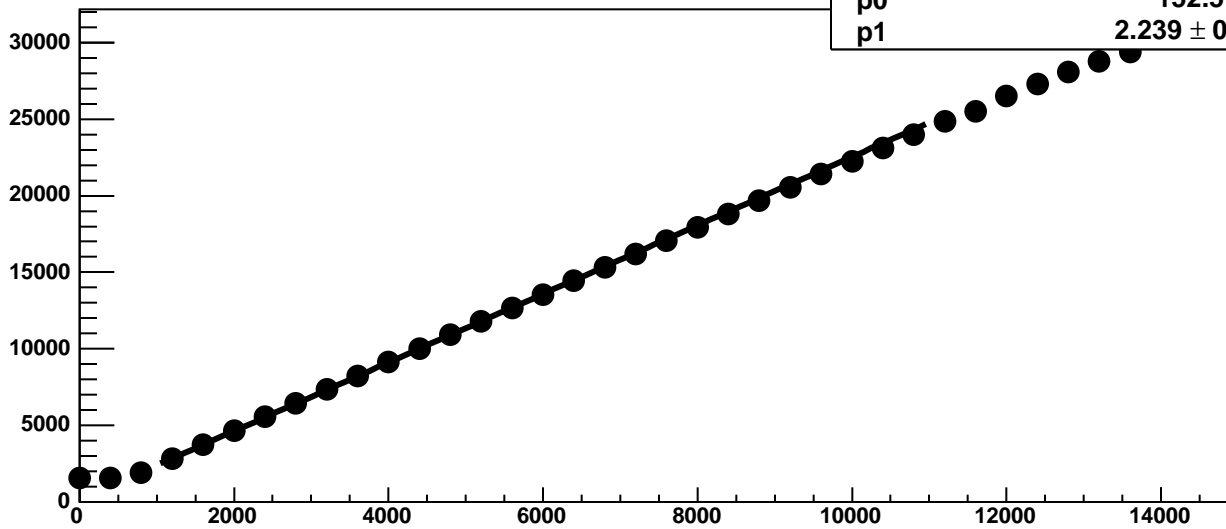


Chip 8, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

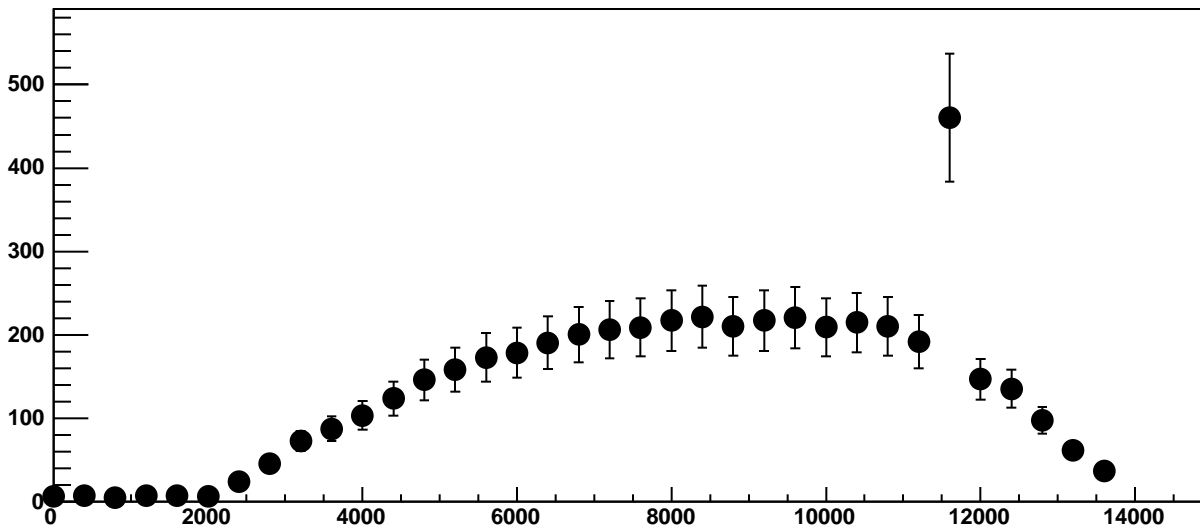


Chip 8, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC

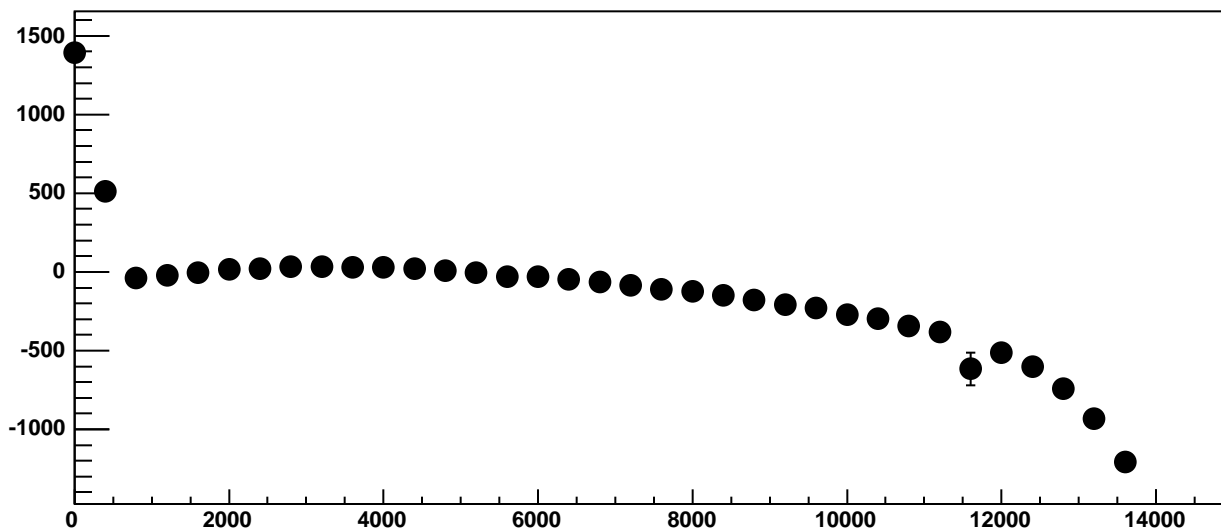
χ^2 / ndf 486.2 / 23
p0 152.5 ± 2.666
p1 2.239 ± 0.001451



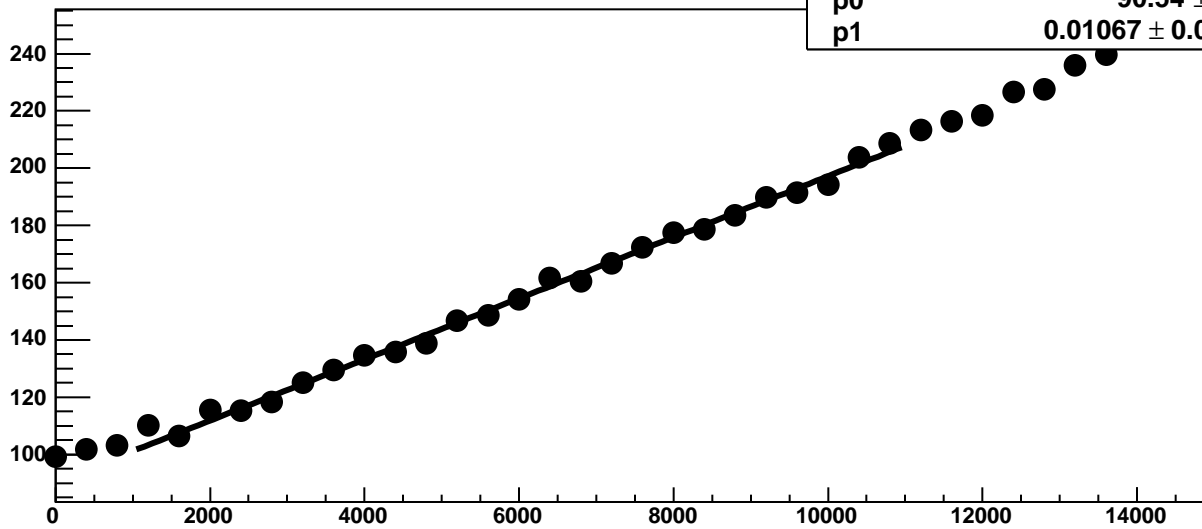
Chip 8, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC

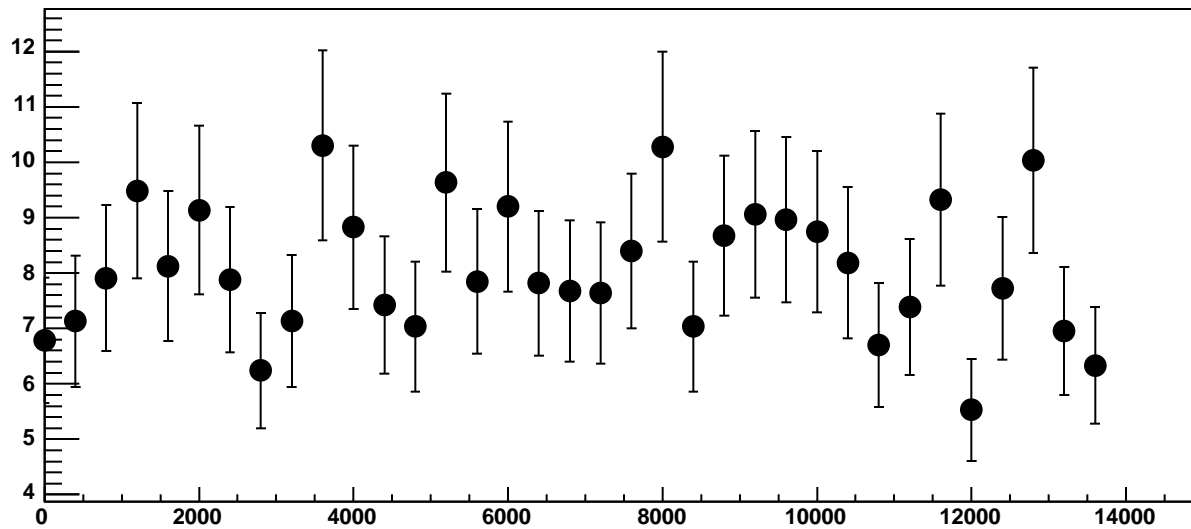


Chip 8, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

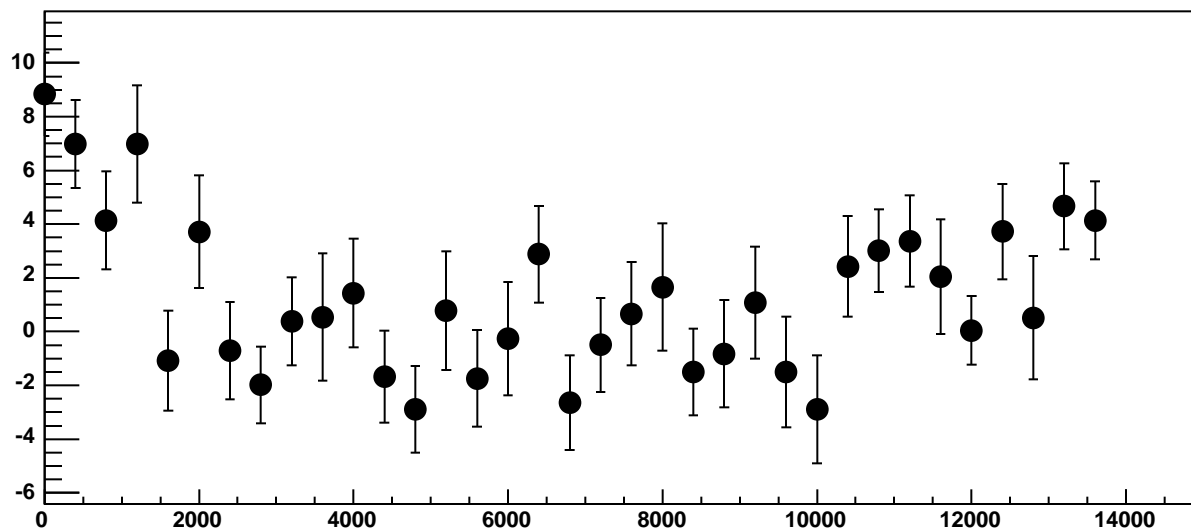


χ^2 / ndf 36.63 / 23
p0 90.54 ± 0.8549
p1 0.01067 ± 0.0001285

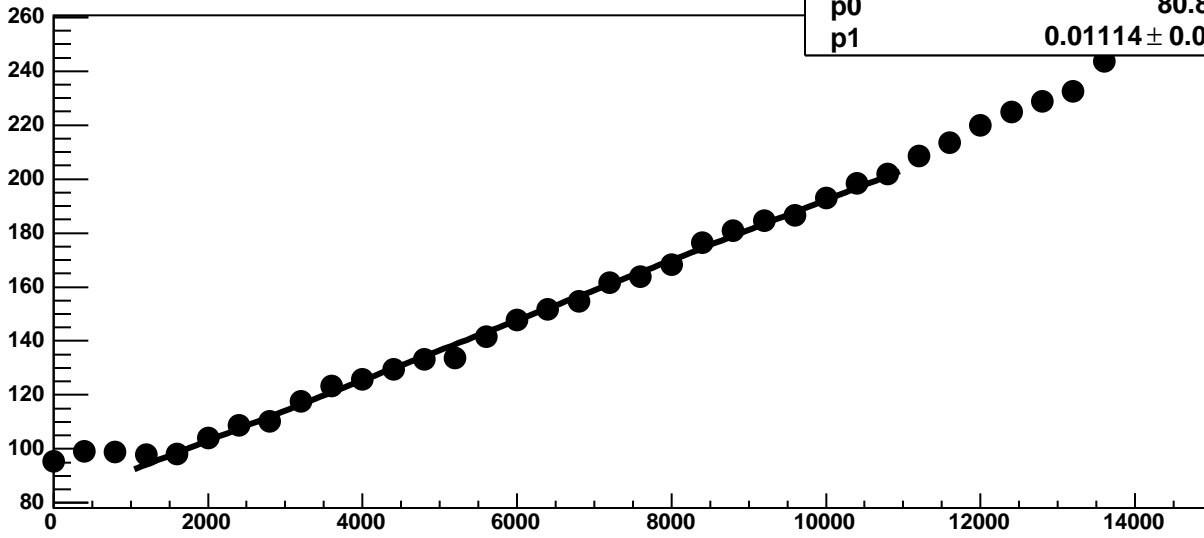
Chip 8, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

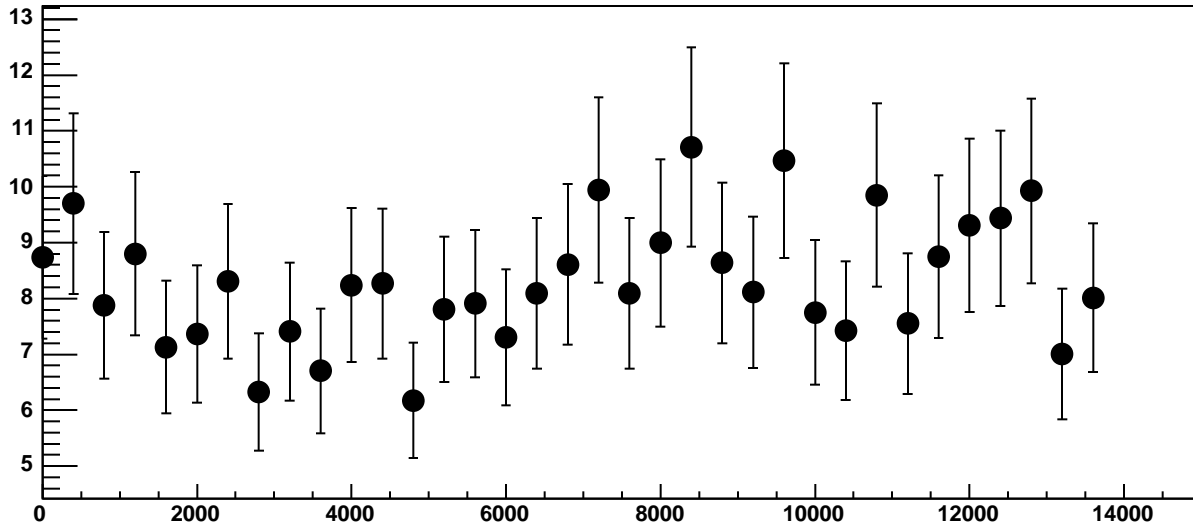


Chip 8, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

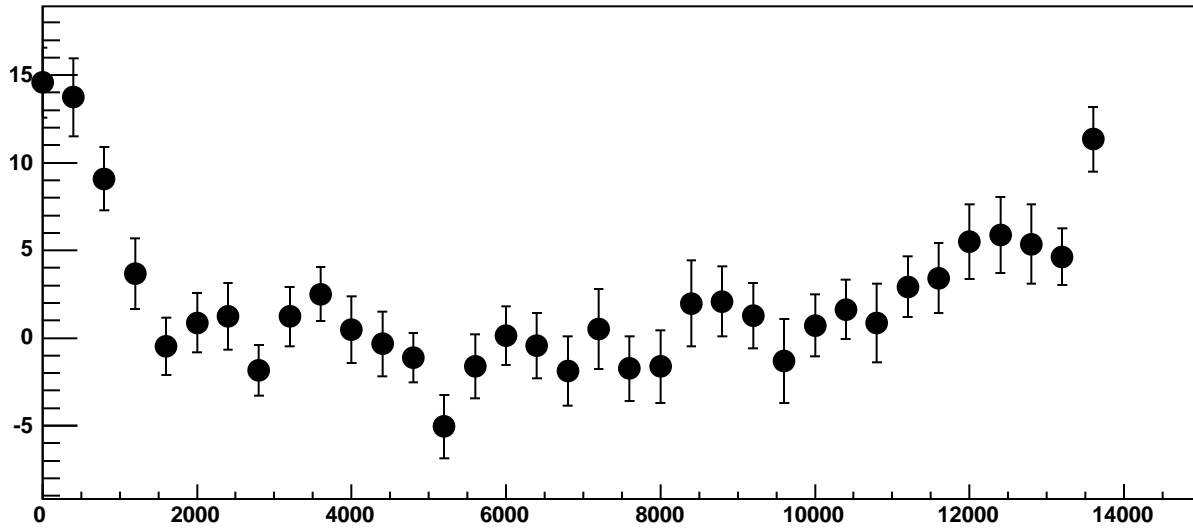


χ^2 / ndf 24.63 / 23
p0 80.8 ± 0.81
p1 0.01114 ± 0.0001287

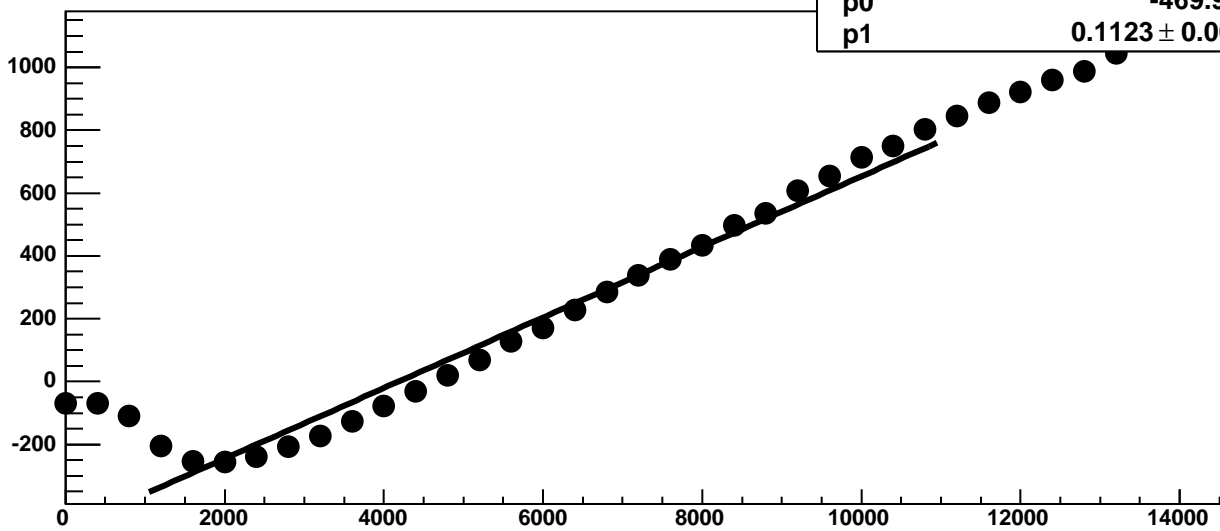
Chip 8, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



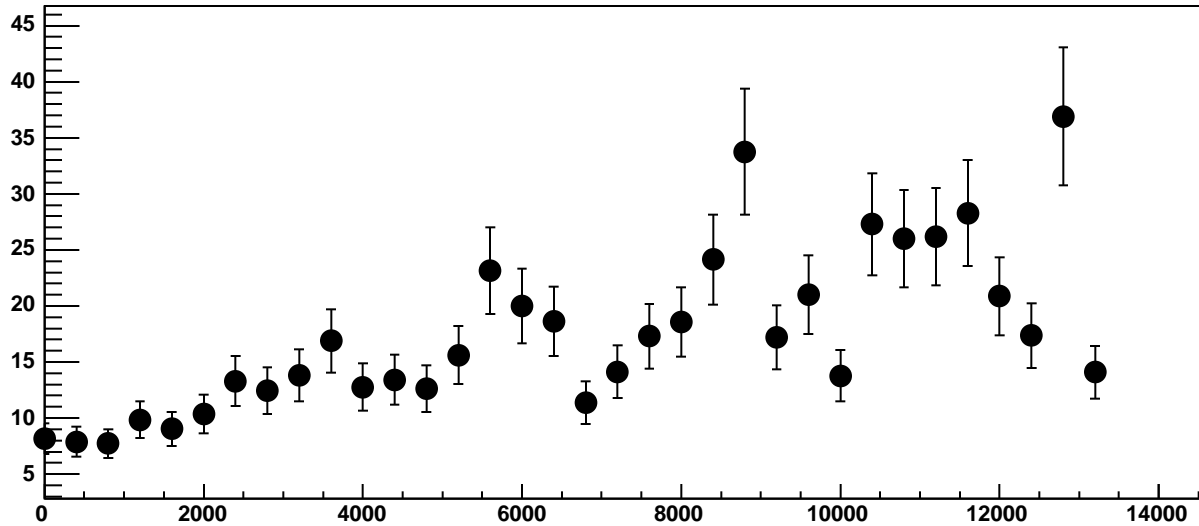
Chip 8, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



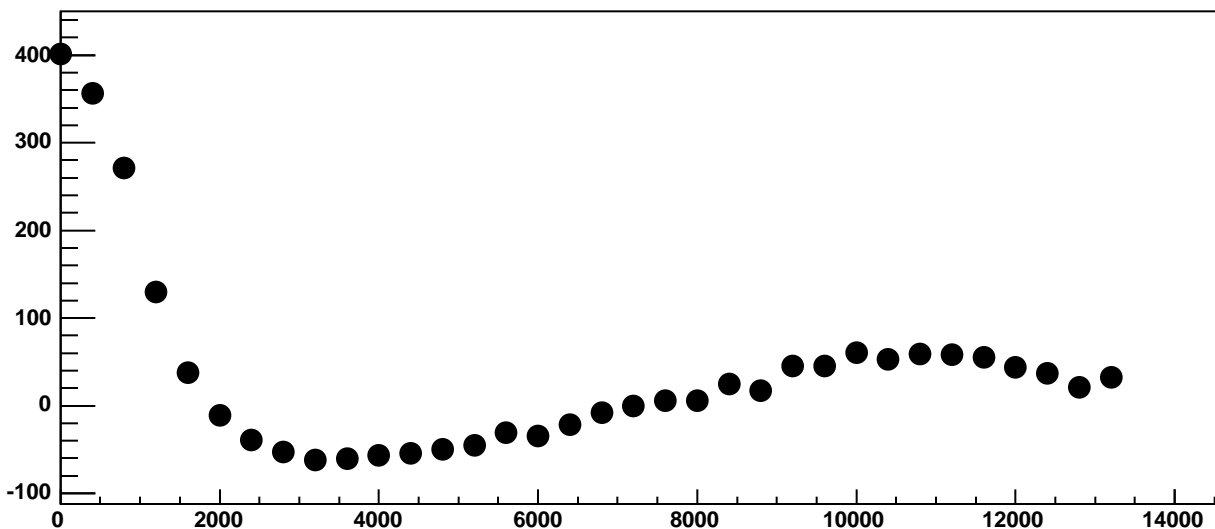
Chip 8, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC



Chip 8, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC

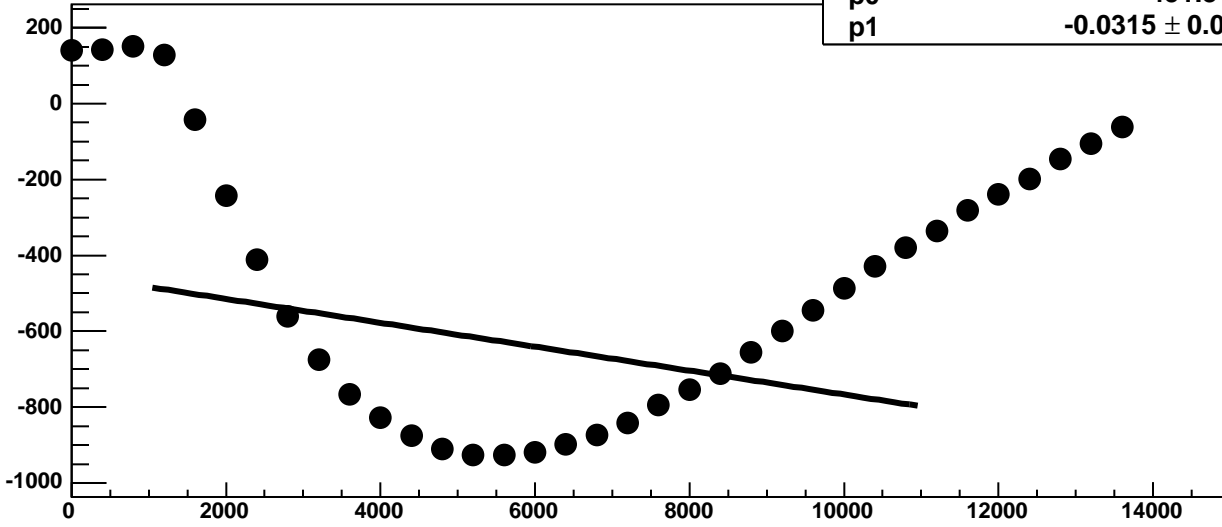


Chip 8, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC

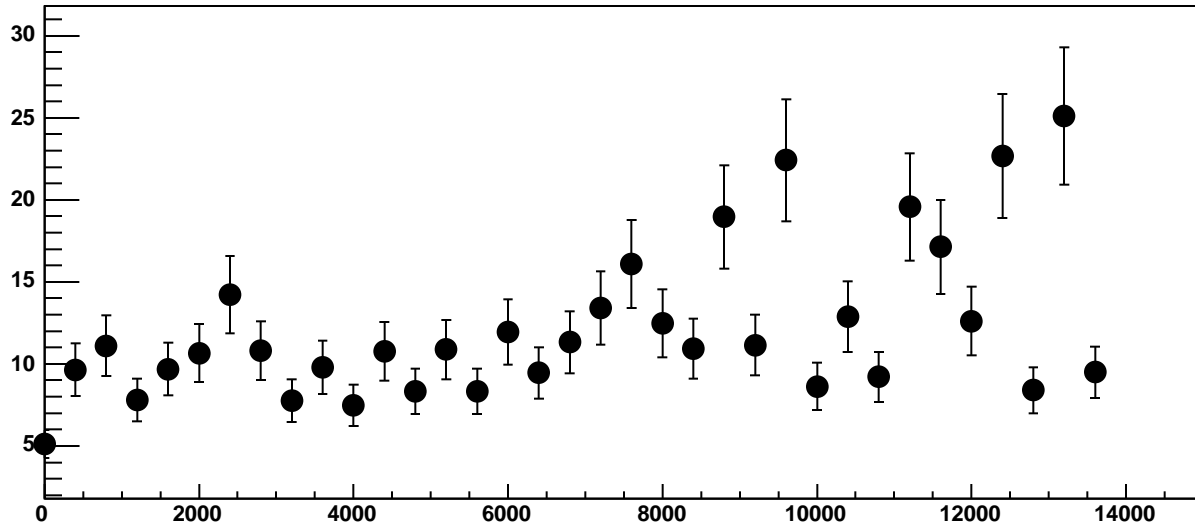


Chip 8, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

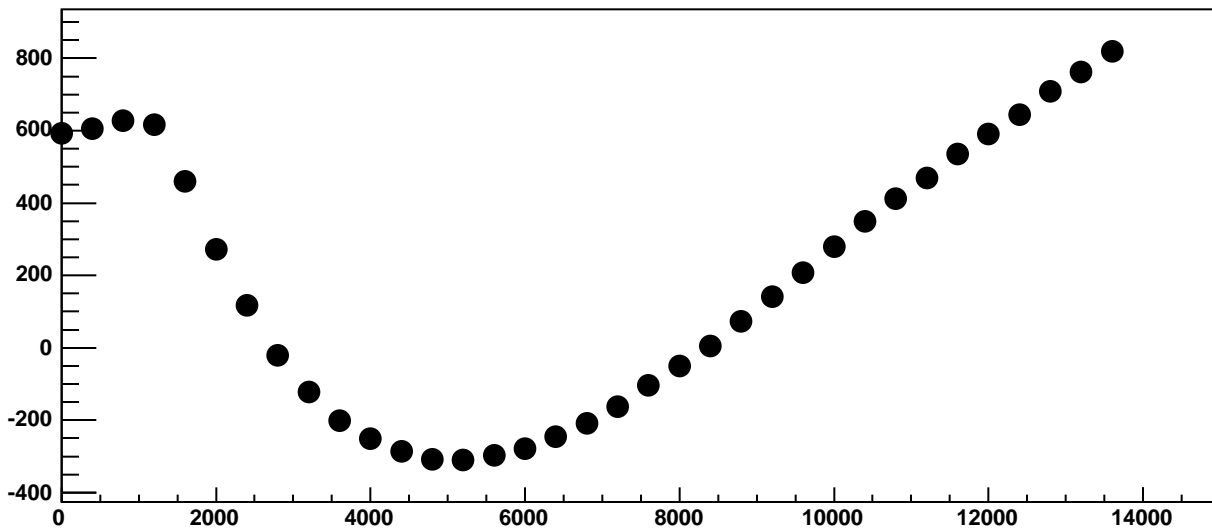
χ^2 / ndf 3.995e+05 / 23
p0 -451.8 ± 1.015
p1 -0.0315 ± 0.0001639



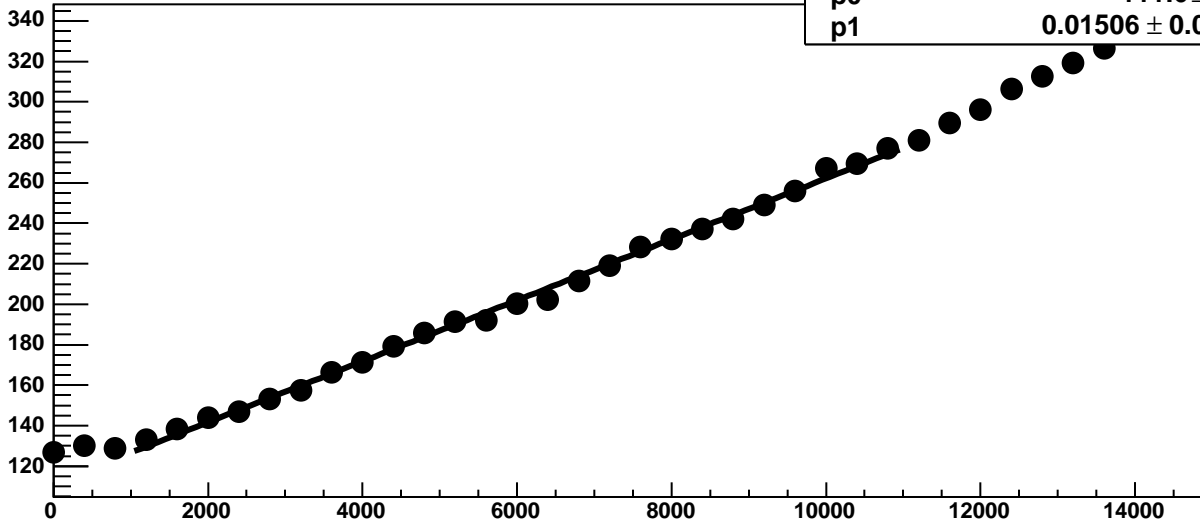
Chip 8, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

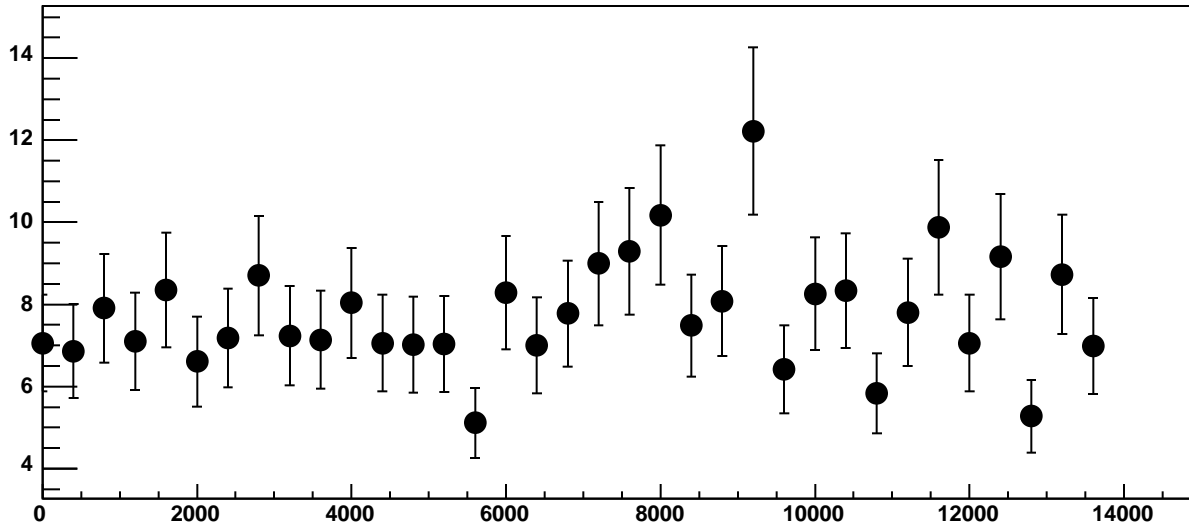


Chip 8, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

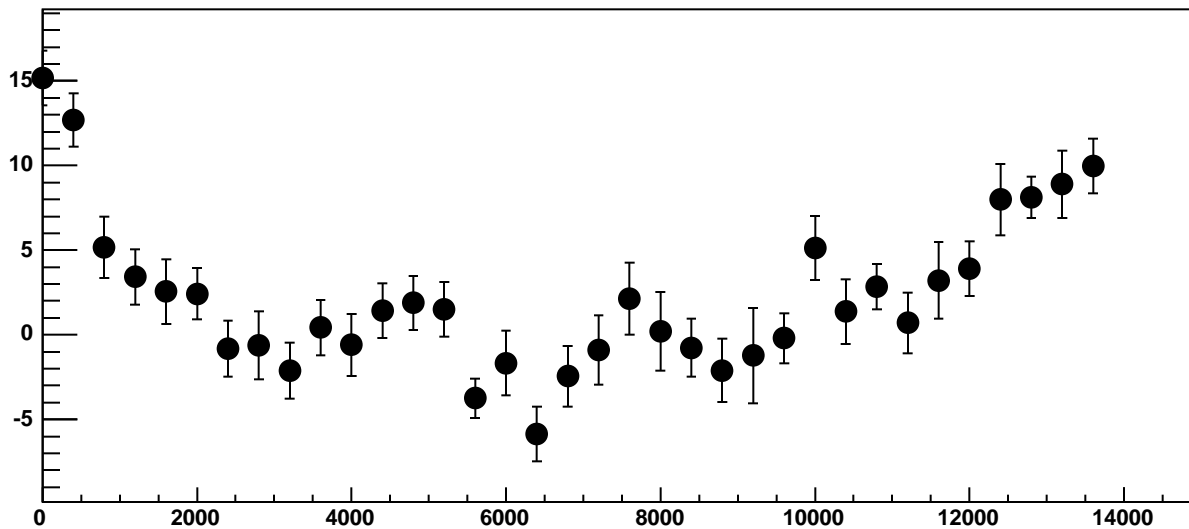


χ^2 / ndf 55.2 / 23
p0 111.6 ± 0.7727
p1 0.01506 ± 0.0001177

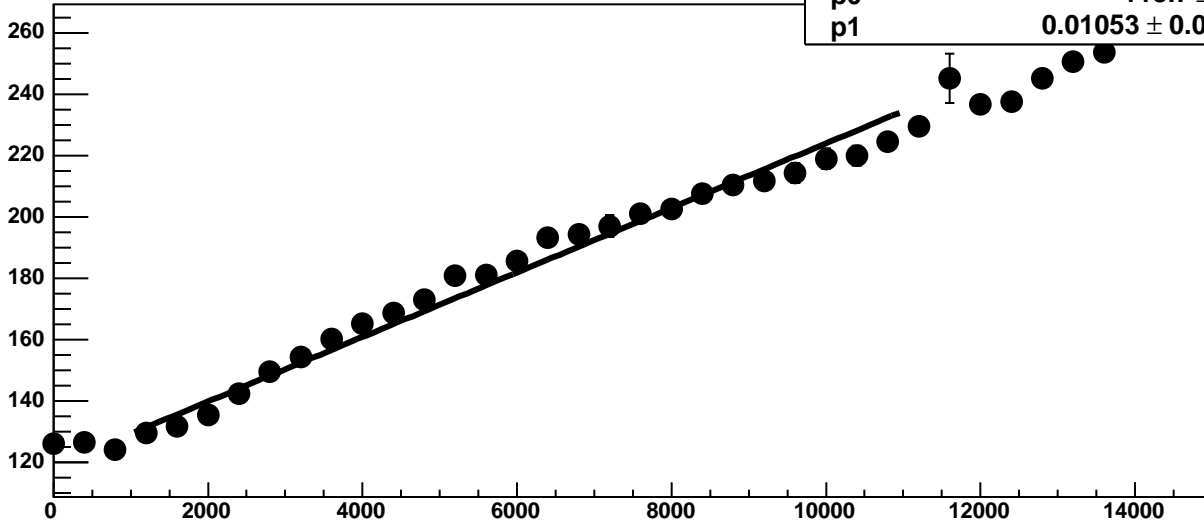
Chip 8, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



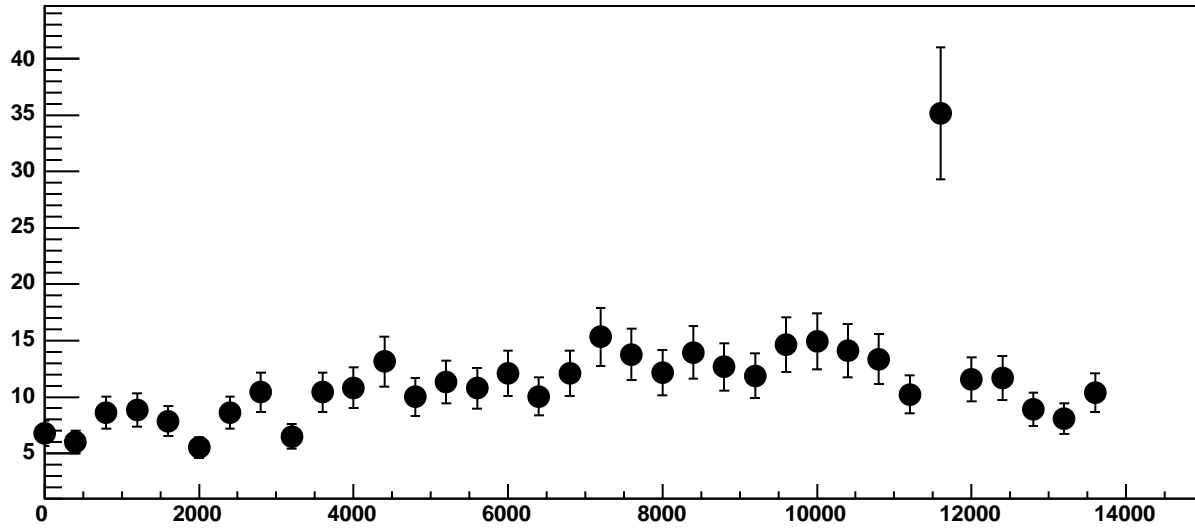
Chip 8, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



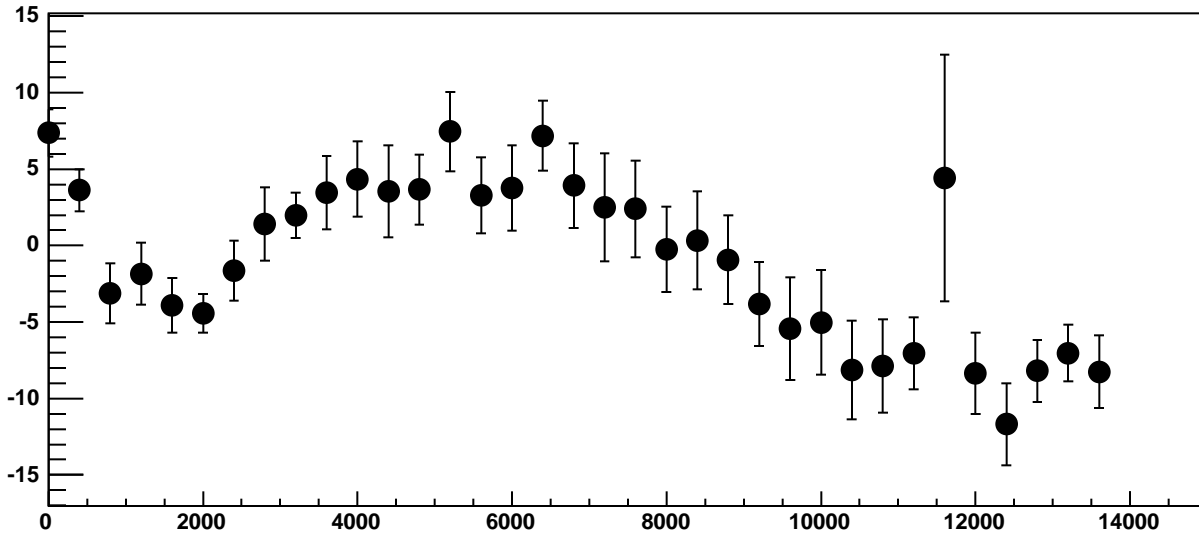
Chip 8, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC



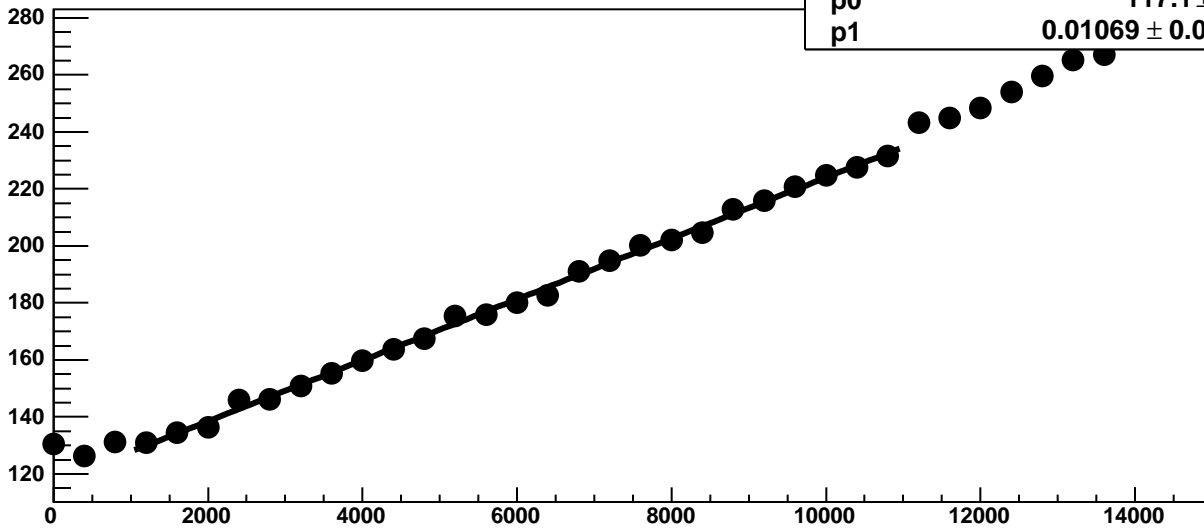
Chip 8, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

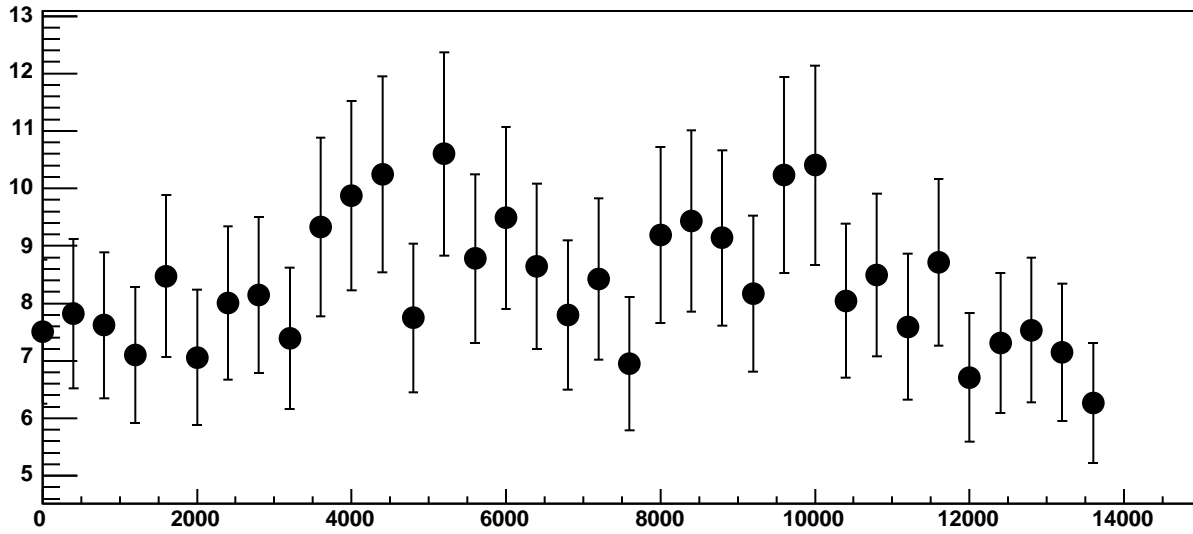


Chip 8, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

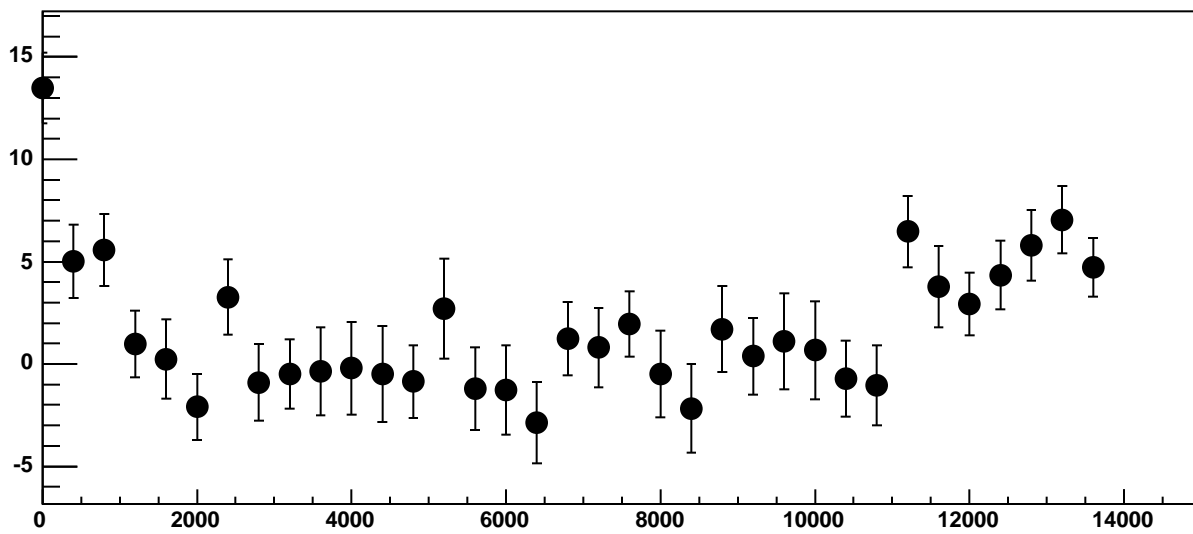


χ^2 / ndf 14.5 / 23
p0 117.1 ± 0.8541
p1 0.01069 ± 0.0001318

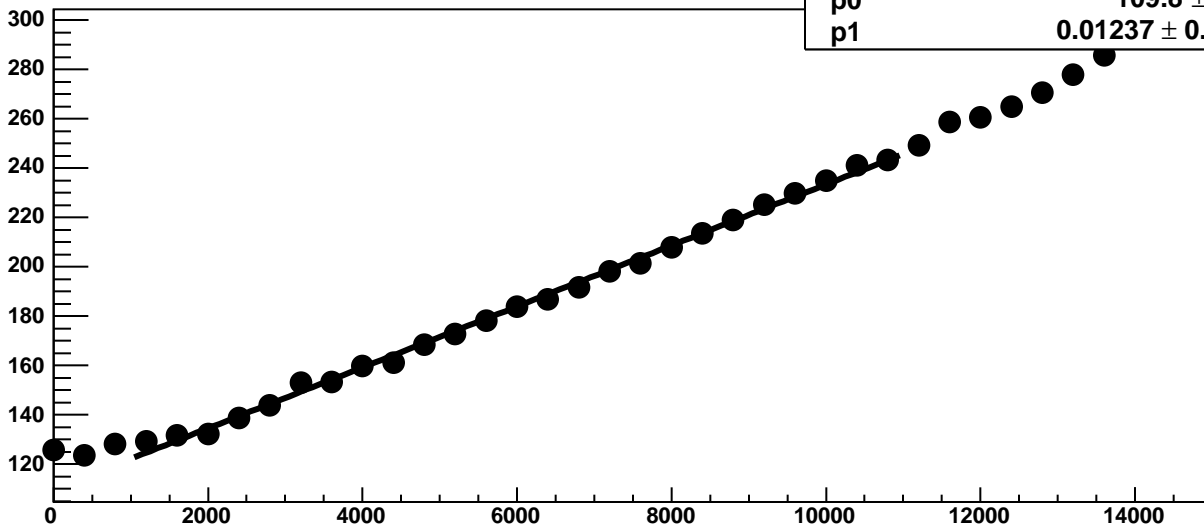
Chip 8, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

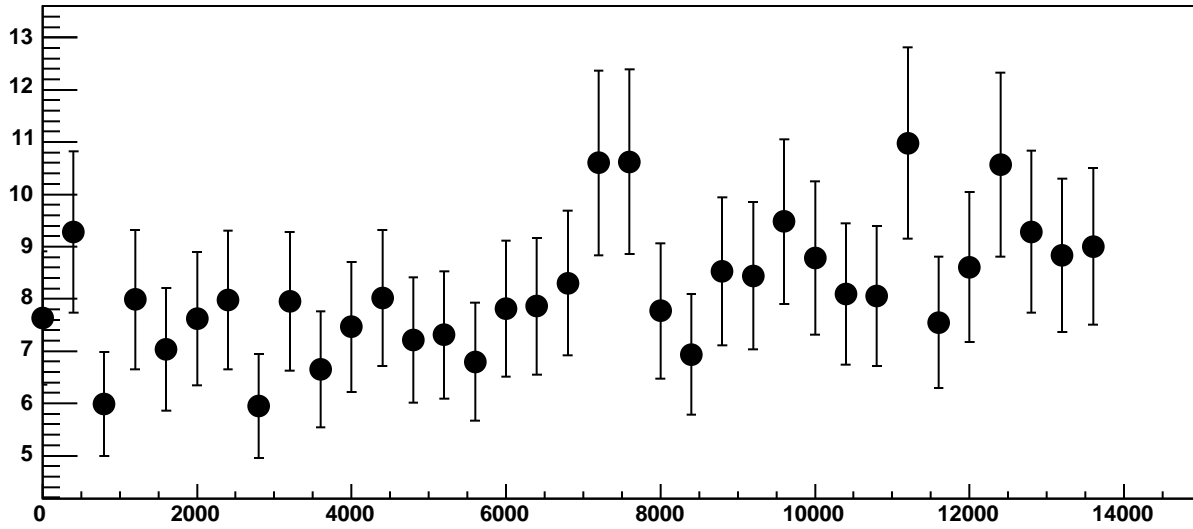


Chip 8, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

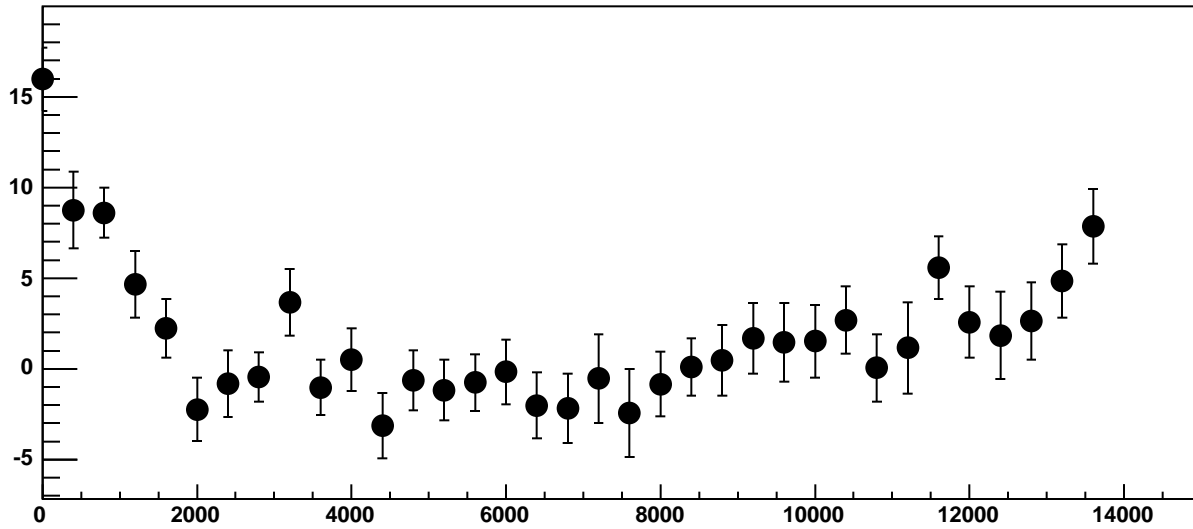


χ^2 / ndf 26.55 / 23
p0 109.8 ± 0.7913
p1 0.01237 ± 0.000125

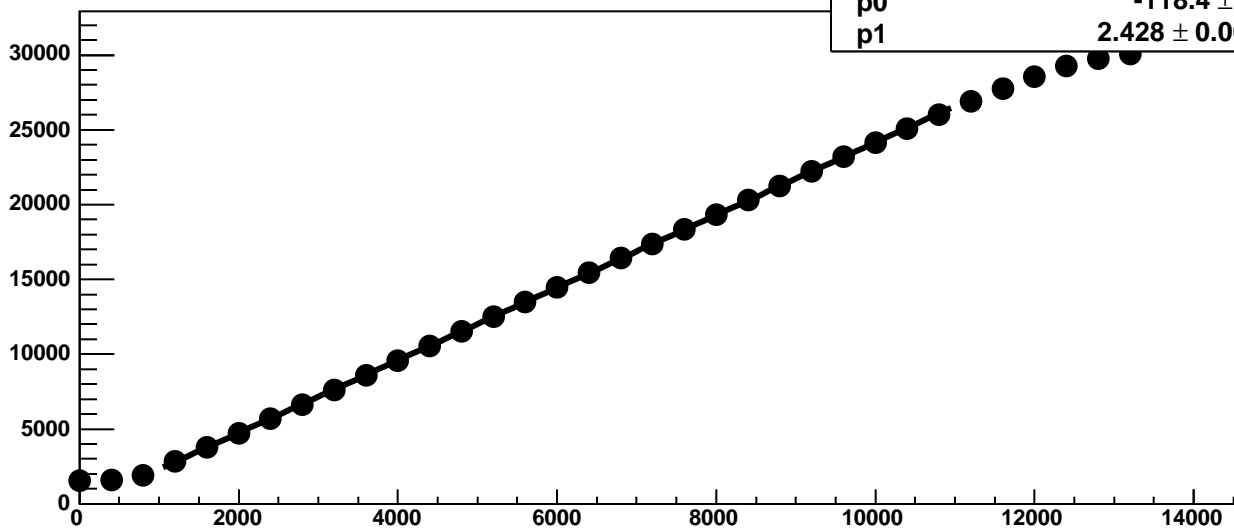
Chip 8, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



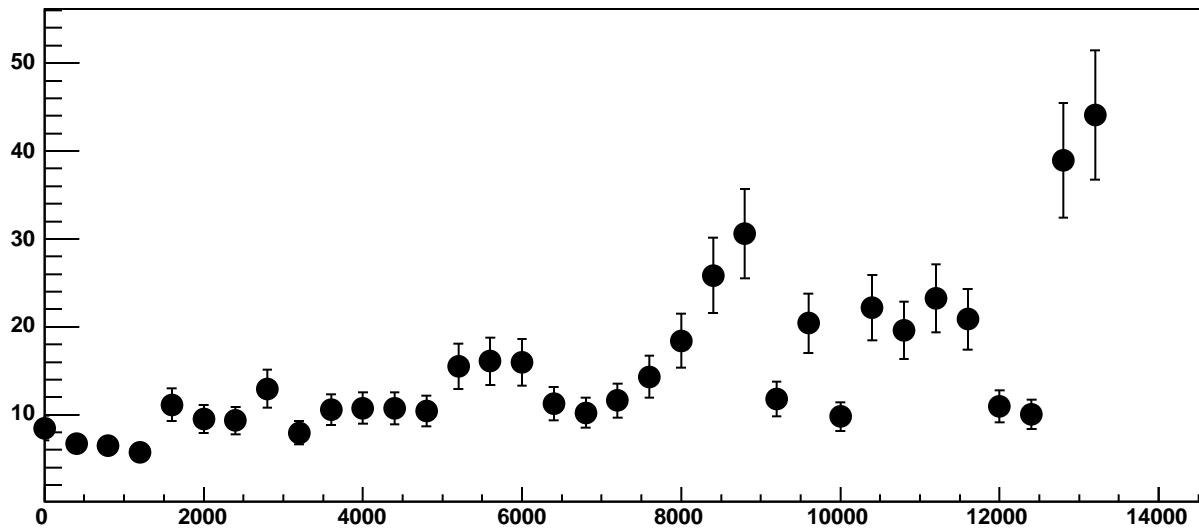
Chip 8, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



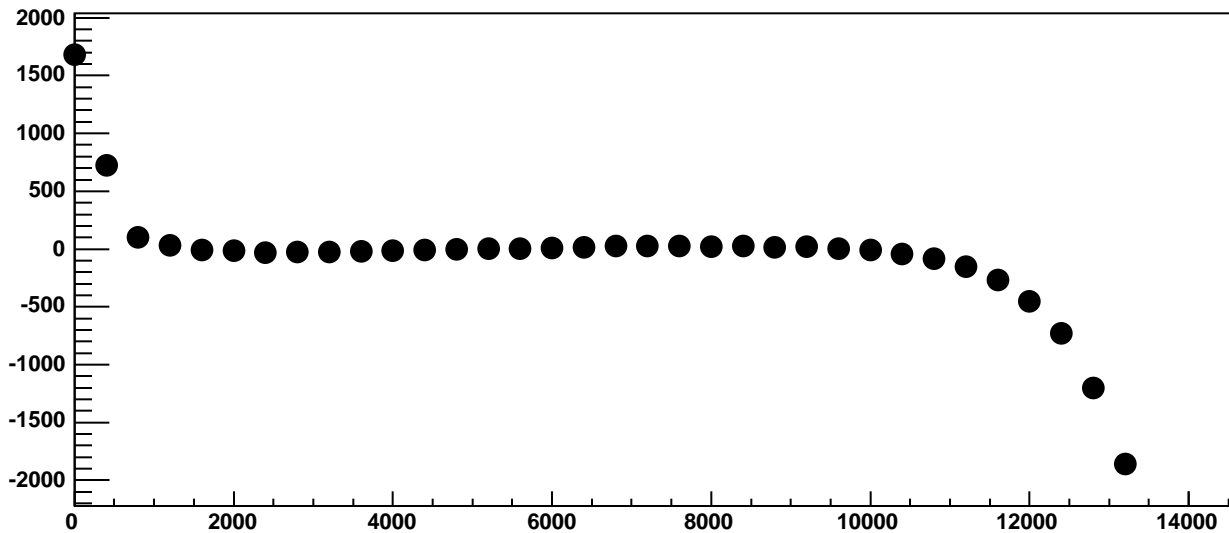
Chip 8, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC



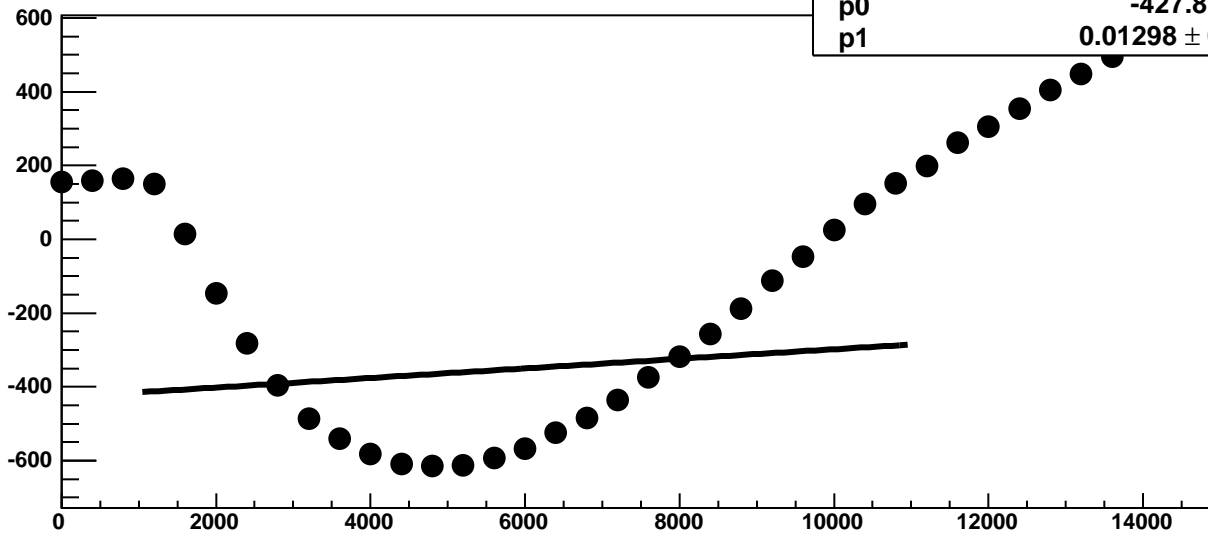
Chip 8, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC



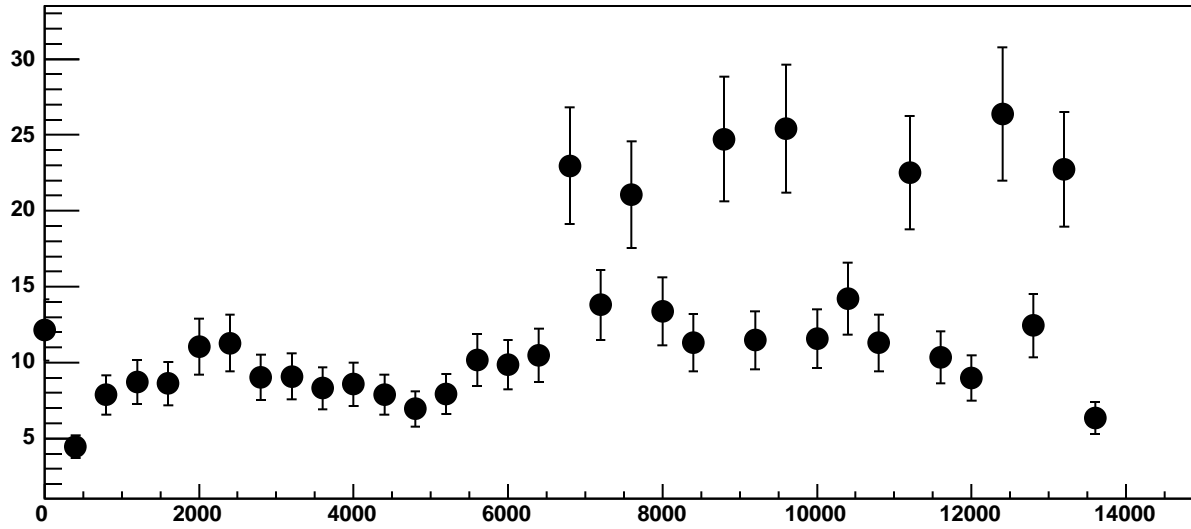
Chip 8, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC



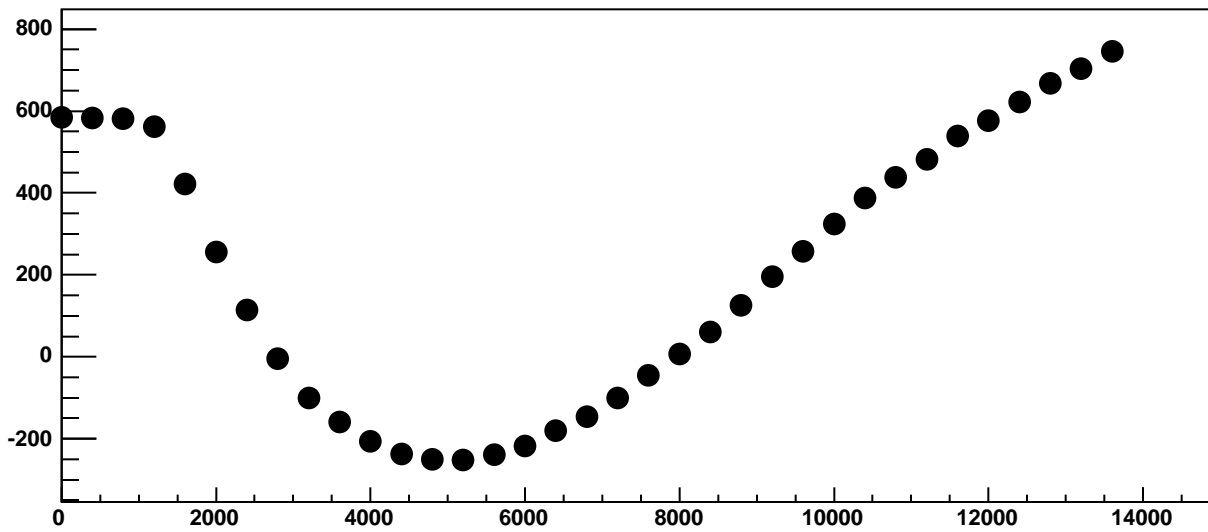
Chip 8, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



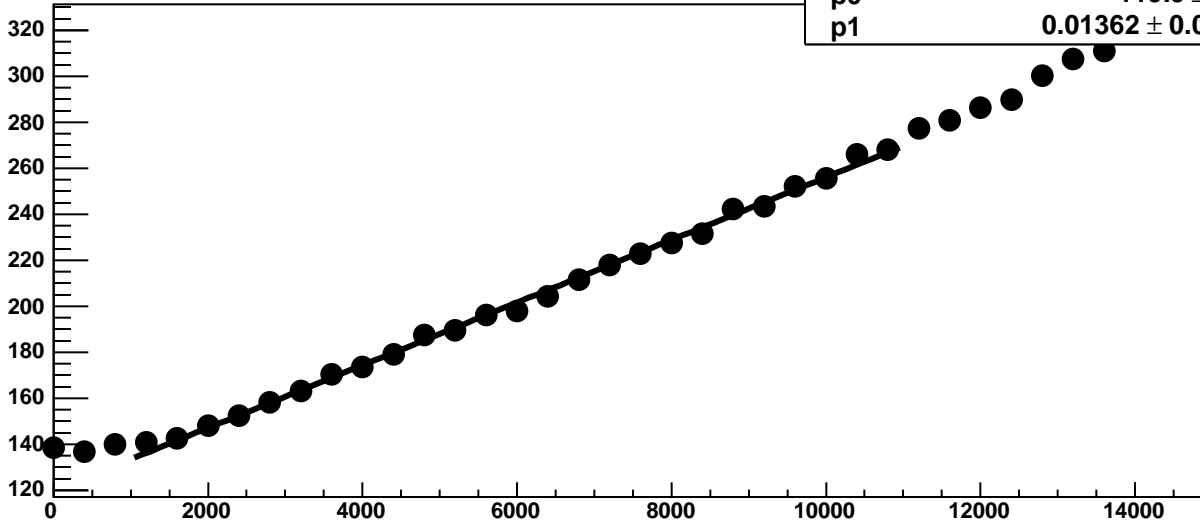
Chip 8, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

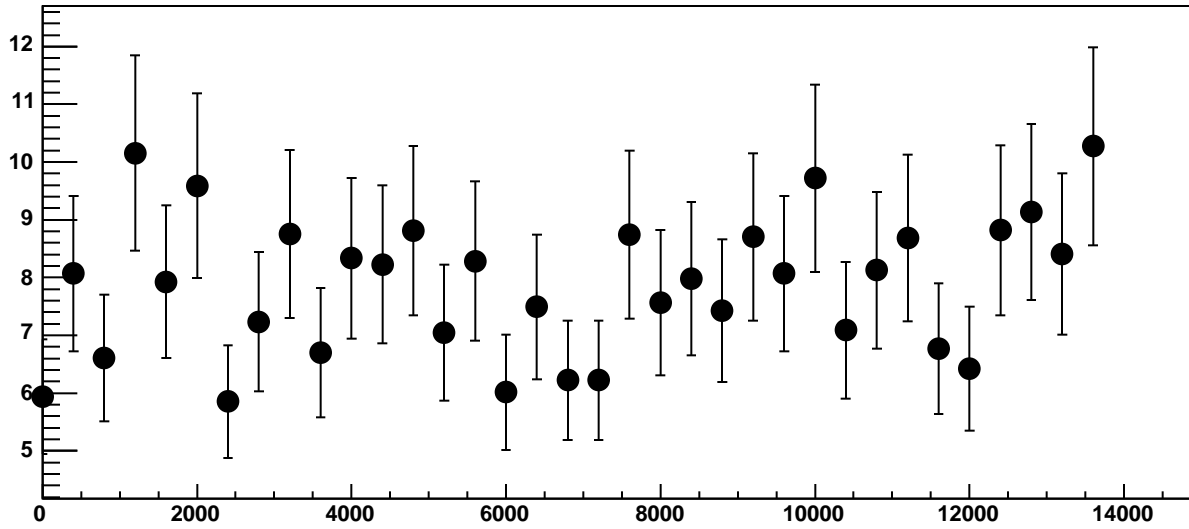


Chip 8, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

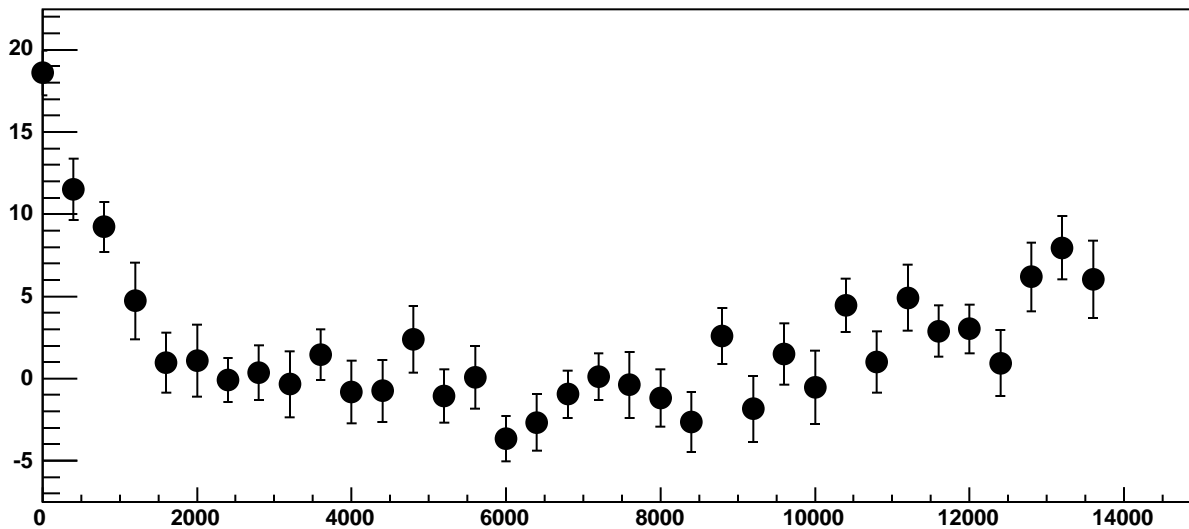


χ^2 / ndf 31.83 / 23
p0 119.9 ± 0.8367
p1 0.01362 ± 0.0001269

Chip 8, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

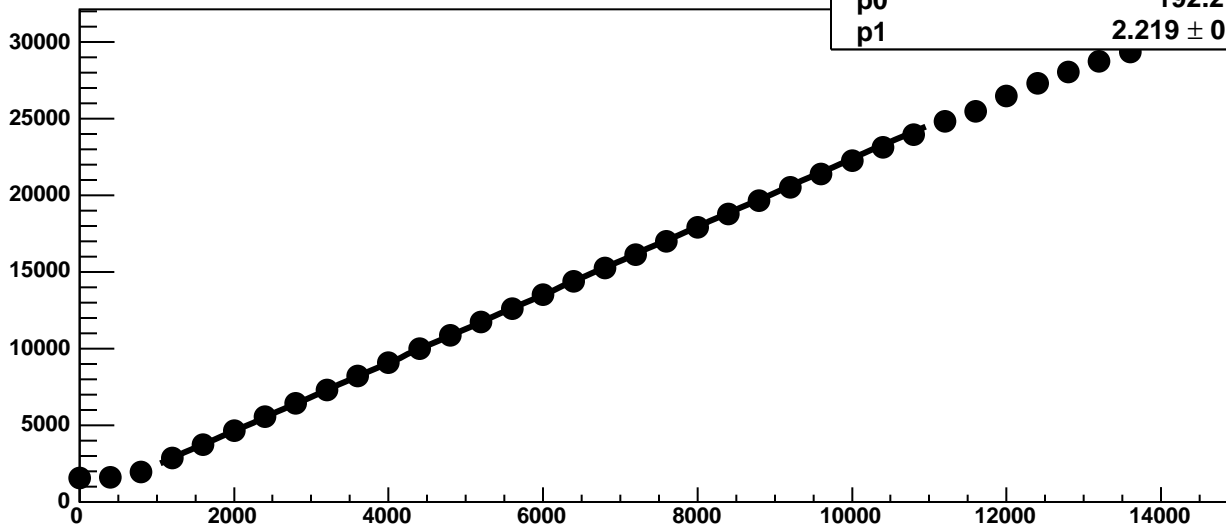


Chip 8, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

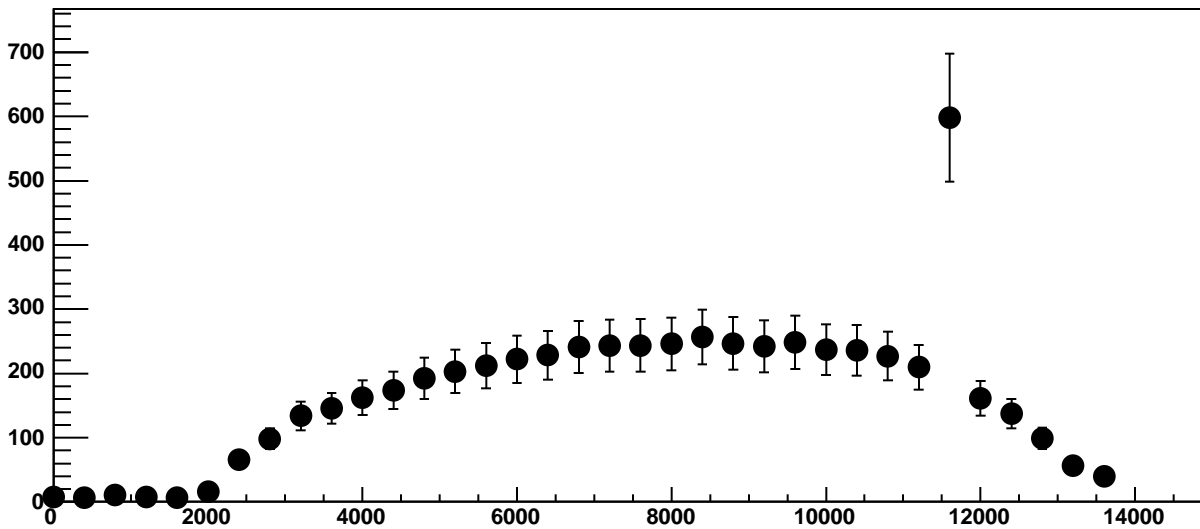


Chip 8, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC

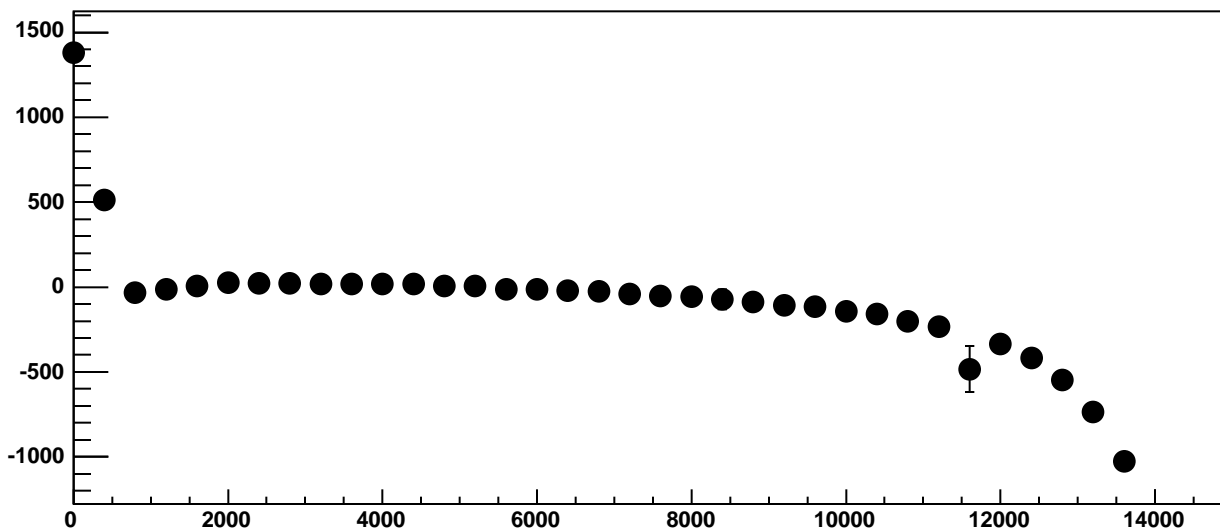
χ^2 / ndf 177.5 / 23
p0 192.2 ± 2.942
p1 2.219 ± 0.001795



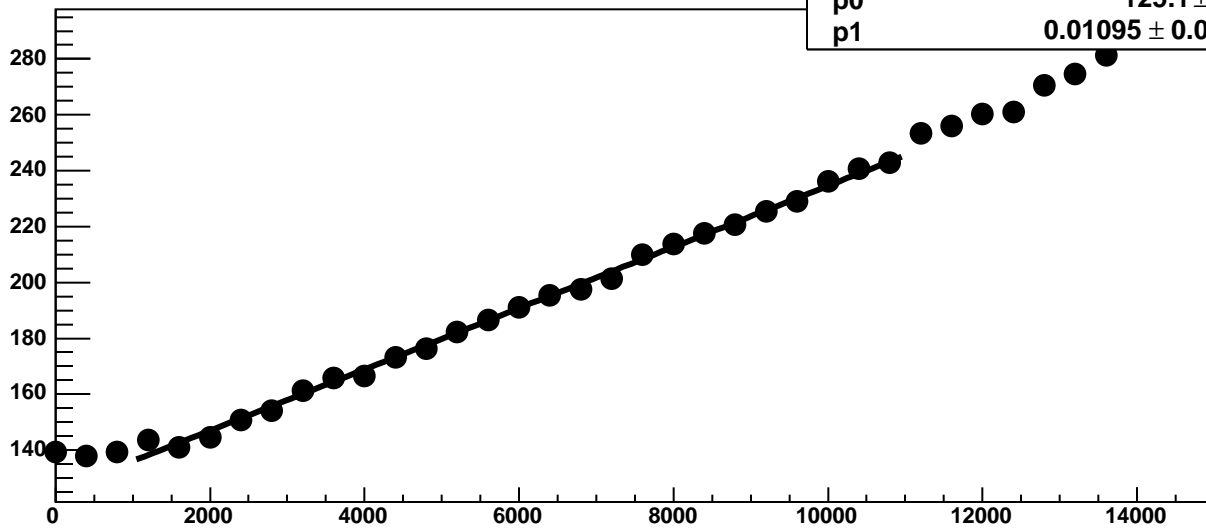
Chip 8, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC

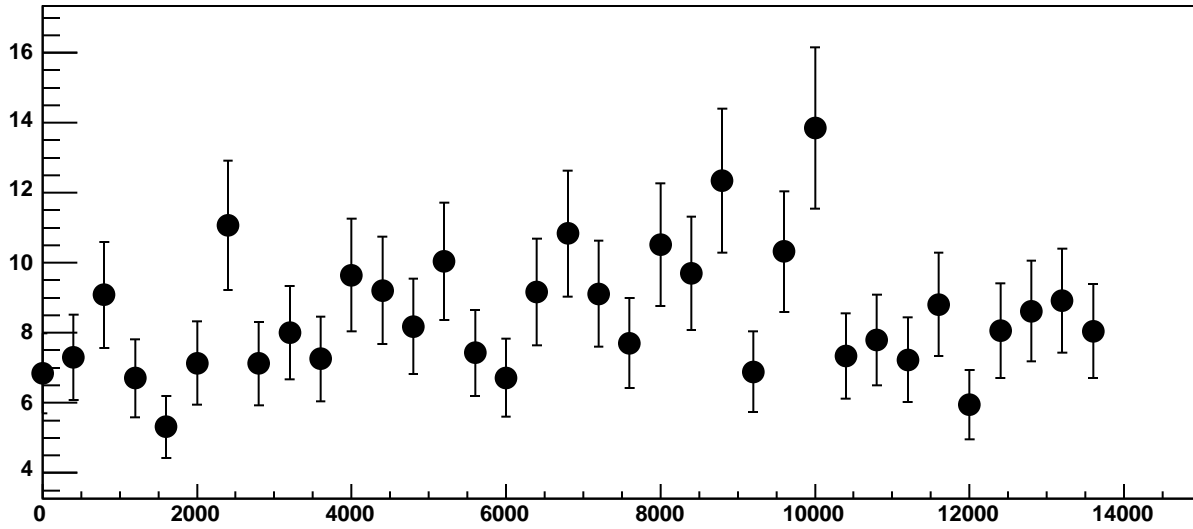


Chip 8, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

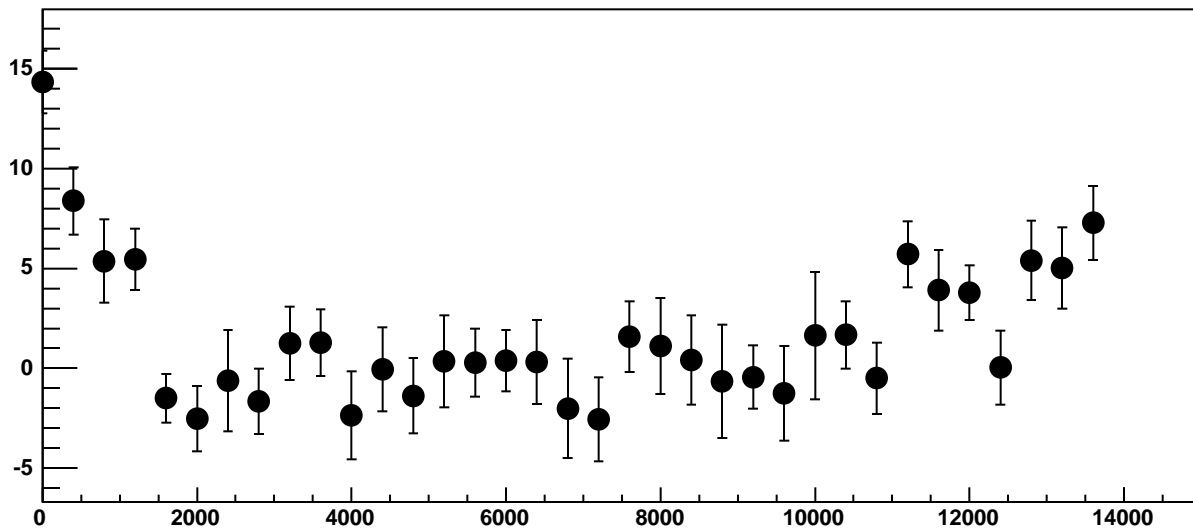


χ^2 / ndf 25.35 / 23
p0 125.1 ± 0.7746
p1 0.01095 ± 0.0001239

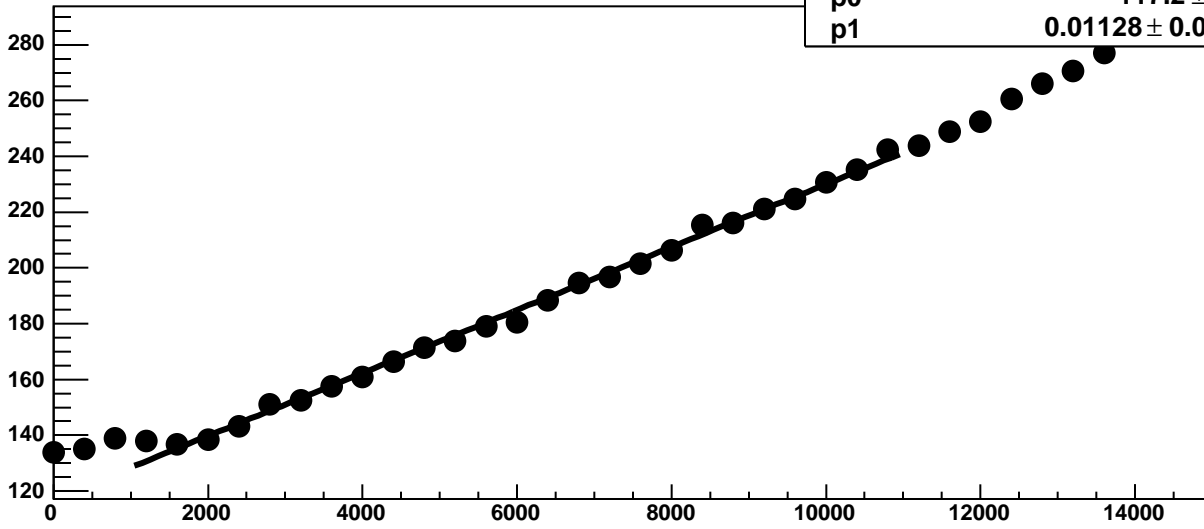
Chip 8, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

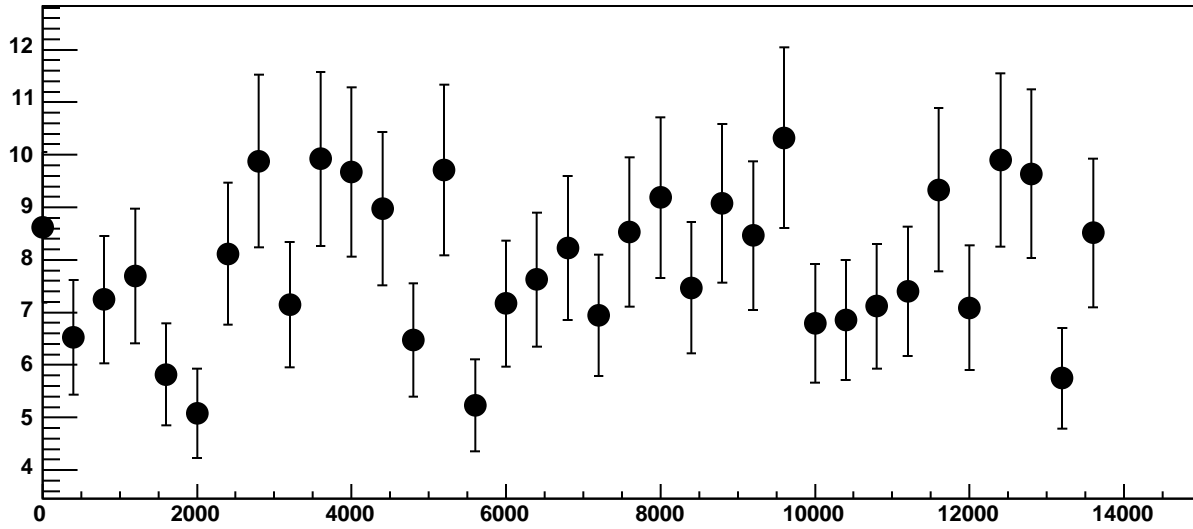


Chip 8, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

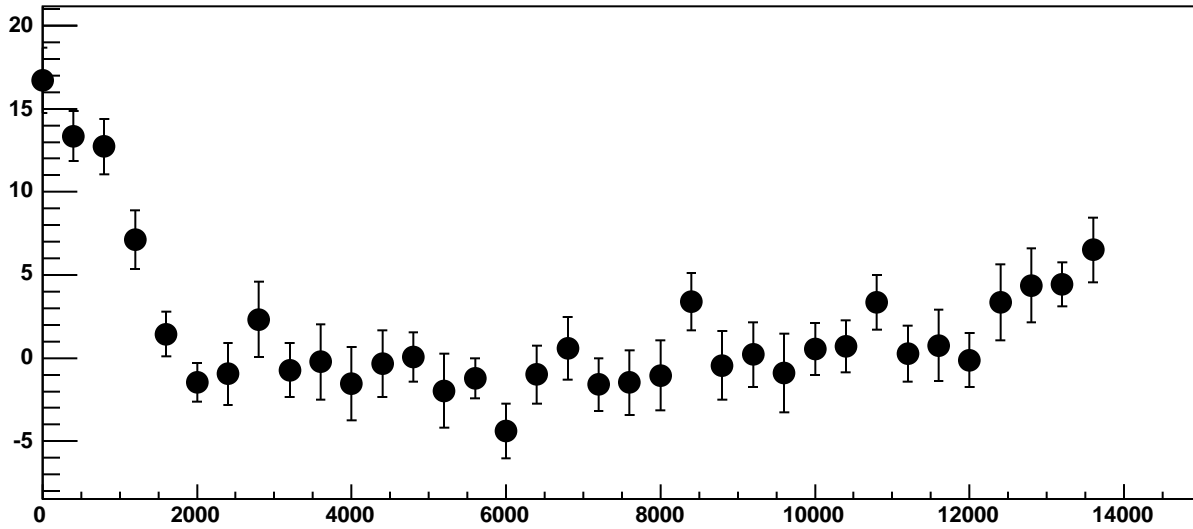


χ^2 / ndf 40.76 / 23
p0 117.2 ± 0.7448
p1 0.01128 ± 0.0001148

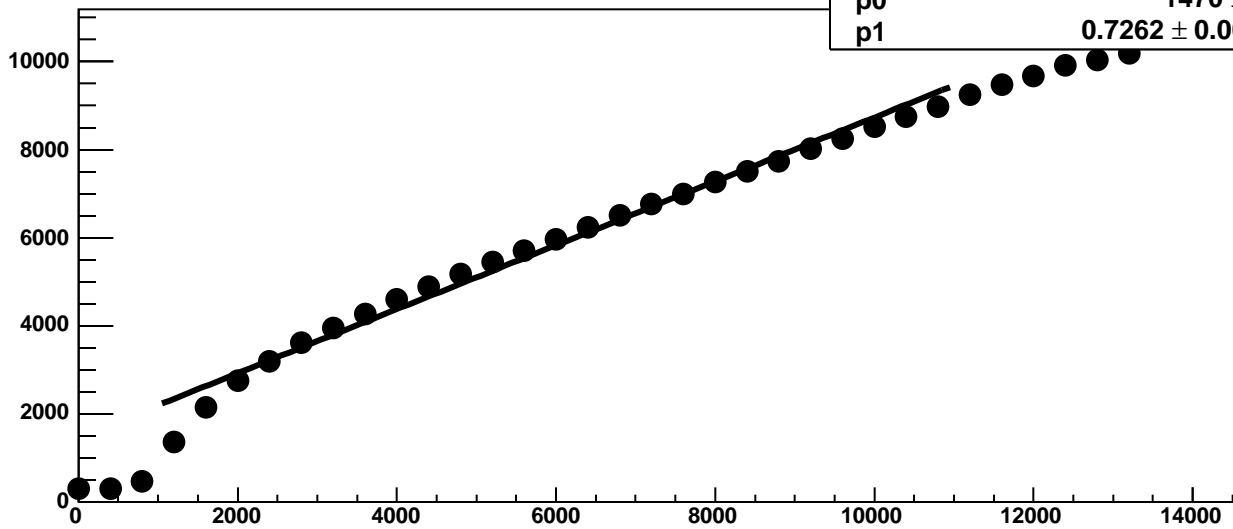
Chip 8, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

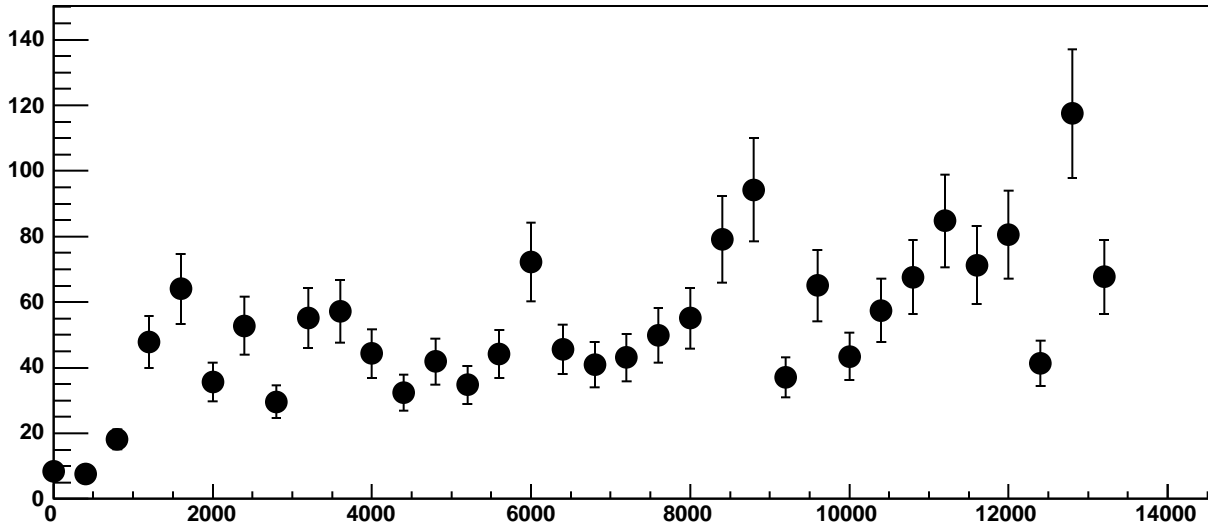


Chip 8, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC

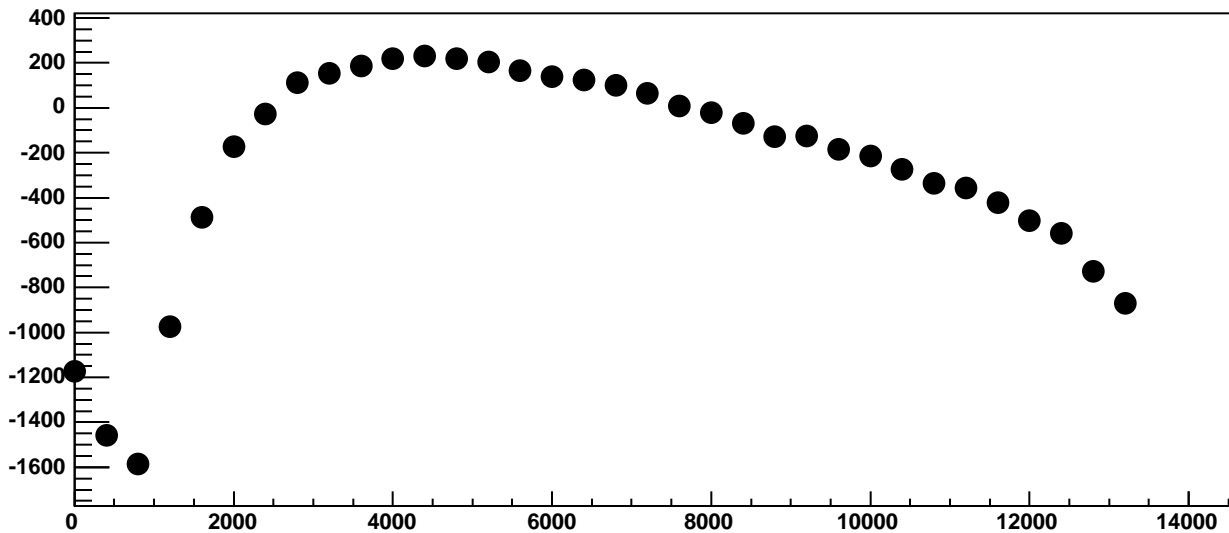


χ^2 / ndf 1.507e+04 / 23
p0 1470 ± 4.784
p1 0.7262 ± 0.0007818

Chip 8, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC

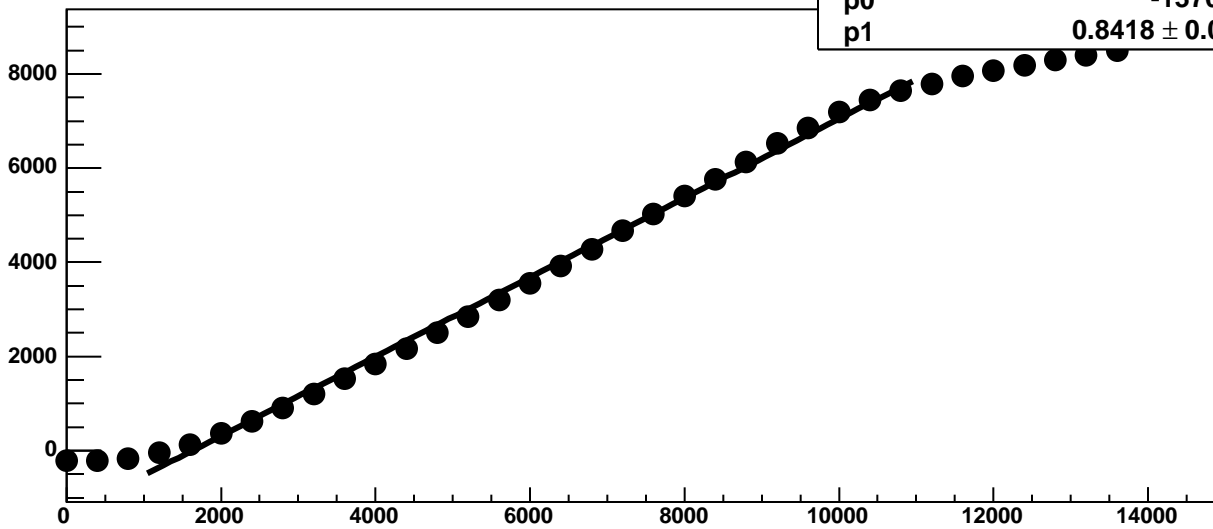


Chip 8, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC

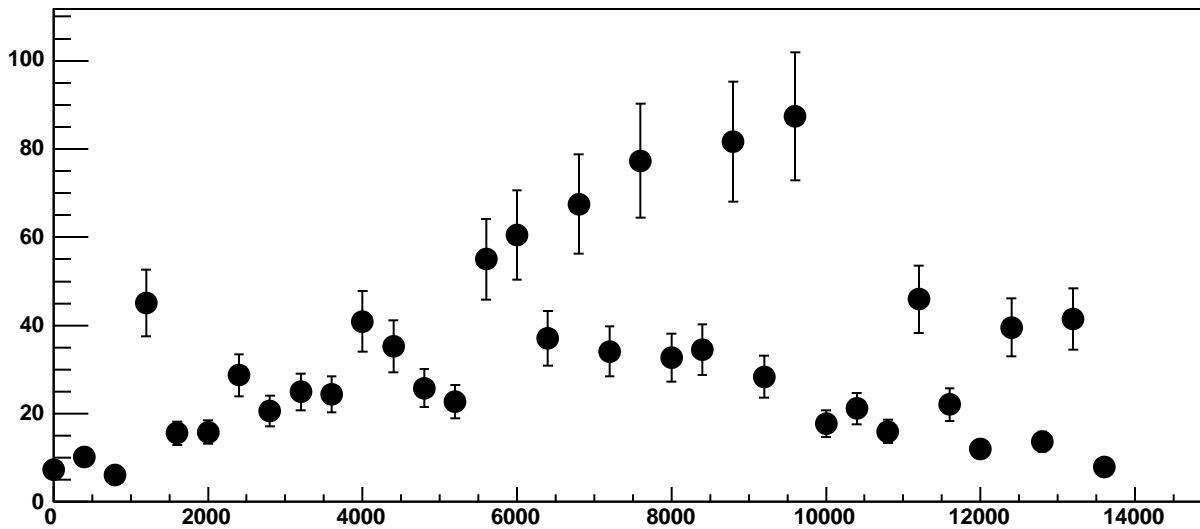


Chip 9, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

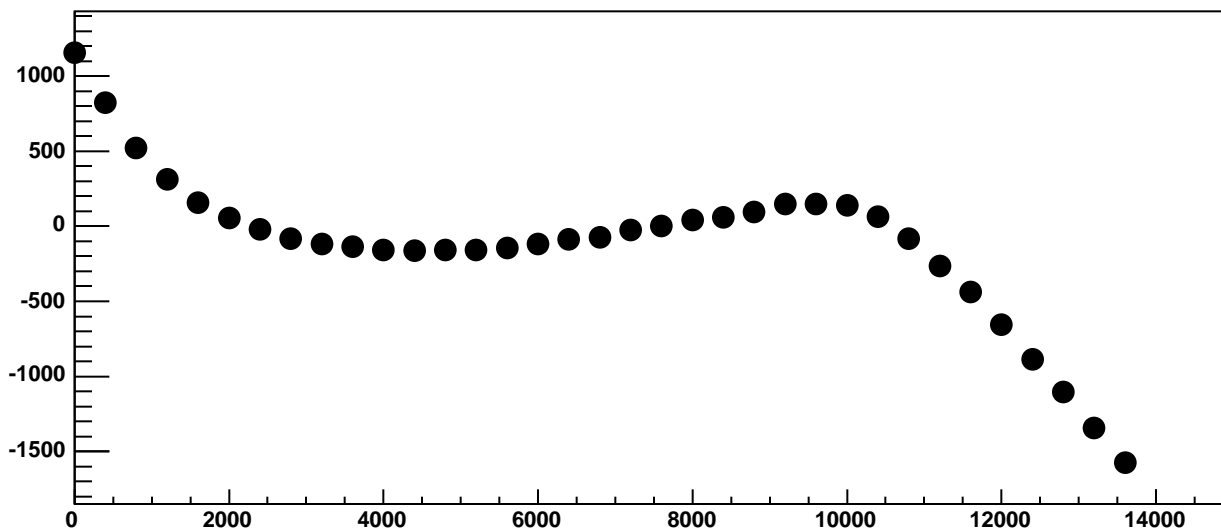
χ^2 / ndf 9604 / 23
p0 -1370 ± 2.33
p1 0.8418 ± 0.0003491



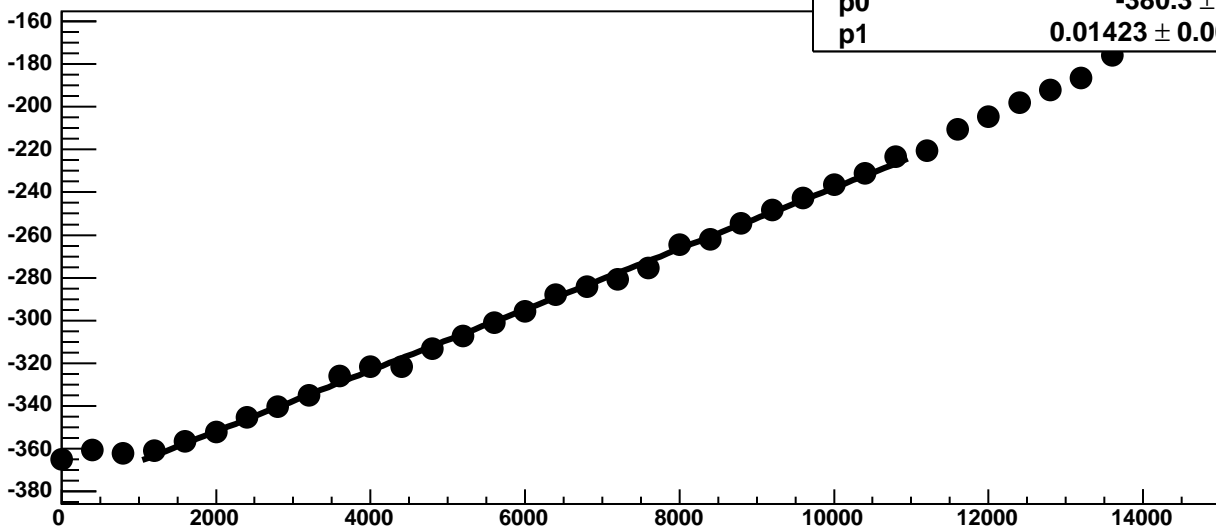
Chip 9, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

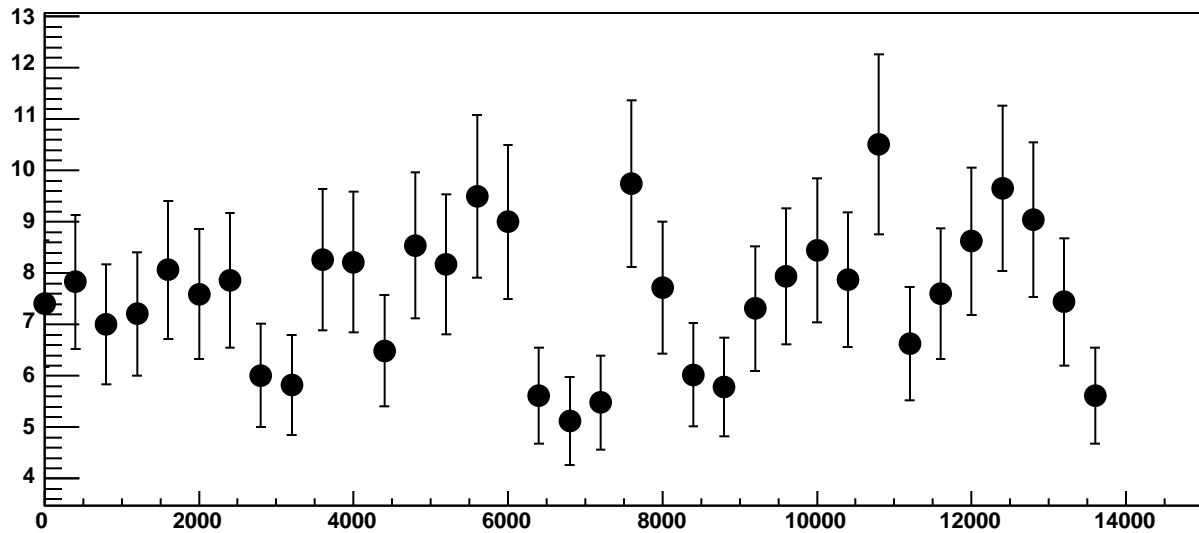


Chip 9, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

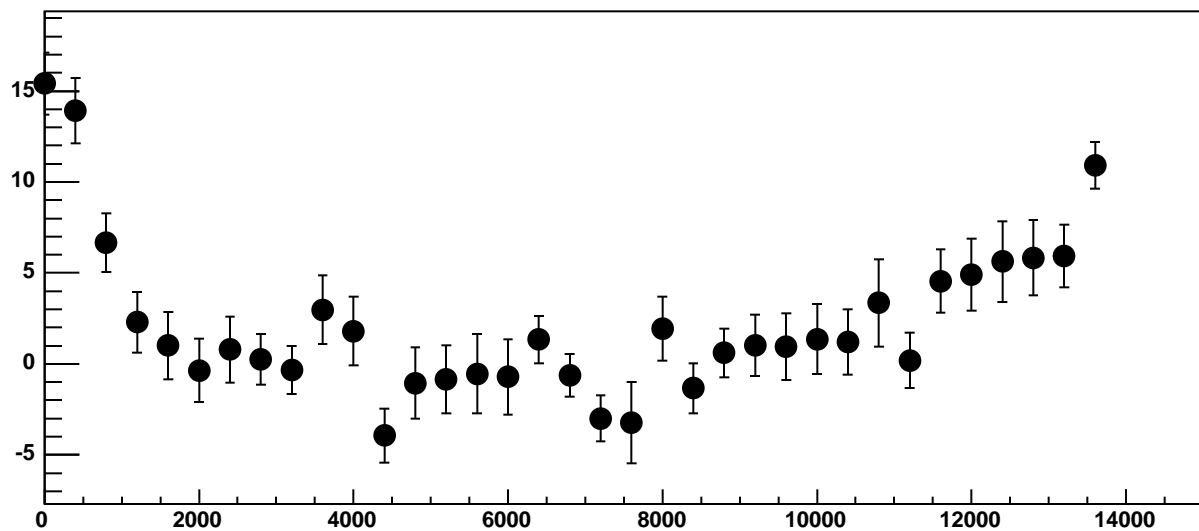


χ^2 / ndf 28.52 / 23
p0 -380.3 ± 0.7832
p1 0.01423 ± 0.0001196

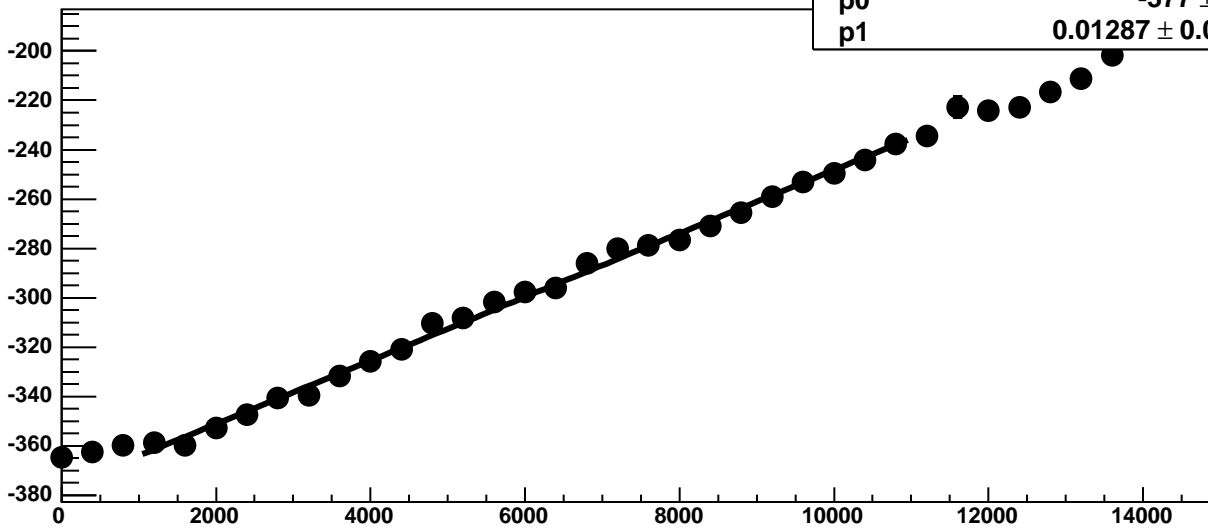
Chip 9, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



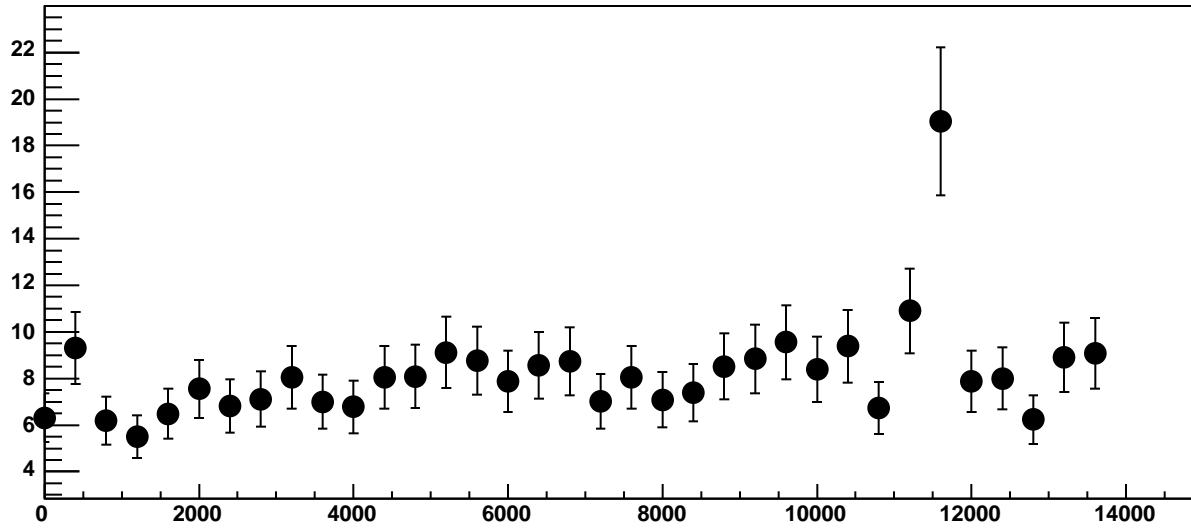
Chip 9, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



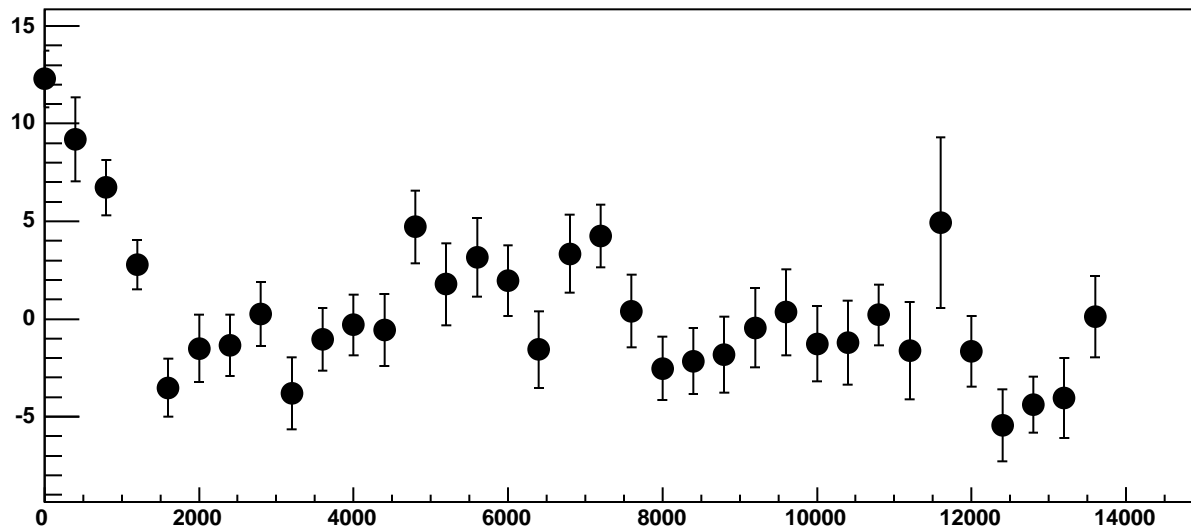
Chip 9, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



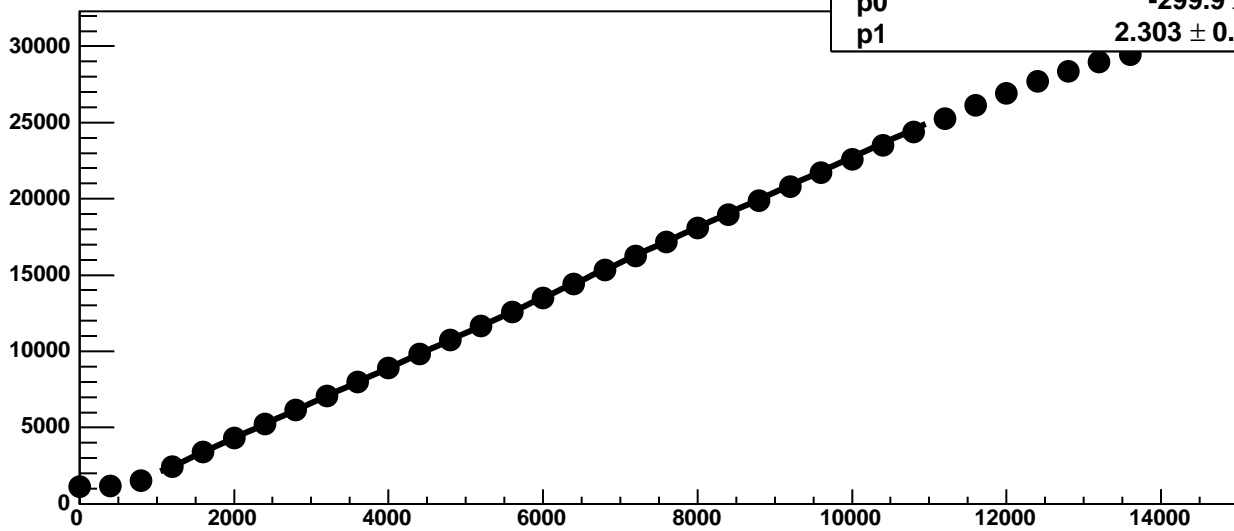
Chip 9, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



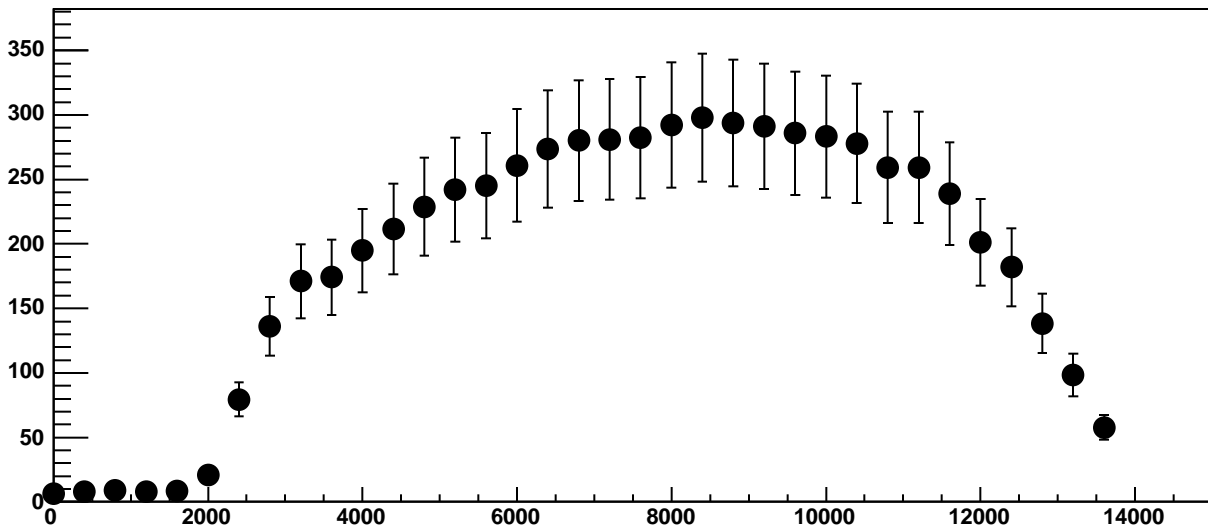
Chip 9, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



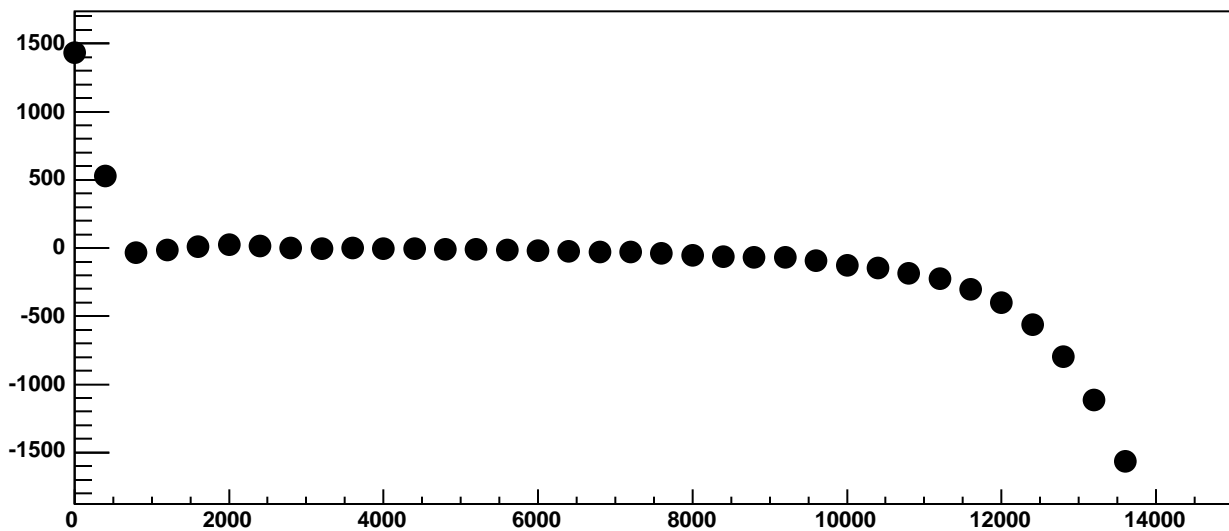
Chip 9, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC



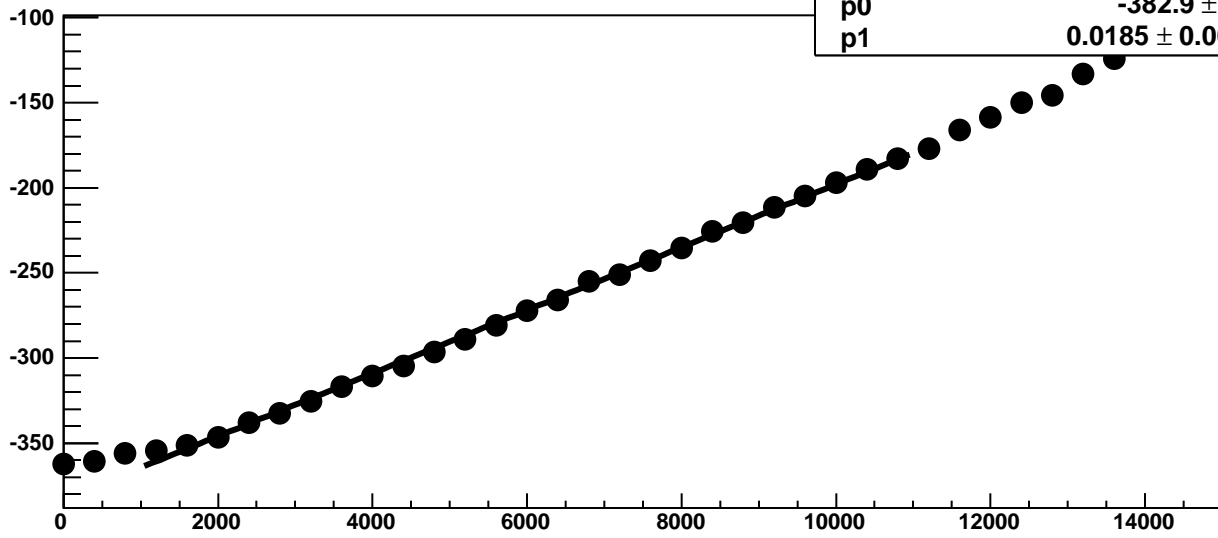
Chip 9, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC

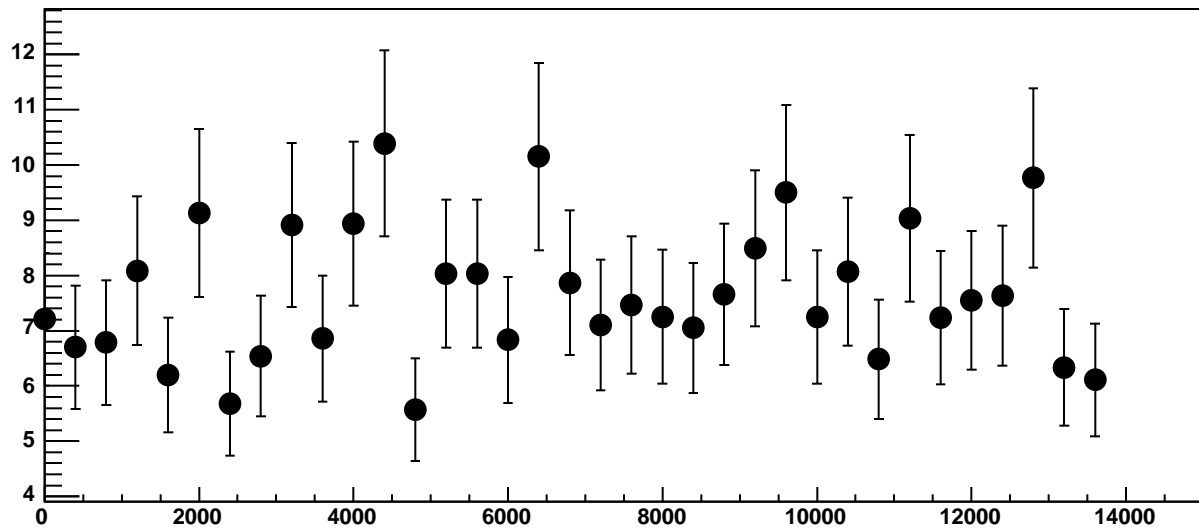


Chip 9, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

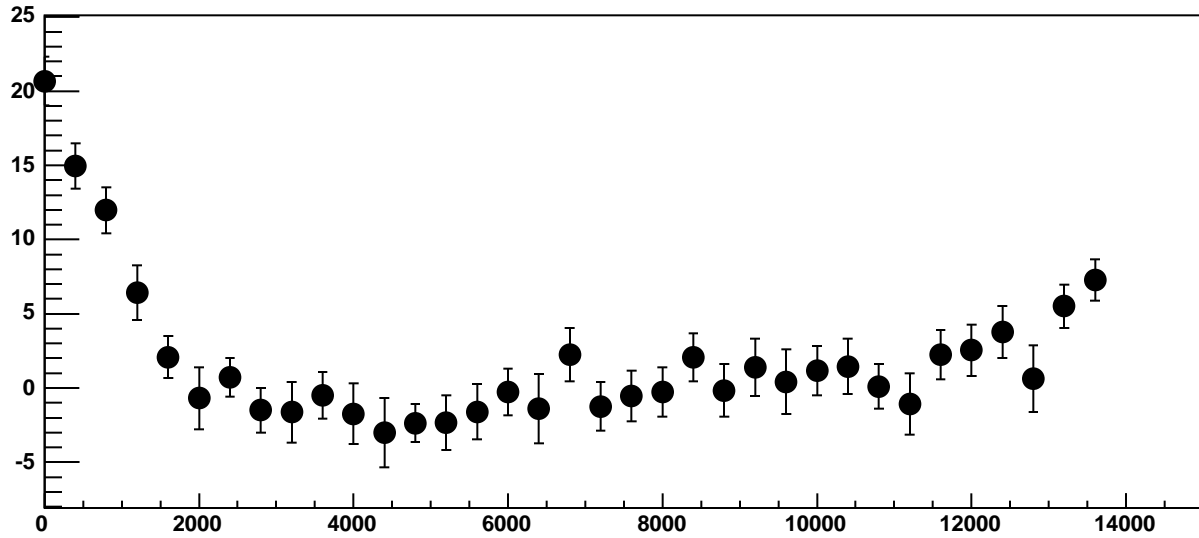


χ^2 / ndf 30.47 / 23
p0 -382.9 ± 0.7634
p1 0.0185 ± 0.0001164

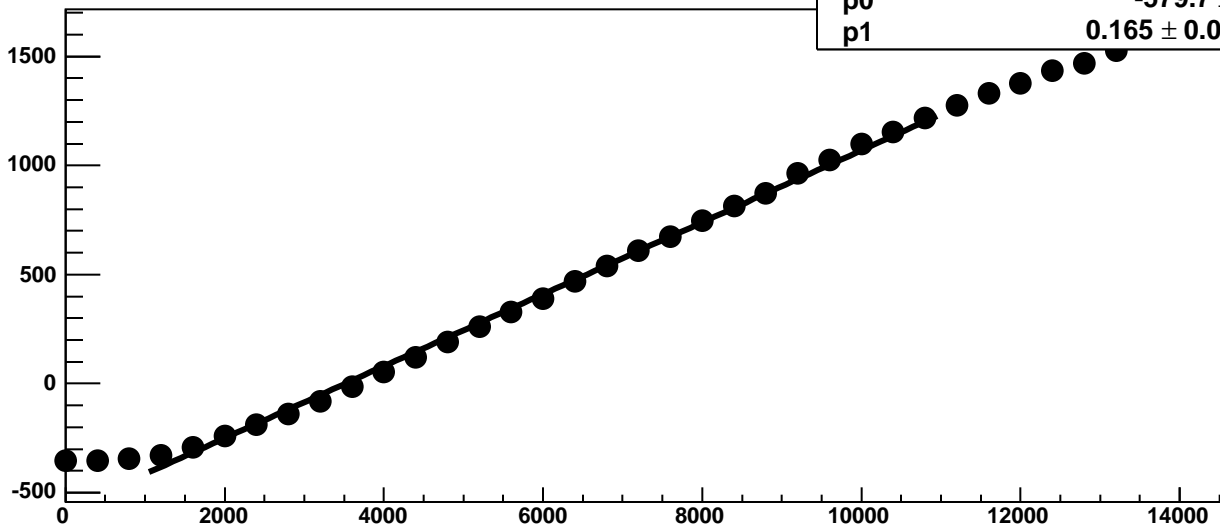
Chip 9, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



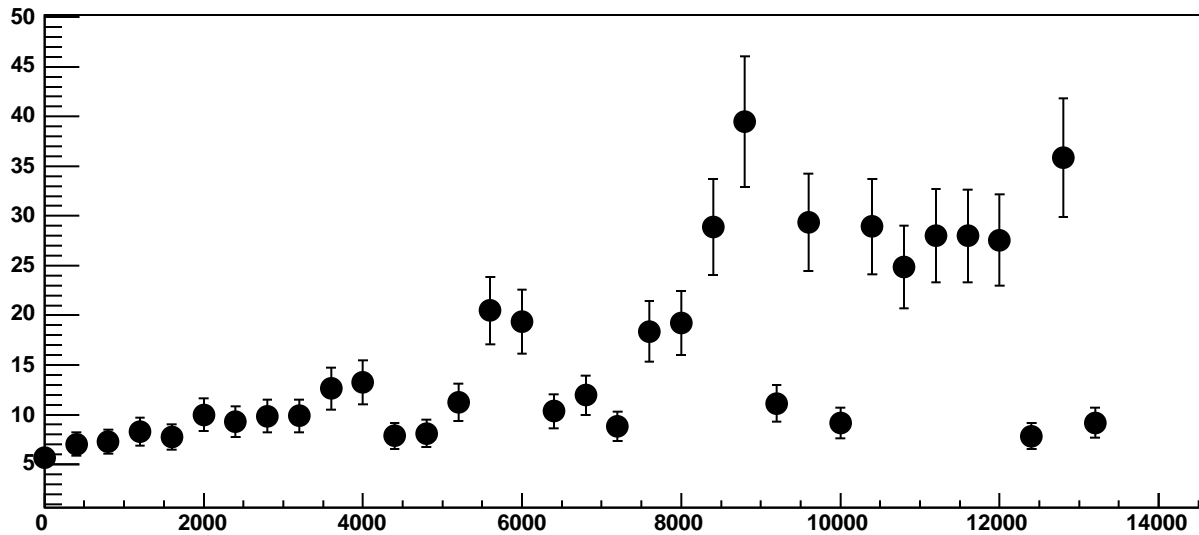
Chip 9, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



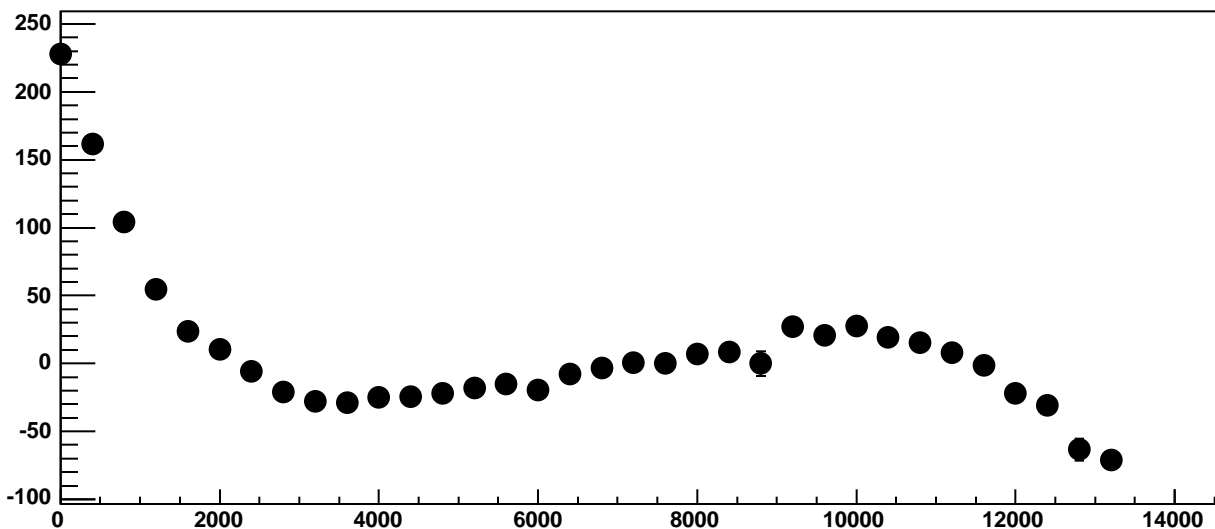
Chip 9, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



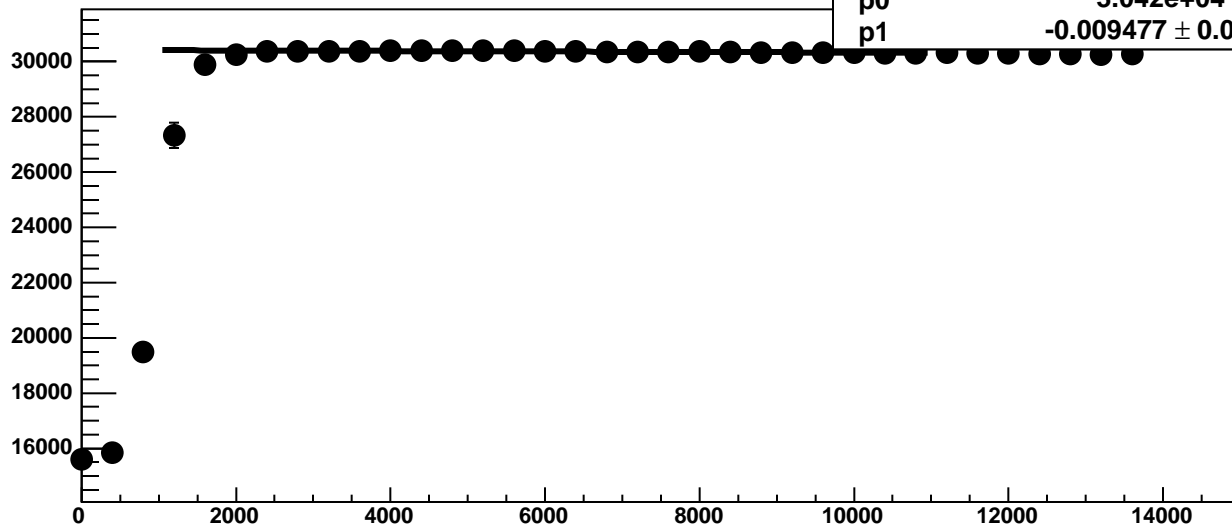
Chip 9, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



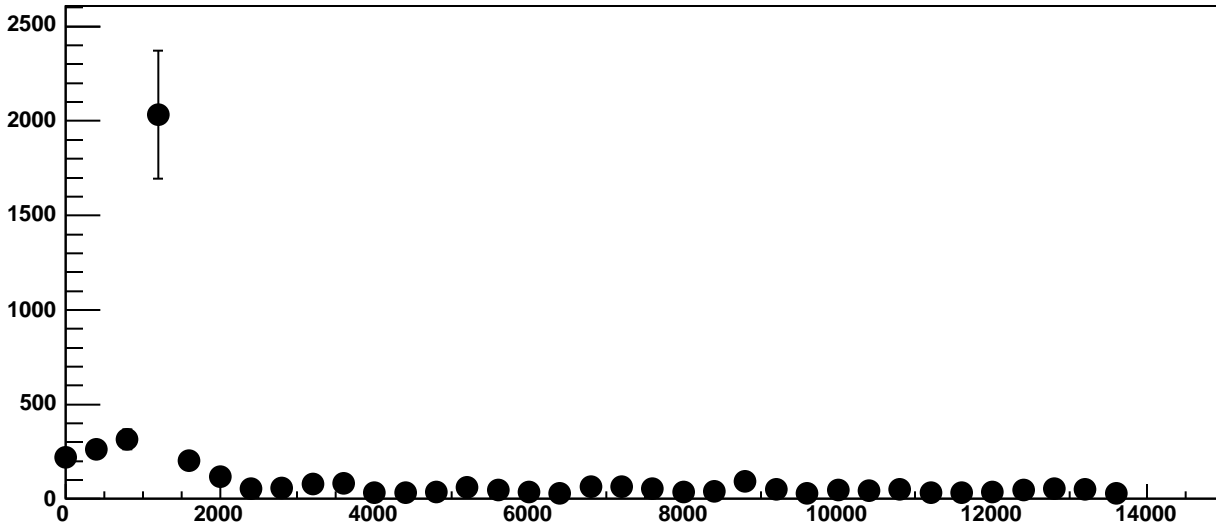
Chip 9, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



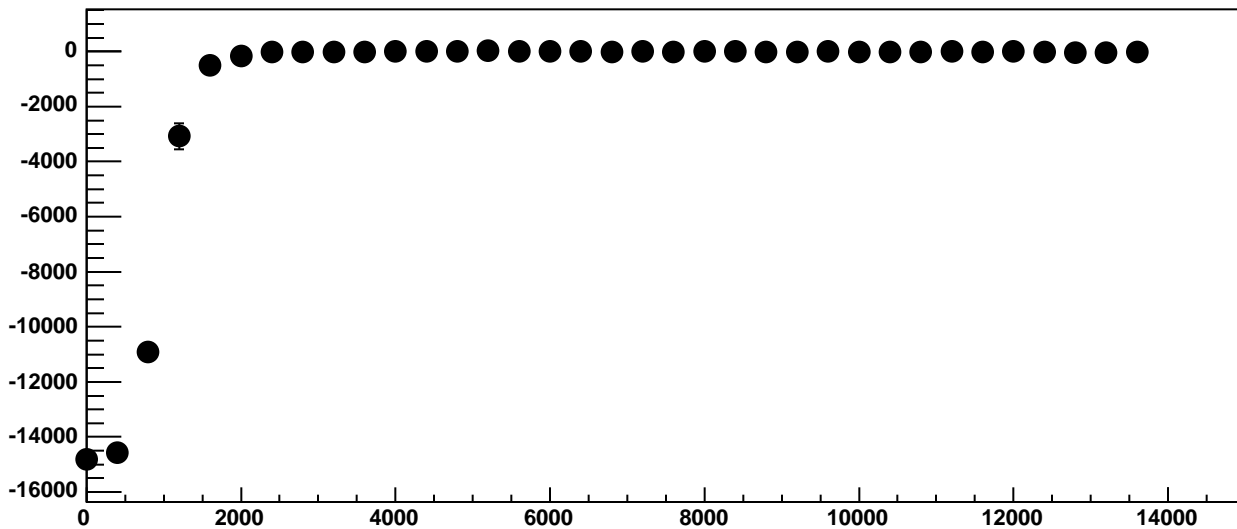
Chip 9, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



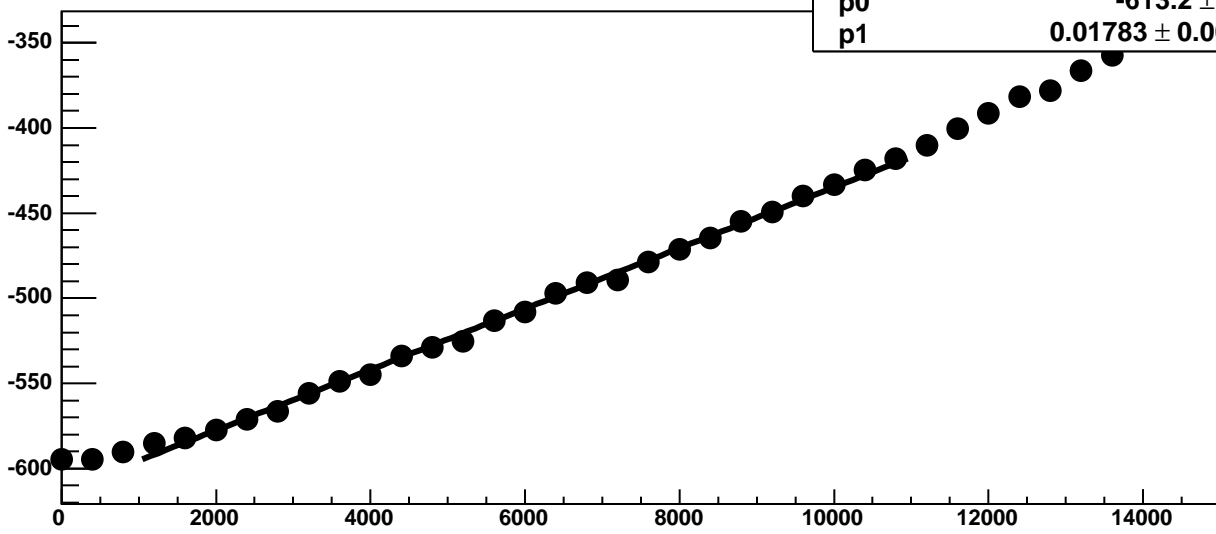
Chip 9, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC

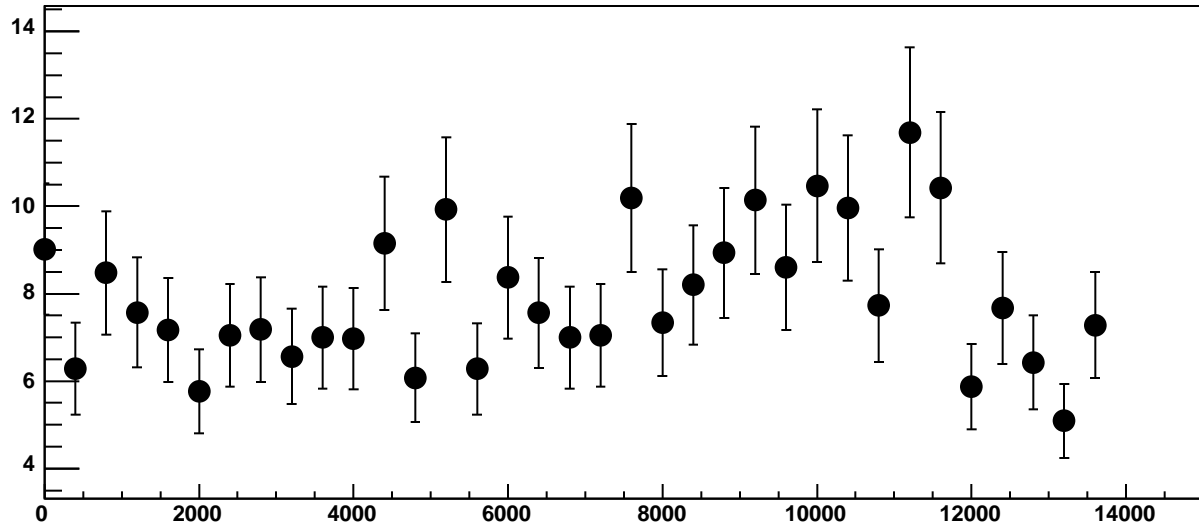


Chip 9, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

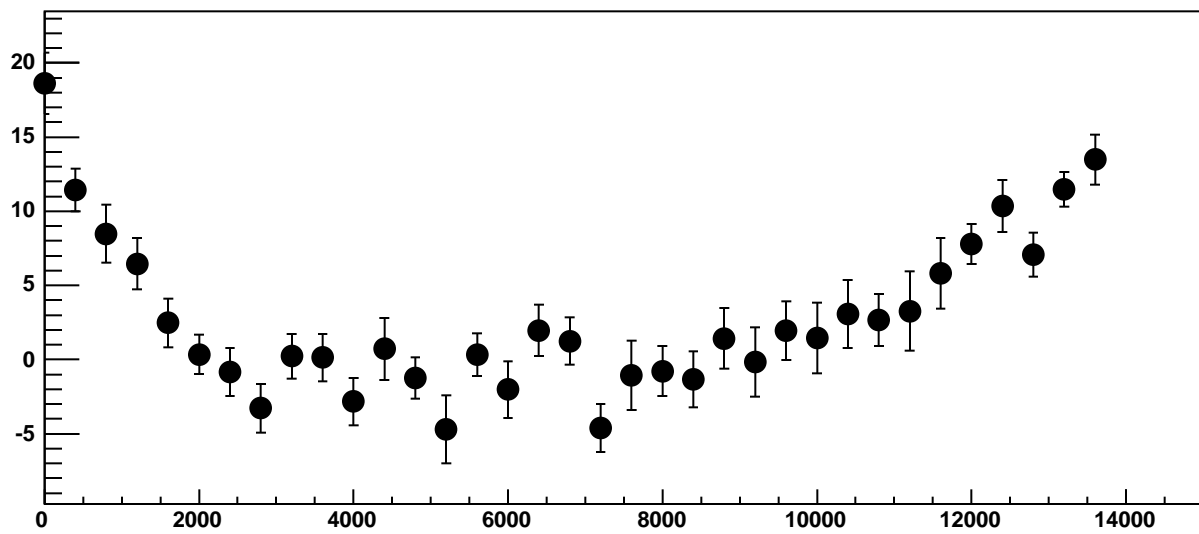


χ^2 / ndf 46.77 / 23
p0 -613.2 ± 0.7645
p1 0.01783 ± 0.0001248

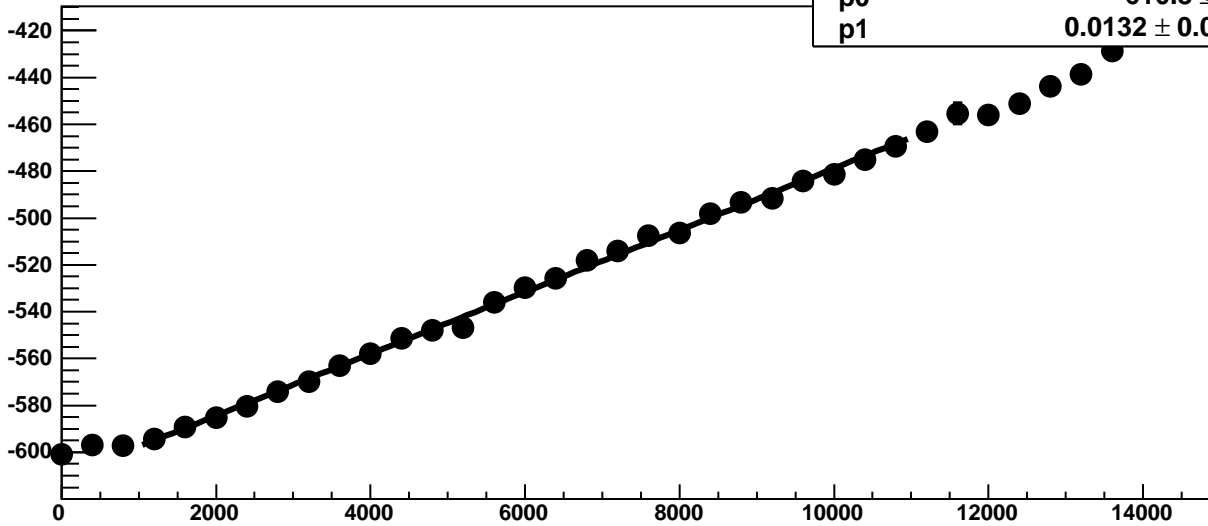
Chip 9, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

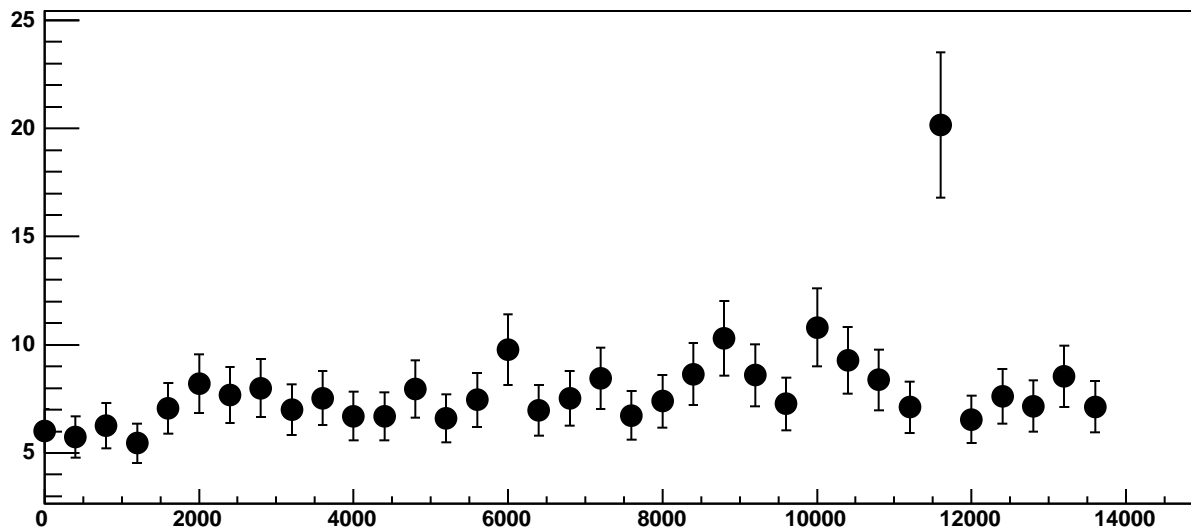


Chip 9, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

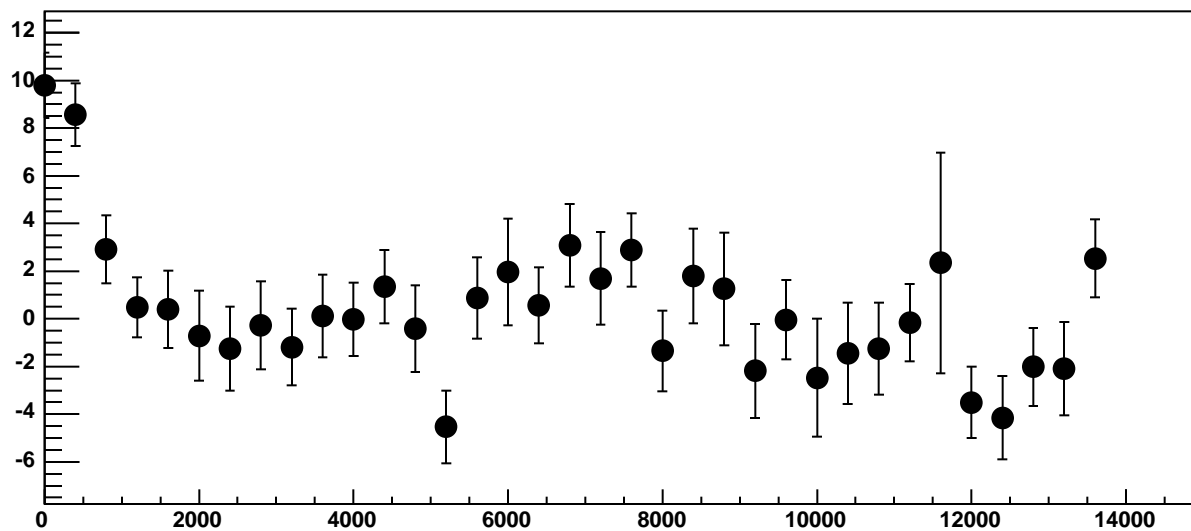


χ^2 / ndf 24.64 / 23
p0 -610.8 ± 0.7596
p1 0.0132 ± 0.0001224

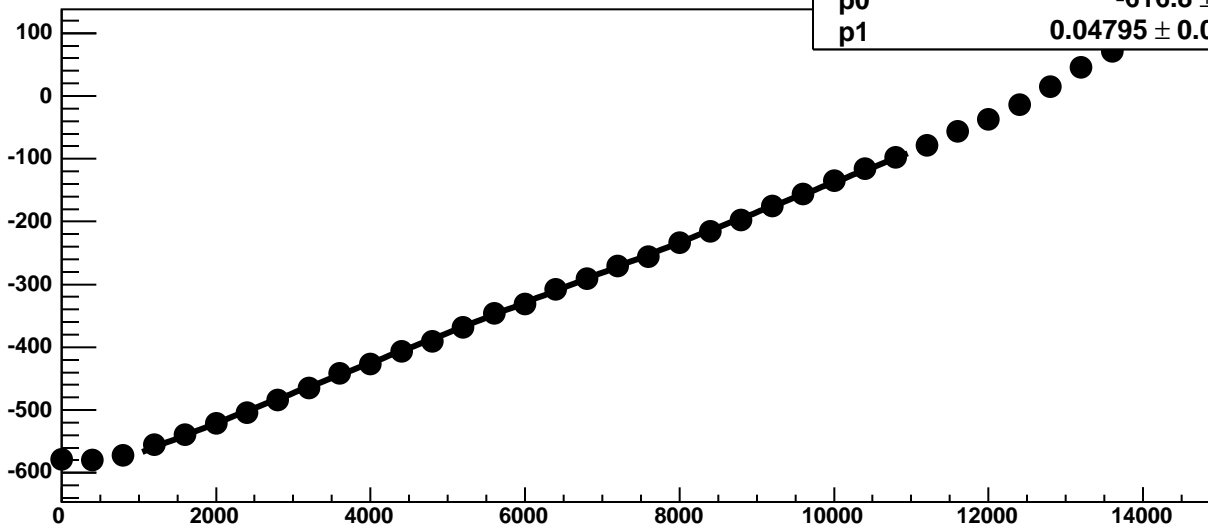
Chip 9, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



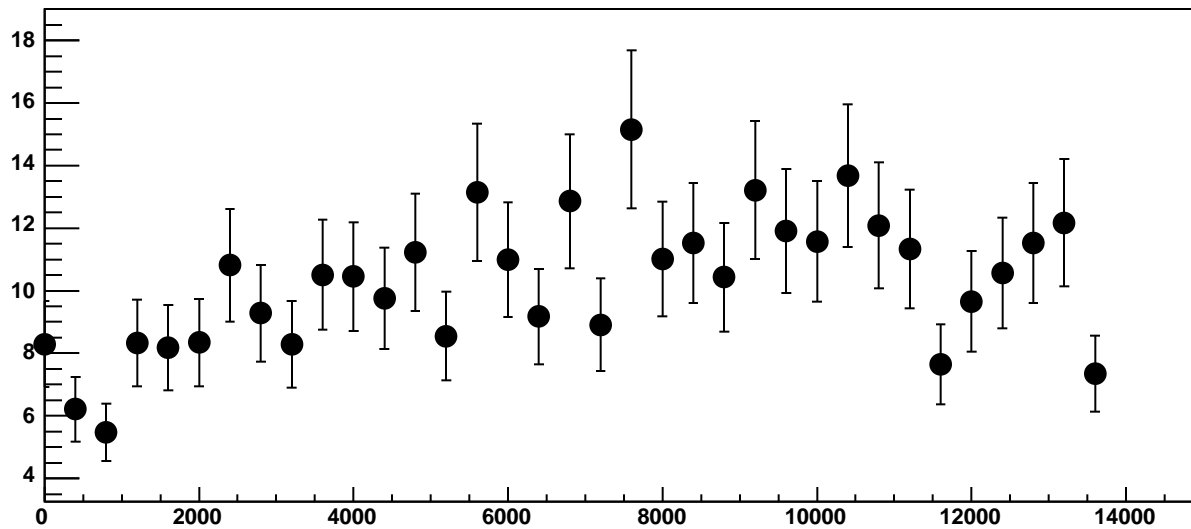
Chip 9, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



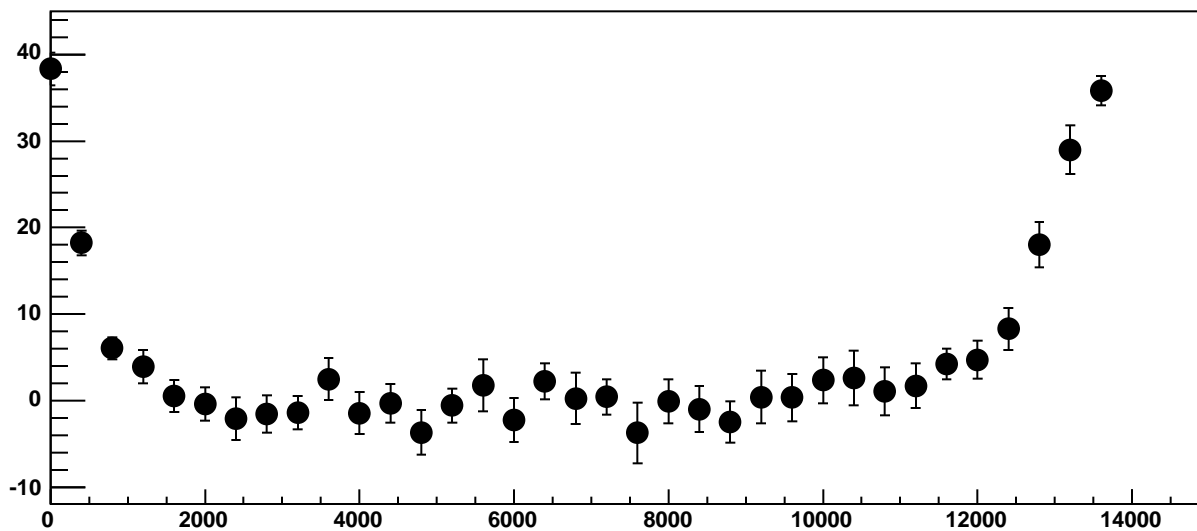
Chip 9, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC



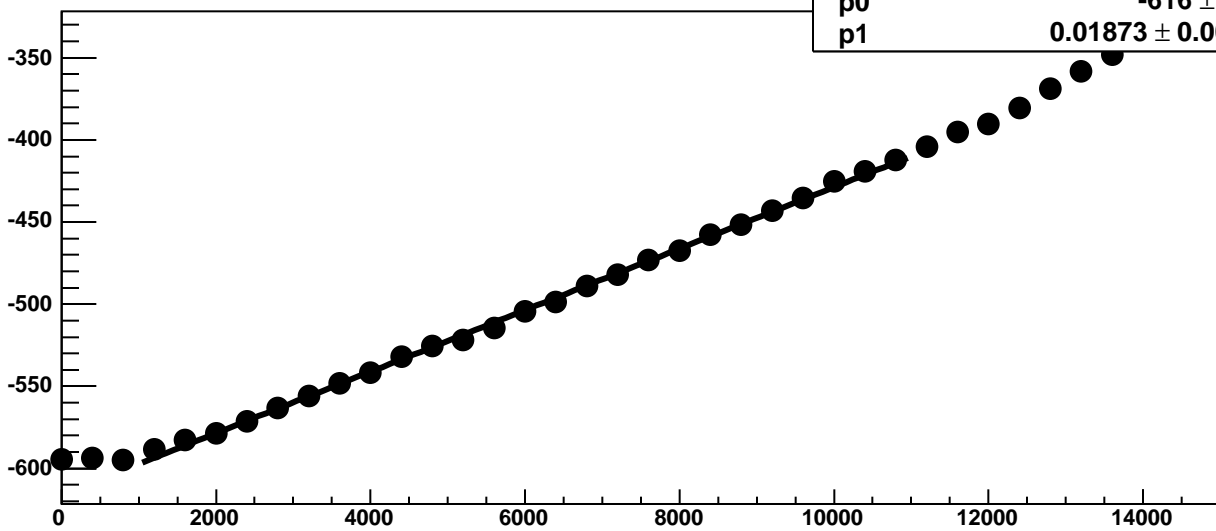
Chip 9, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC

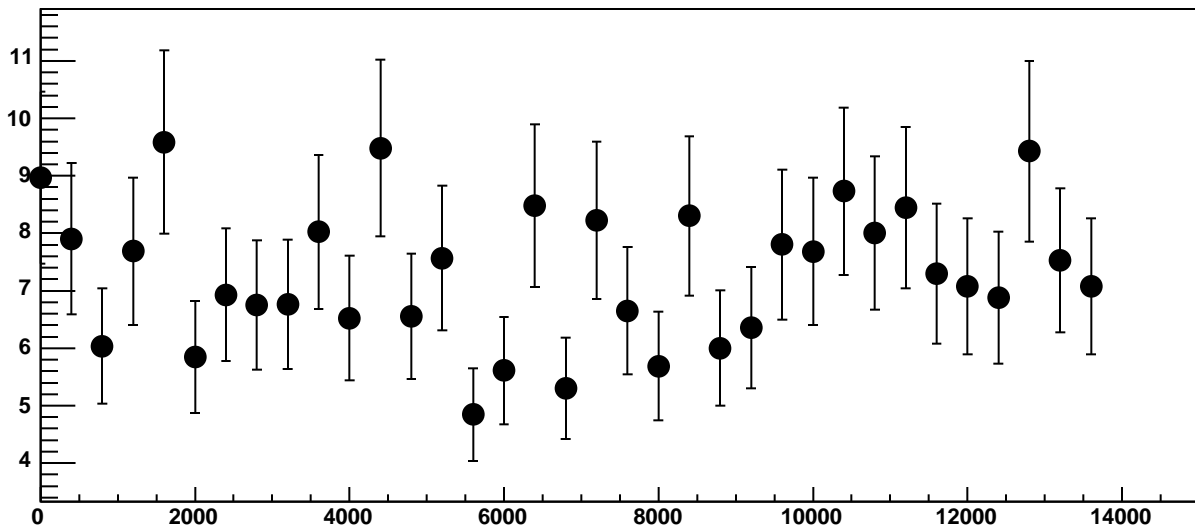


Chip 9, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

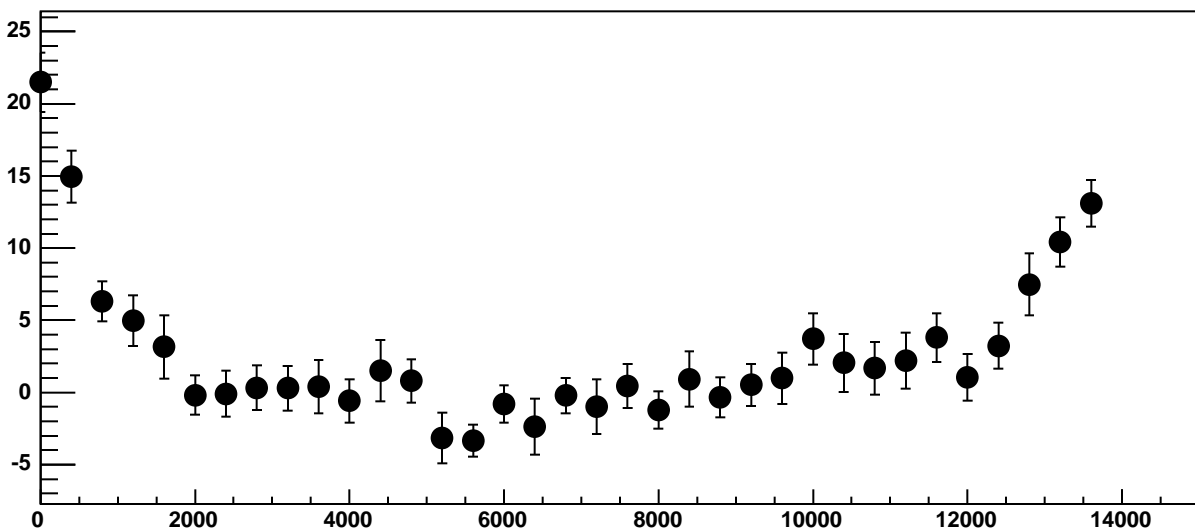


χ^2 / ndf 33.44 / 23
p0 -616 ± 0.7636
p1 0.01873 ± 0.0001165

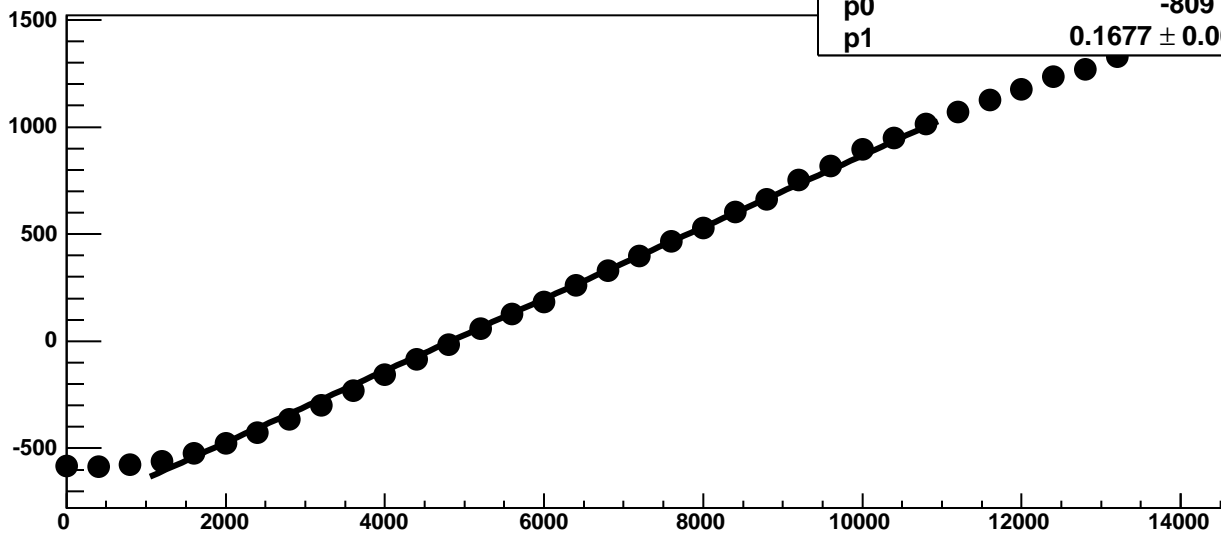
Chip 9, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



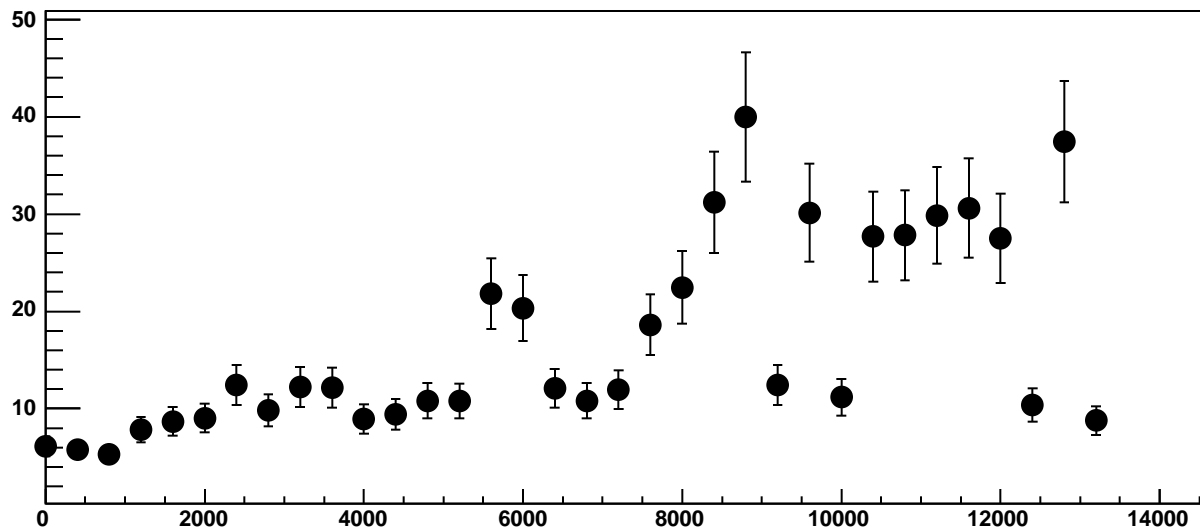
Chip 9, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



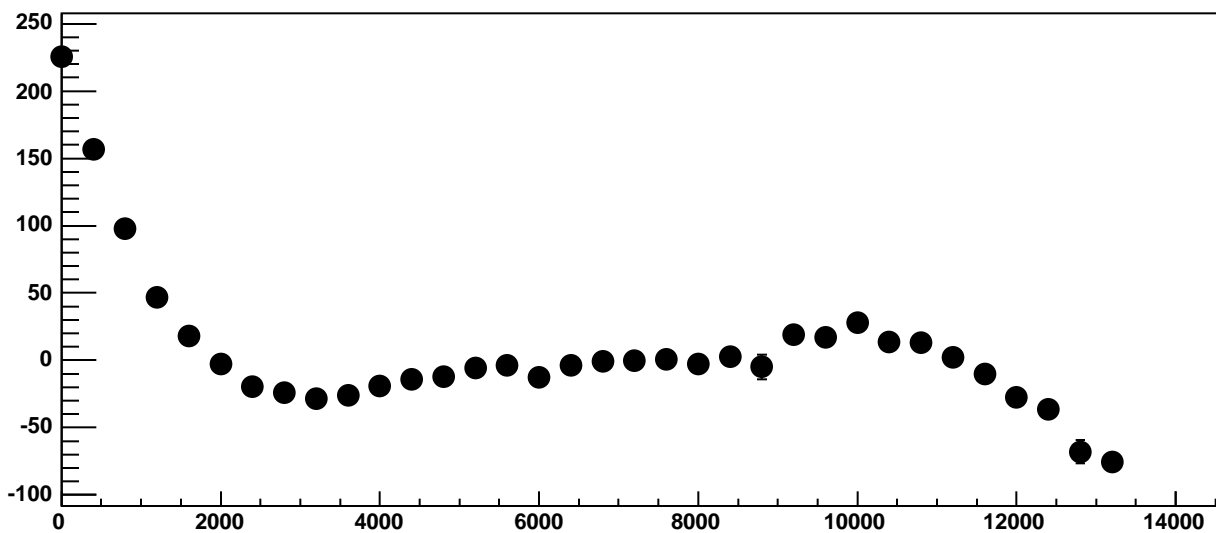
Chip 9, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



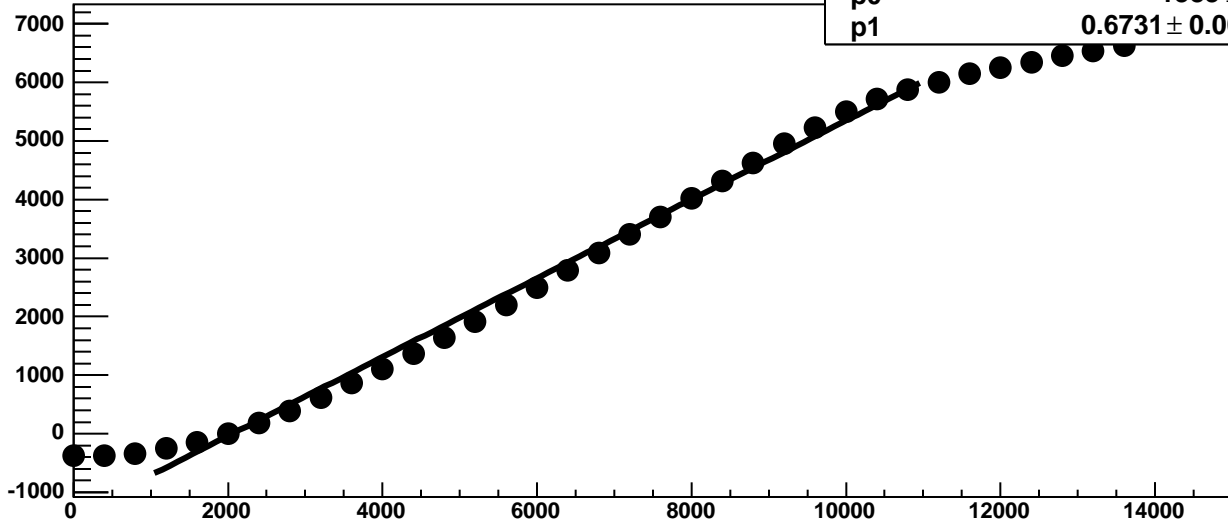
Chip 9, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



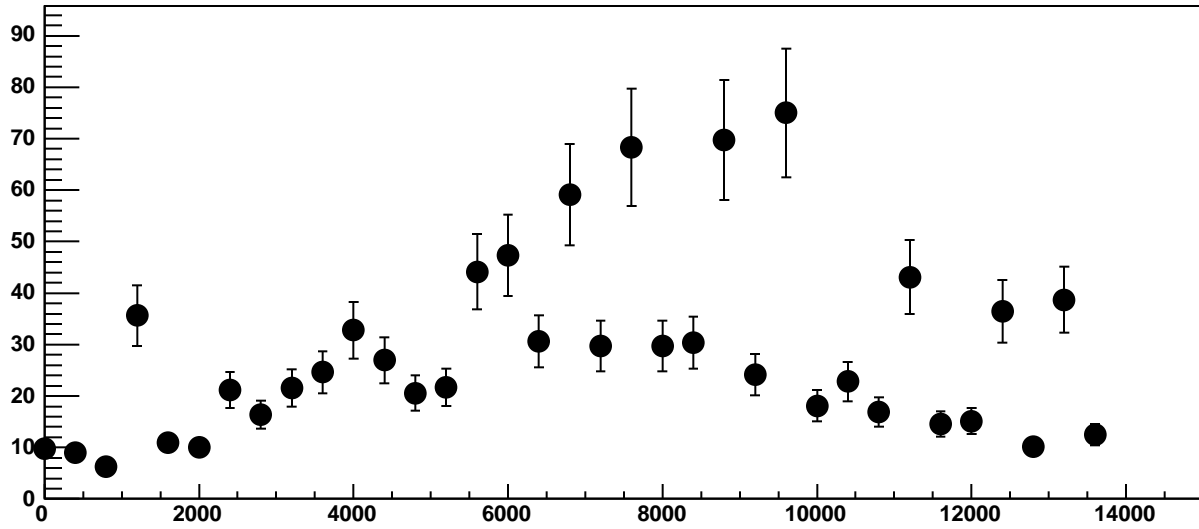
Chip 9, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



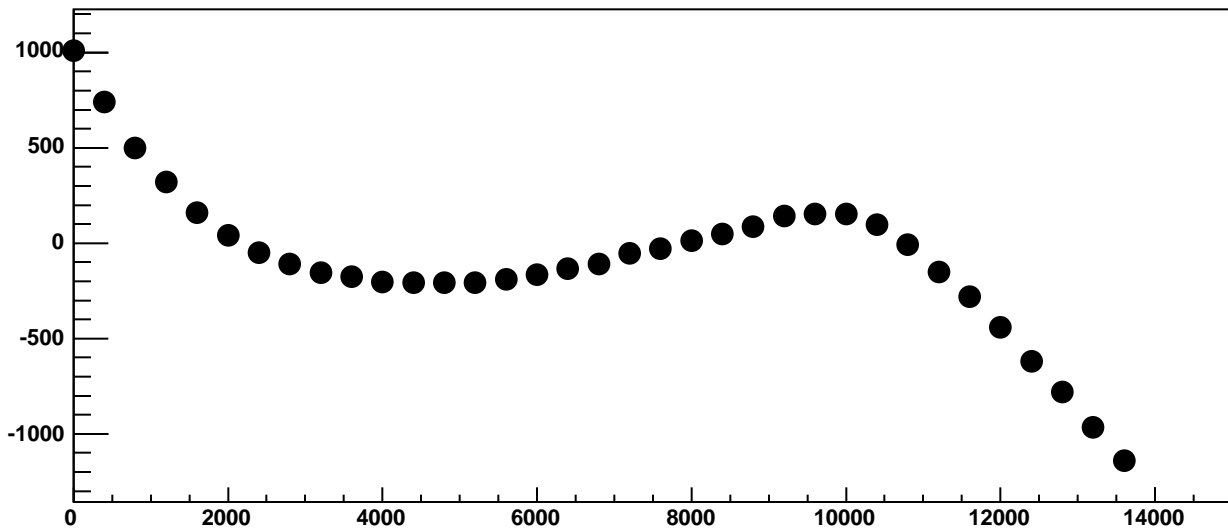
Chip 9, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC



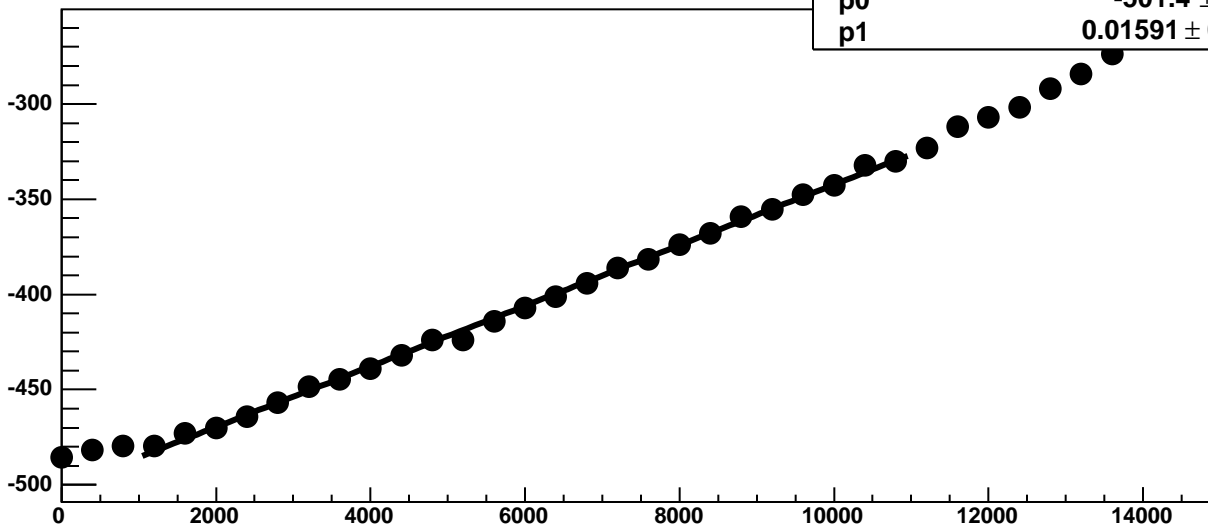
Chip 9, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

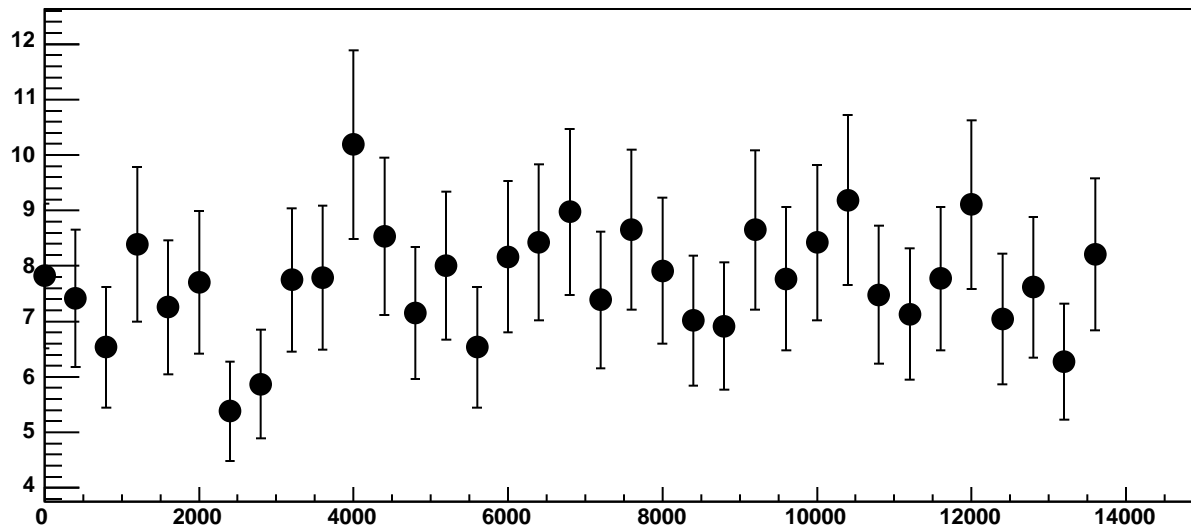


Chip 9, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

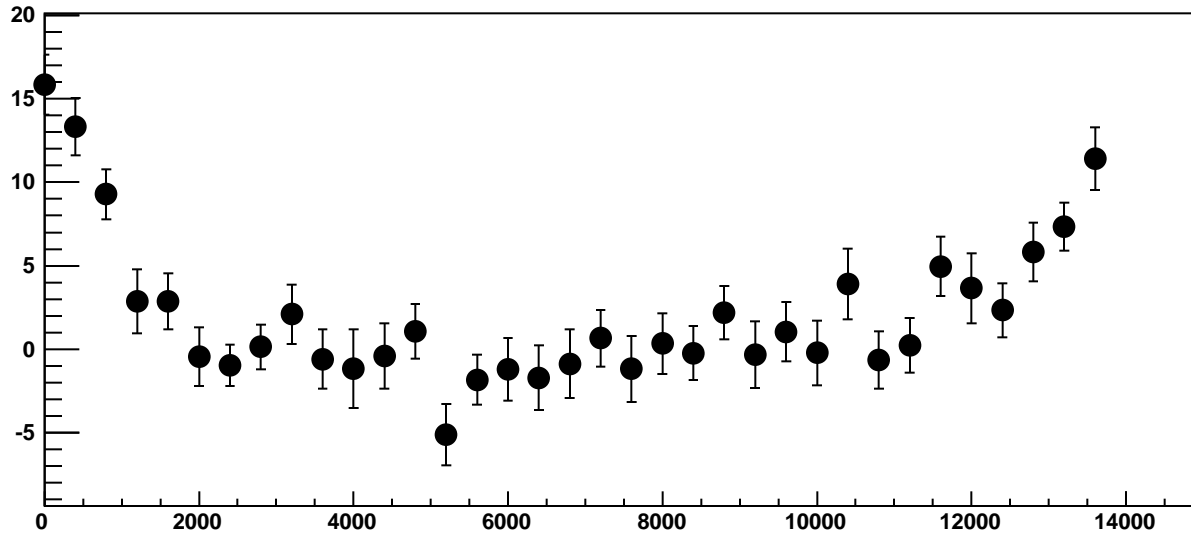


χ^2 / ndf 25.07 / 23
p0 -501.4 ± 0.7716
p1 0.01591 ± 0.00012

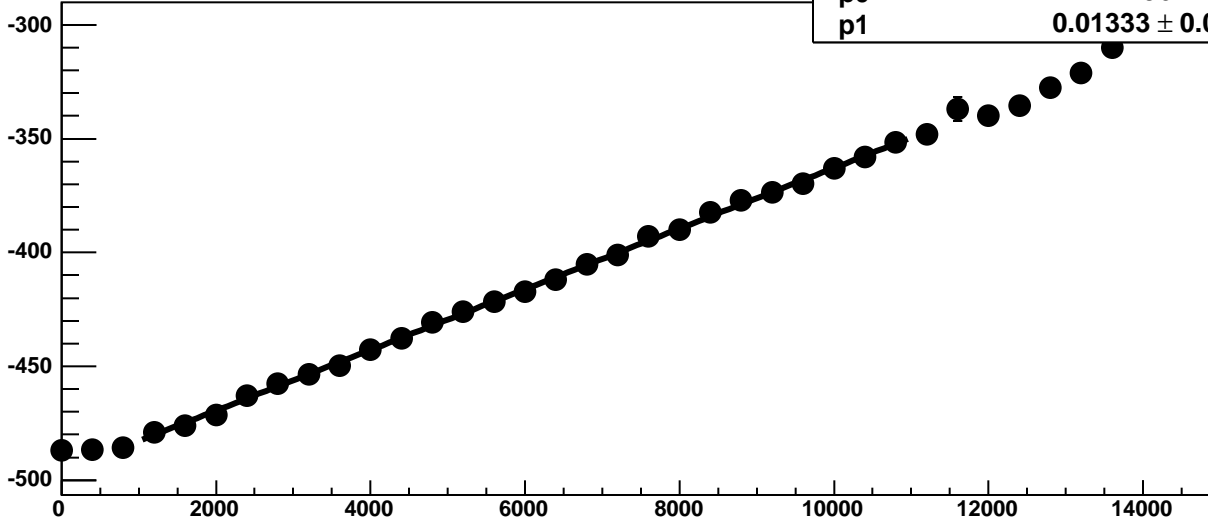
Chip 9, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

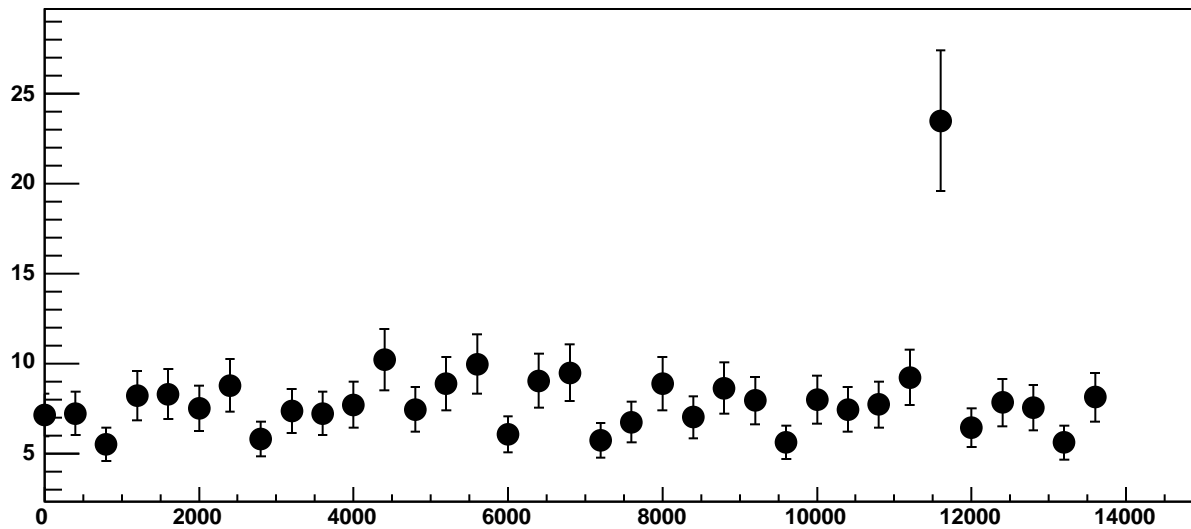


Chip 9, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

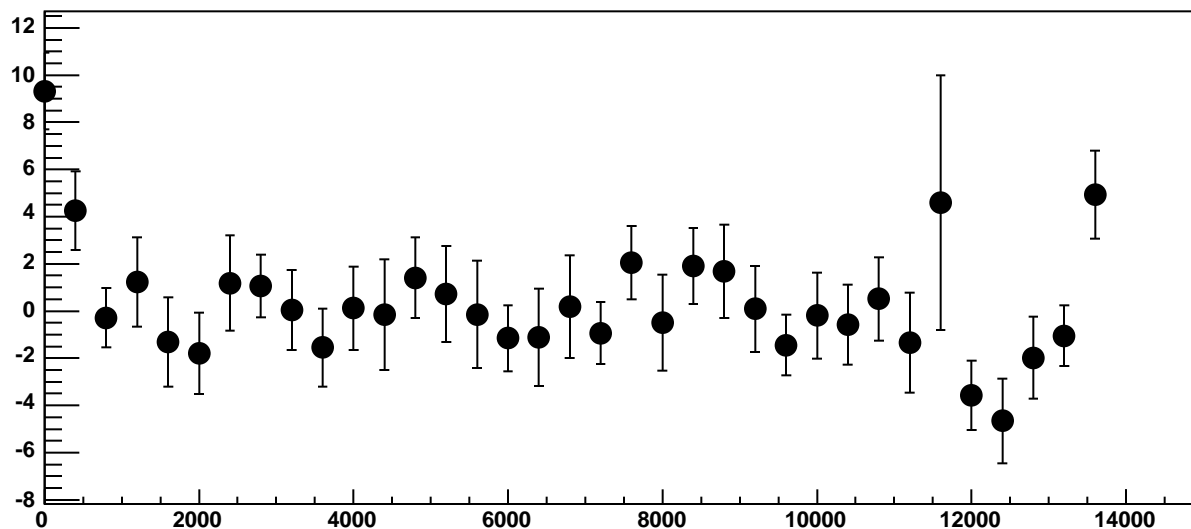


χ^2 / ndf 11.61 / 23
p0 -496.1 ± 0.8072
p1 0.01333 ± 0.0001192

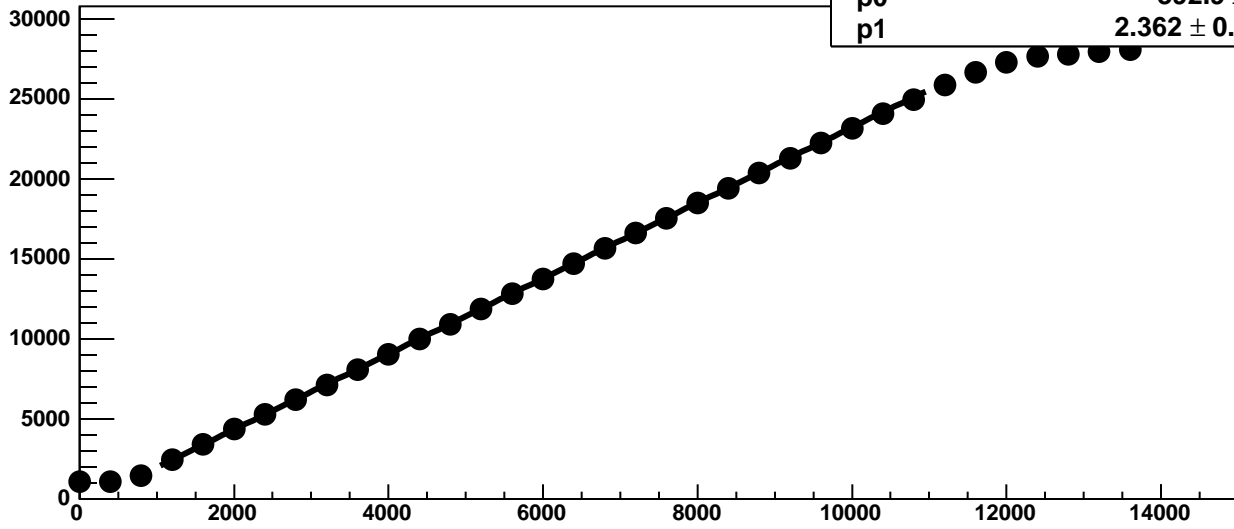
Chip 9, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



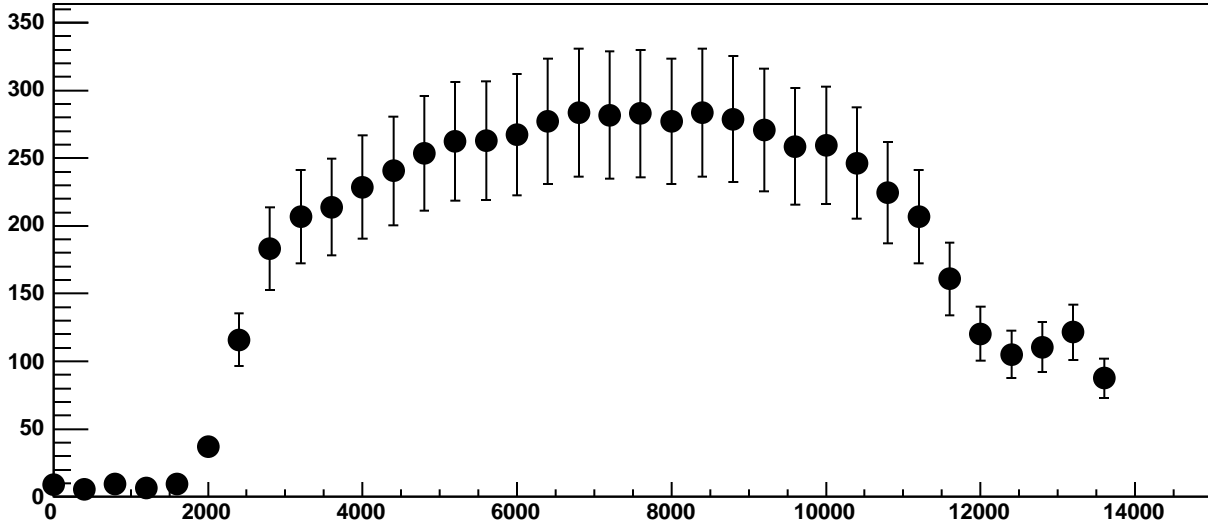
Chip 9, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



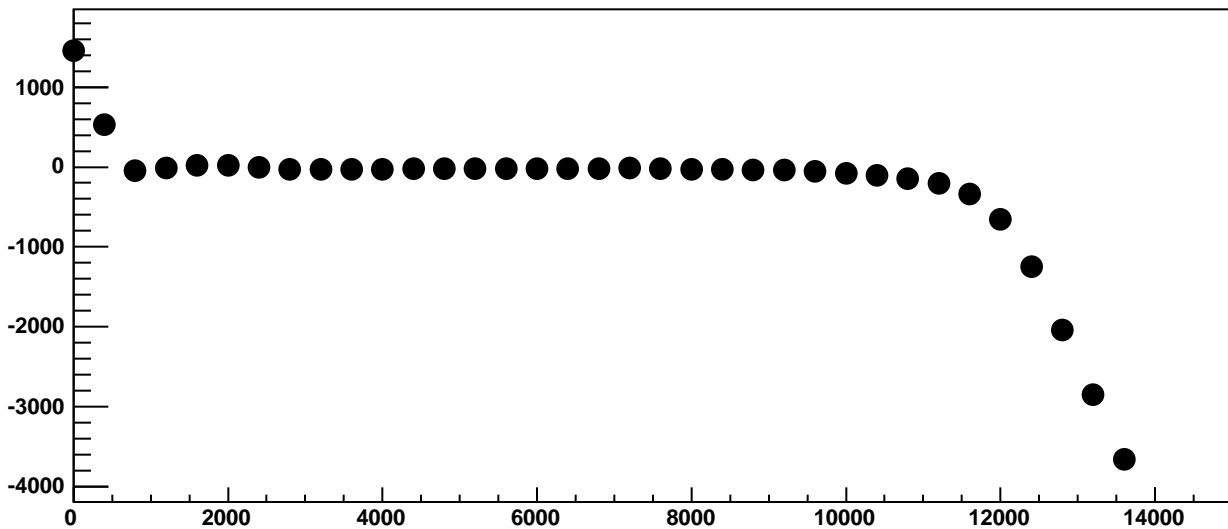
Chip 9, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC



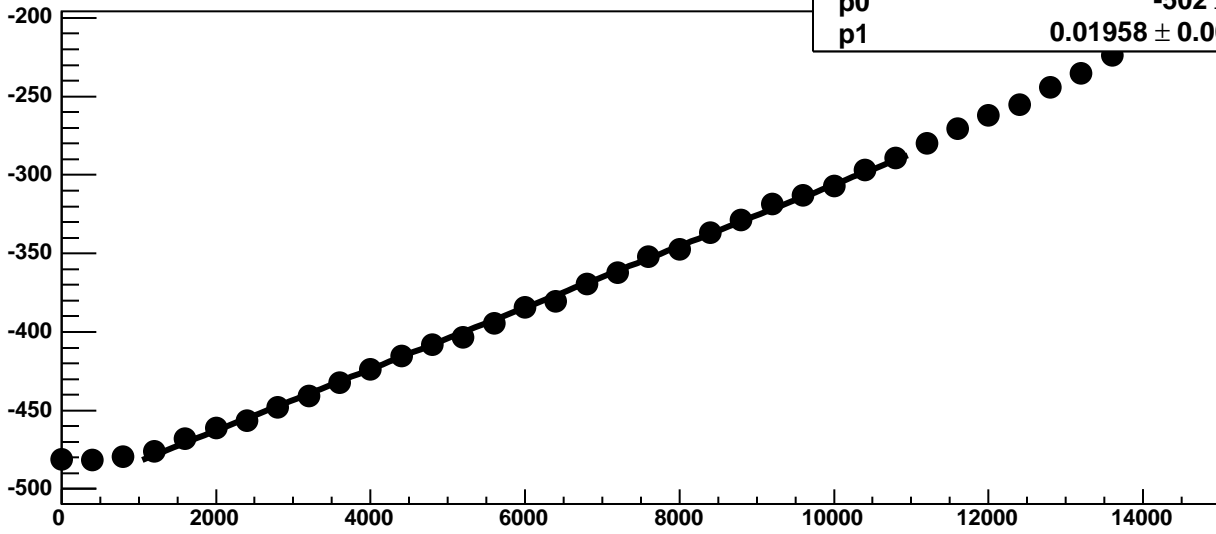
Chip 9, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC

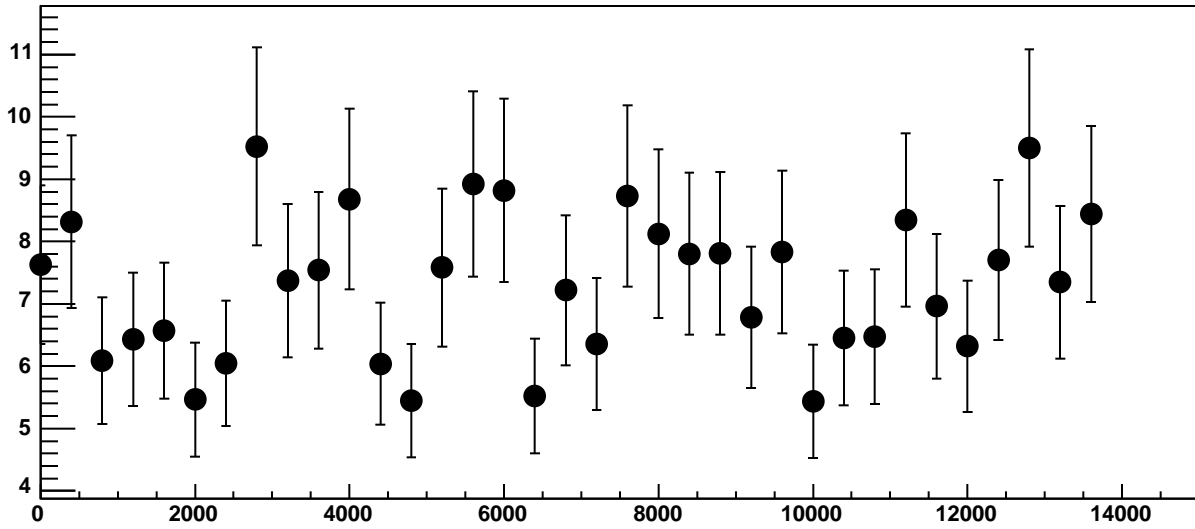


Chip 9, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

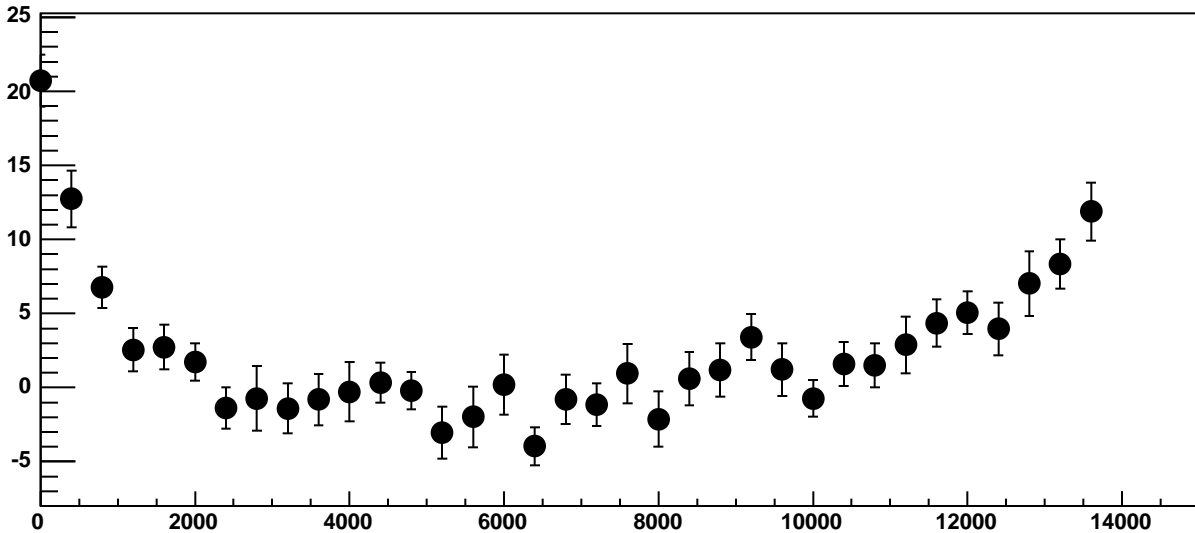


χ^2 / ndf 34.81 / 23
p0 -502 ± 0.697
p1 0.01958 ± 0.0001047

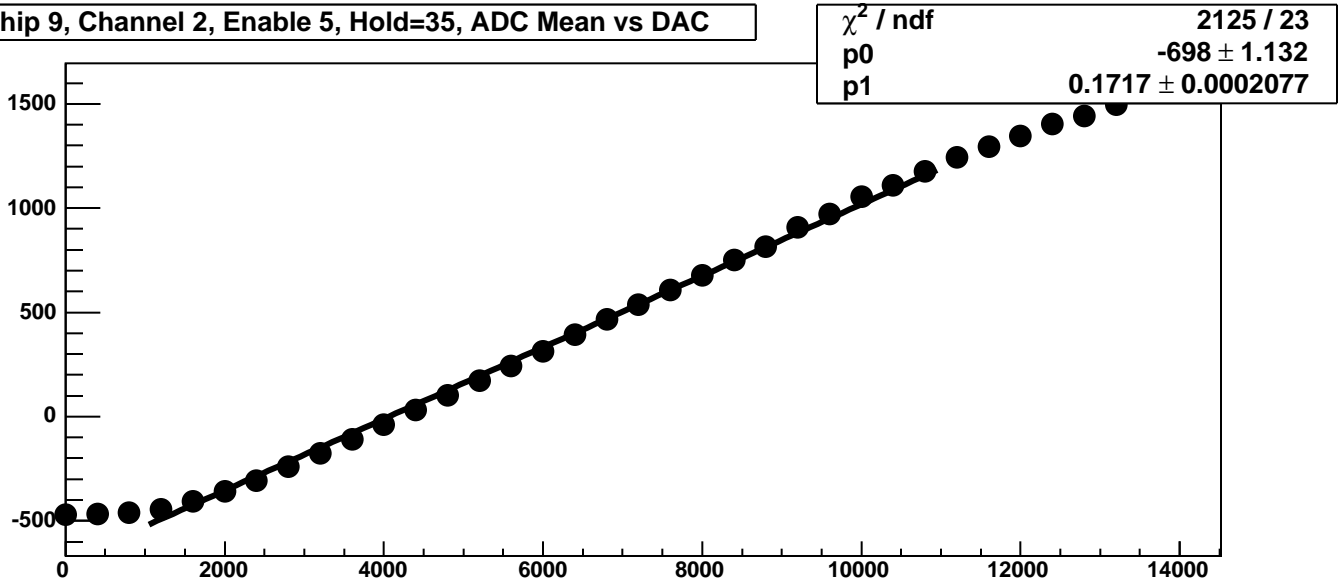
Chip 9, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



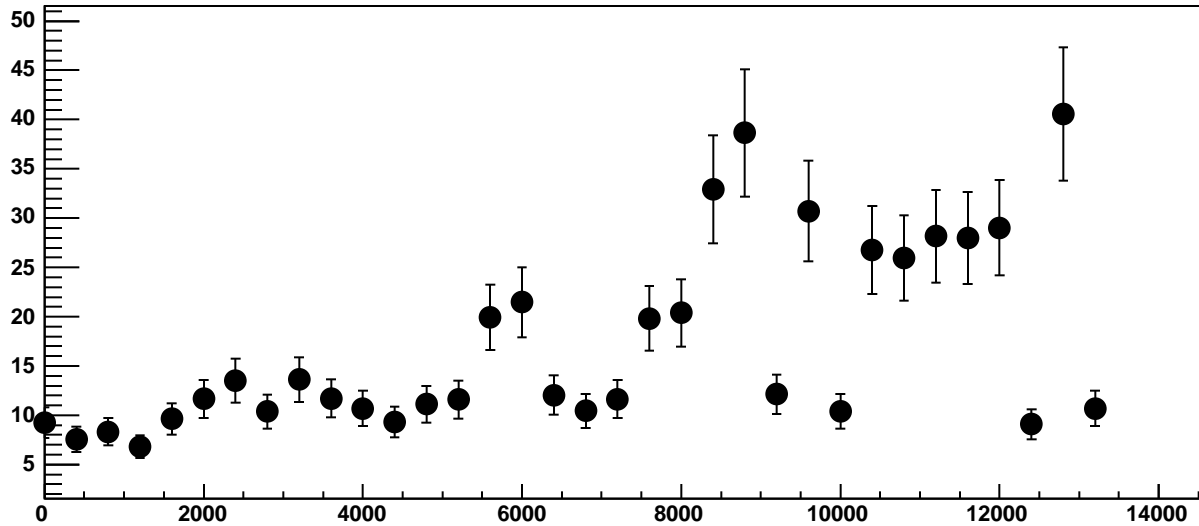
Chip 9, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



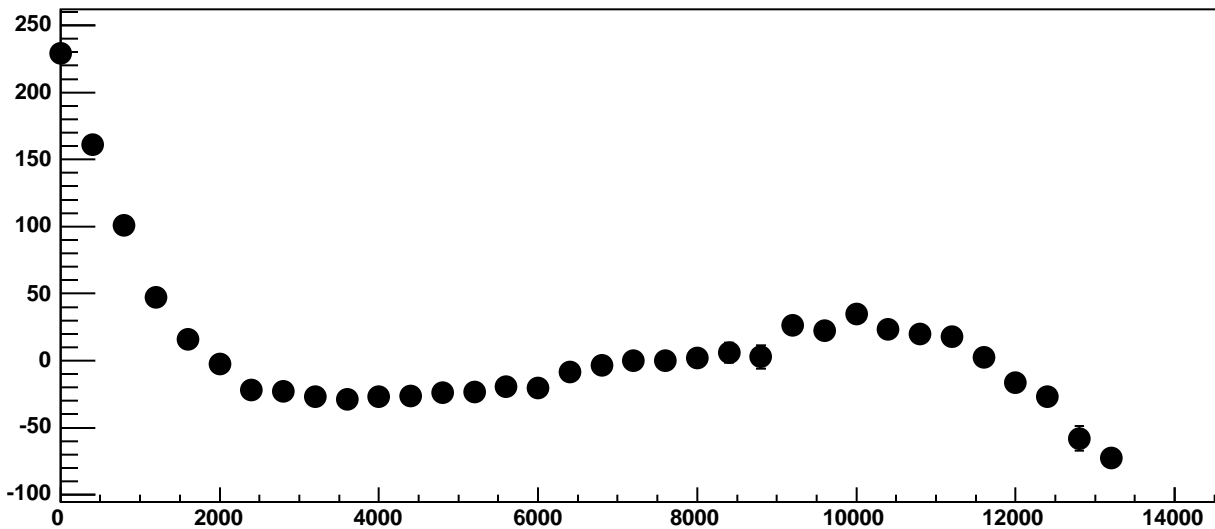
Chip 9, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



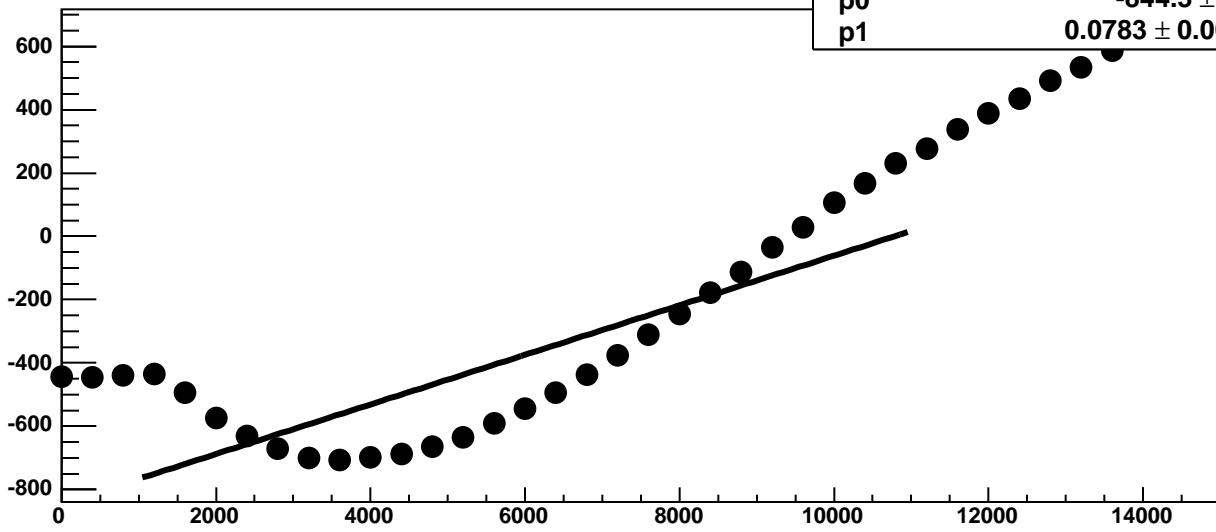
Chip 9, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 9, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1.071e+05 / 23

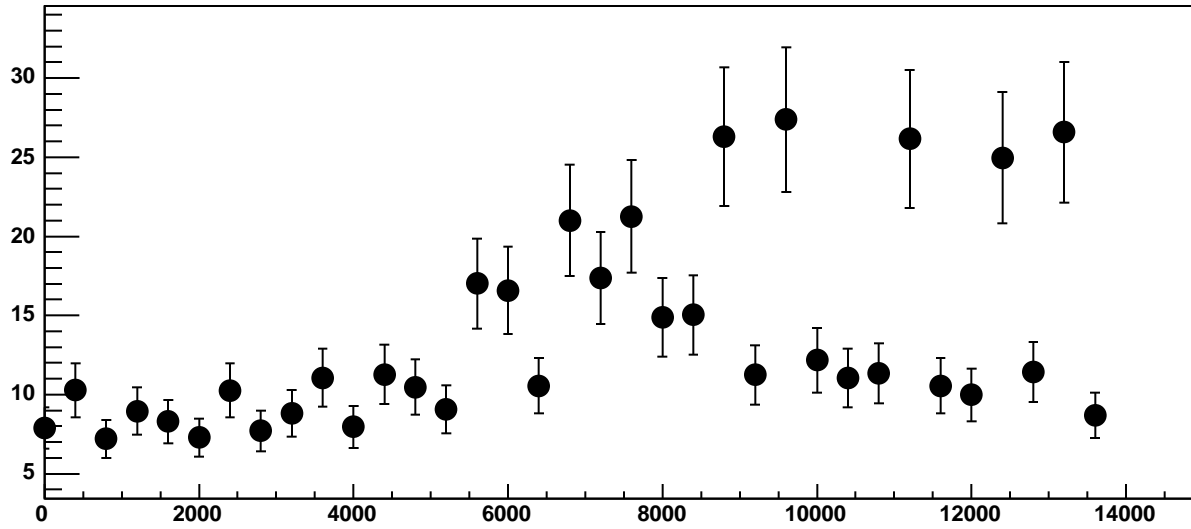
p0

-844.3 ± 0.9782

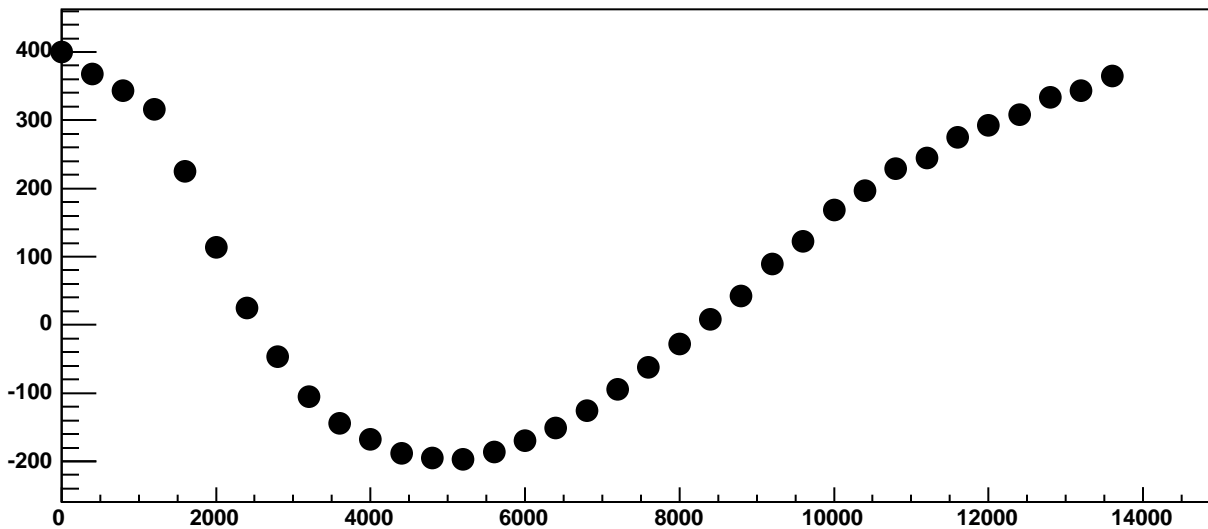
p1

0.0783 ± 0.0001737

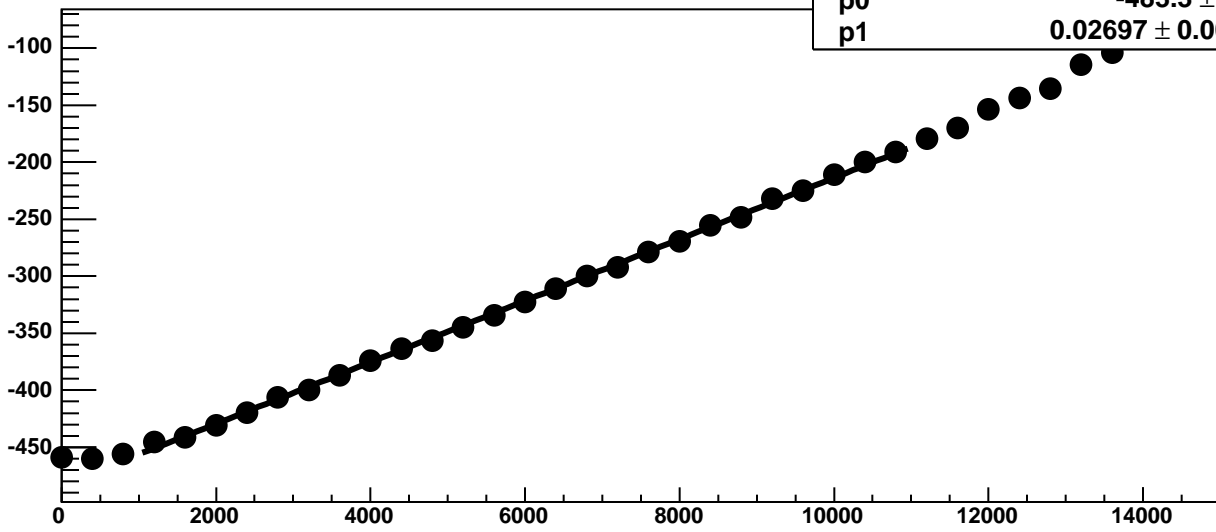
Chip 9, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

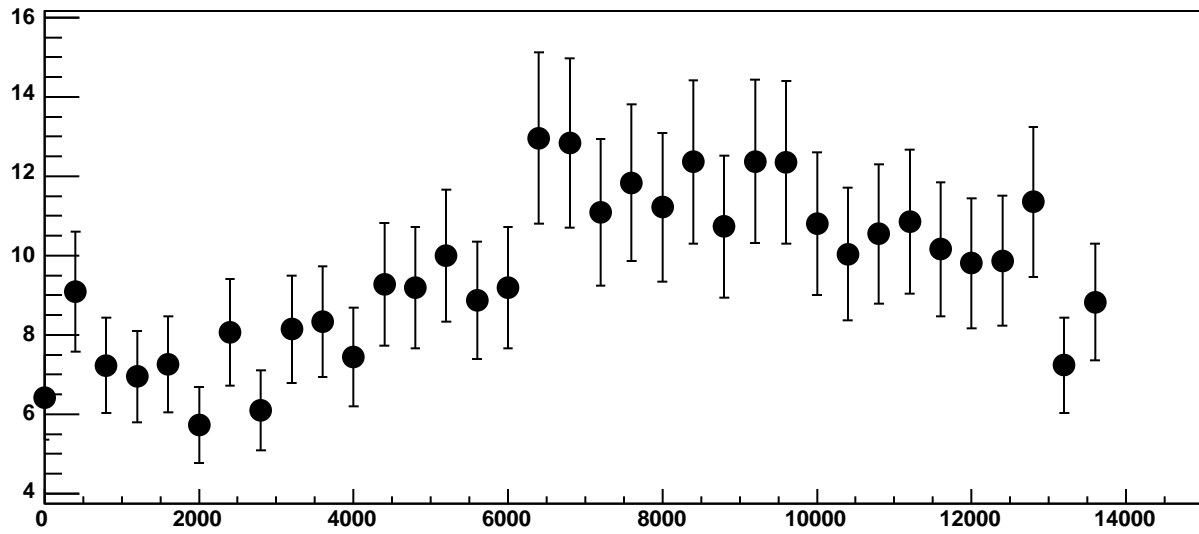


Chip 9, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC

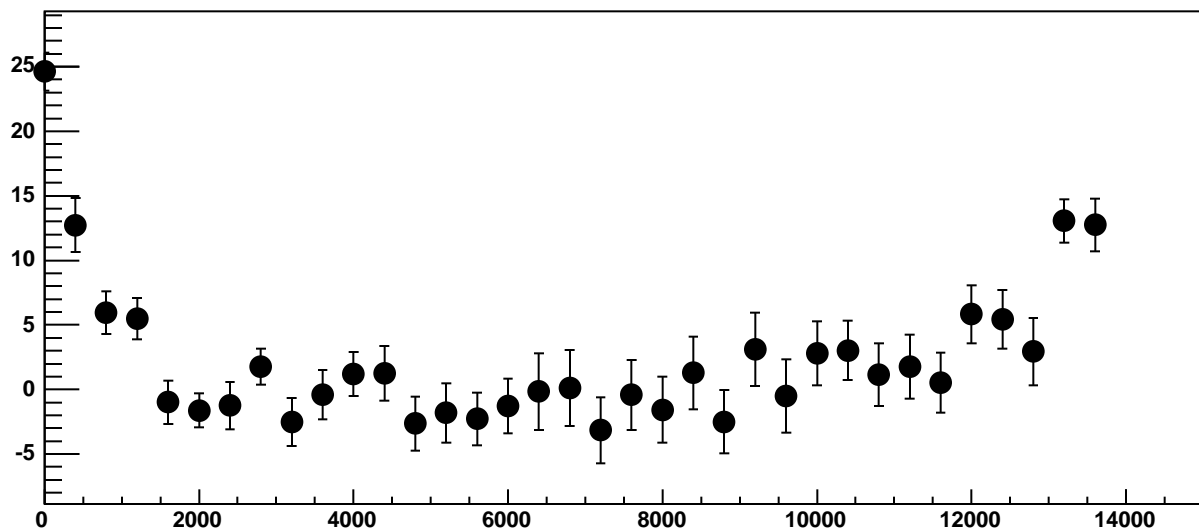


χ^2 / ndf 29.87 / 23
p0 -483.3 ± 0.8052
p1 0.02697 ± 0.0001426

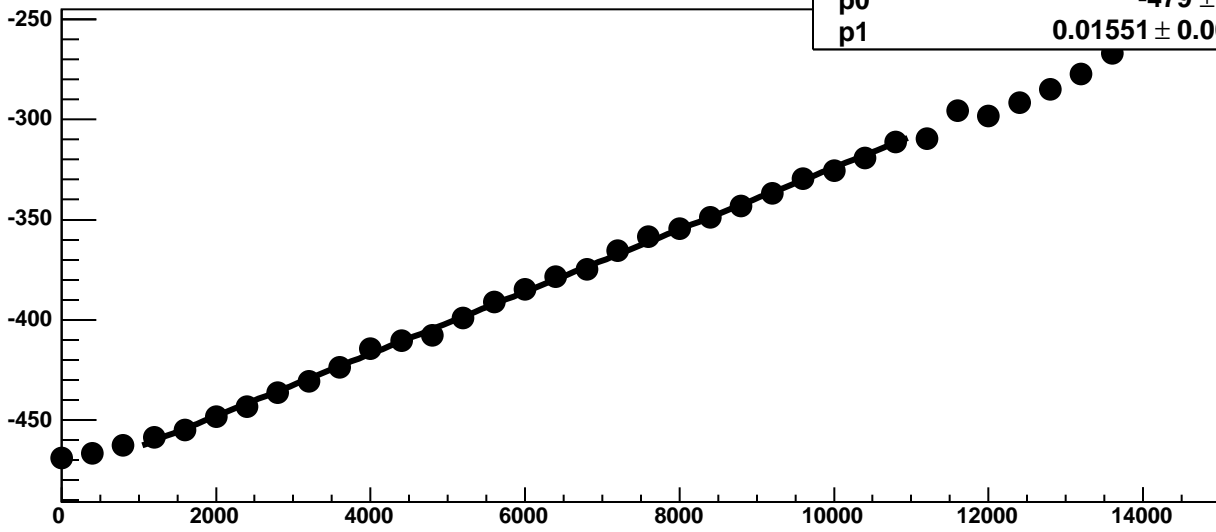
Chip 9, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC

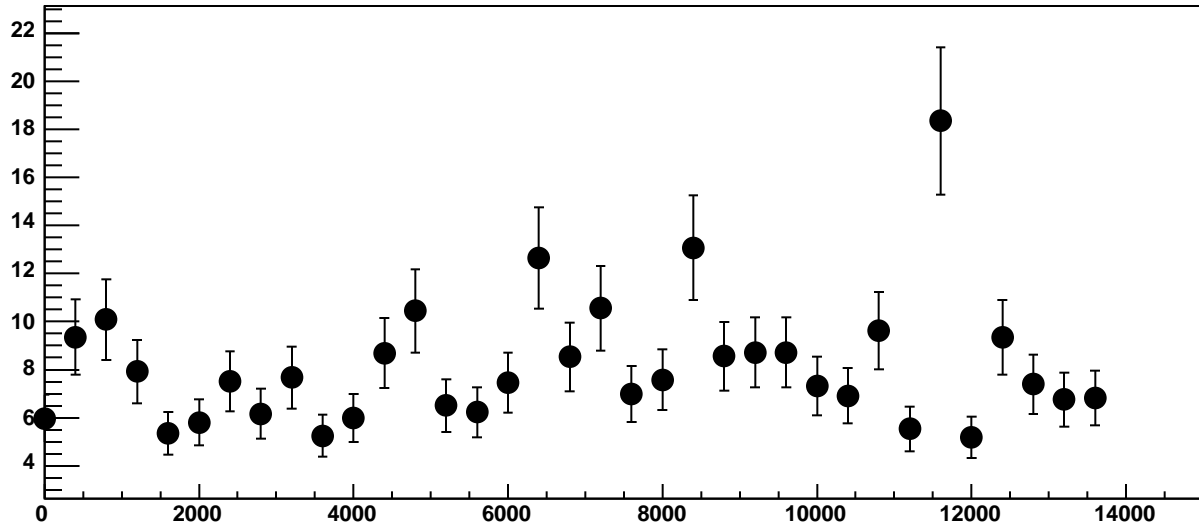


Chip 9, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

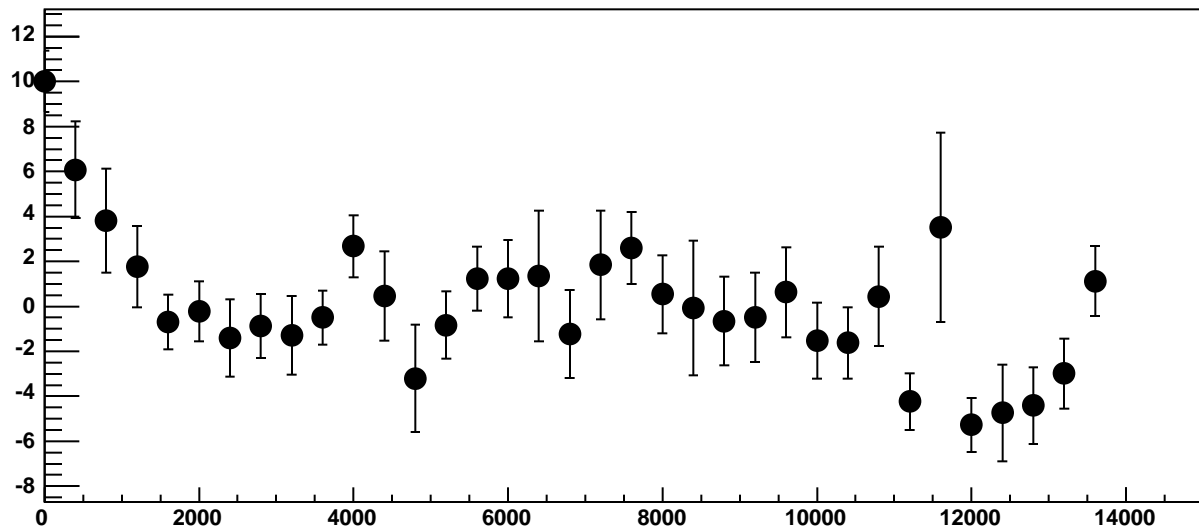


χ^2 / ndf 16.32 / 23
p0 -479 ± 0.7087
p1 0.01551 ± 0.0001163

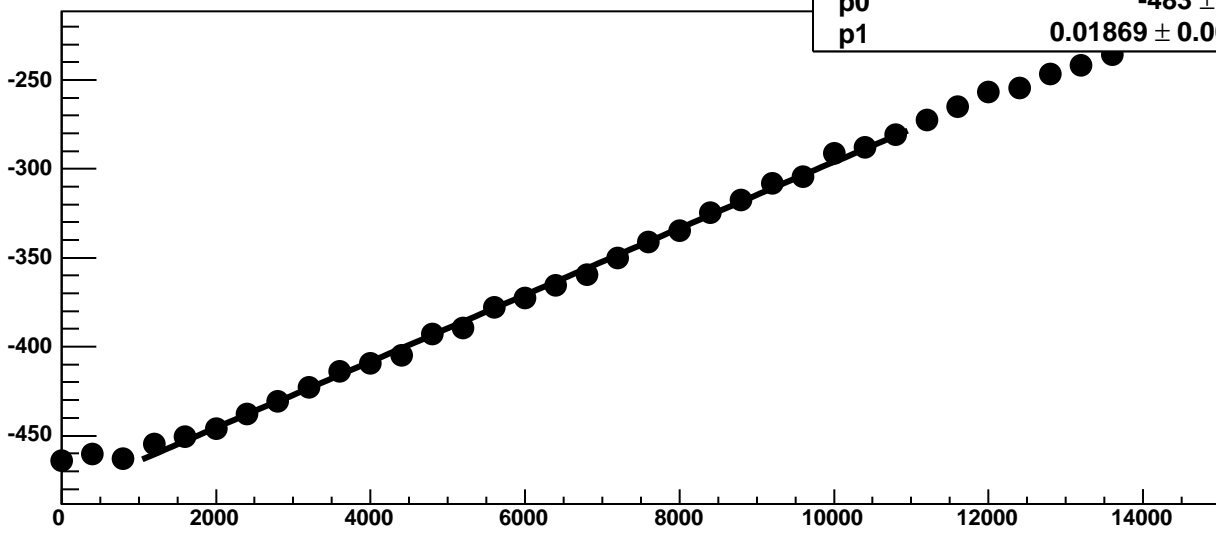
Chip 9, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

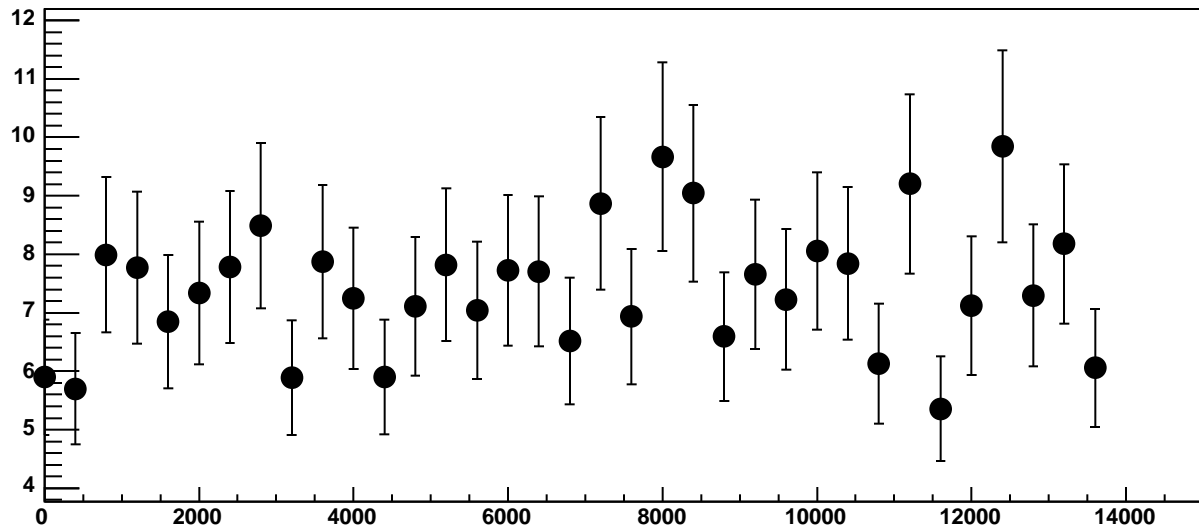


Chip 9, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

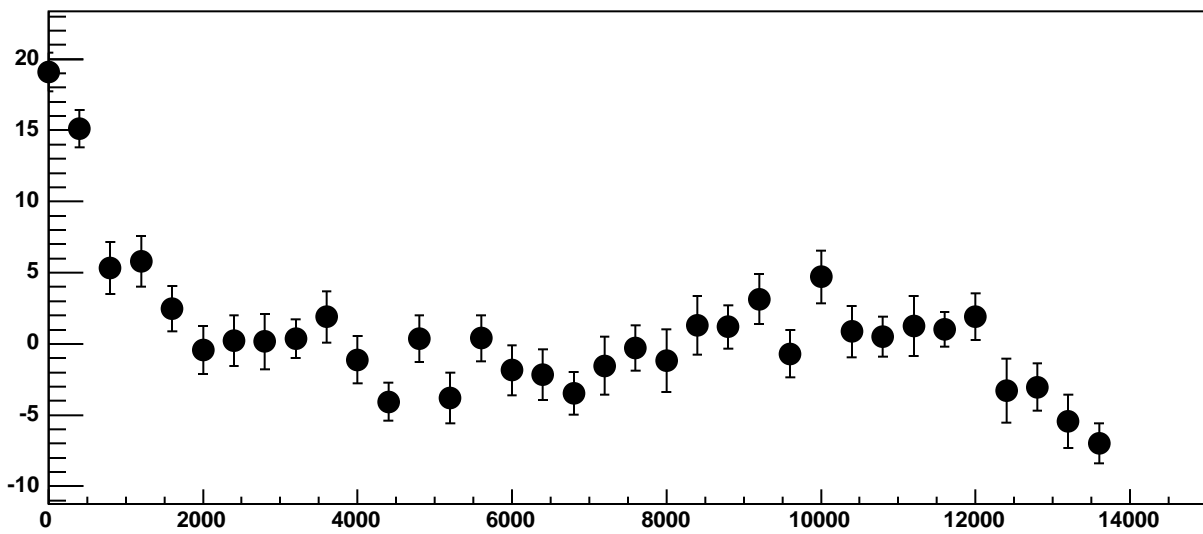


χ^2 / ndf 48.5 / 23
p0 -483 ± 0.7649
p1 0.01869 ± 0.0001156

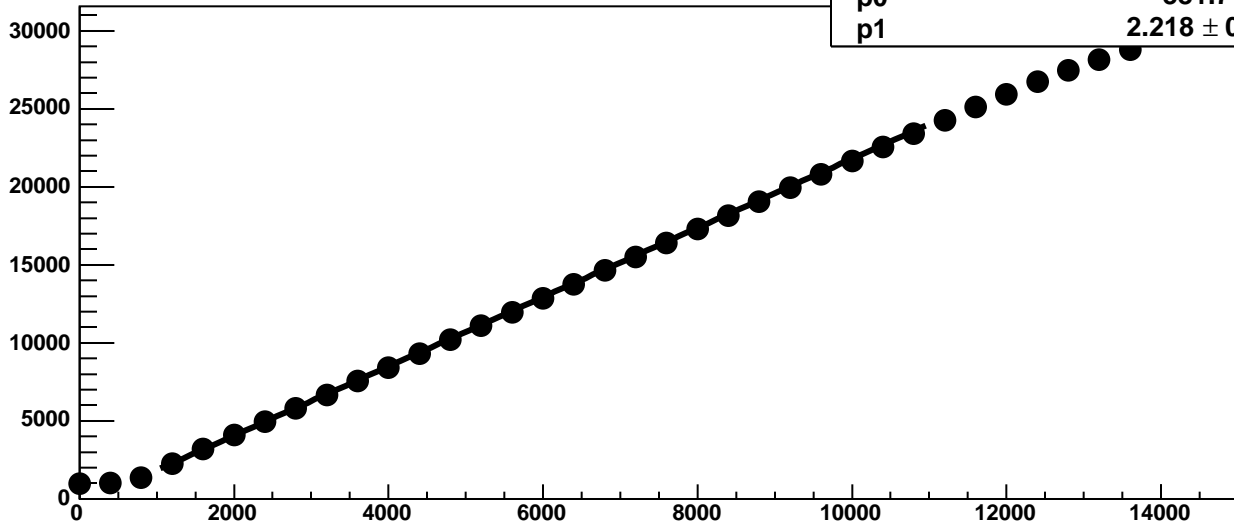
Chip 9, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



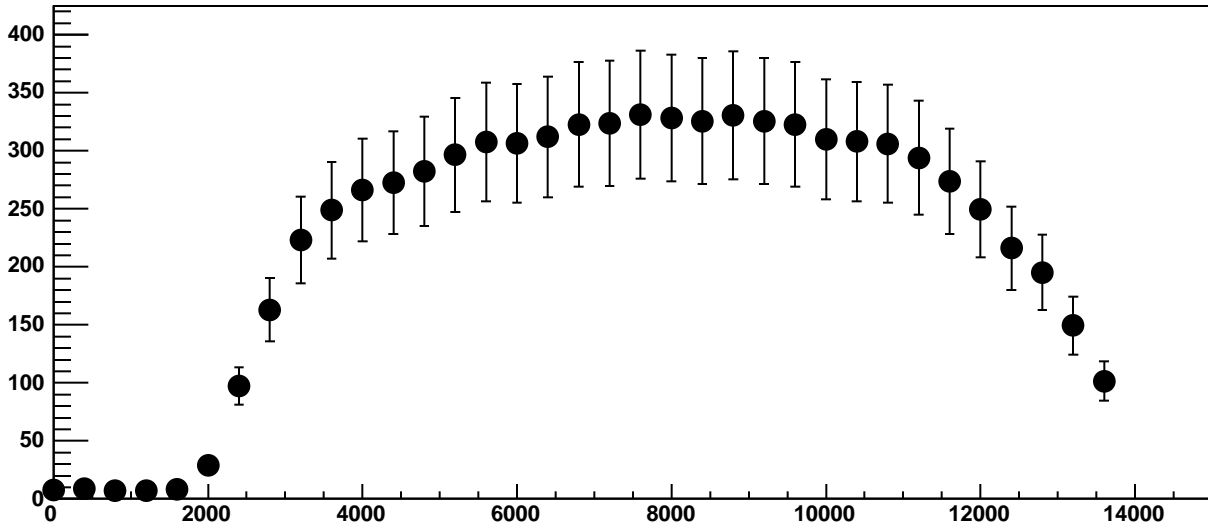
Chip 9, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



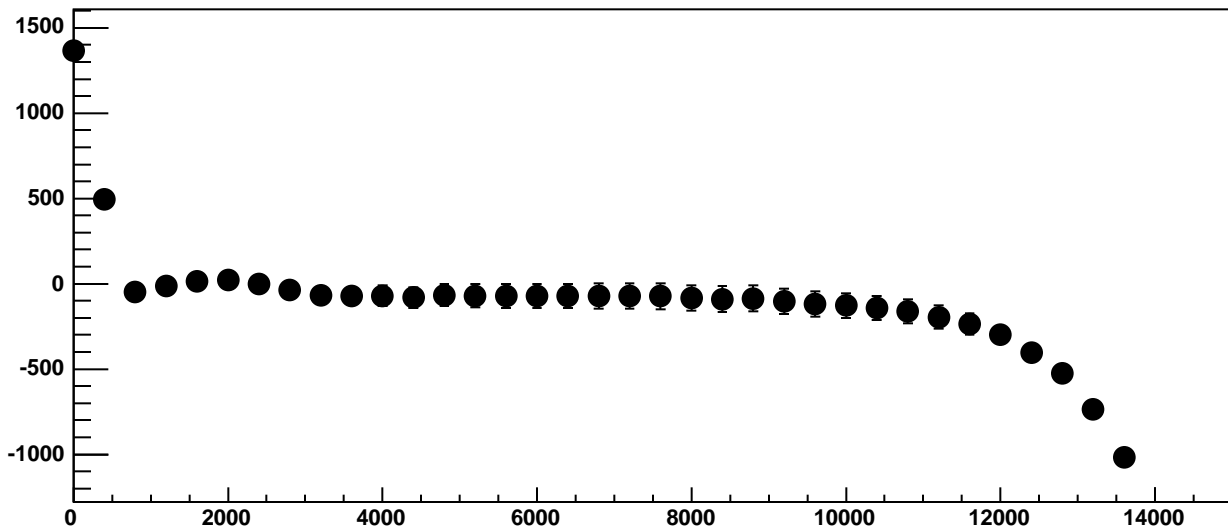
Chip 9, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC



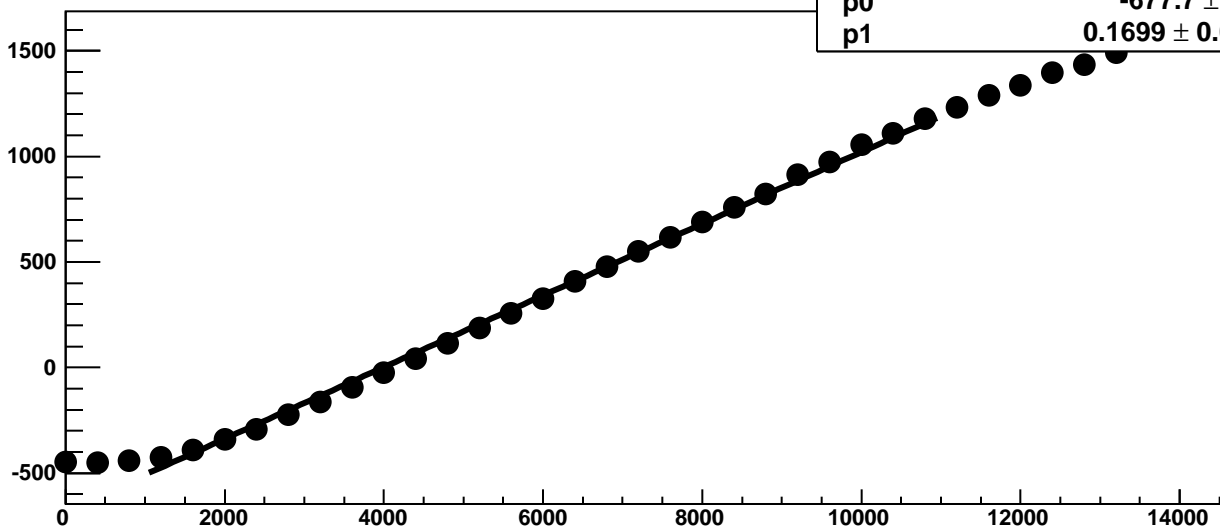
Chip 9, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



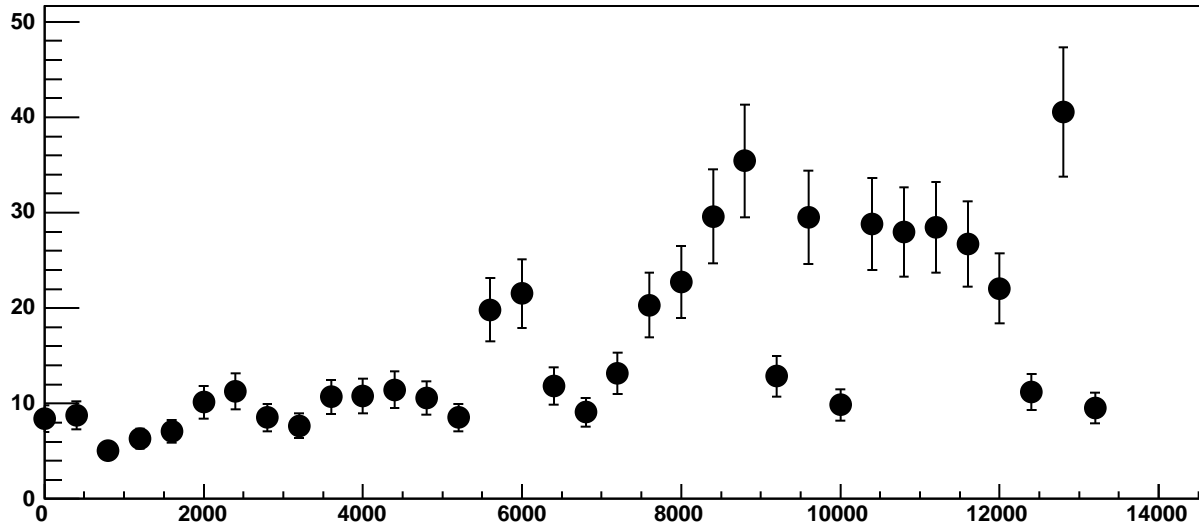
Chip 9, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC



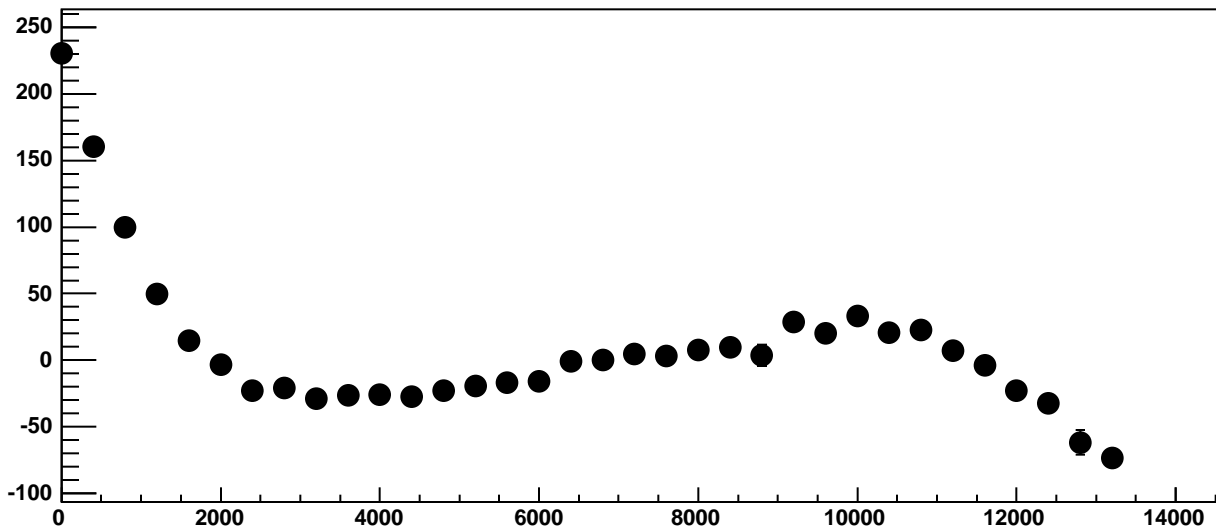
Chip 9, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



Chip 9, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC

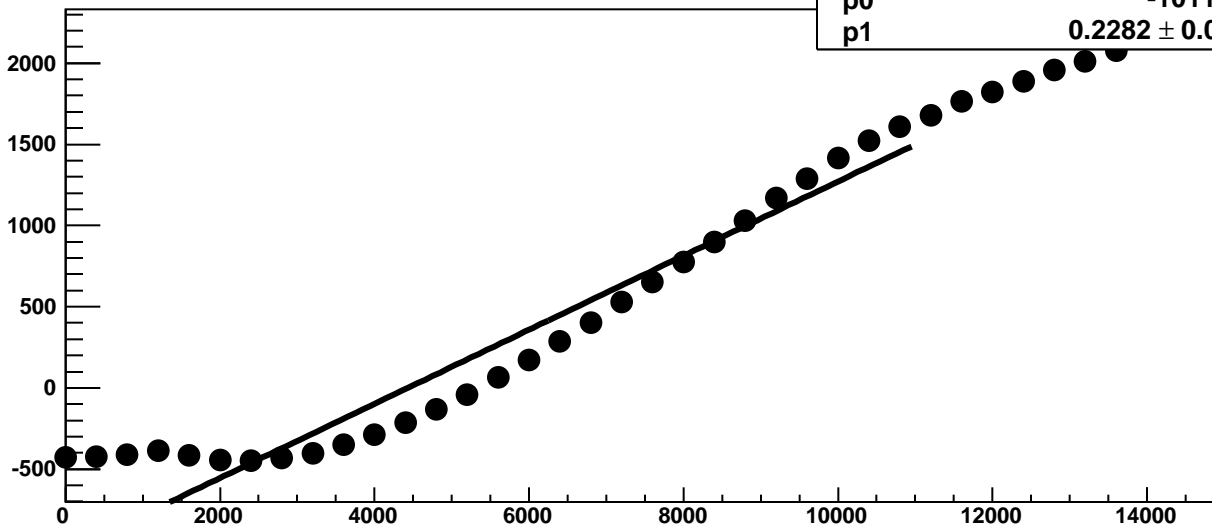


Chip 9, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

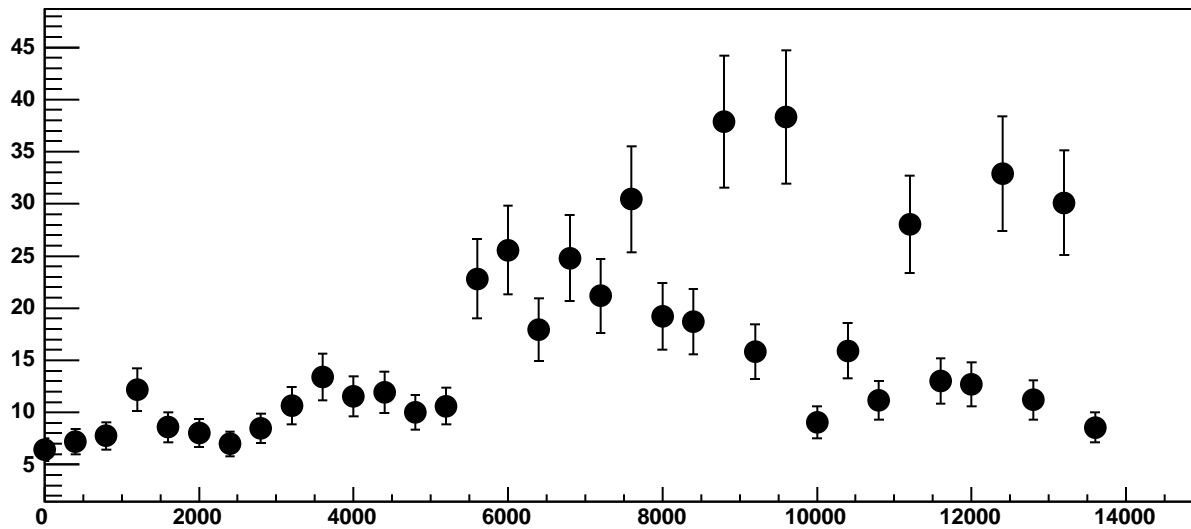


Chip 9, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

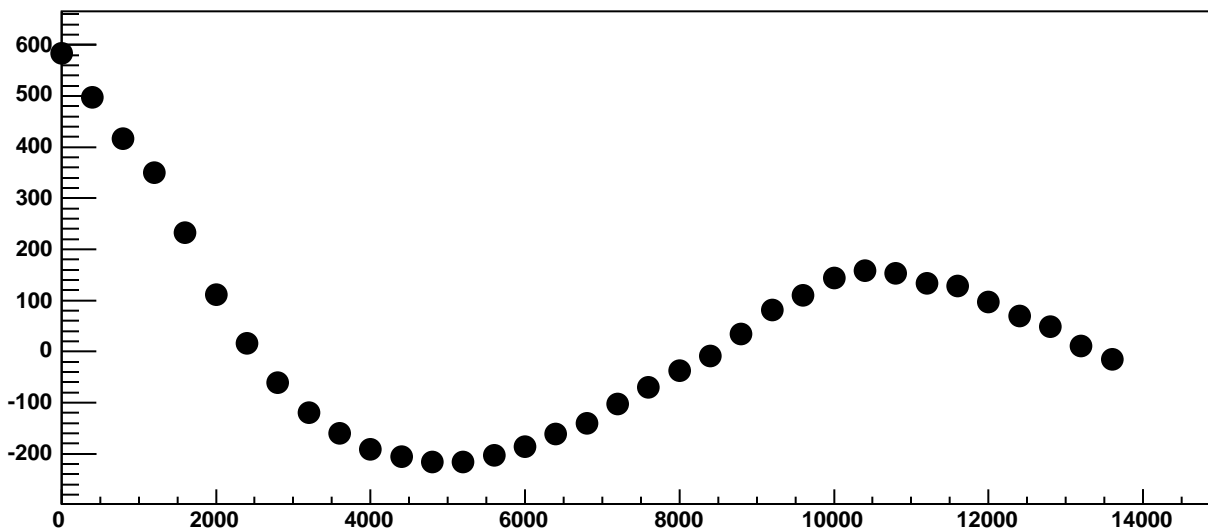
χ^2 / ndf 8.313e+04 / 23
p0 -1011 ± 1.043
p1 0.2282 ± 0.0001868



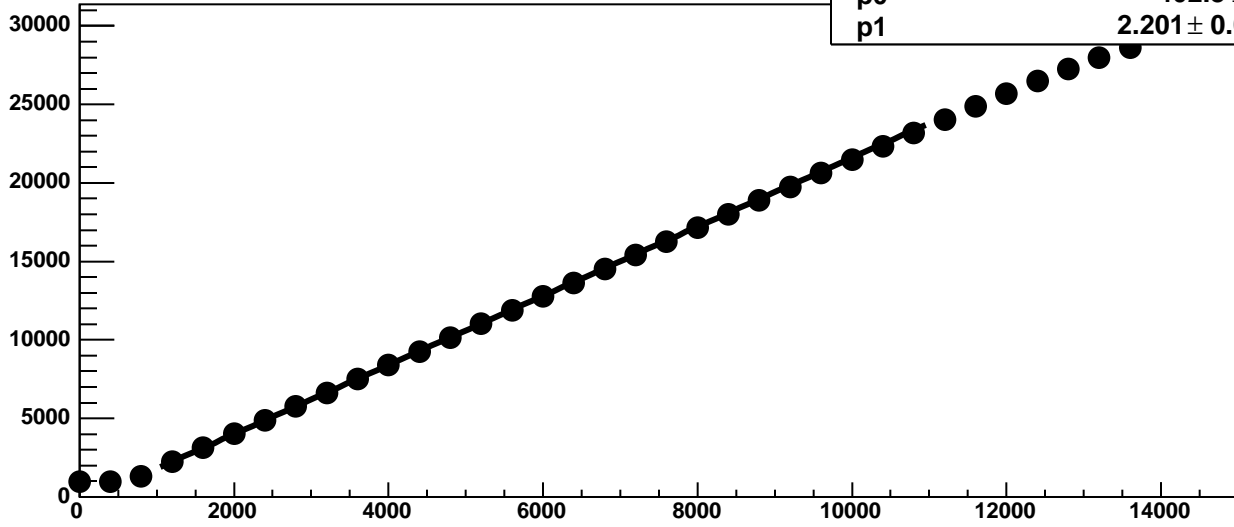
Chip 9, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

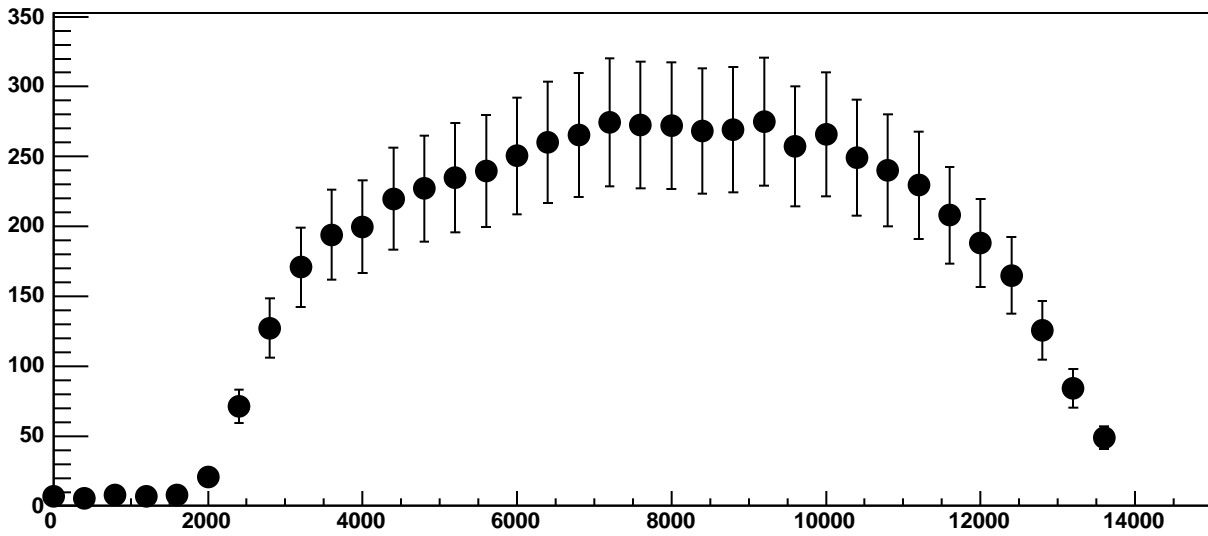


Chip 9, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC

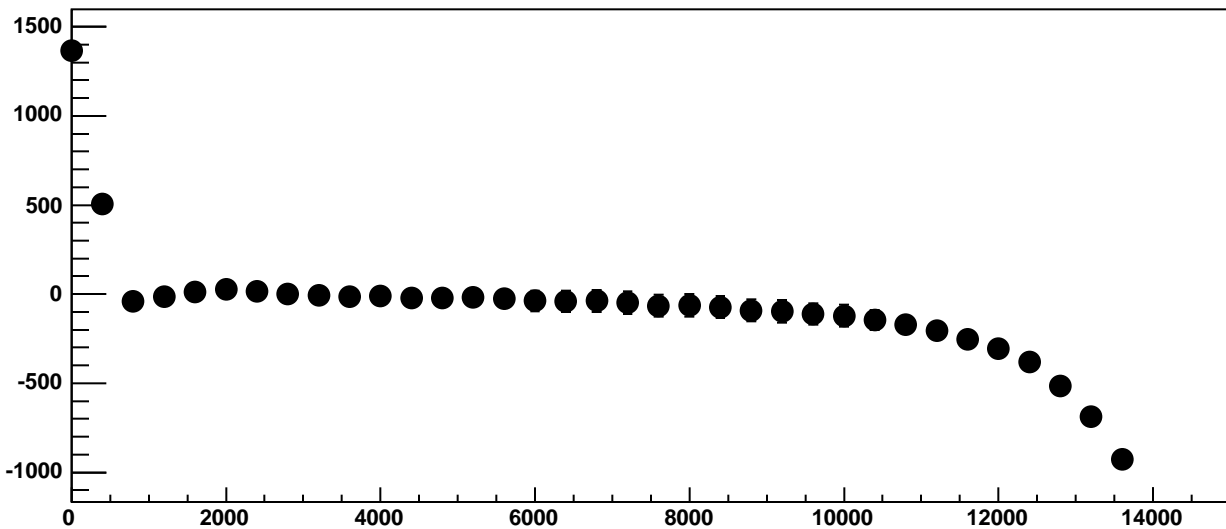


χ^2 / ndf 183.9 / 23
p0 -402.8 ± 3.108
p1 2.201 ± 0.001965

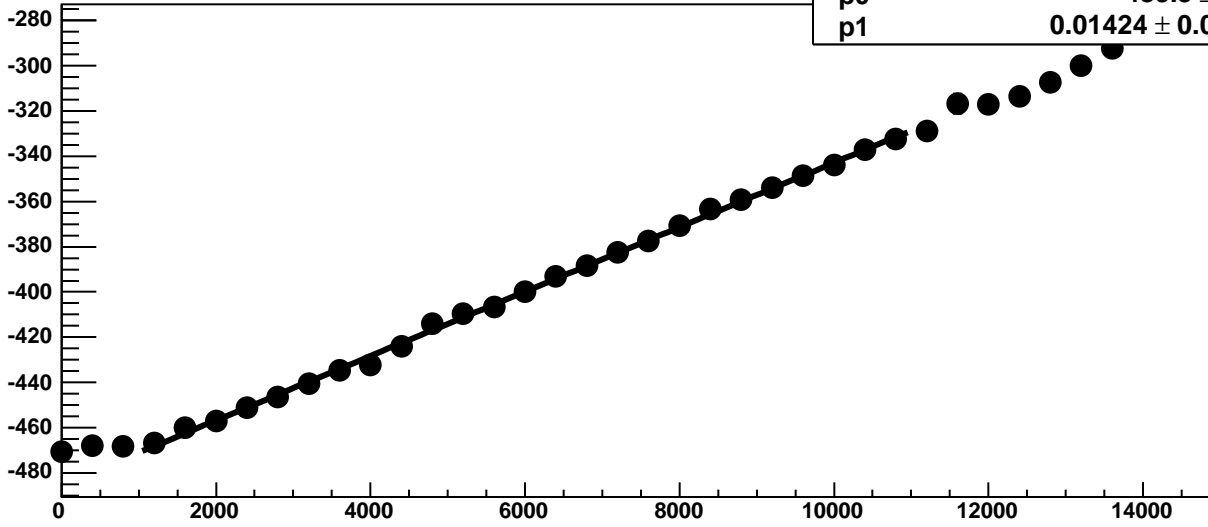
Chip 9, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



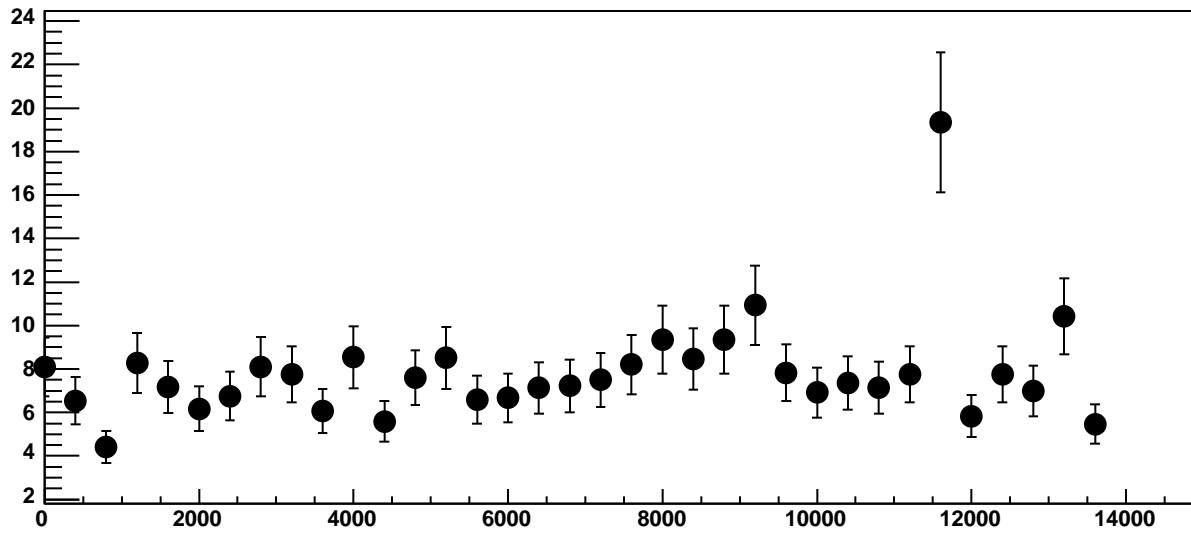
Chip 9, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC



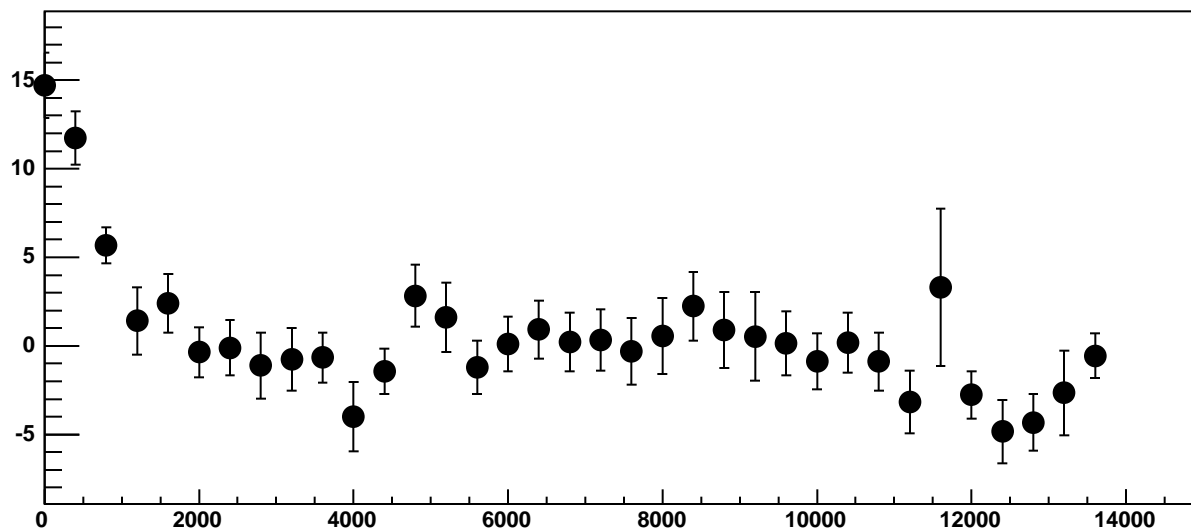
Chip 9, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



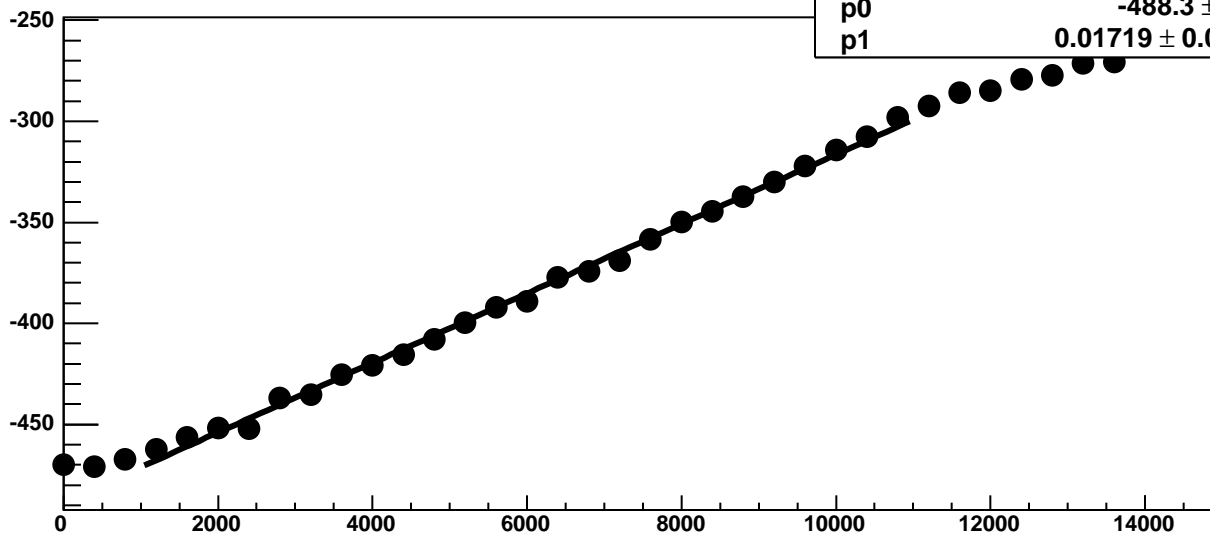
Chip 9, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



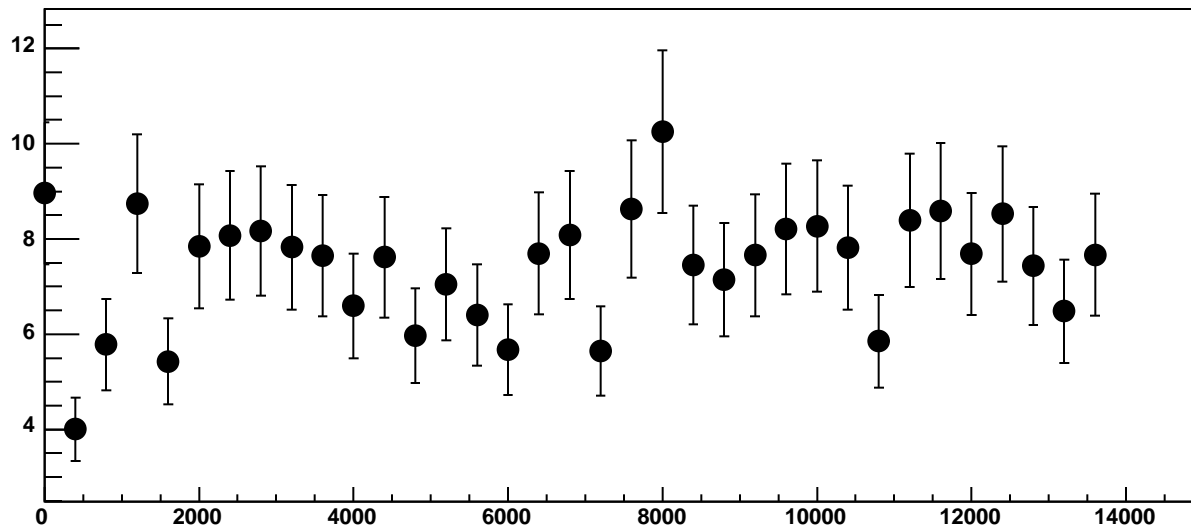
Chip 9, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



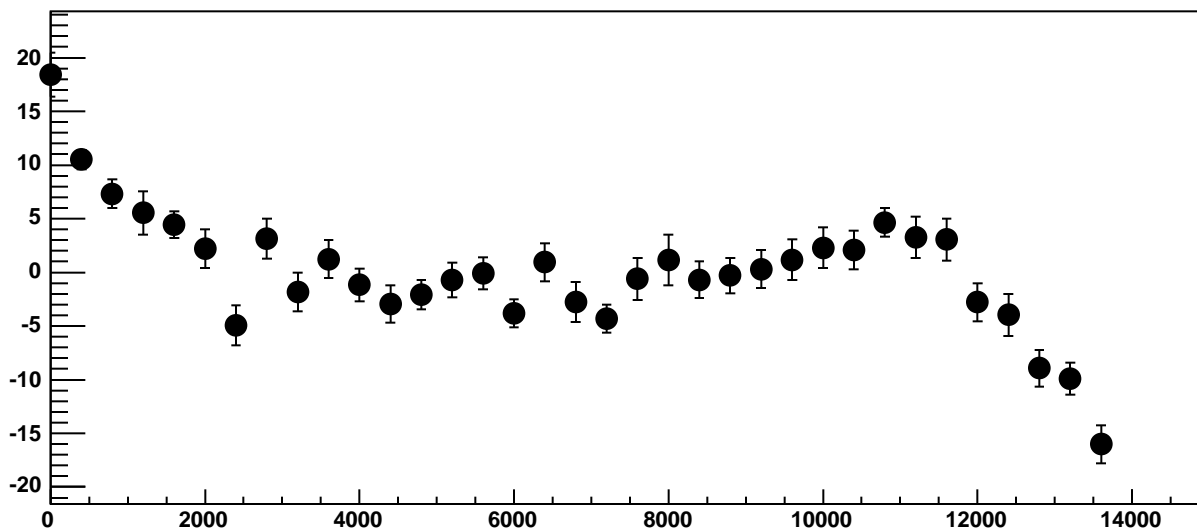
Chip 9, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



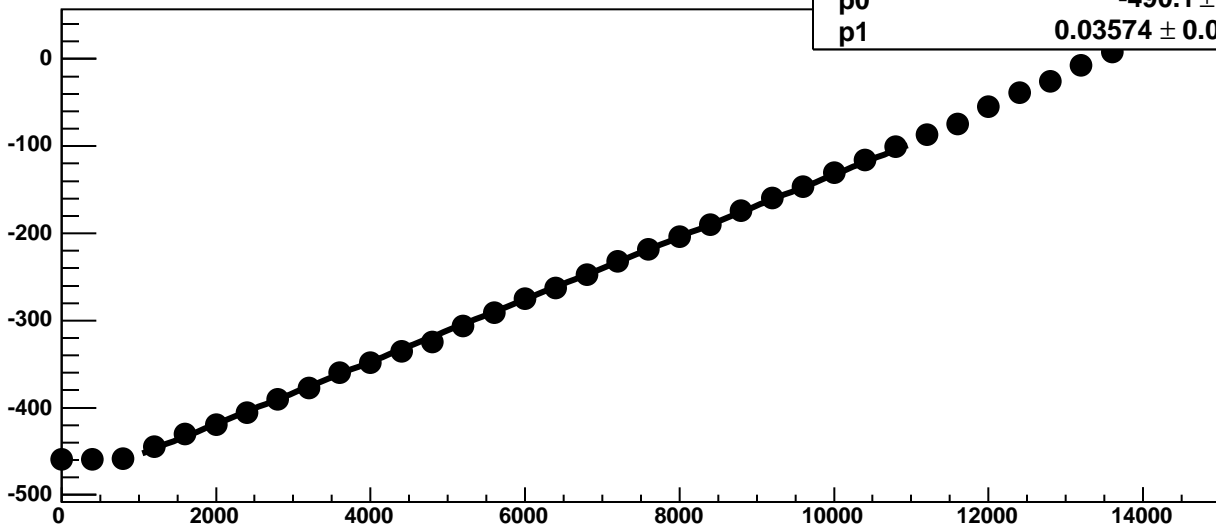
Chip 9, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

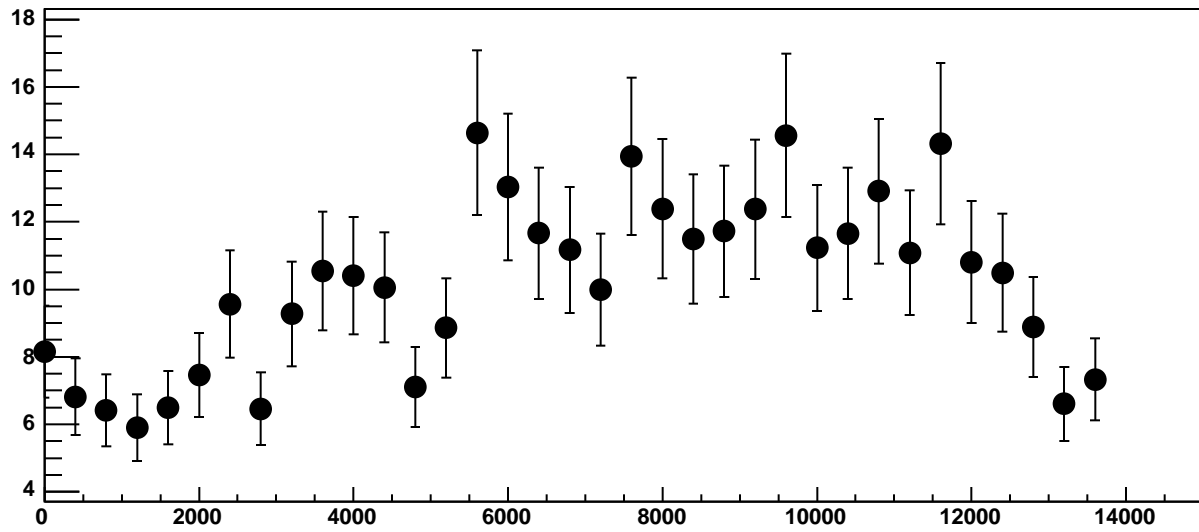


Chip 9, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

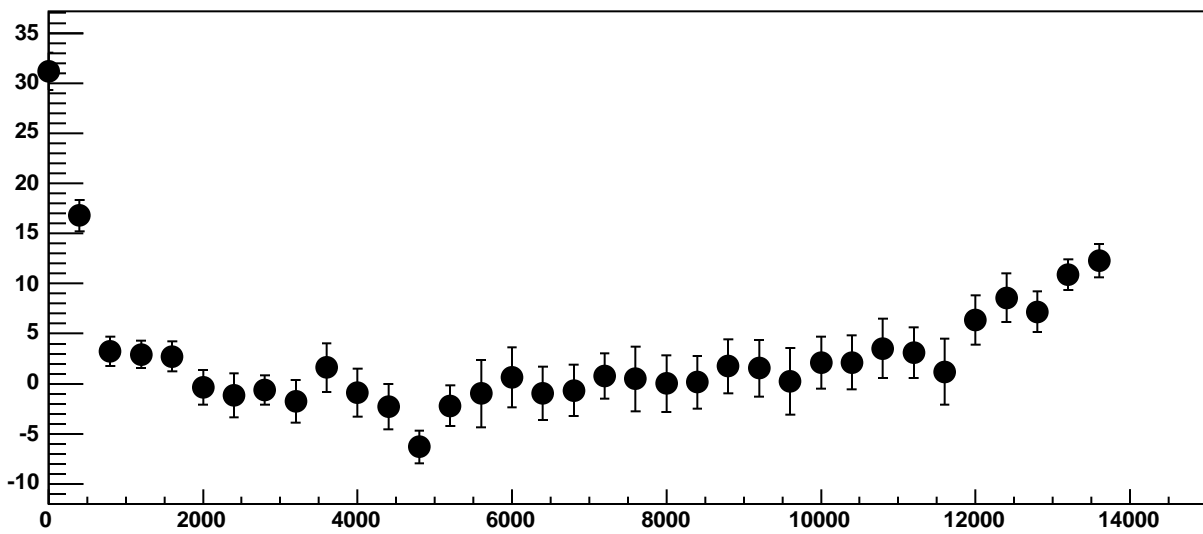


χ^2 / ndf 30.79 / 23
p0 -490.1 ± 0.8405
p1 0.03574 ± 0.0001511

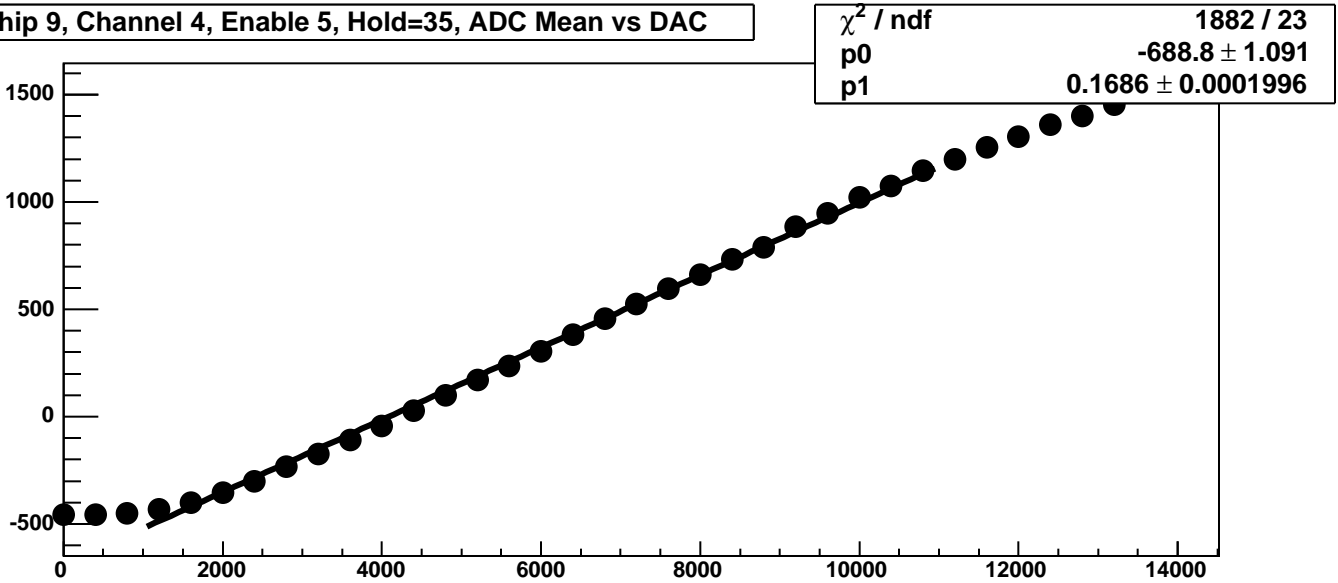
Chip 9, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



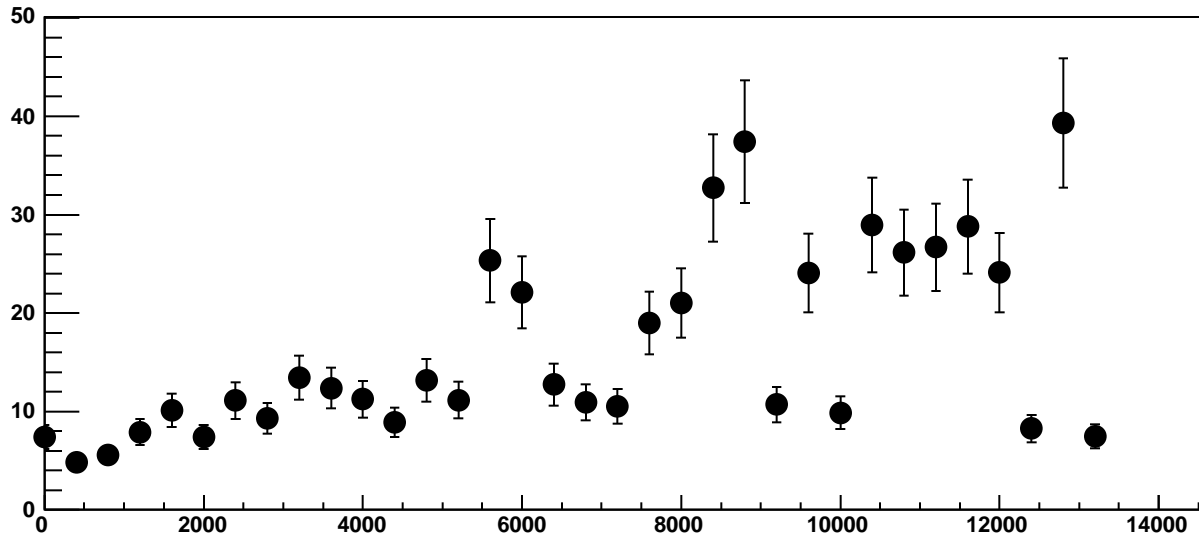
Chip 9, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC



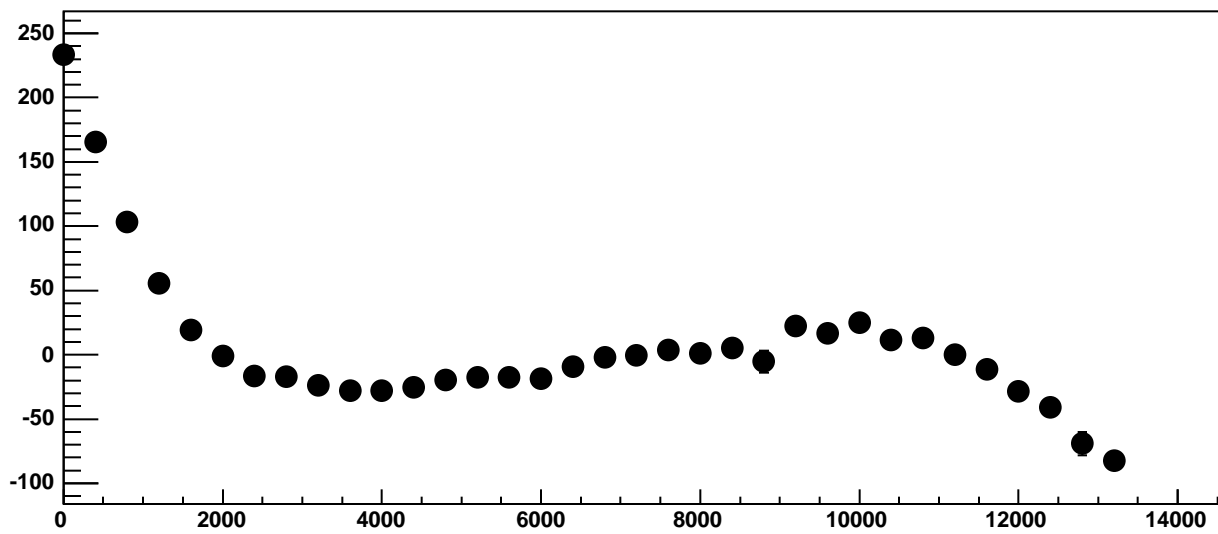
Chip 9, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



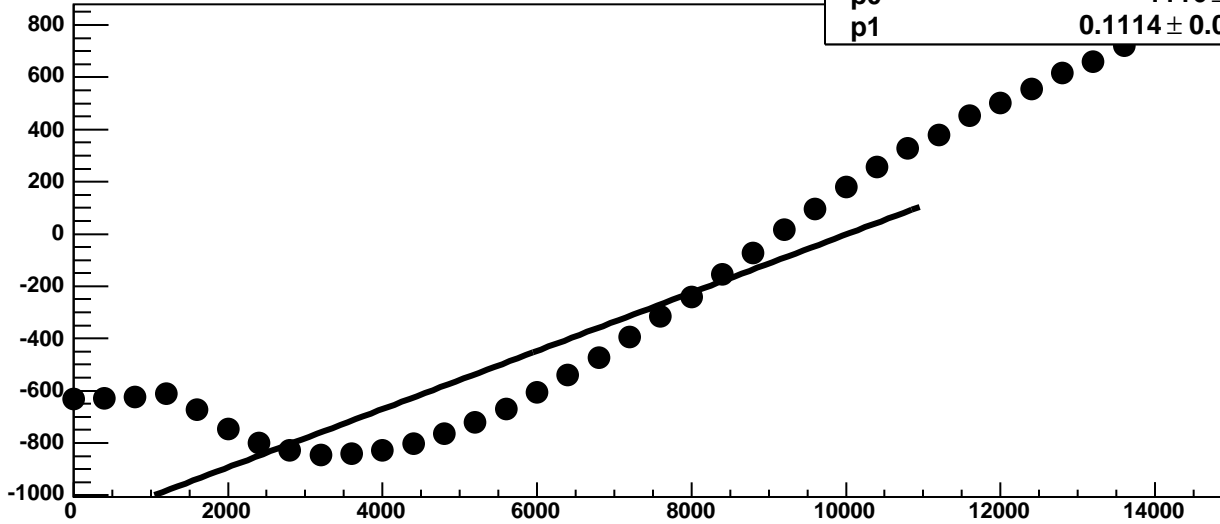
Chip 9, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

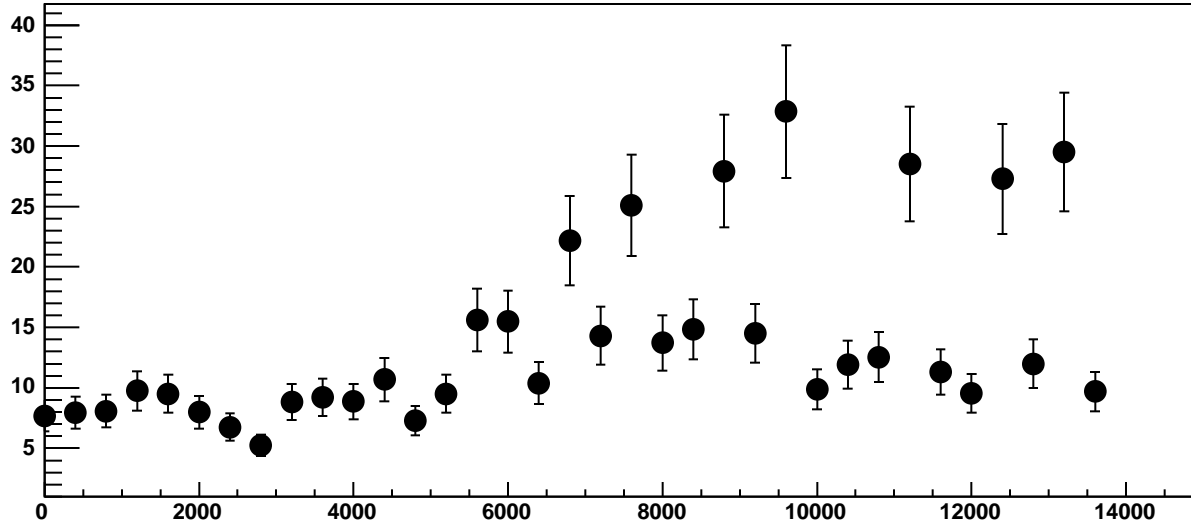


Chip 9, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

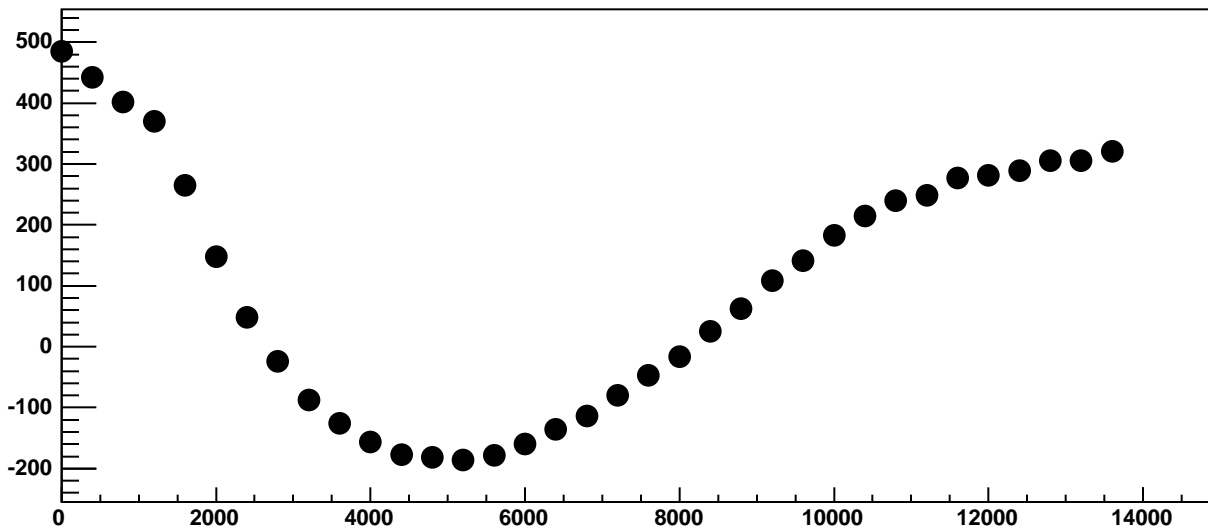


χ^2 / ndf 1.157e+05 / 23
p0 -1116 ± 0.9253
p1 0.1114 ± 0.0001734

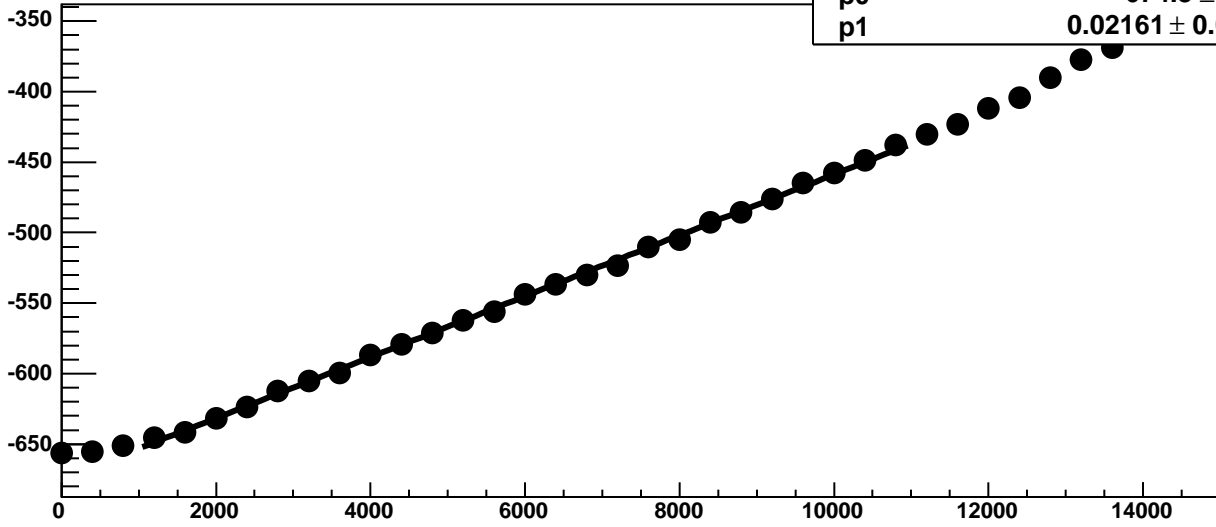
Chip 9, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

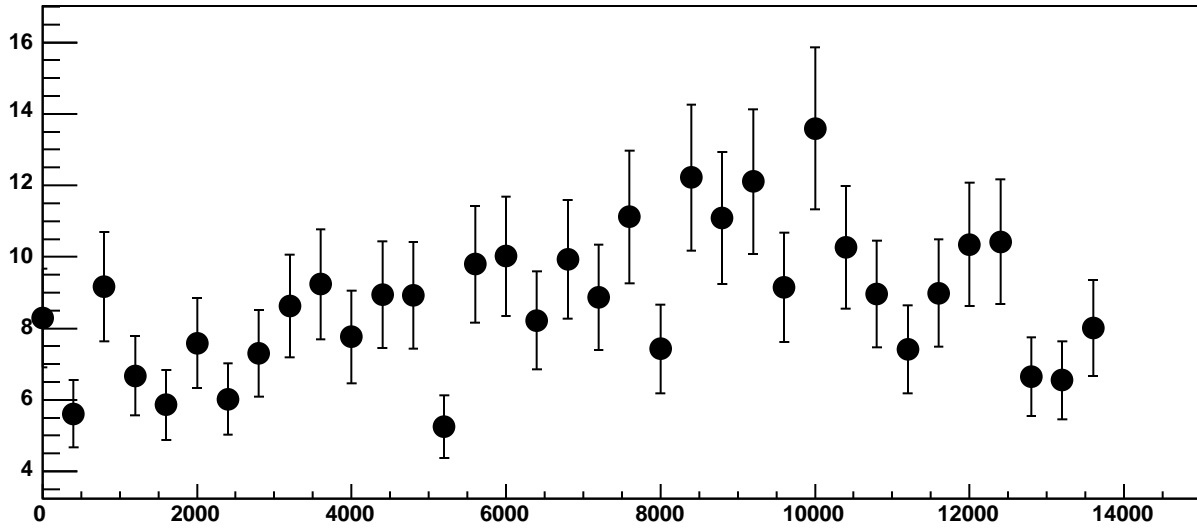


Chip 9, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

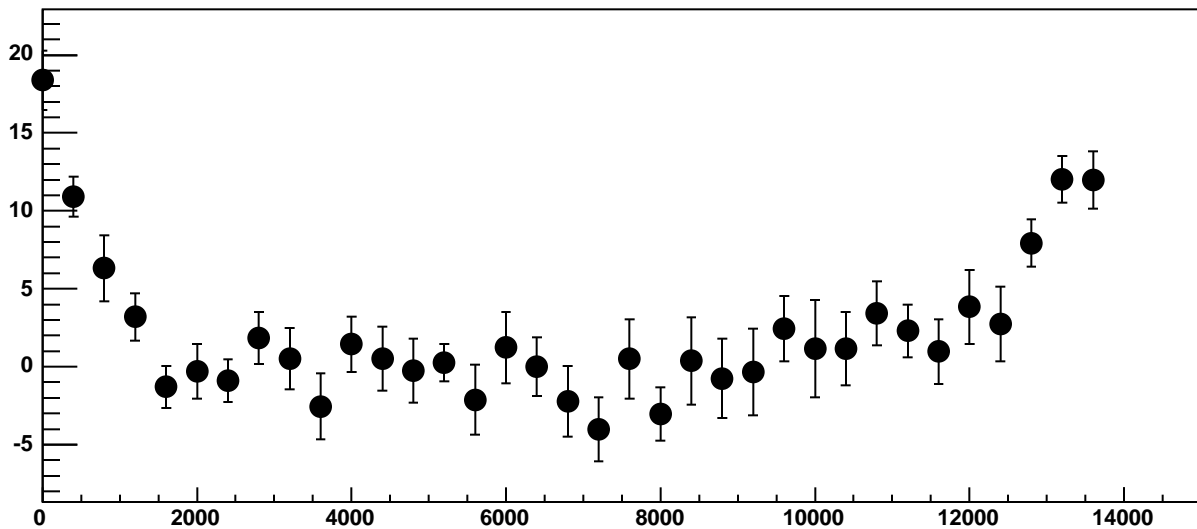


χ^2 / ndf 23.08 / 23
p0 -674.8 ± 0.7823
p1 0.02161 ± 0.000134

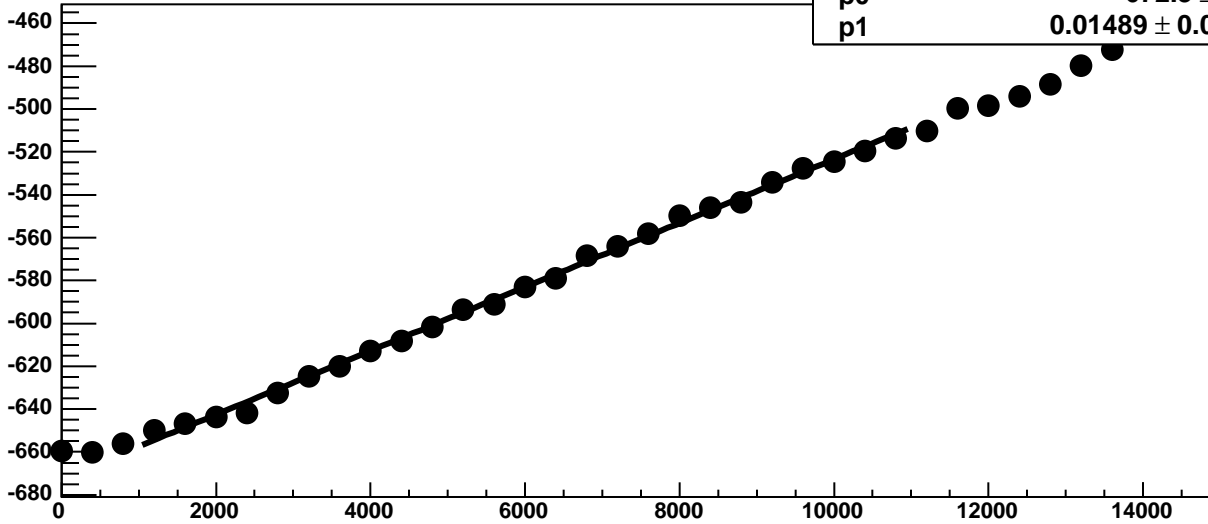
Chip 9, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

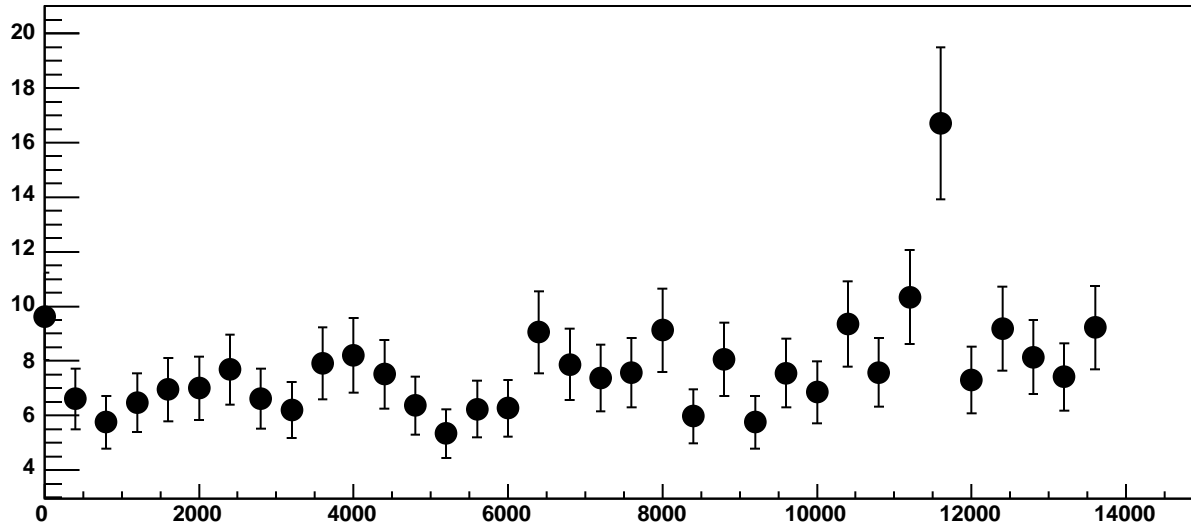


Chip 9, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

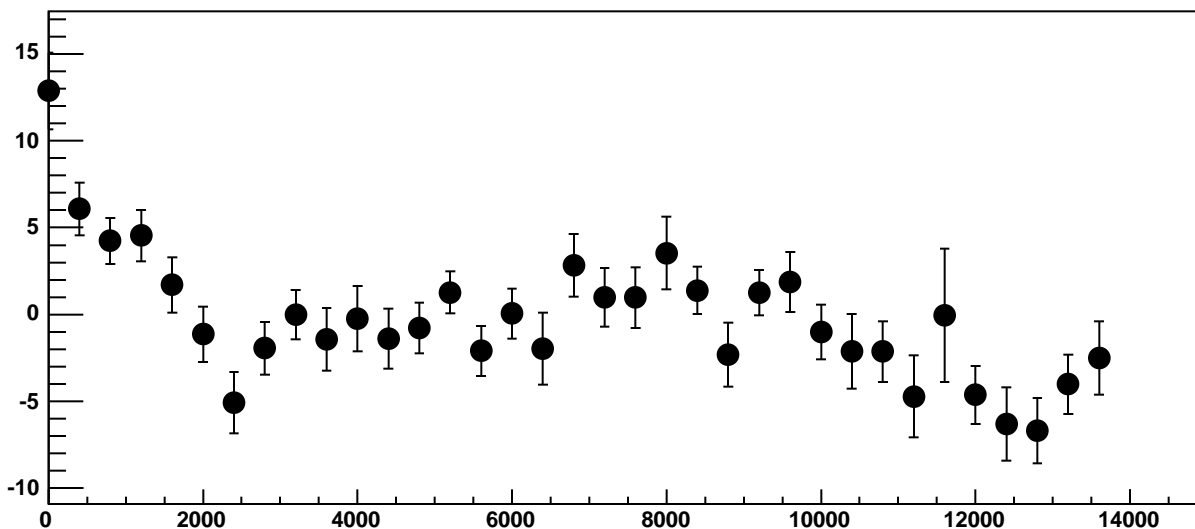


χ^2 / ndf 40.33 / 23
p0 -672.3 ± 0.7369
p1 0.01489 ± 0.0001134

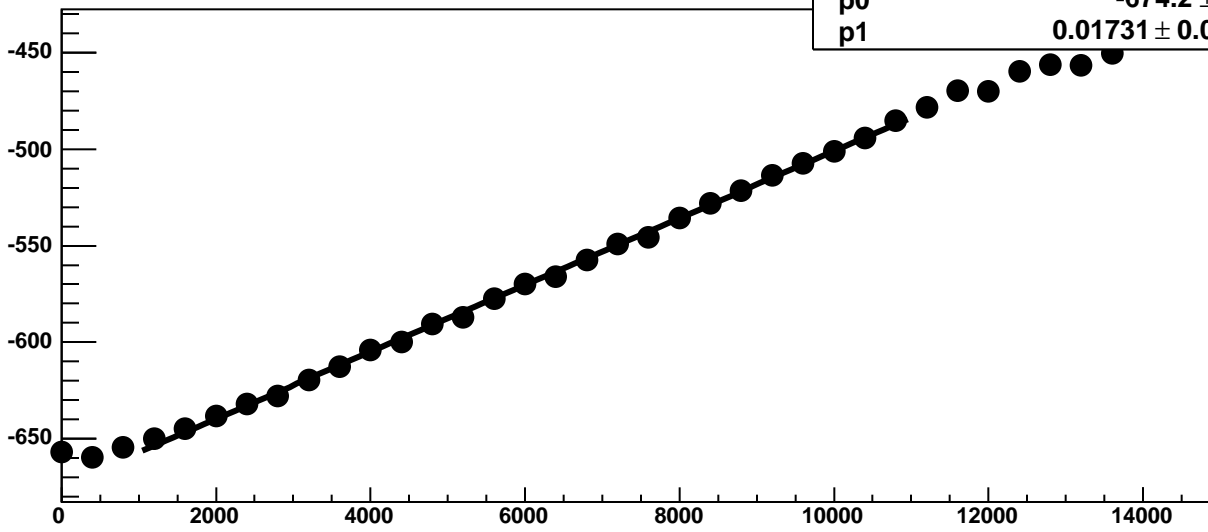
Chip 9, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

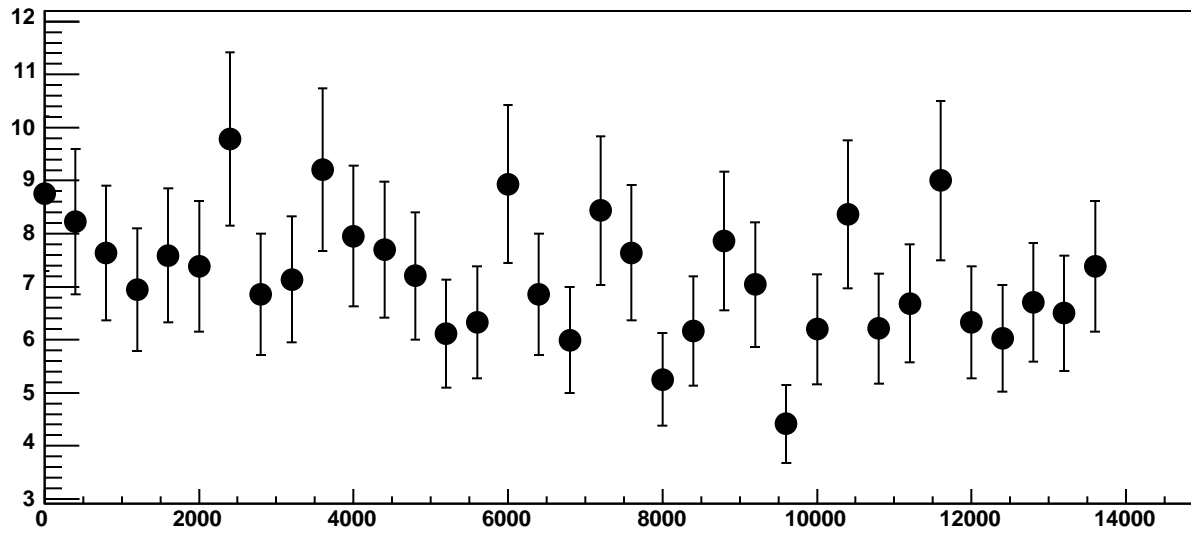


Chip 9, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

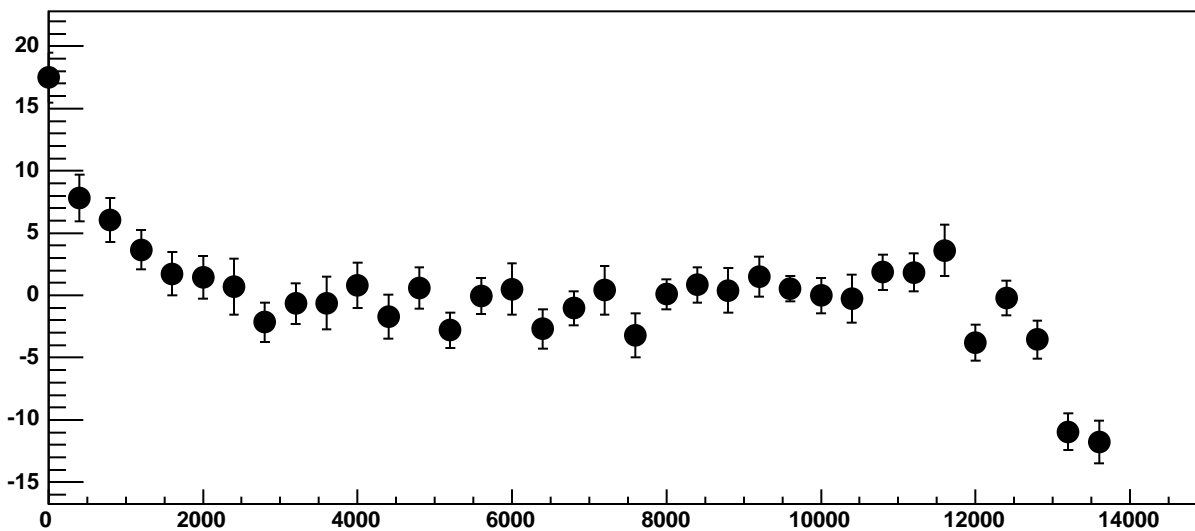


χ^2 / ndf 24.68 / 23
p0 -674.2 ± 0.7736
p1 0.01731 ± 0.0001094

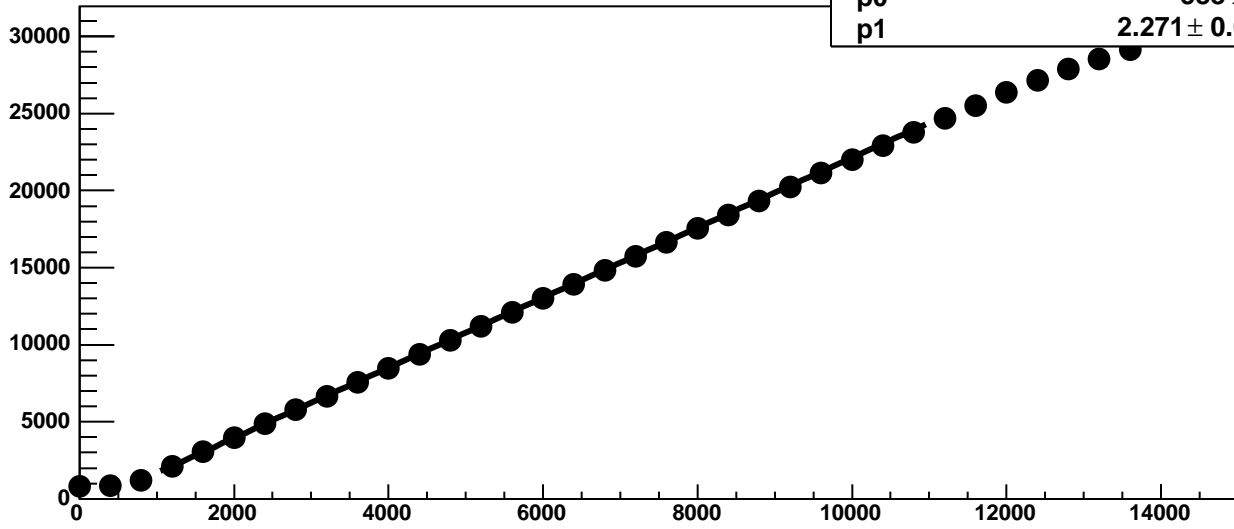
Chip 9, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



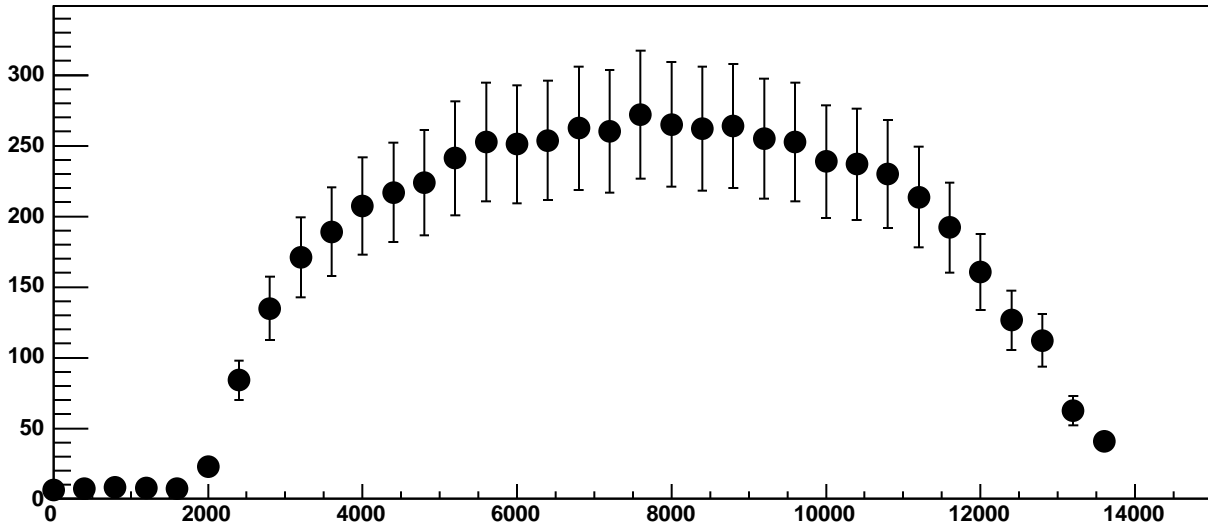
Chip 9, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



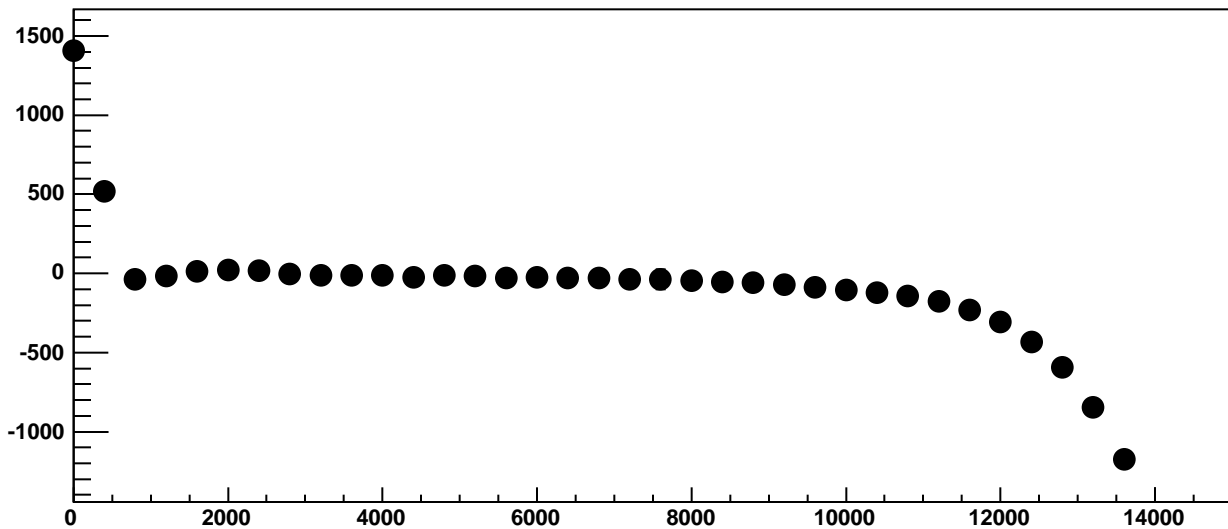
Chip 9, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC



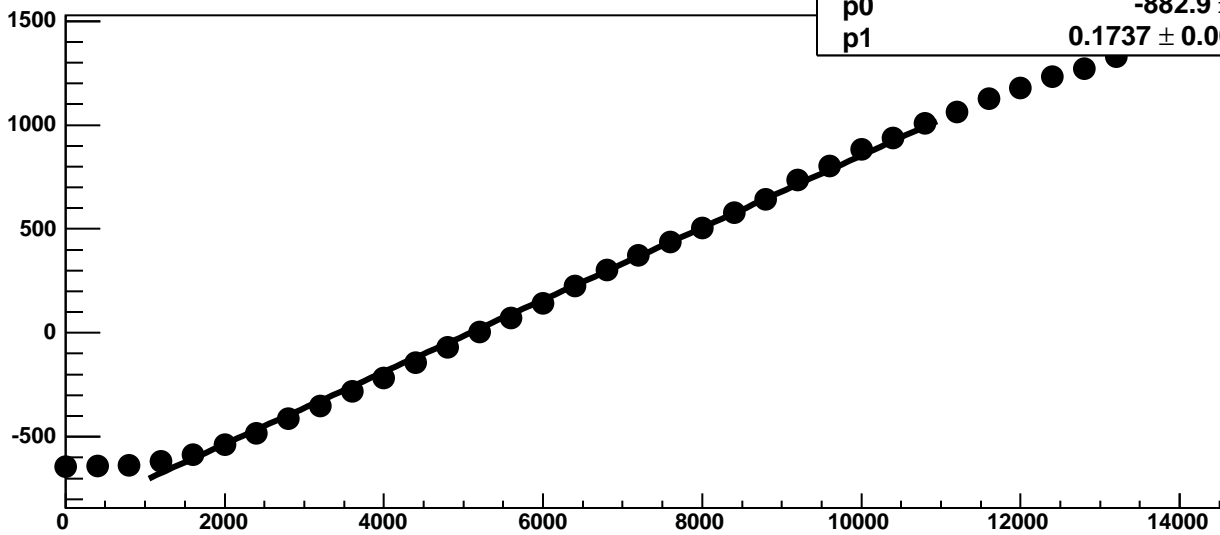
Chip 9, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC



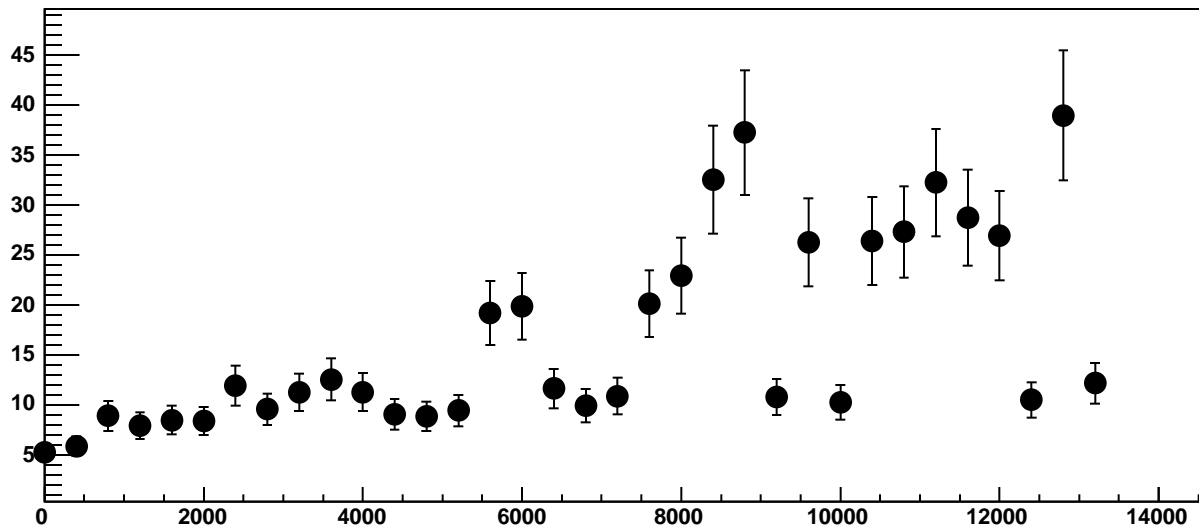
Chip 9, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC



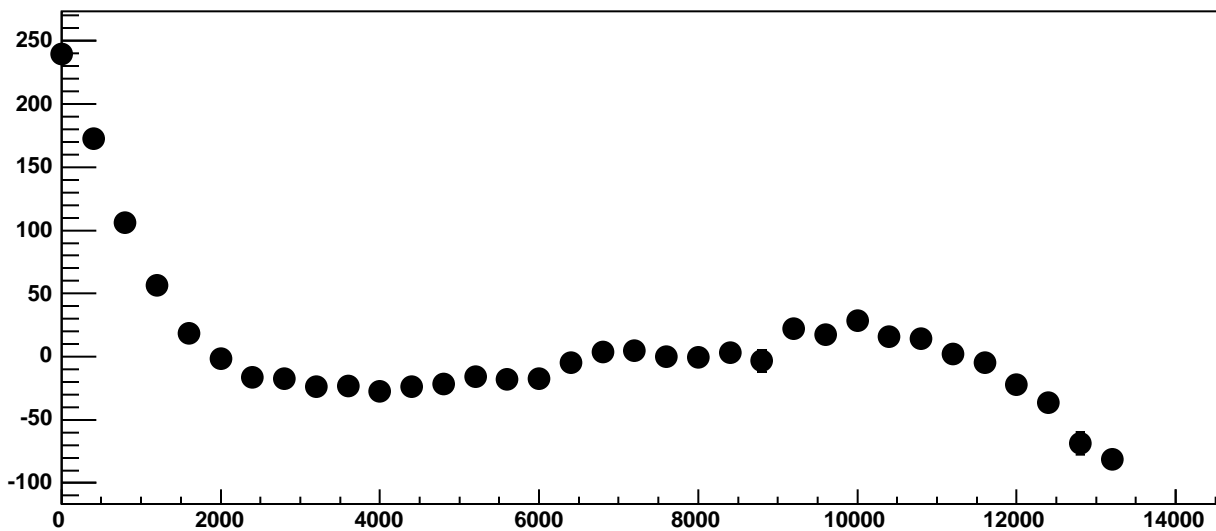
Chip 9, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



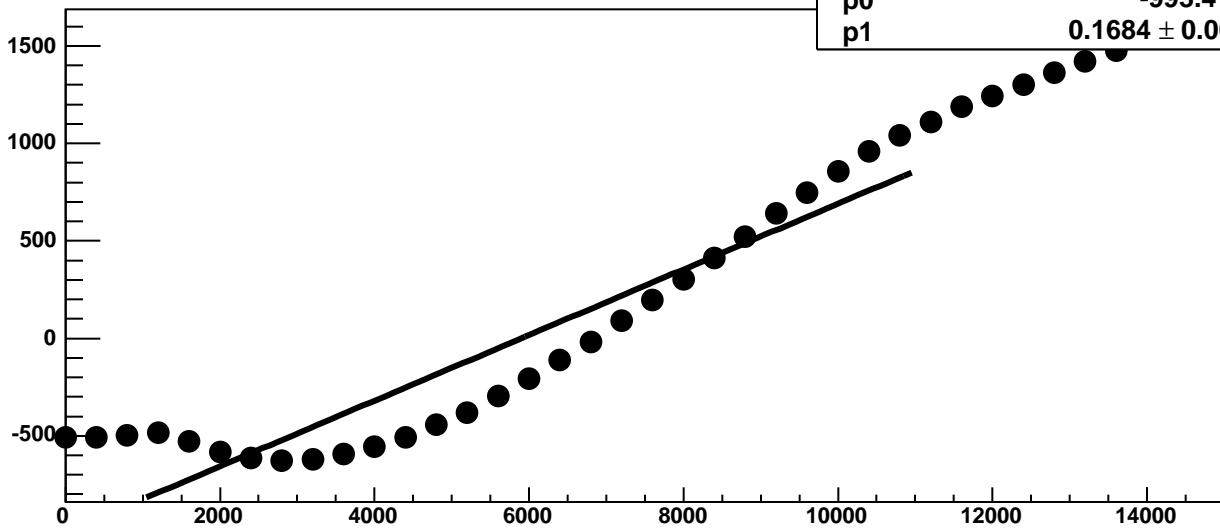
Chip 9, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



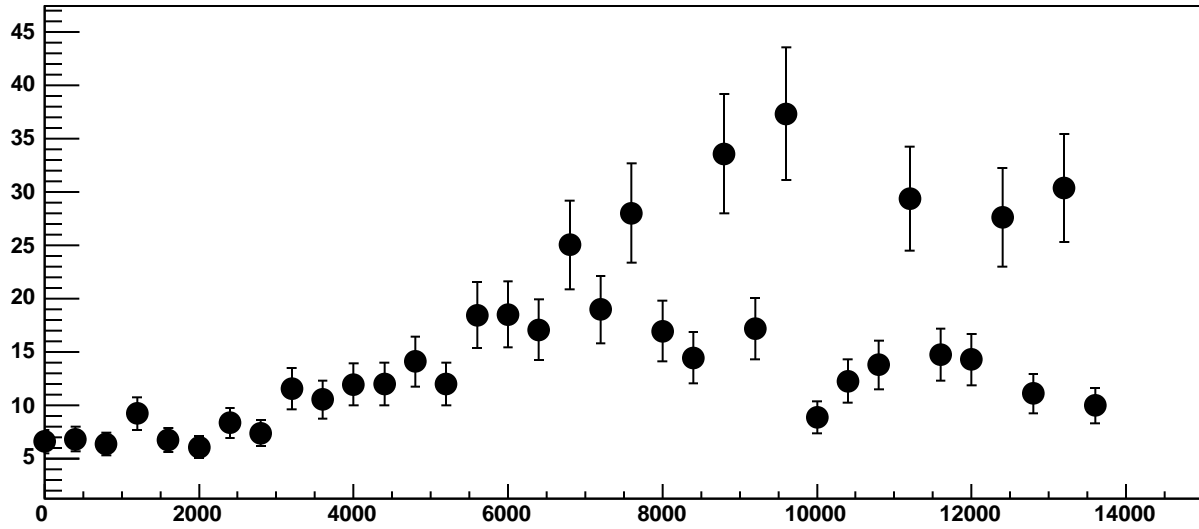
Chip 9, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



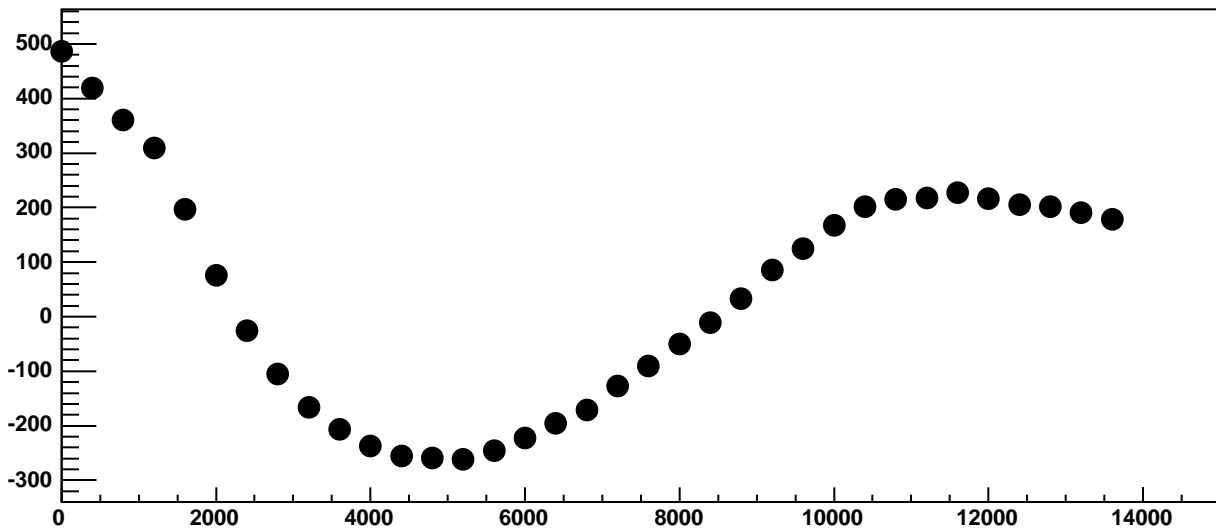
Chip 9, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



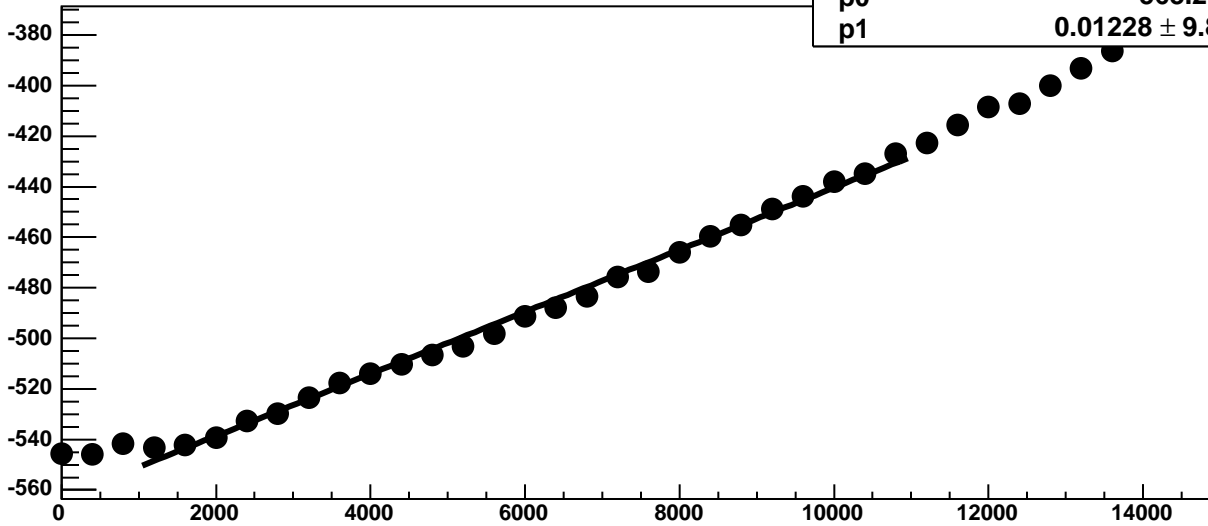
Chip 9, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

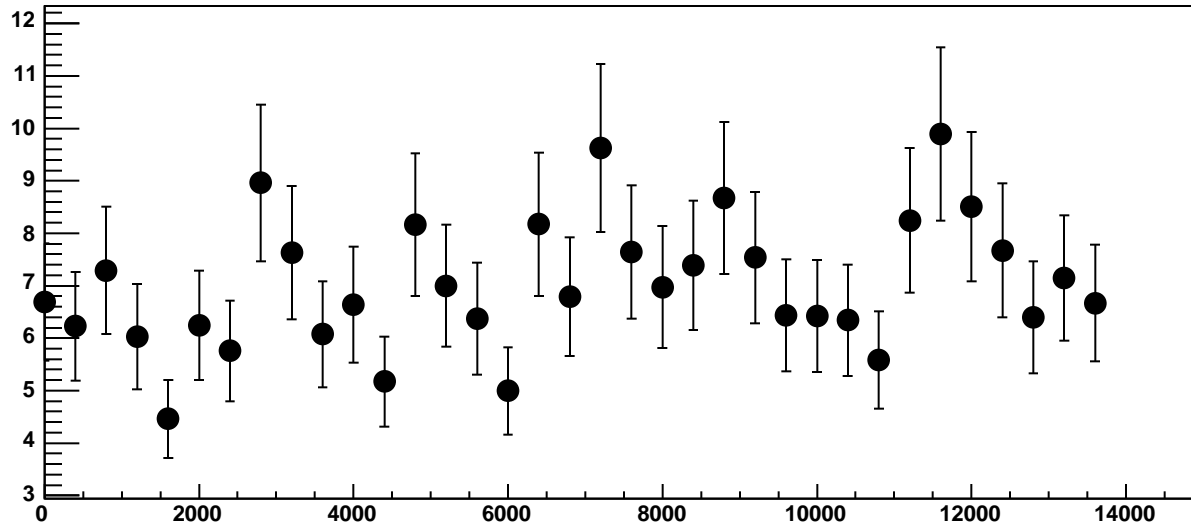


Chip 9, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

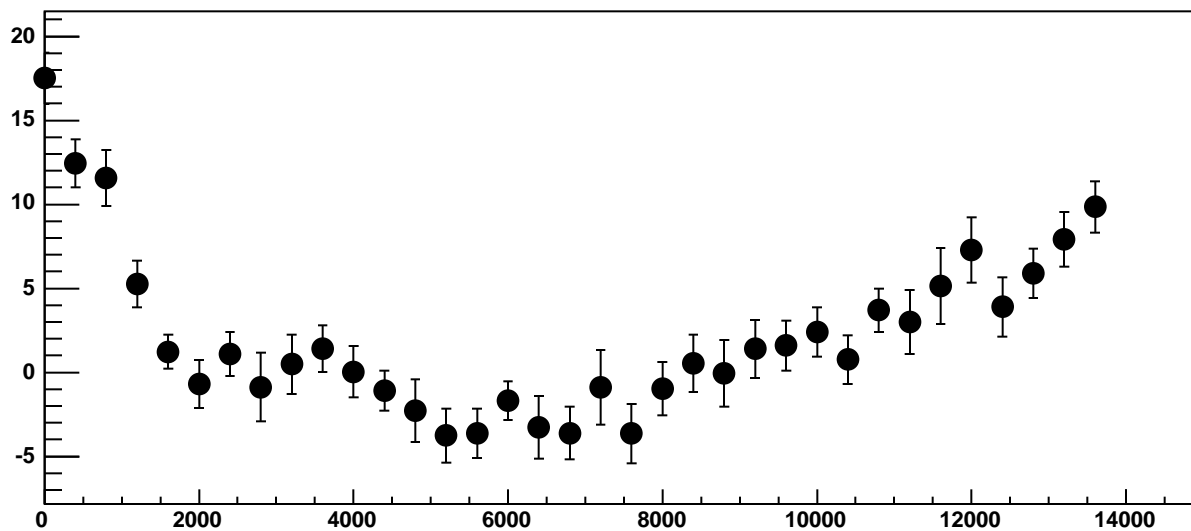


χ^2 / ndf 60.59 / 23
p0 -563.2 ± 0.634
p1 $0.01228 \pm 9.812e-05$

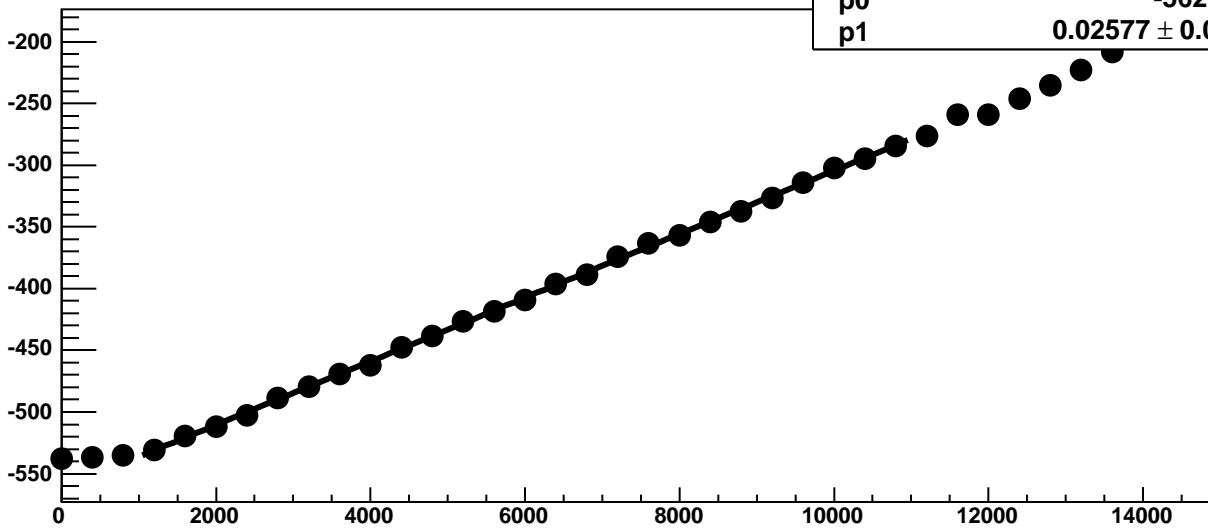
Chip 9, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

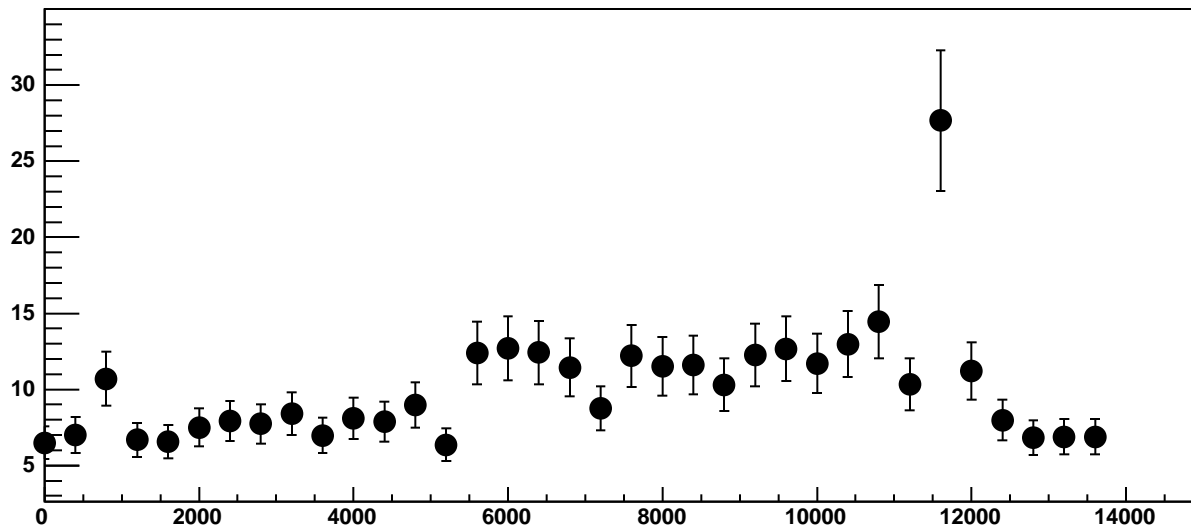


Chip 9, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC

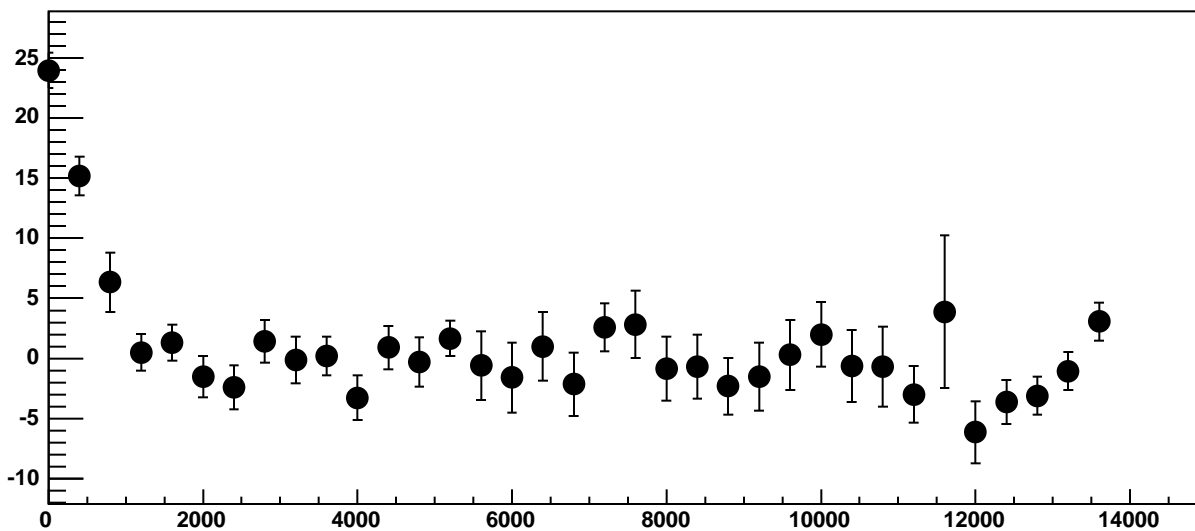


χ^2 / ndf 14.6 / 23
p0 -562 ± 0.847
p1 0.02577 ± 0.0001531

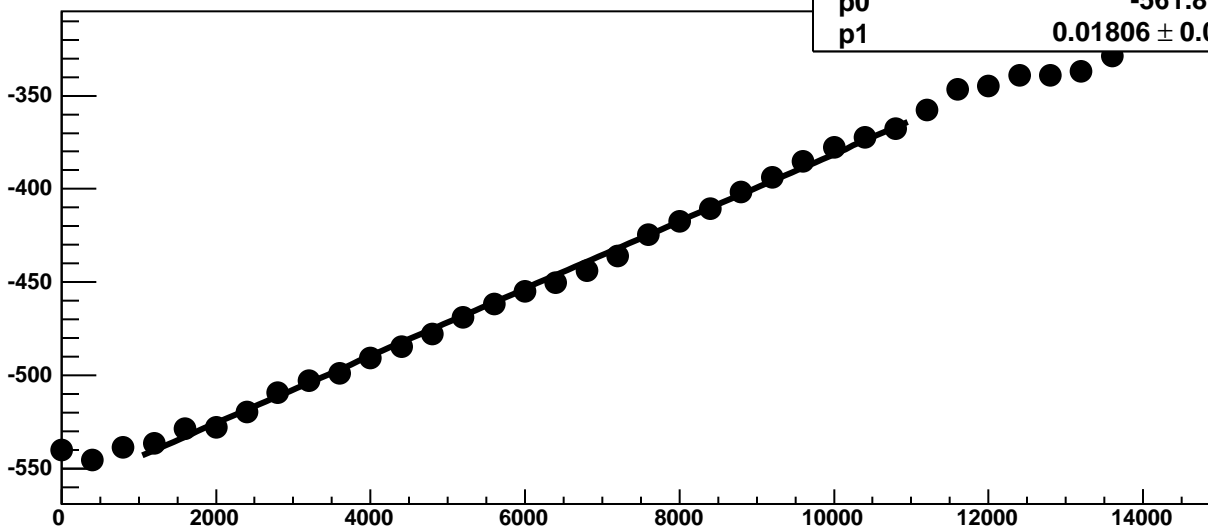
Chip 9, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC

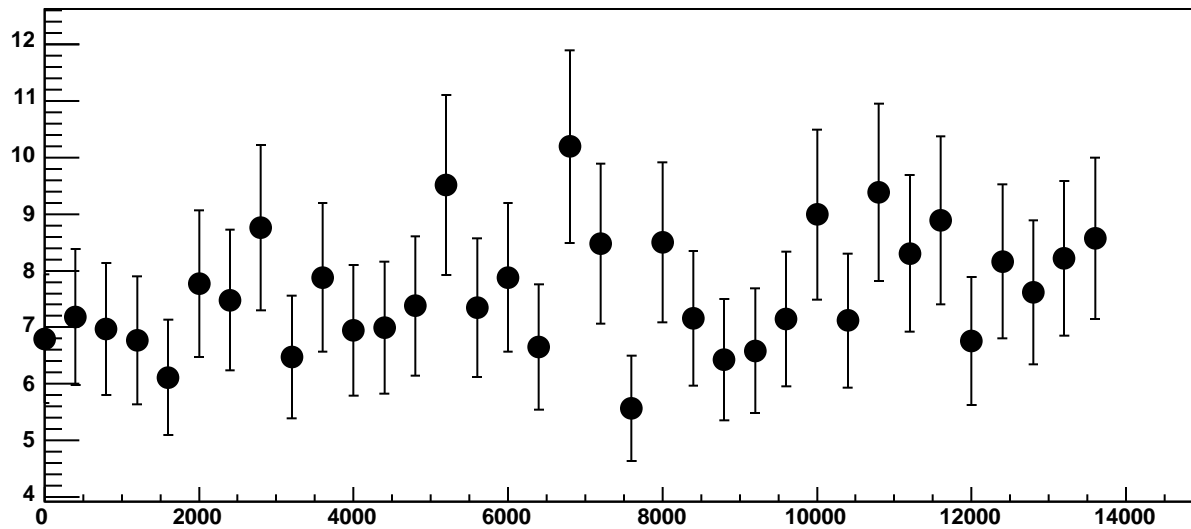


Chip 9, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

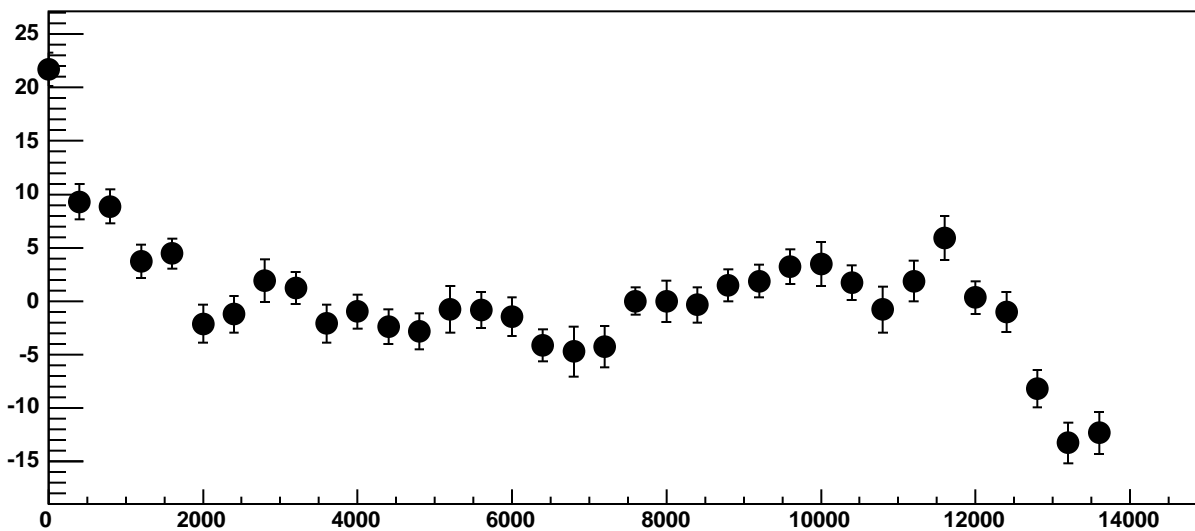


χ^2 / ndf 54.21 / 23
p0 -561.8 ± 0.763
p1 0.01806 ± 0.0001162

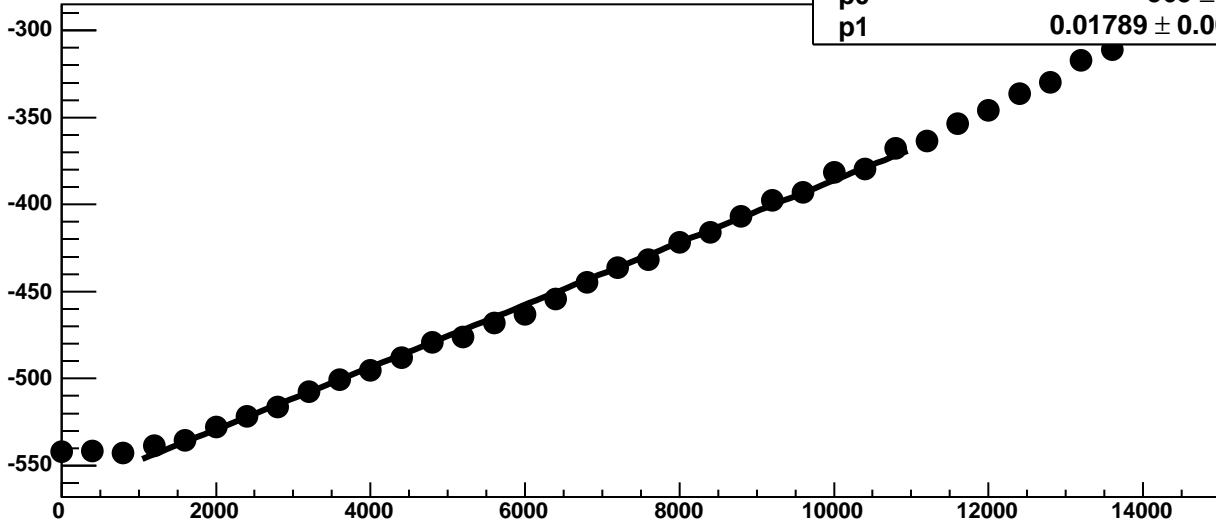
Chip 9, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



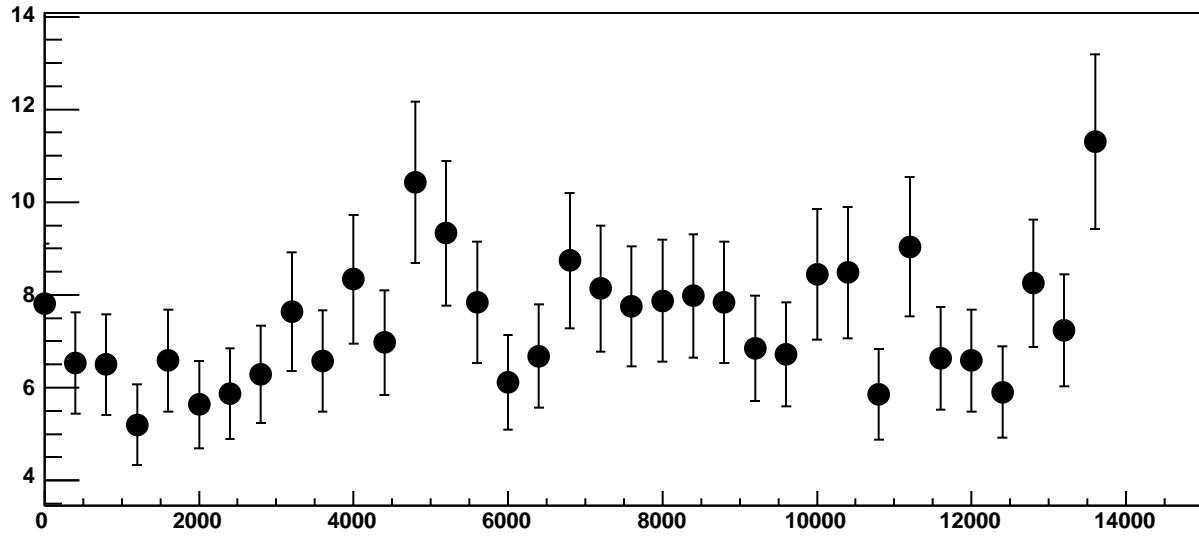
Chip 9, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



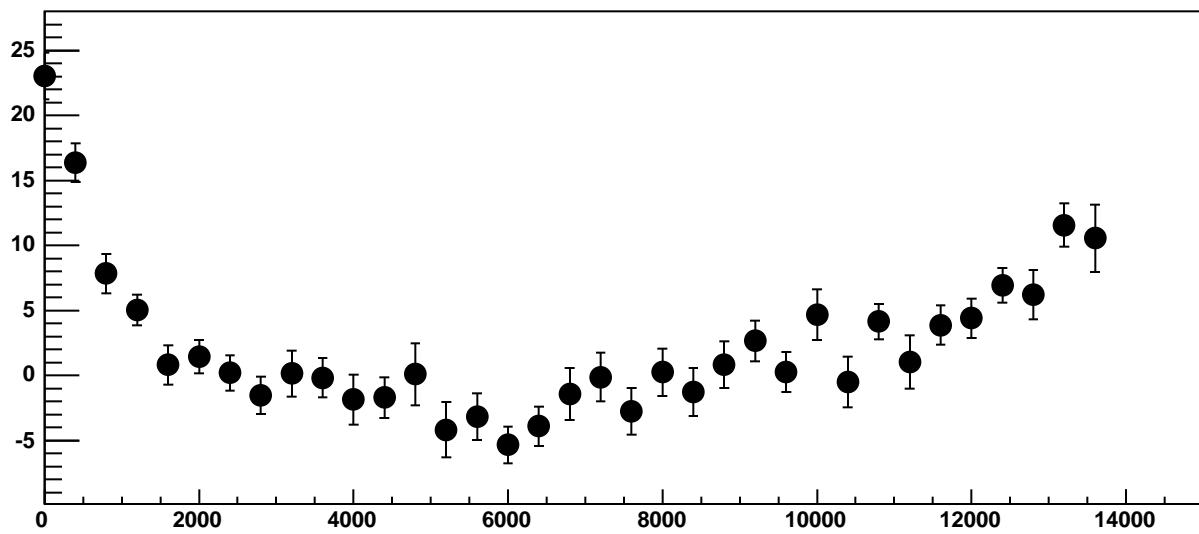
Chip 9, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



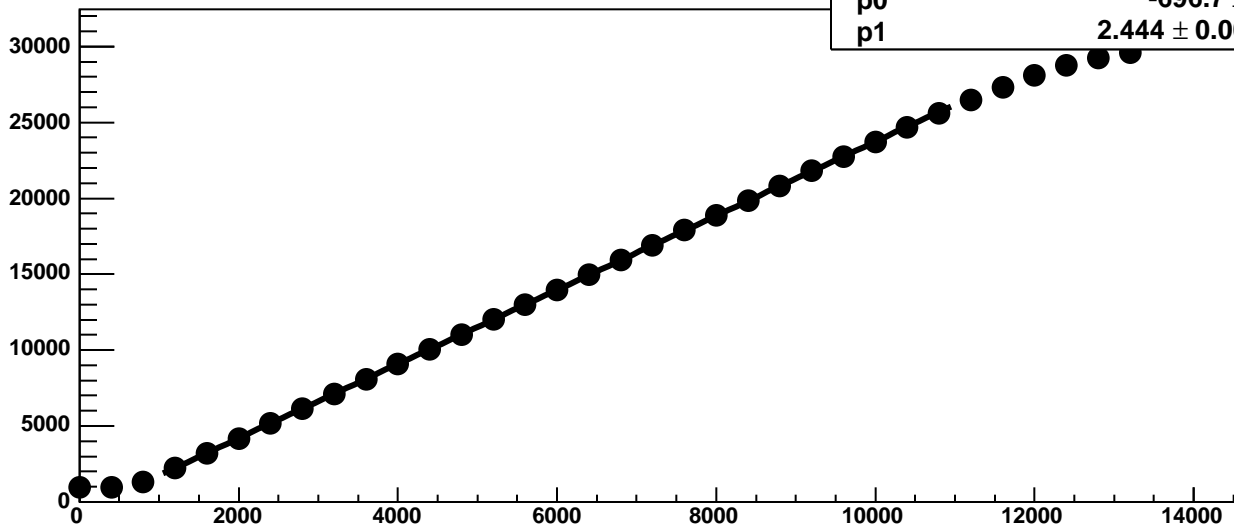
Chip 9, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



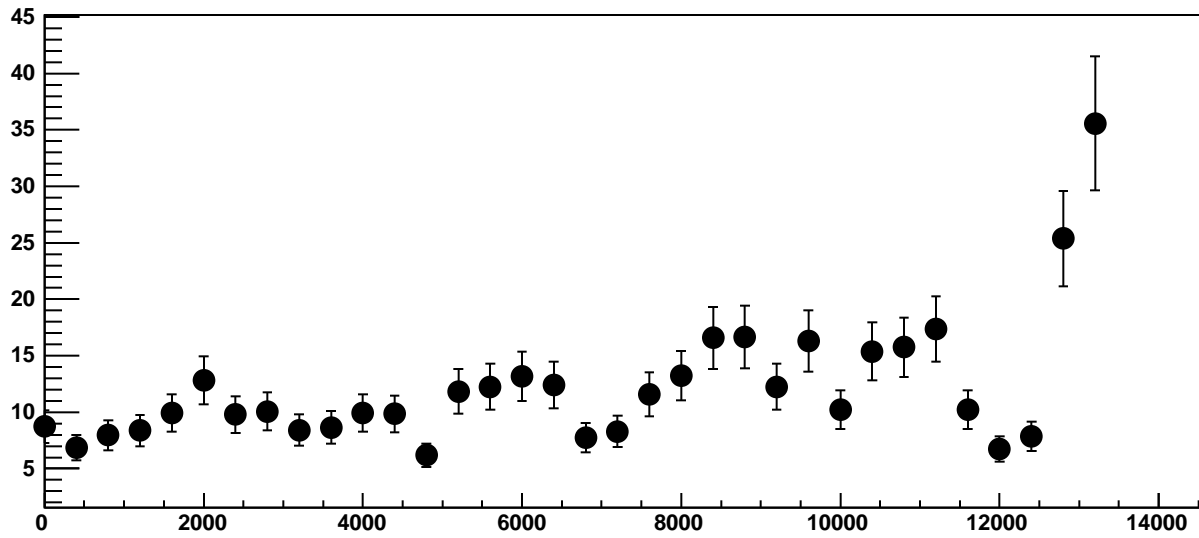
Chip 9, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



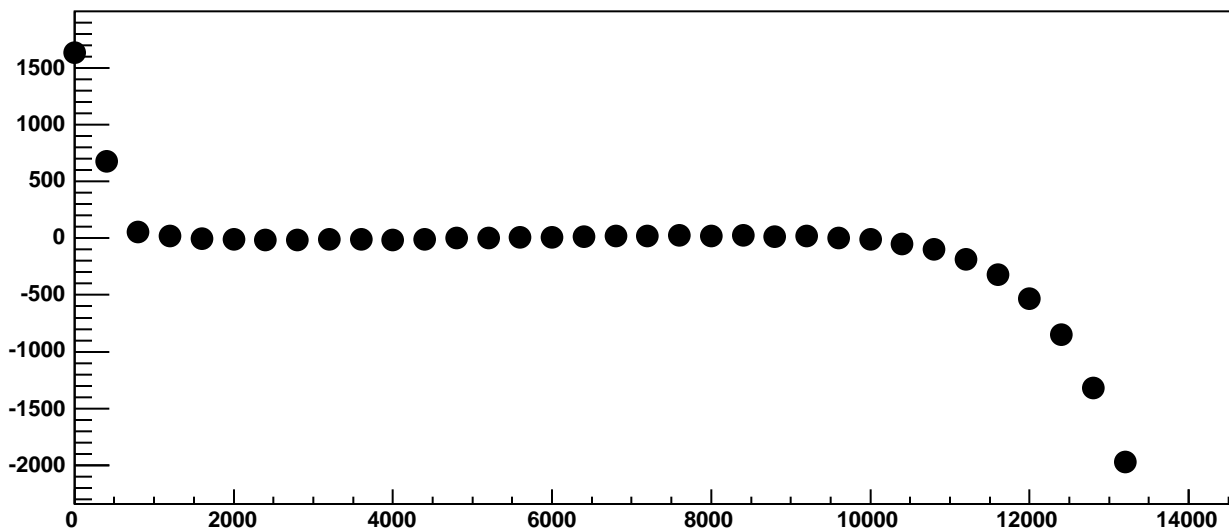
Chip 9, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC



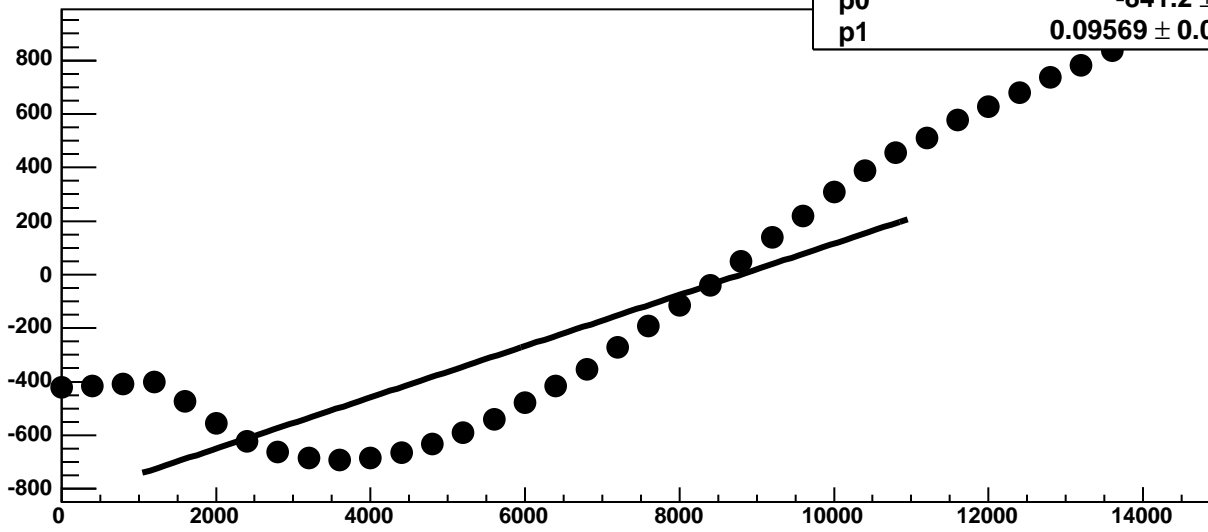
Chip 9, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC

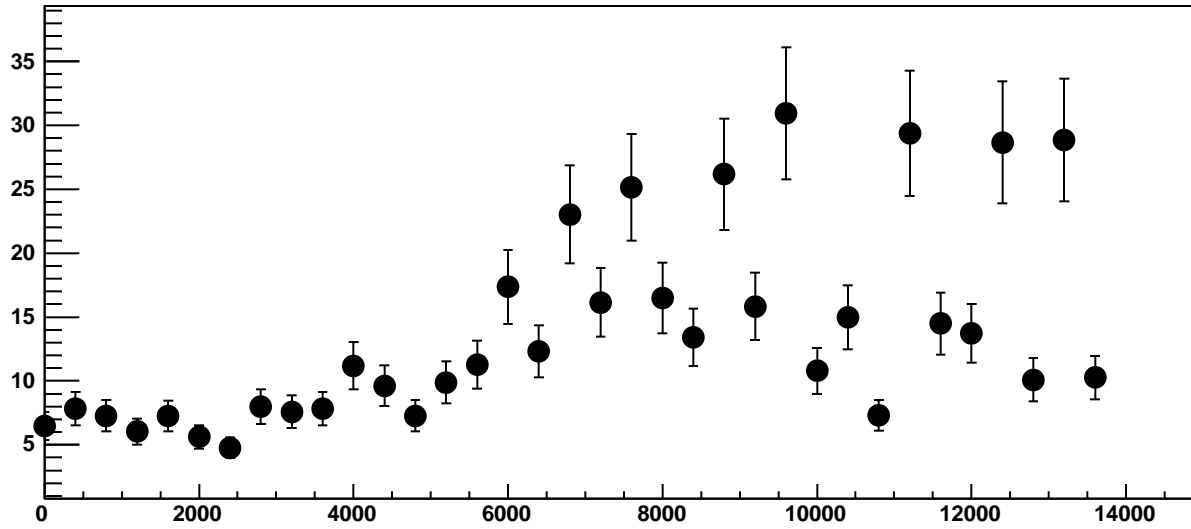


Chip 9, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

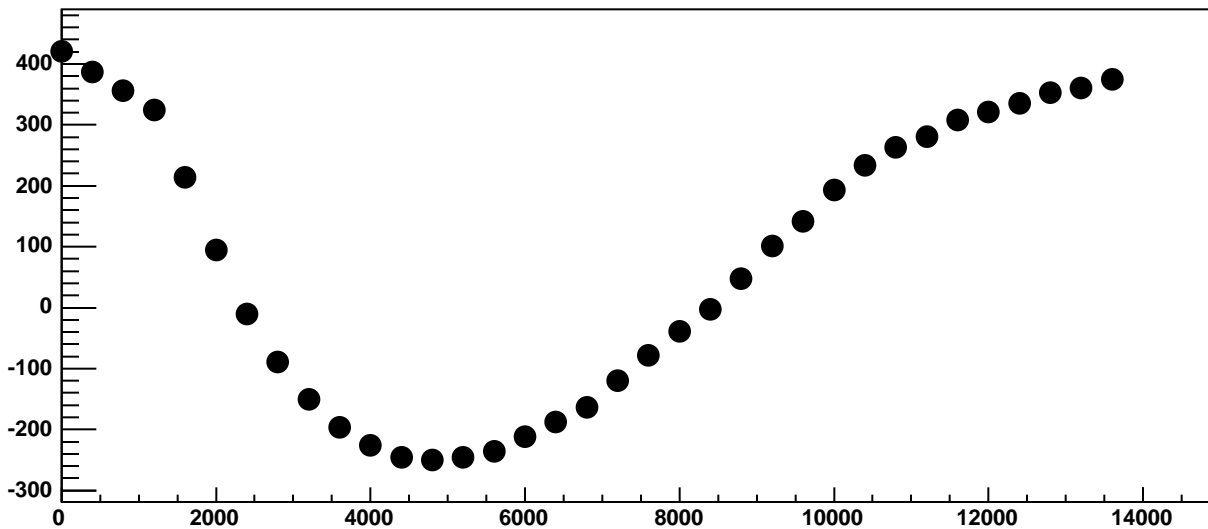


χ^2 / ndf 2.074e+05 / 23
p0 -841.2 ± 0.7543
p1 0.09569 ± 0.0001464

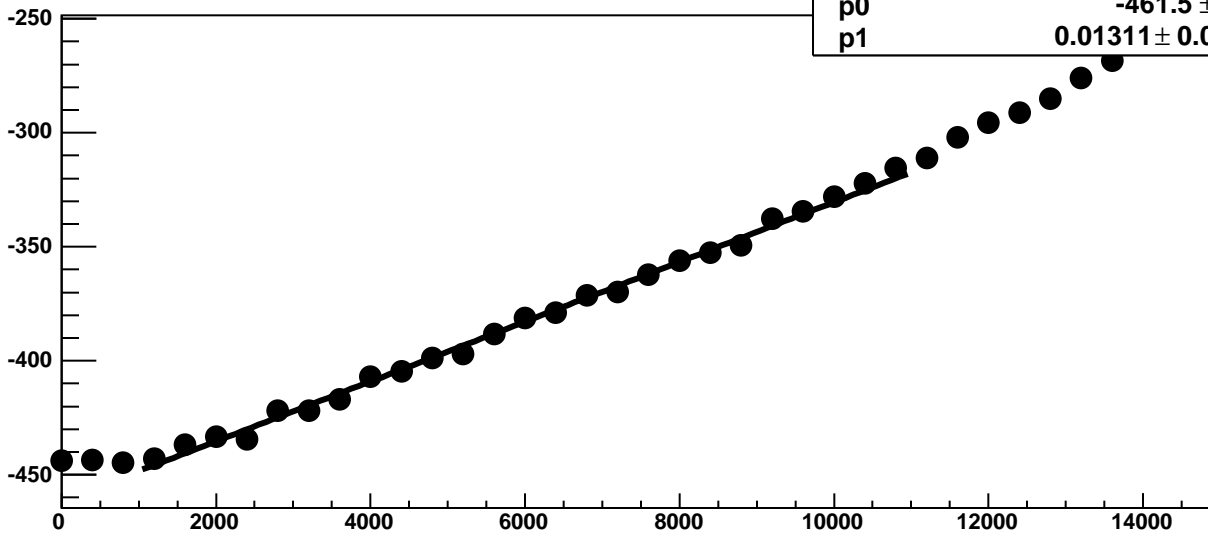
Chip 9, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

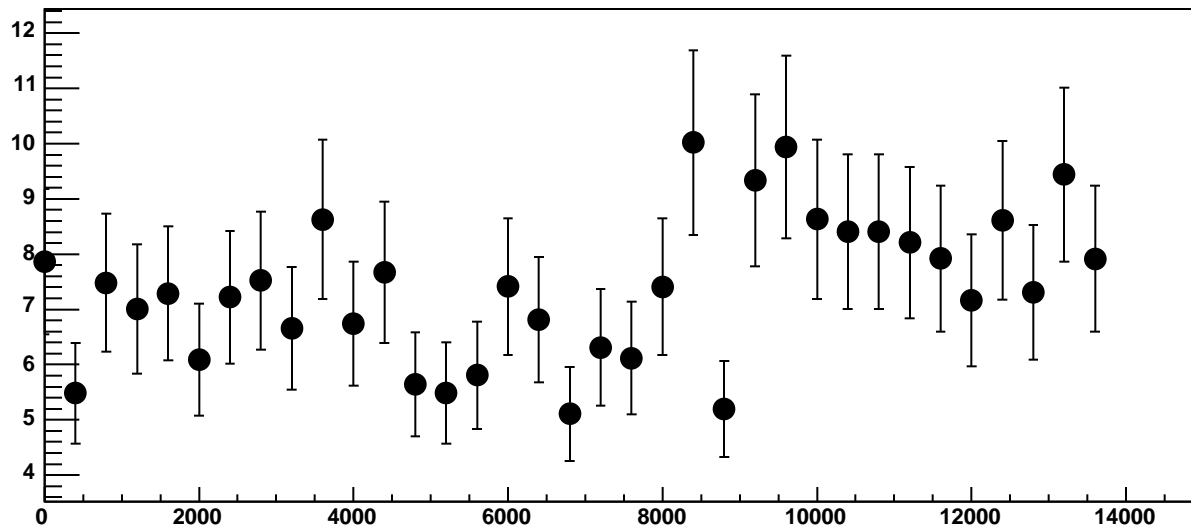


Chip 9, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

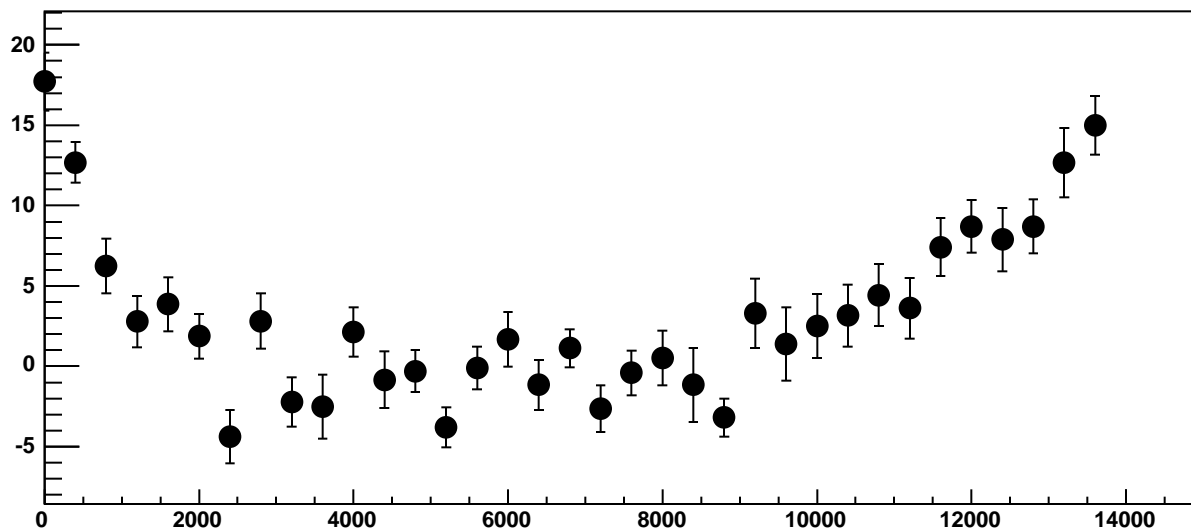


χ^2 / ndf 60.41 / 23
p0 -461.5 ± 0.7532
p1 0.01311 ± 0.0001186

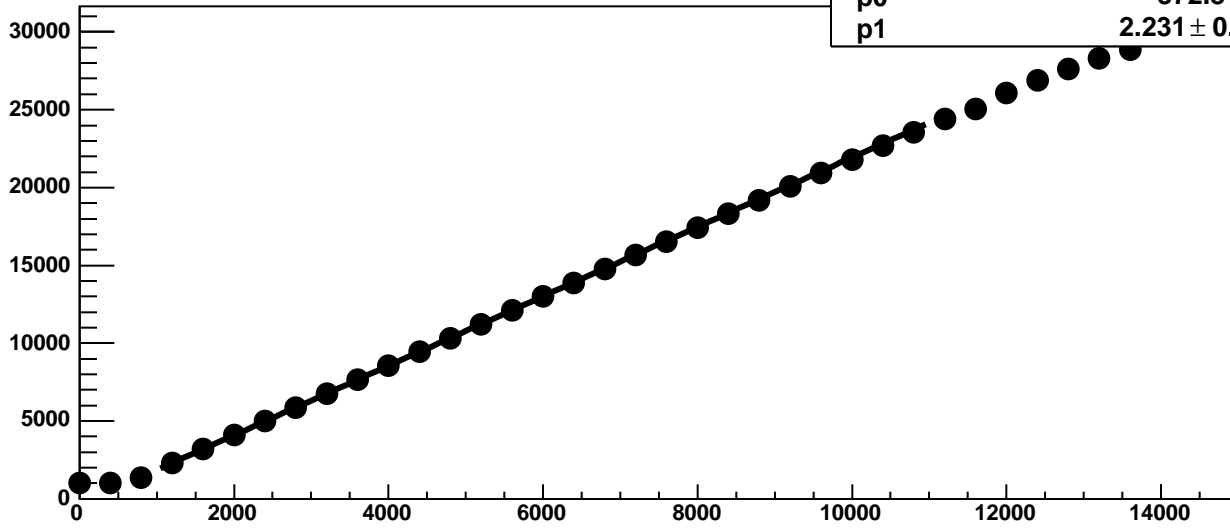
Chip 9, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

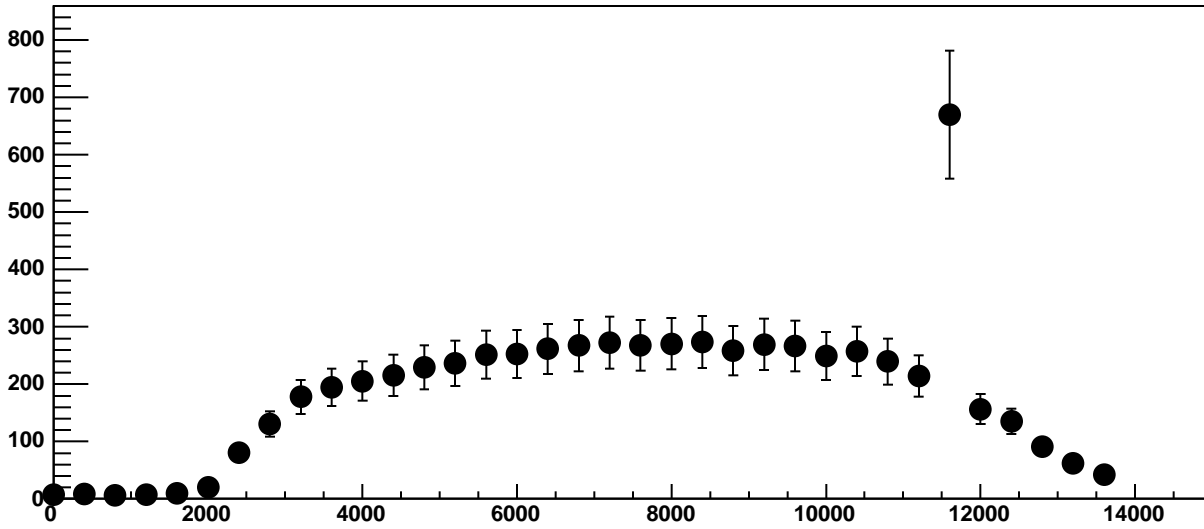


Chip 9, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC

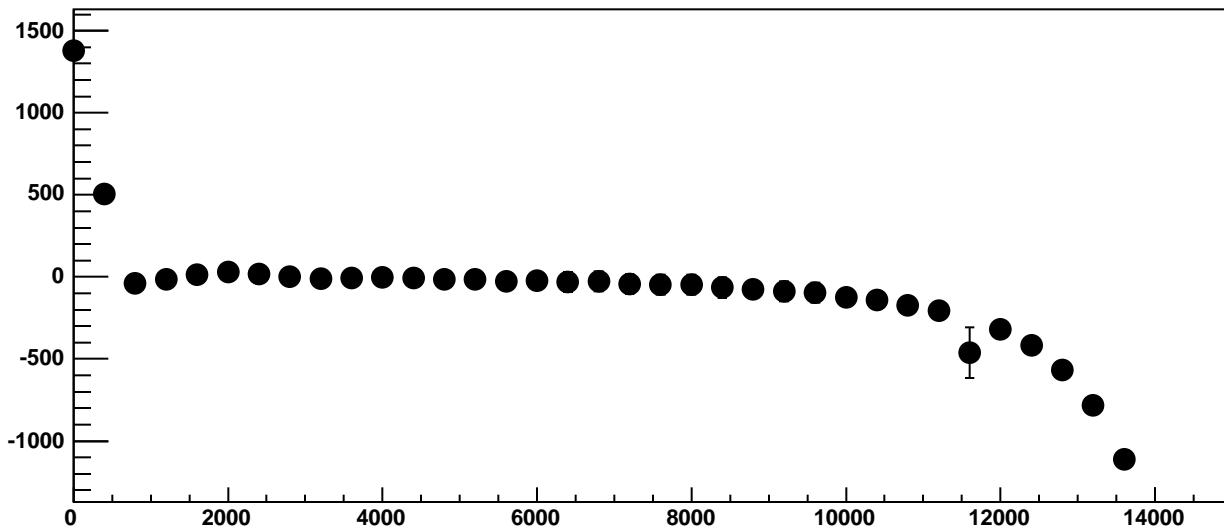


χ^2 / ndf 169.5 / 23
p0 -372.8 ± 3.224
p1 2.231 ± 0.001991

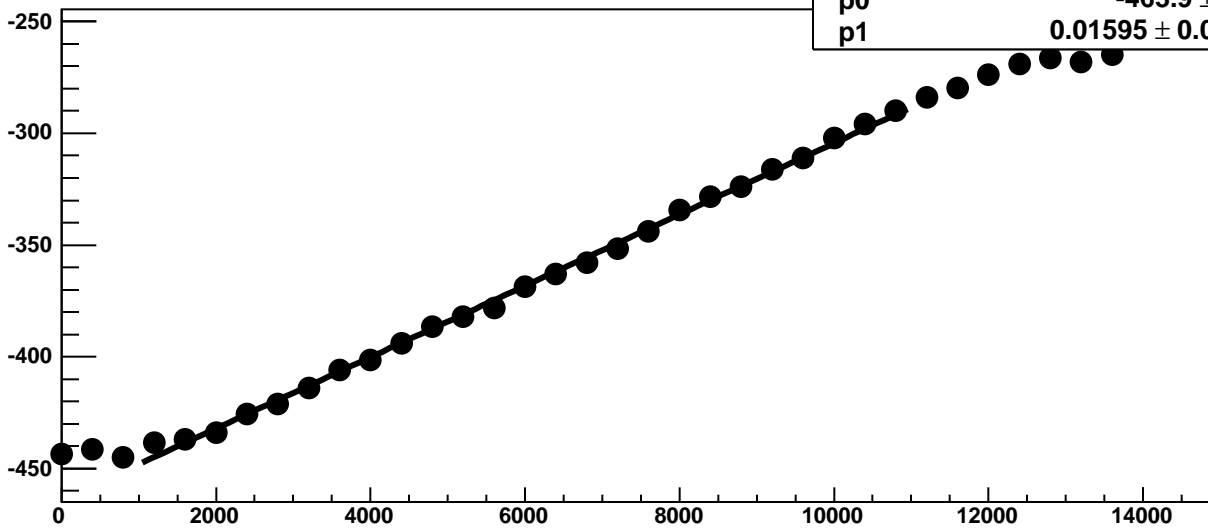
Chip 9, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC

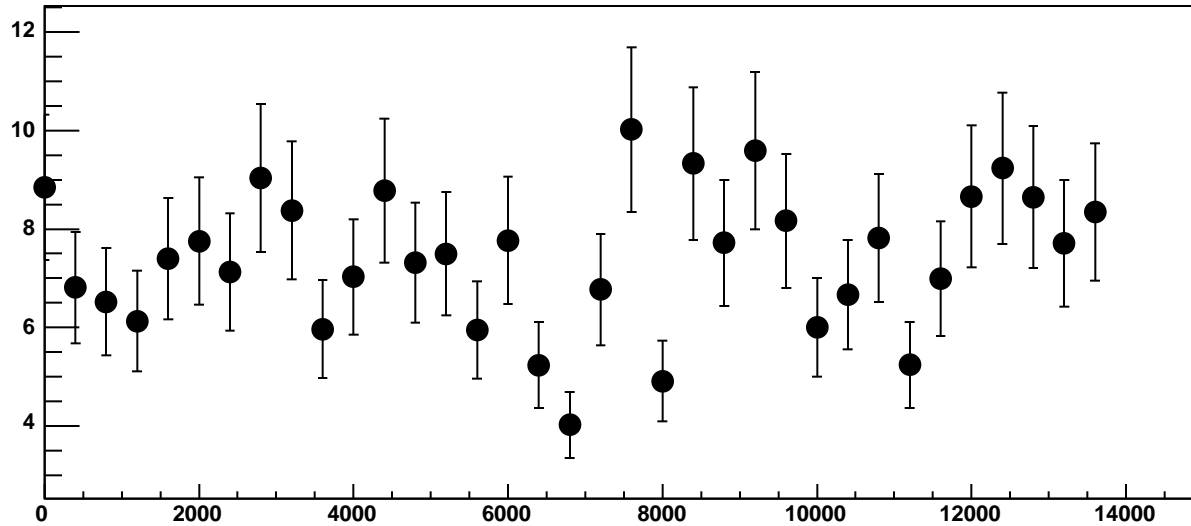


Chip 9, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

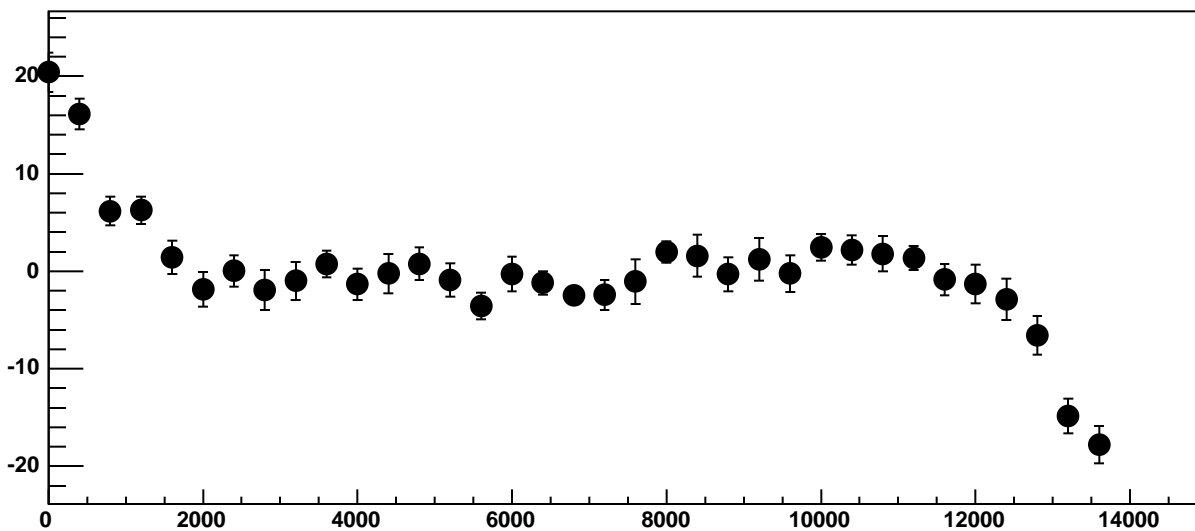


χ^2 / ndf 52.11 / 23
p0 -463.9 ± 0.7577
p1 0.01595 ± 0.0001137

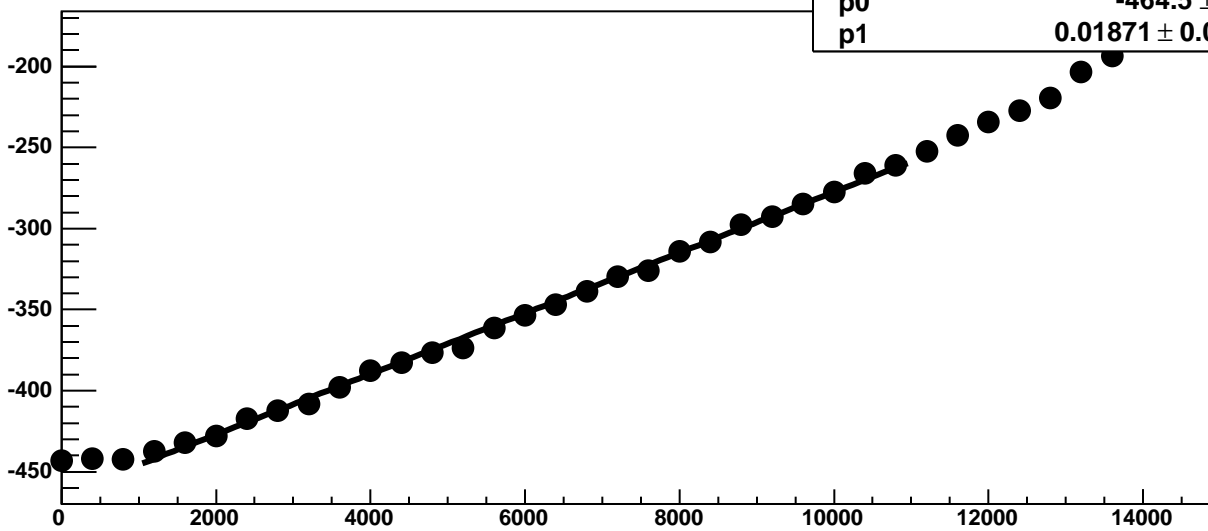
Chip 9, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

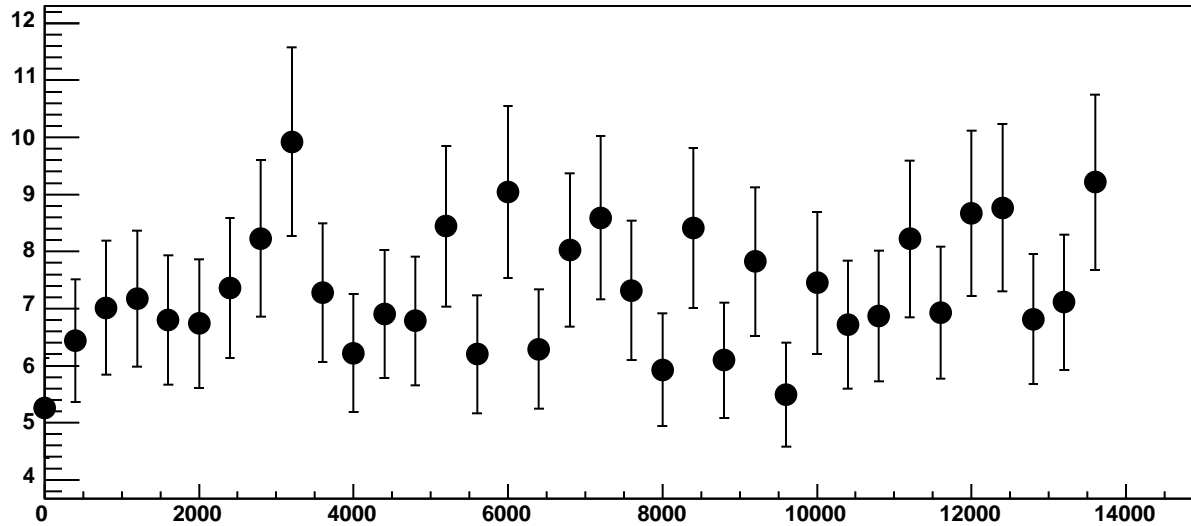


Chip 9, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

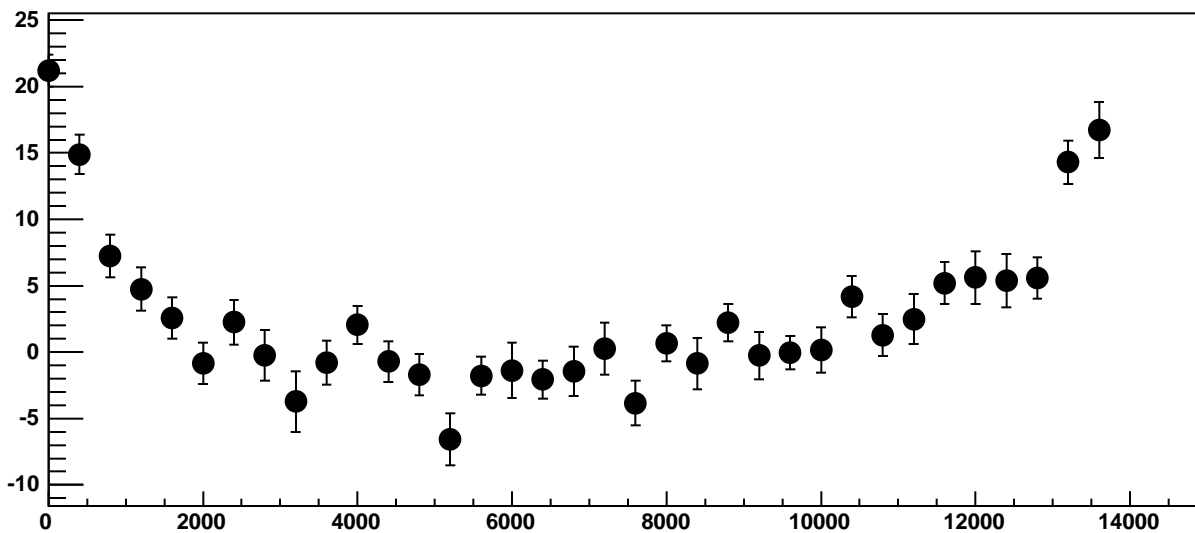


χ^2 / ndf 52.05 / 23
p0 -464.5 ± 0.7559
p1 0.01871 ± 0.0001109

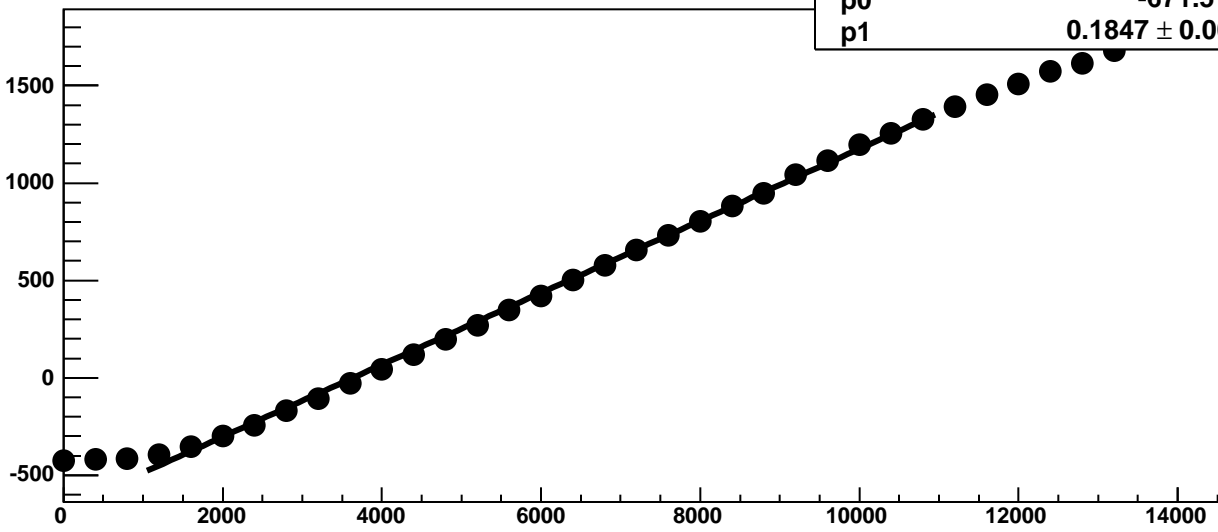
Chip 9, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



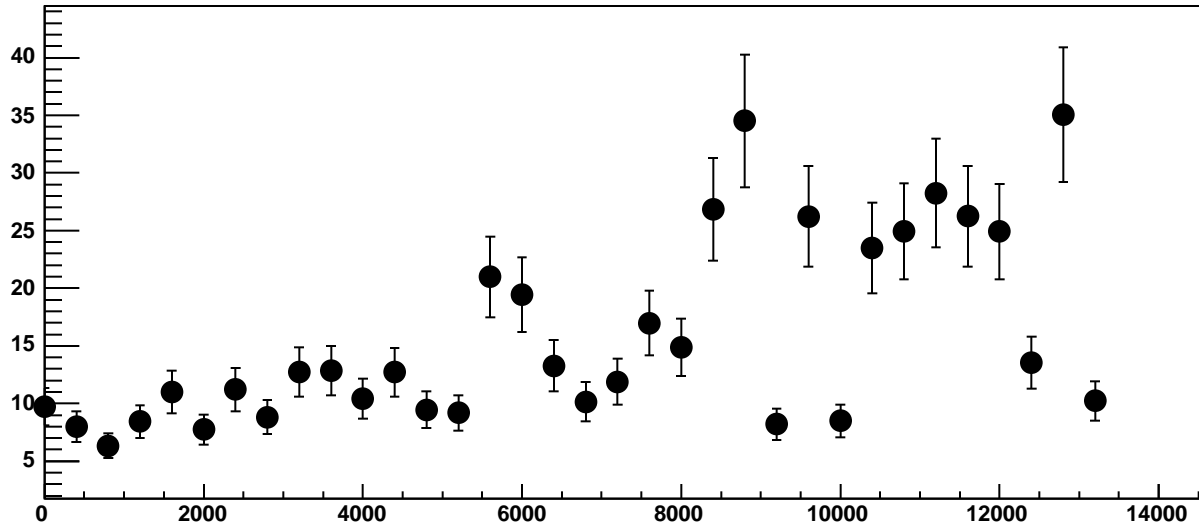
Chip 9, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



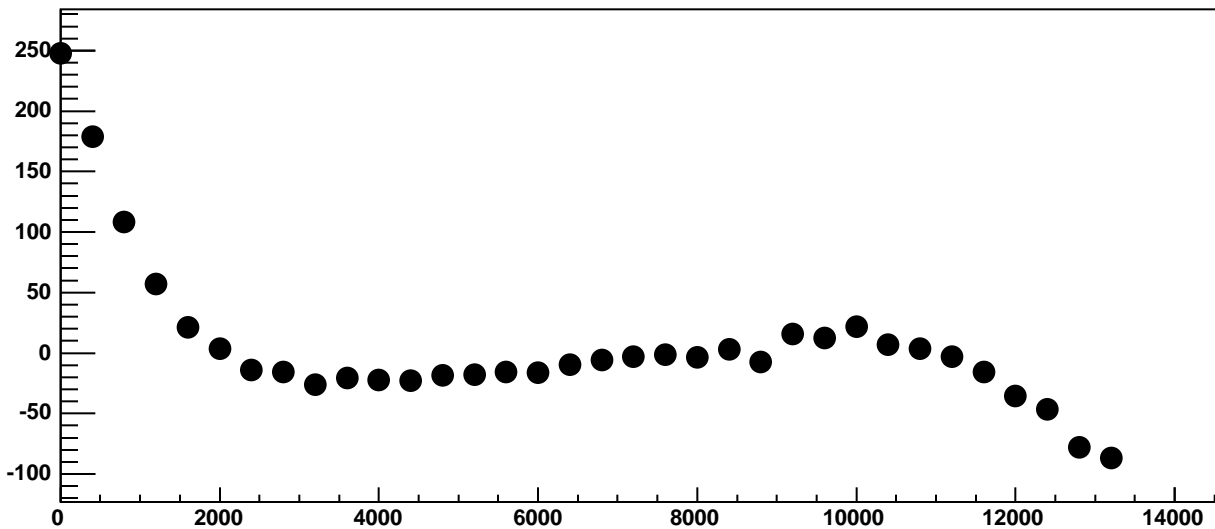
Chip 9, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC



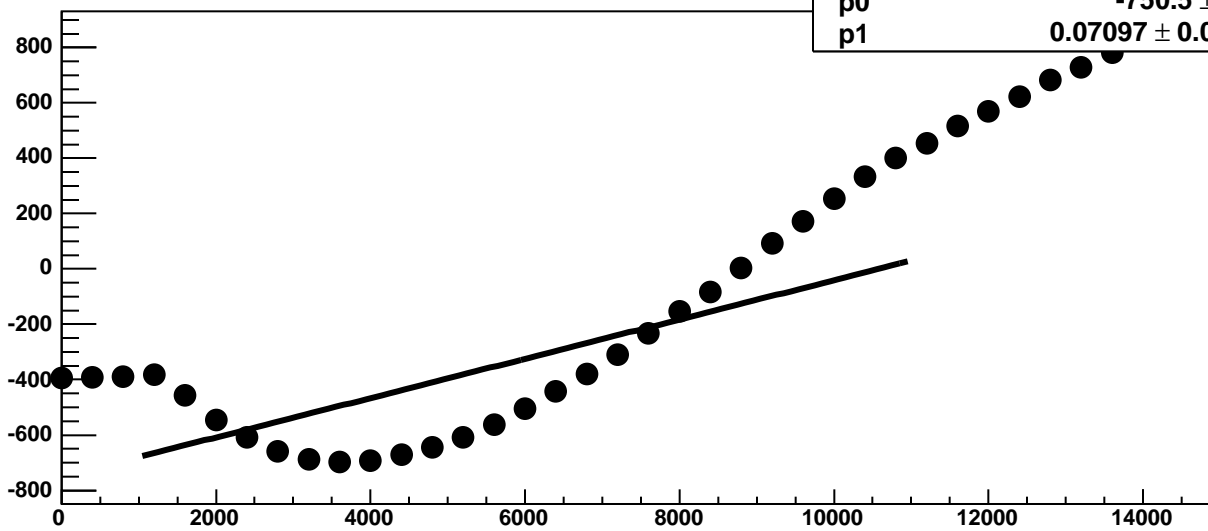
Chip 9, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC

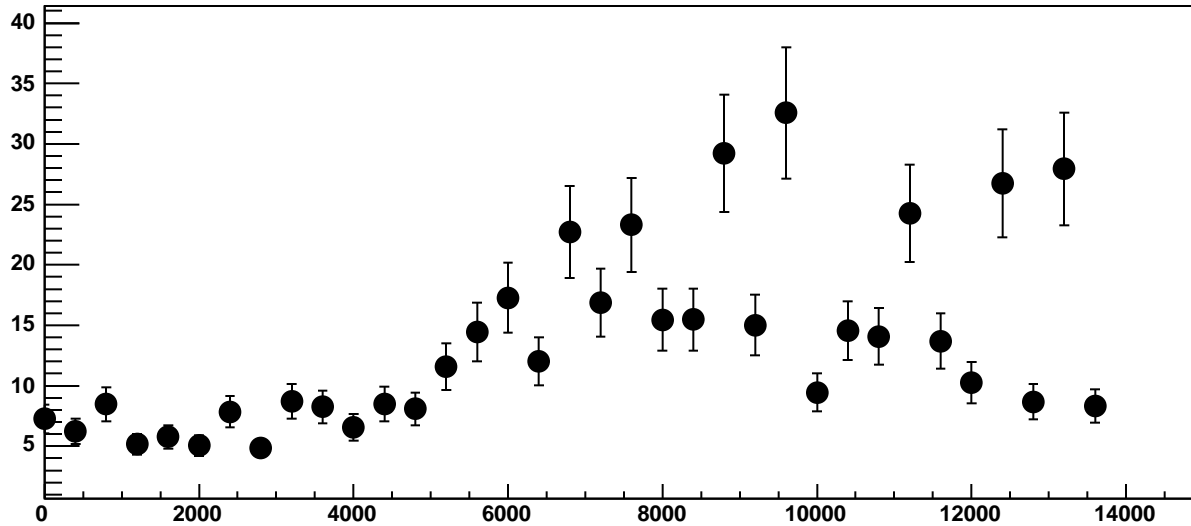


Chip 9, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

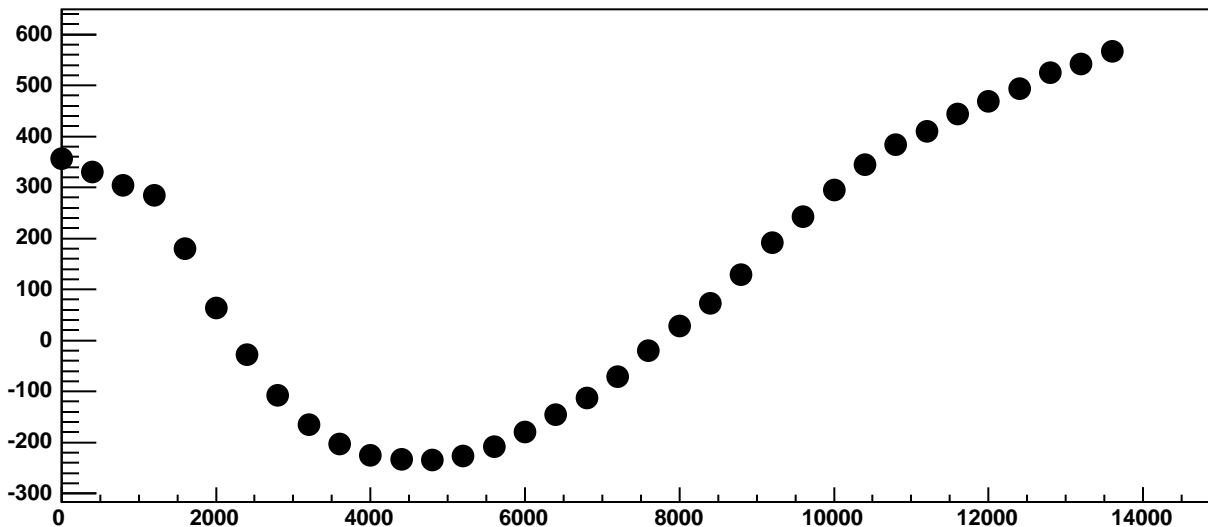


χ^2 / ndf 2.256e+05 / 23
p0 -750.5 ± 0.7328
p1 0.07097 ± 0.0001605

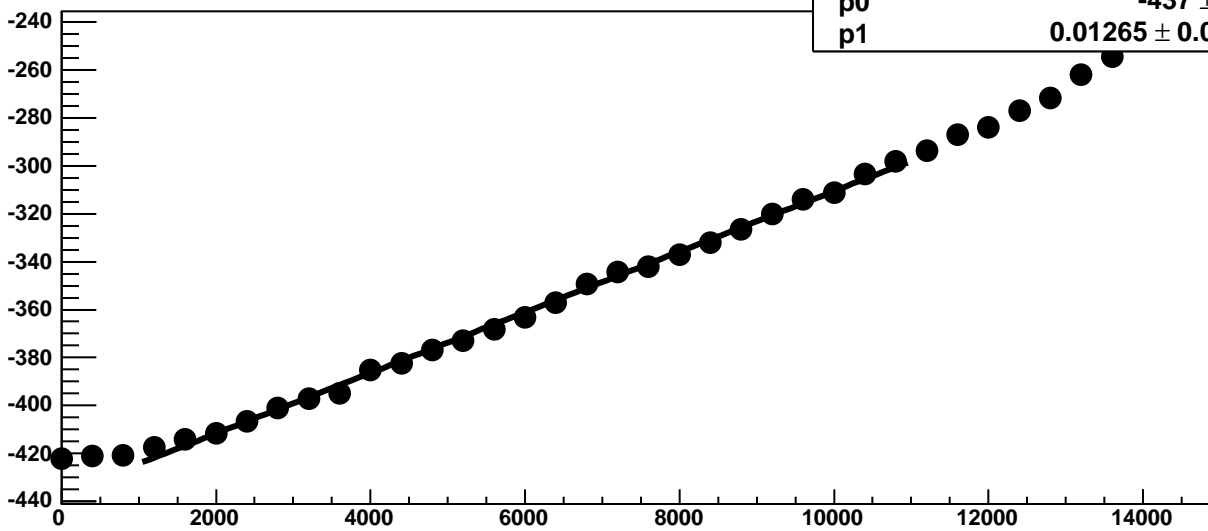
Chip 9, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

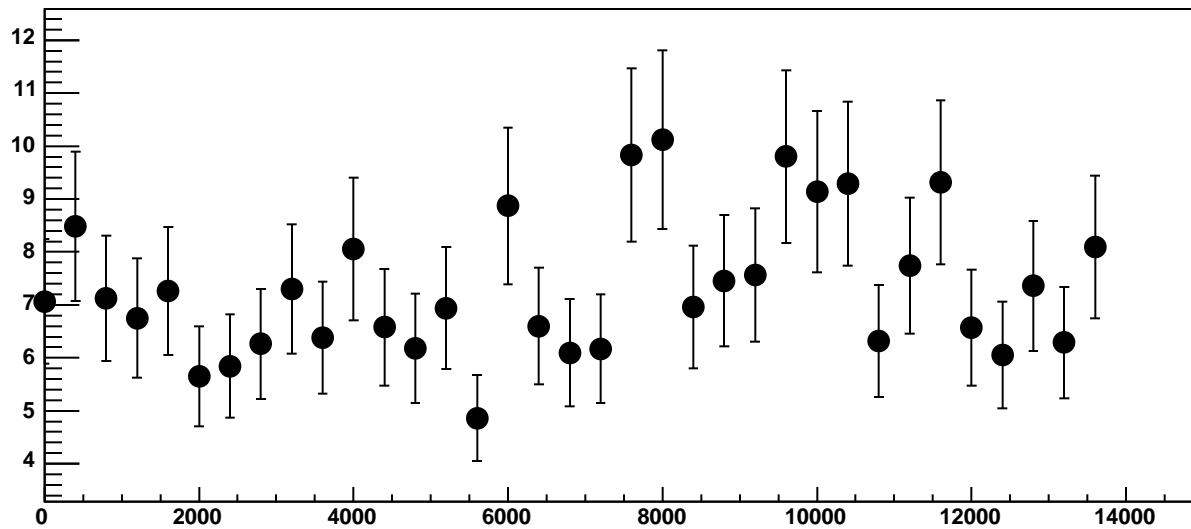


Chip 9, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

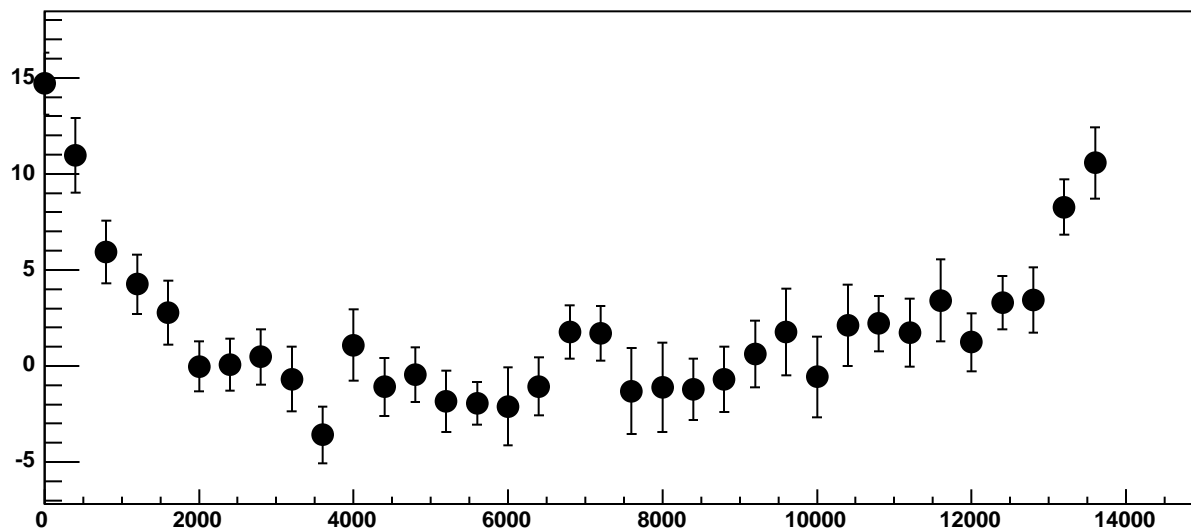


χ^2 / ndf 31.95 / 23
p0 -437 ± 0.7068
p1 0.01265 ± 0.0001138

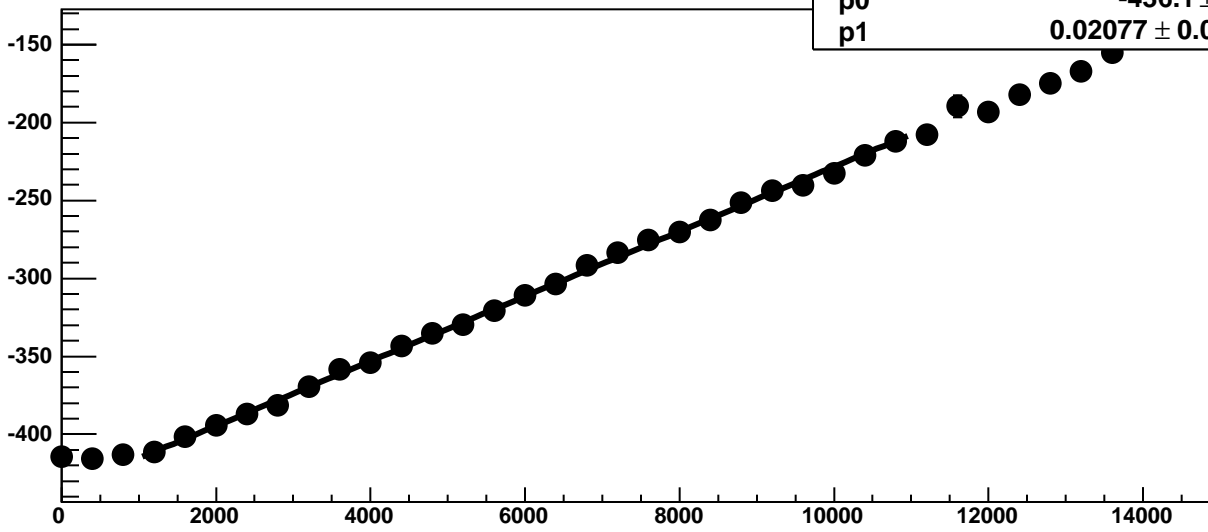
Chip 9, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

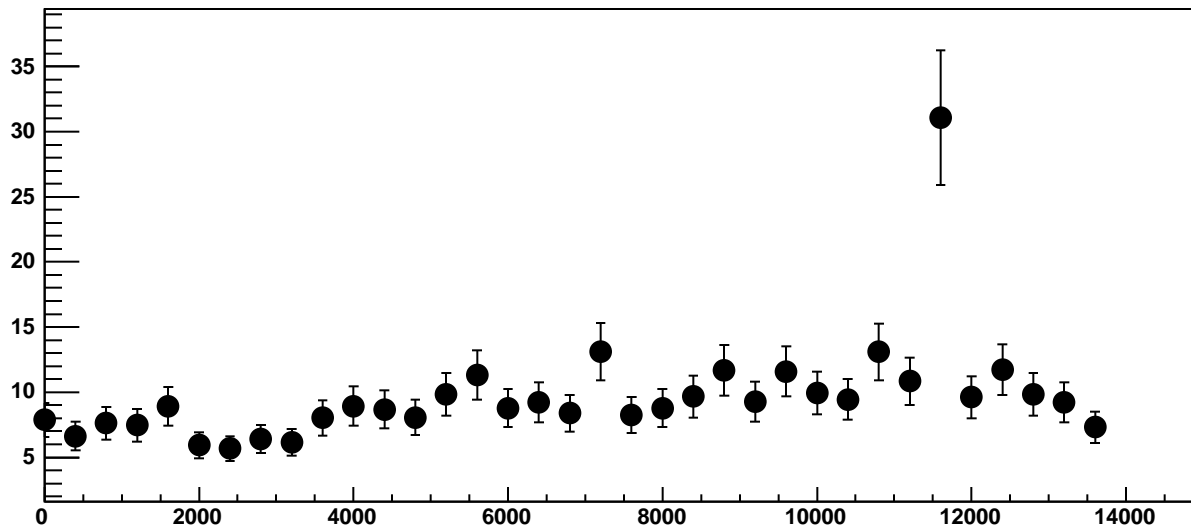


Chip 9, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC

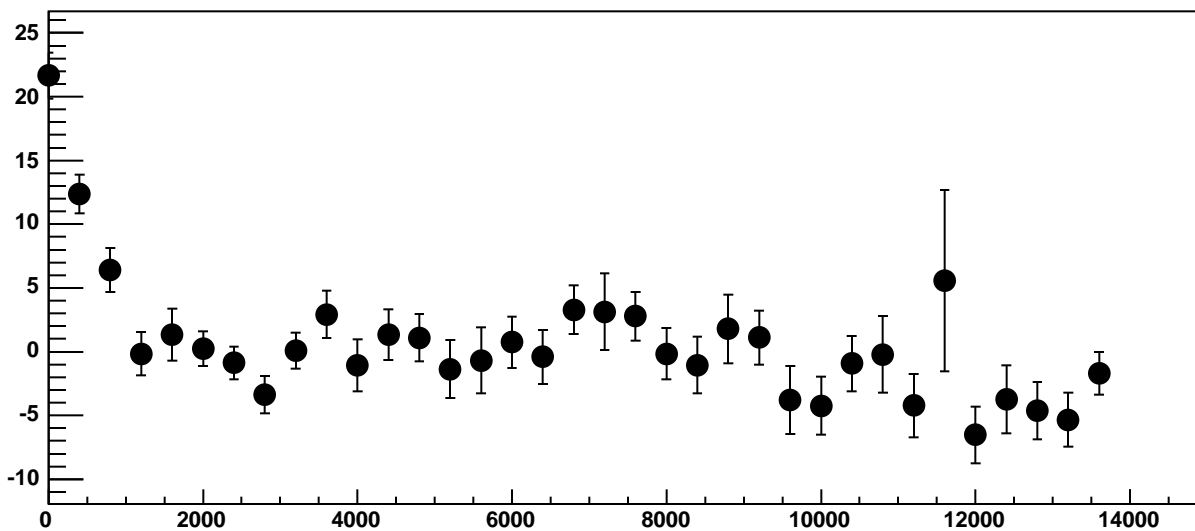


χ^2 / ndf 23.13 / 23
p0 -436.1 ± 0.7918
p1 0.02077 ± 0.0001366

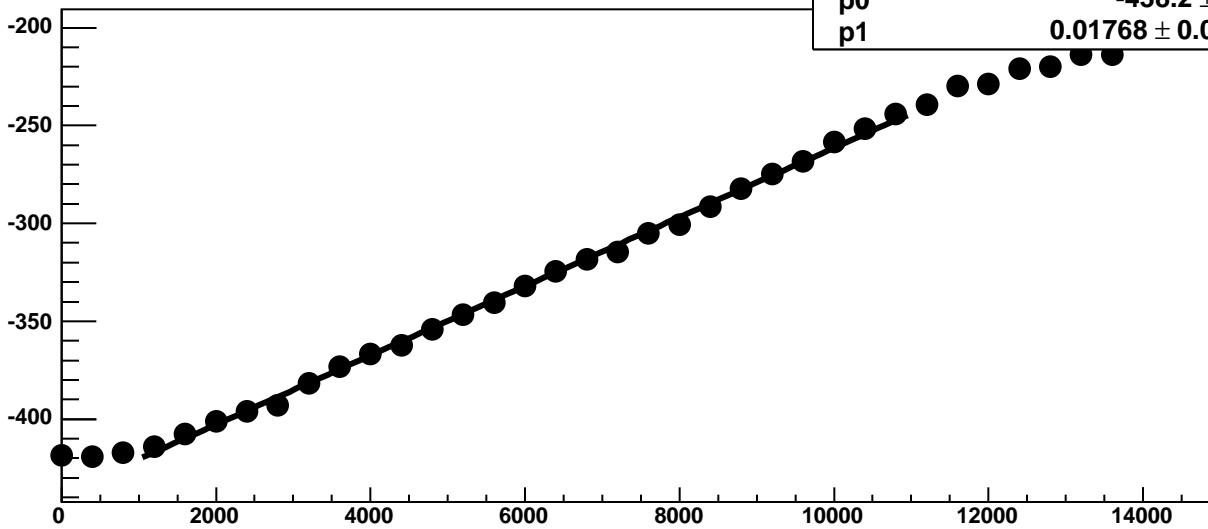
Chip 9, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC

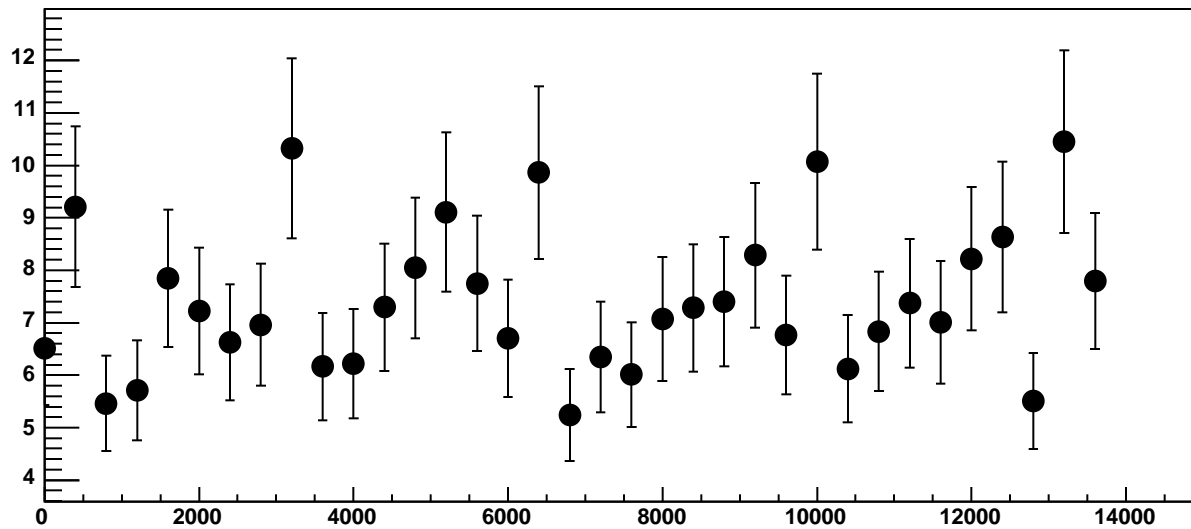


Chip 9, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

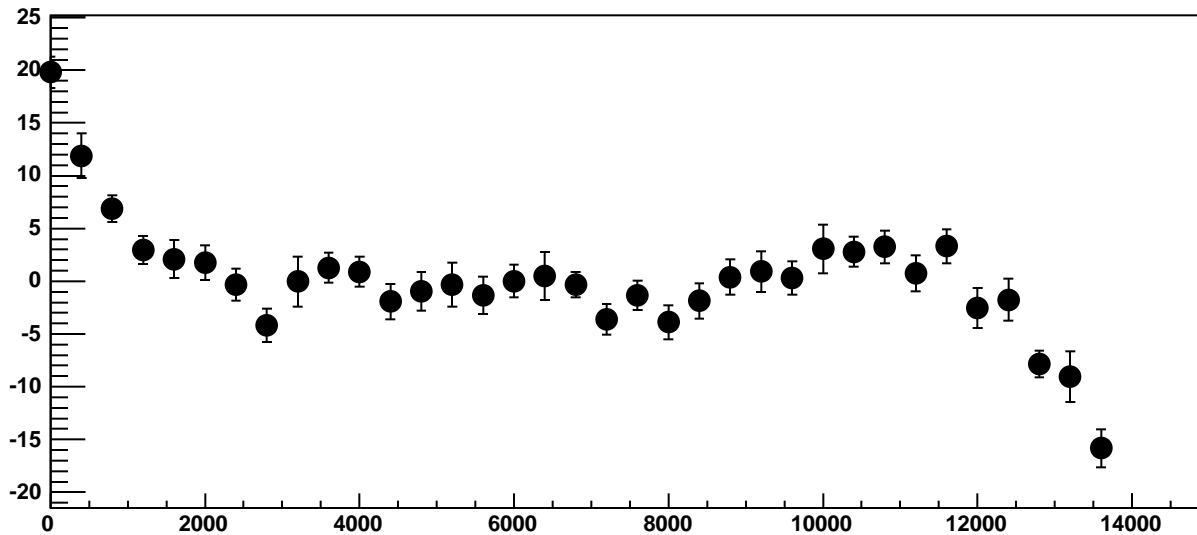


χ^2 / ndf 42.43 / 23
p0 -438.2 ± 0.7335
p1 0.01768 ± 0.0001106

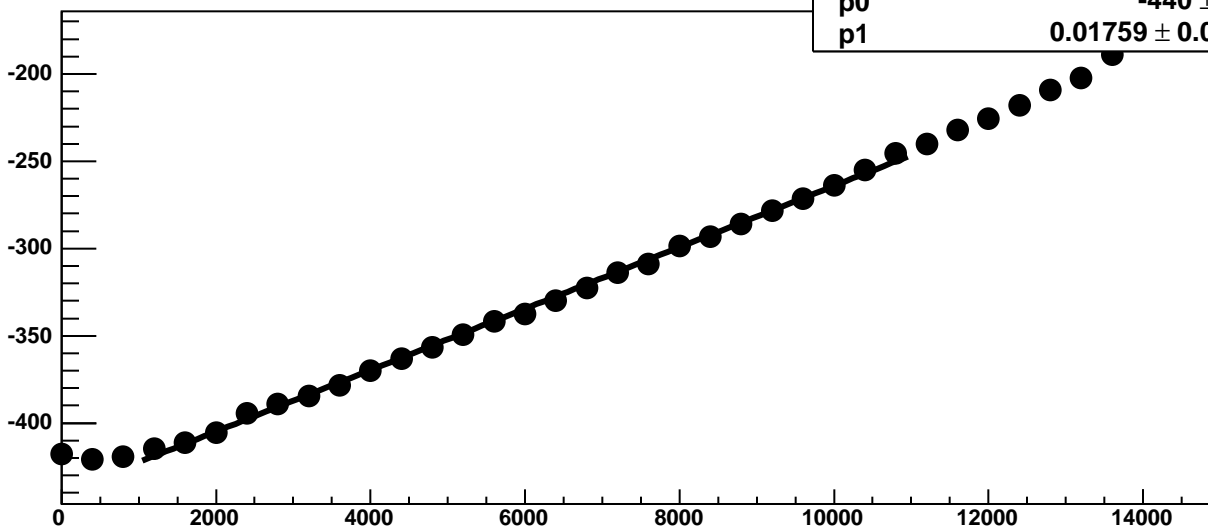
Chip 9, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

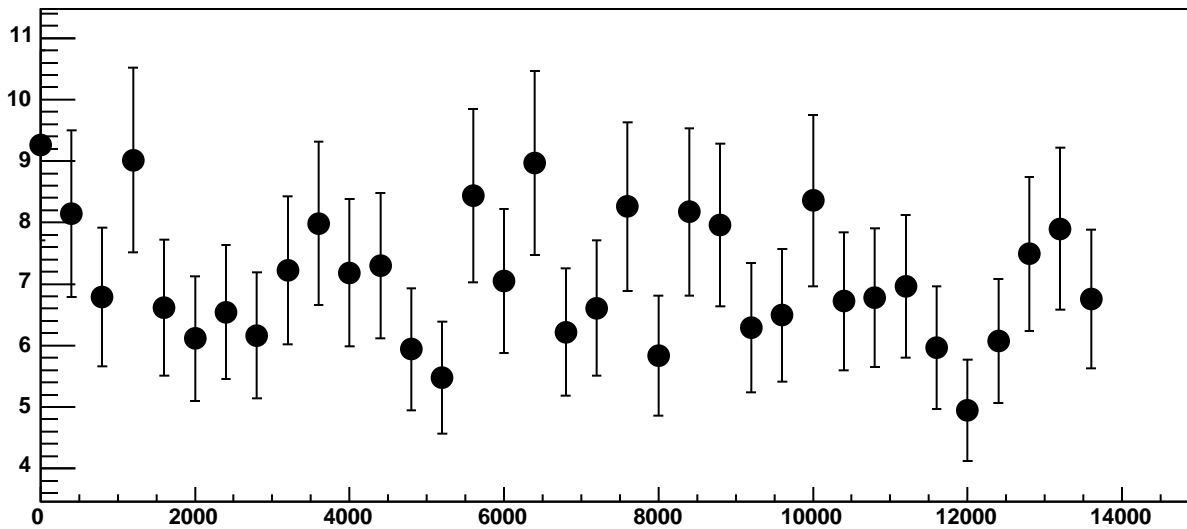


Chip 9, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

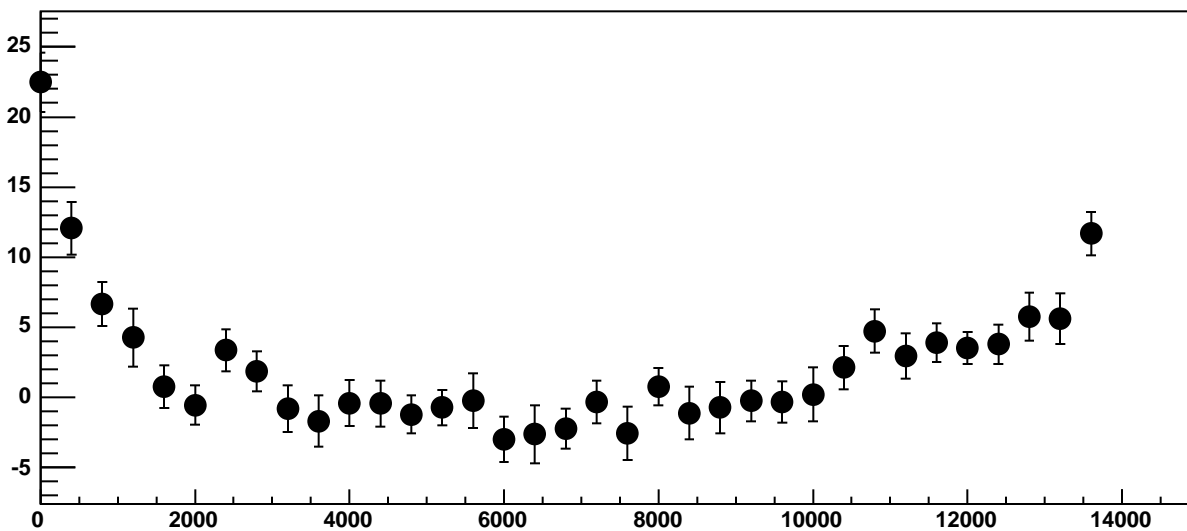


χ^2 / ndf 35.25 / 23
p0 -440 ± 0.7312
p1 0.01759 ± 0.0001106

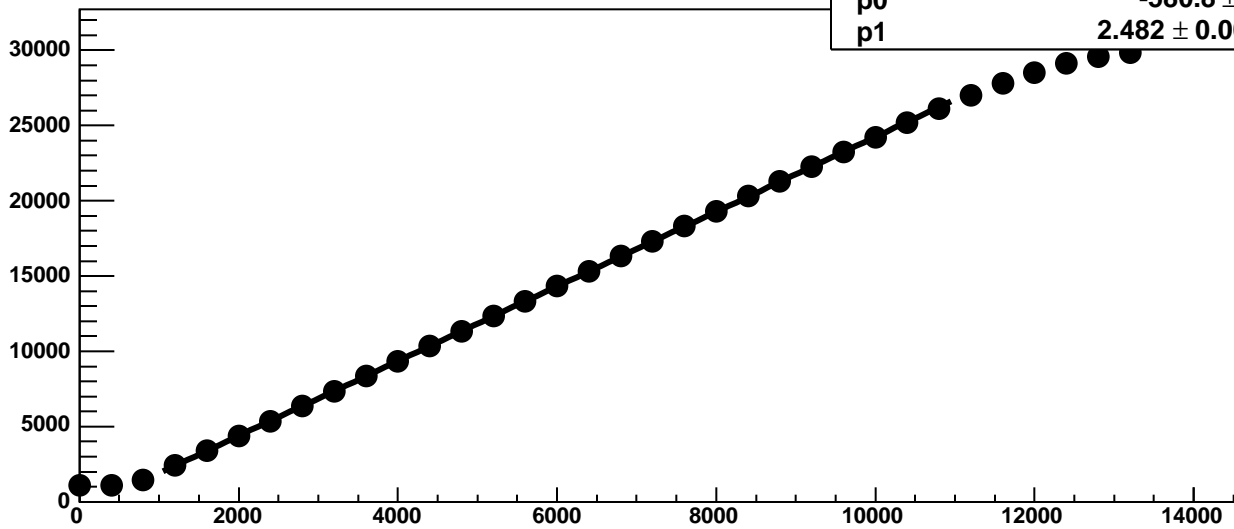
Chip 9, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 9, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

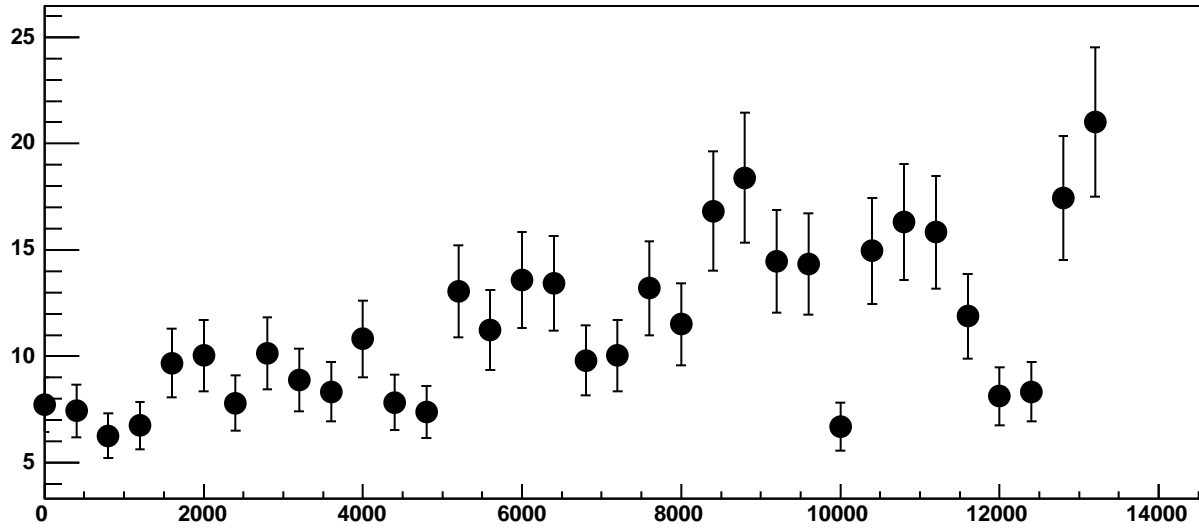


Chip 9, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC

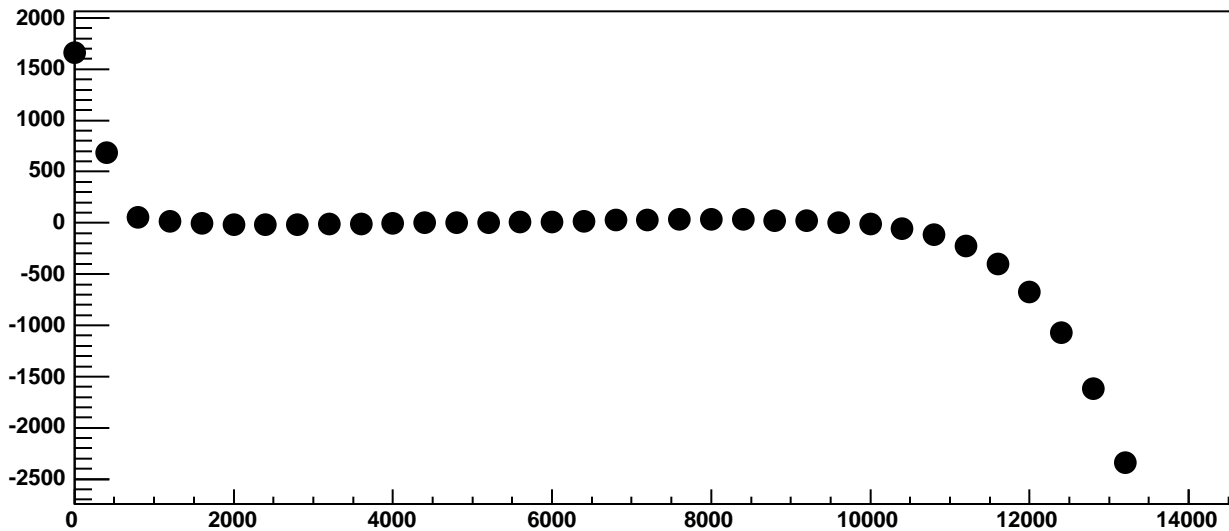


χ^2 / ndf 2560 / 23
p0 -580.8 ± 0.9481
p1 2.482 ± 0.0001599

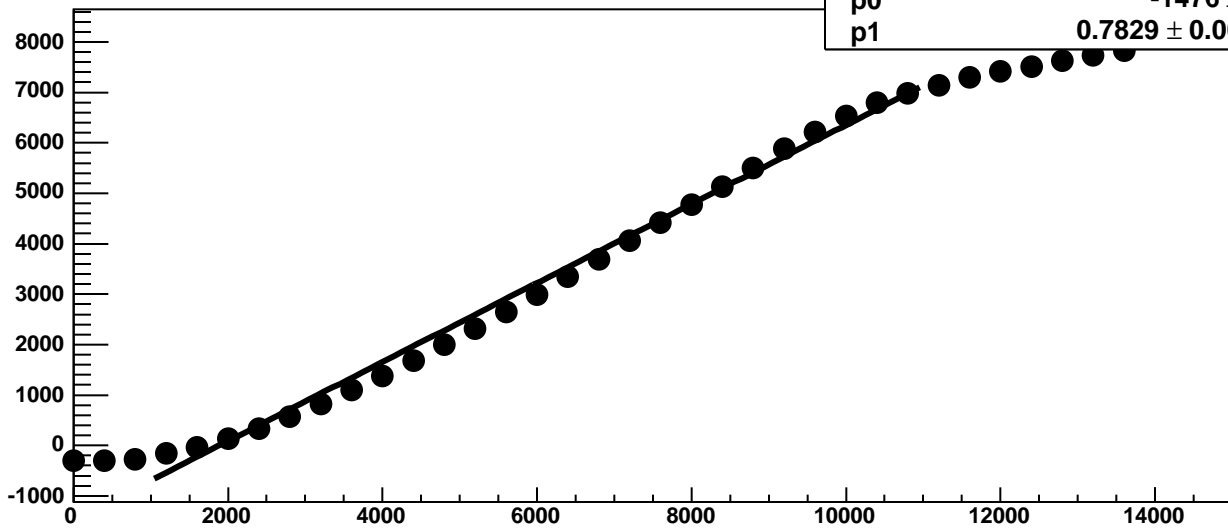
Chip 9, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC

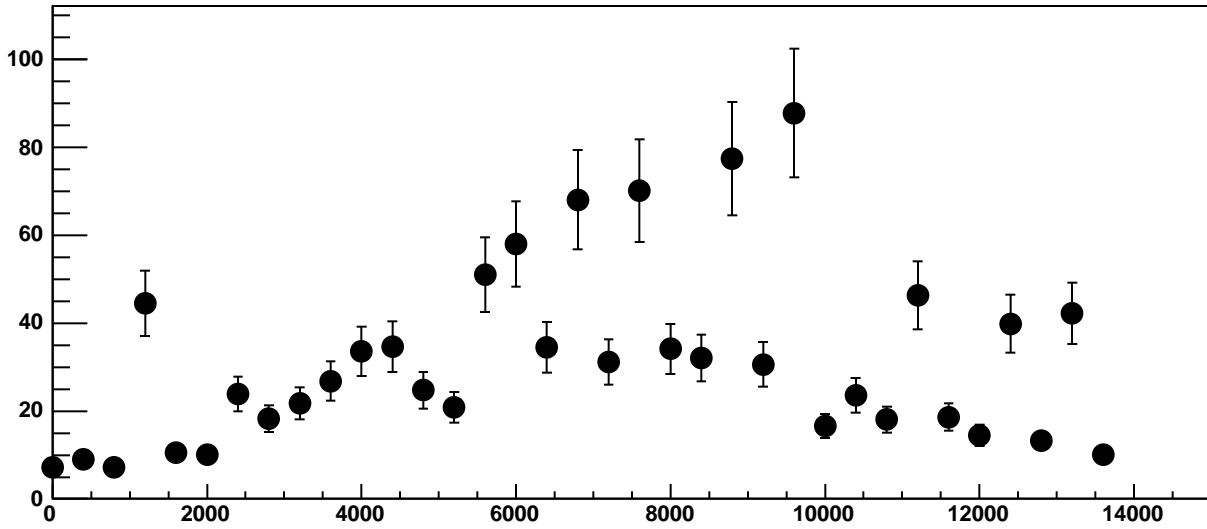


Chip 9, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

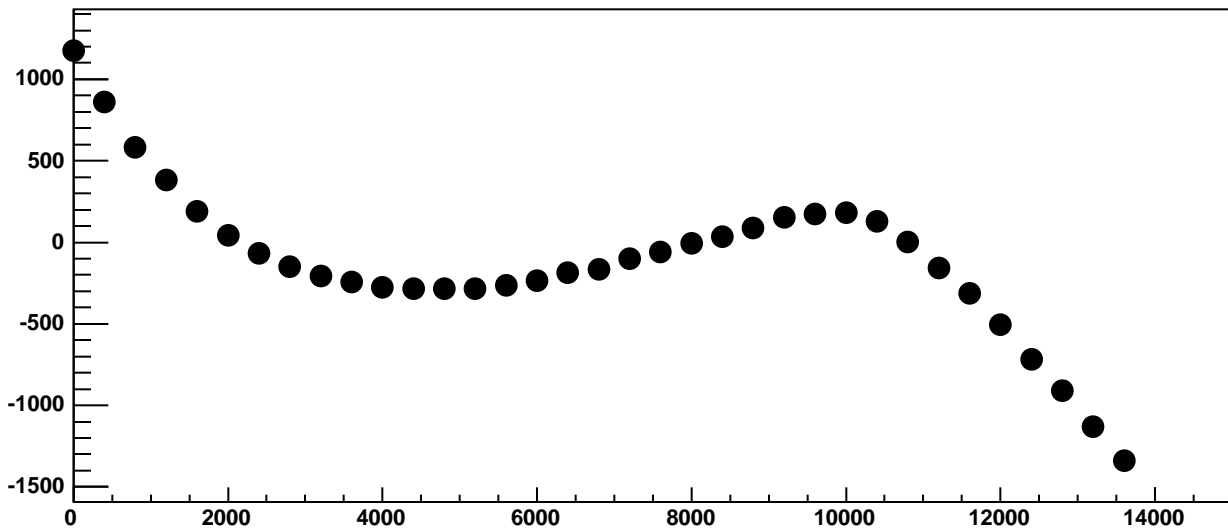


χ^2 / ndf 2.628e+04 / 23
p0 -1476 ± 1.797
p1 0.7829 ± 0.0003156

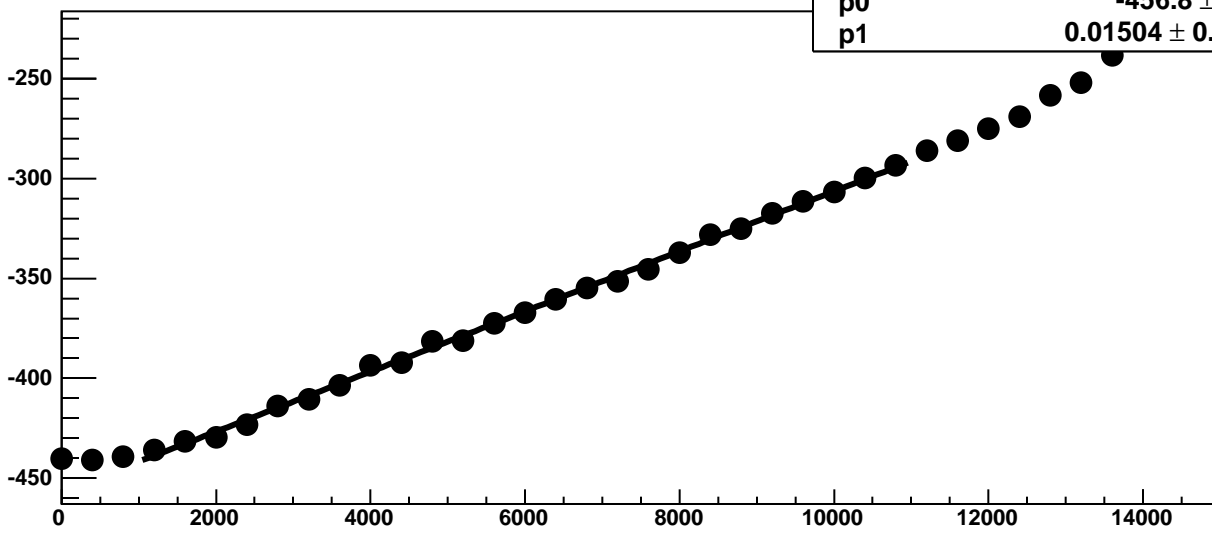
Chip 9, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

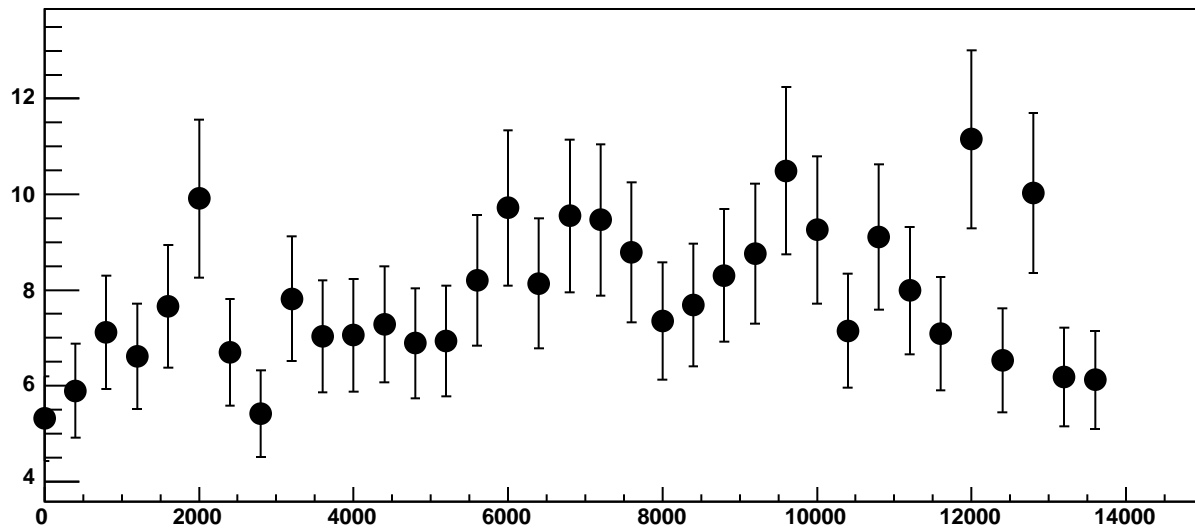


Chip 9, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

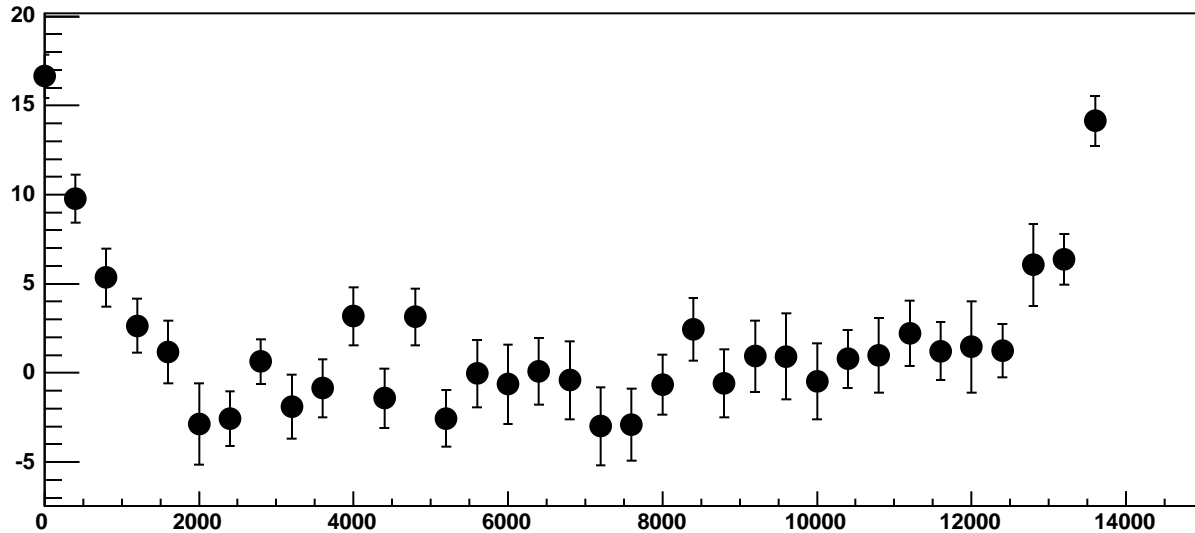


χ^2 / ndf 27.7 / 23
p0 -456.8 ± 0.7728
p1 0.01504 ± 0.000124

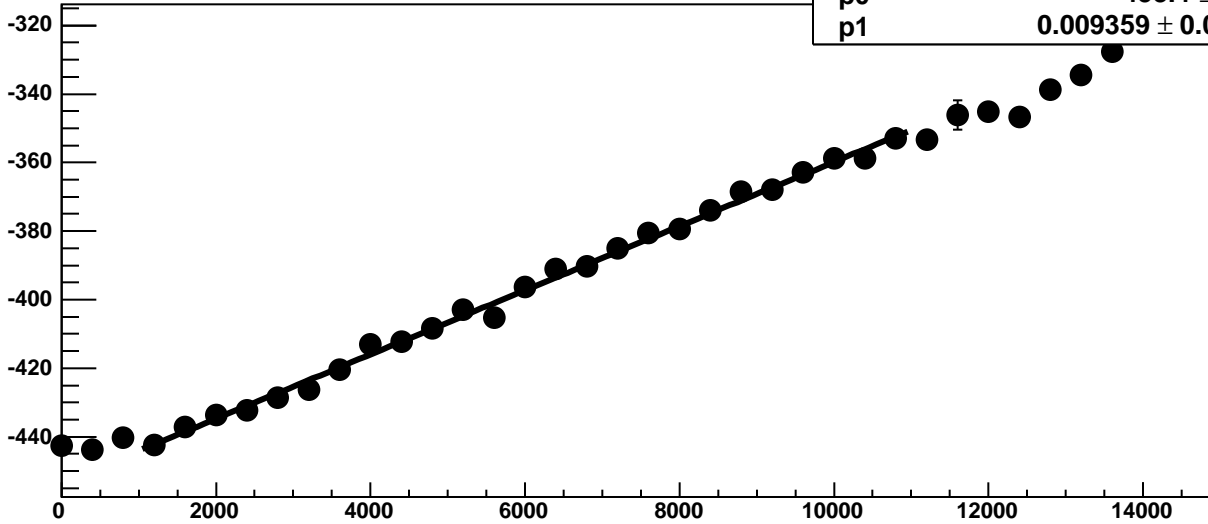
Chip 9, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

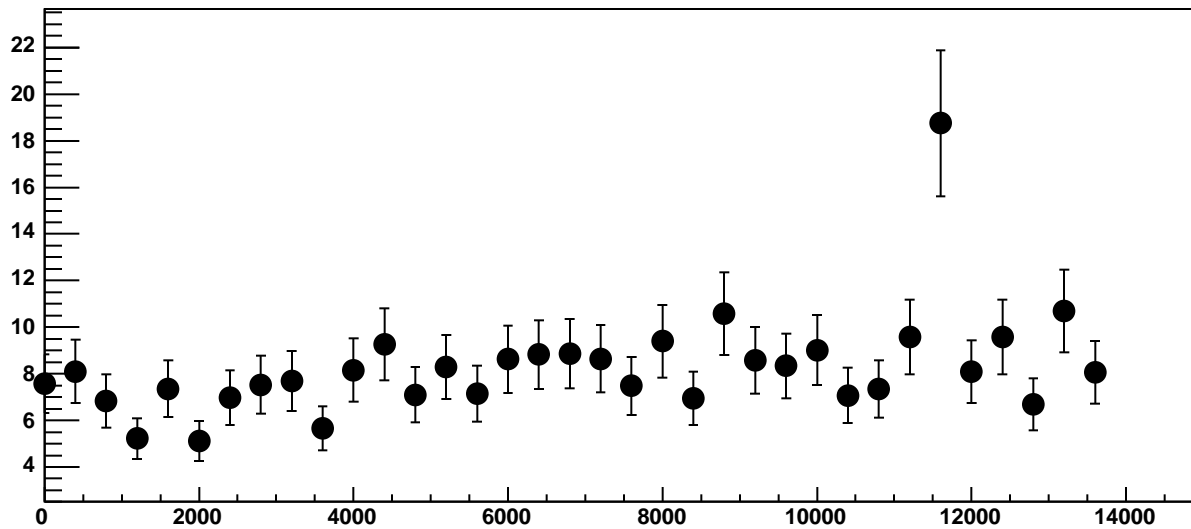


Chip 9, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

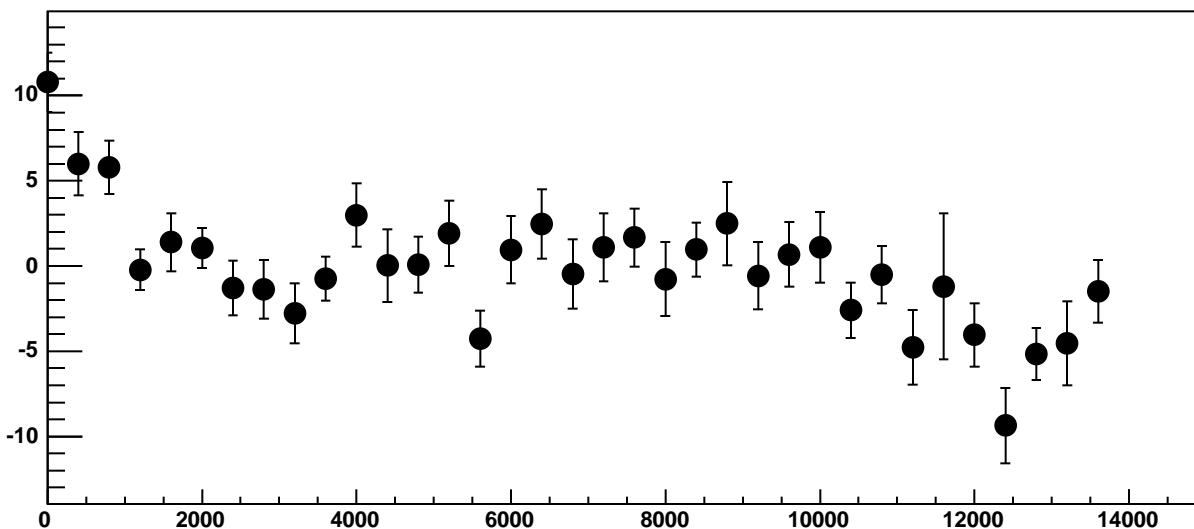


χ^2 / ndf 23.65 / 23
p0 -453.4 ± 0.6943
p1 0.009359 ± 0.0001122

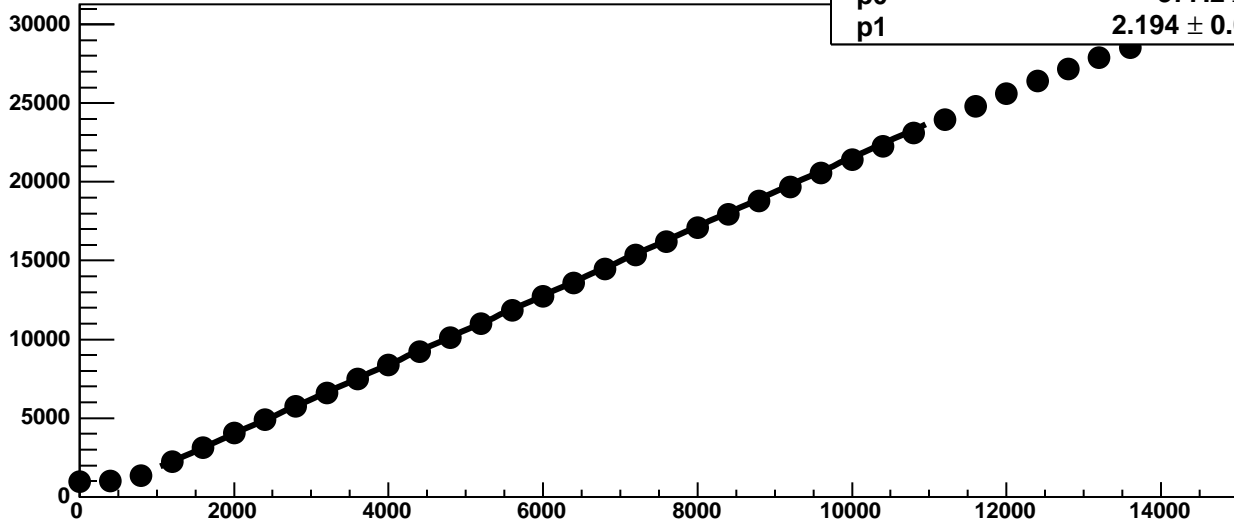
Chip 9, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



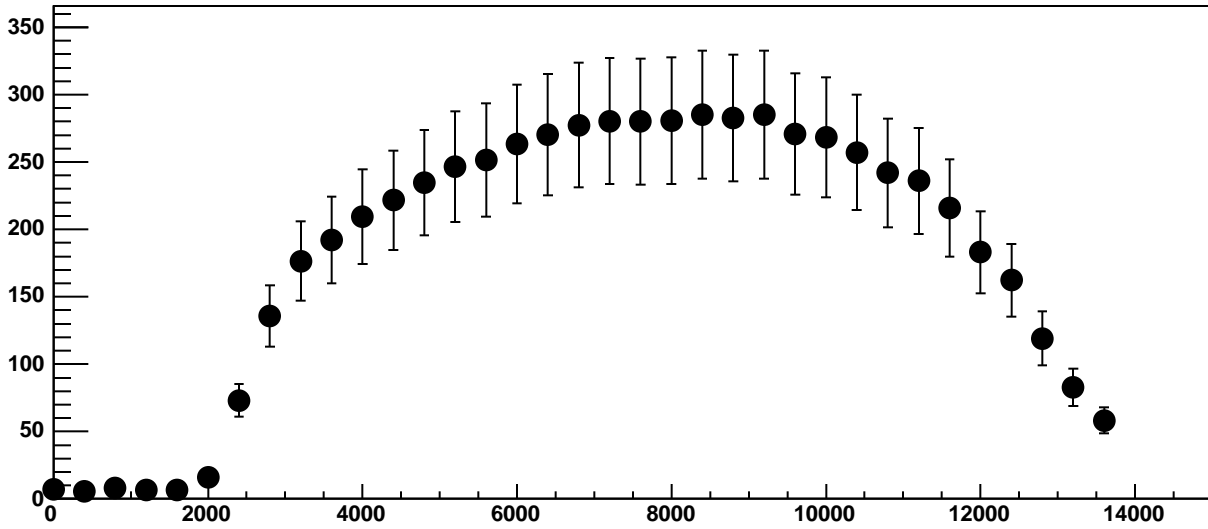
Chip 9, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



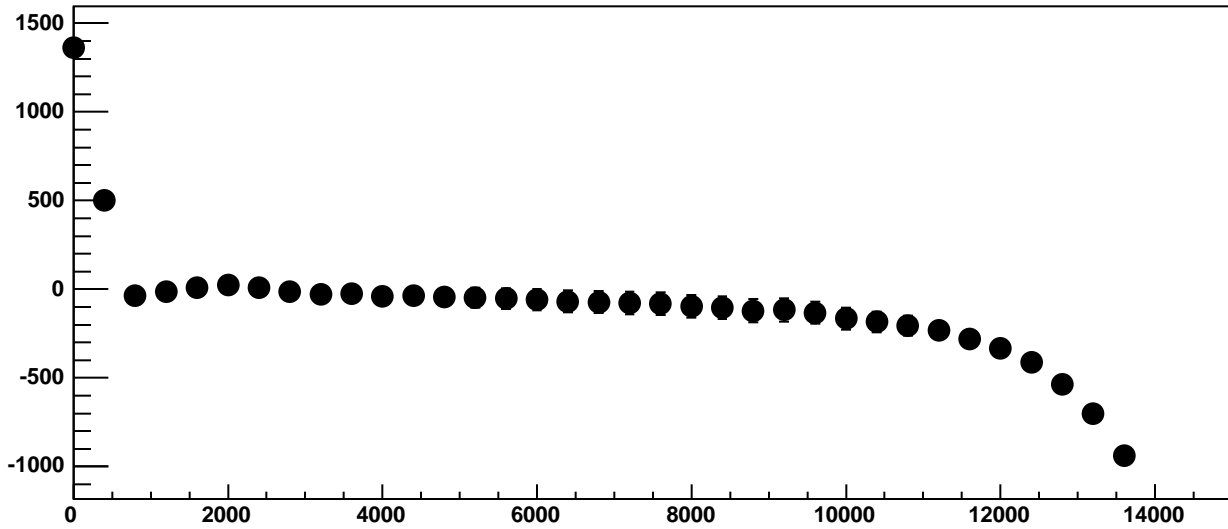
Chip 9, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC



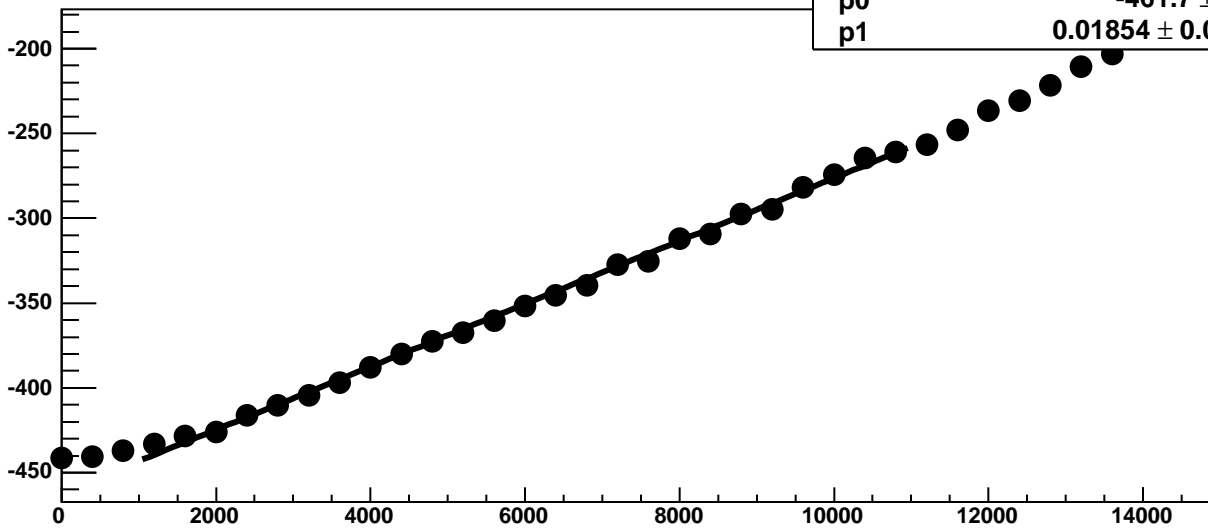
Chip 9, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC



Chip 9, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

58.3 / 23

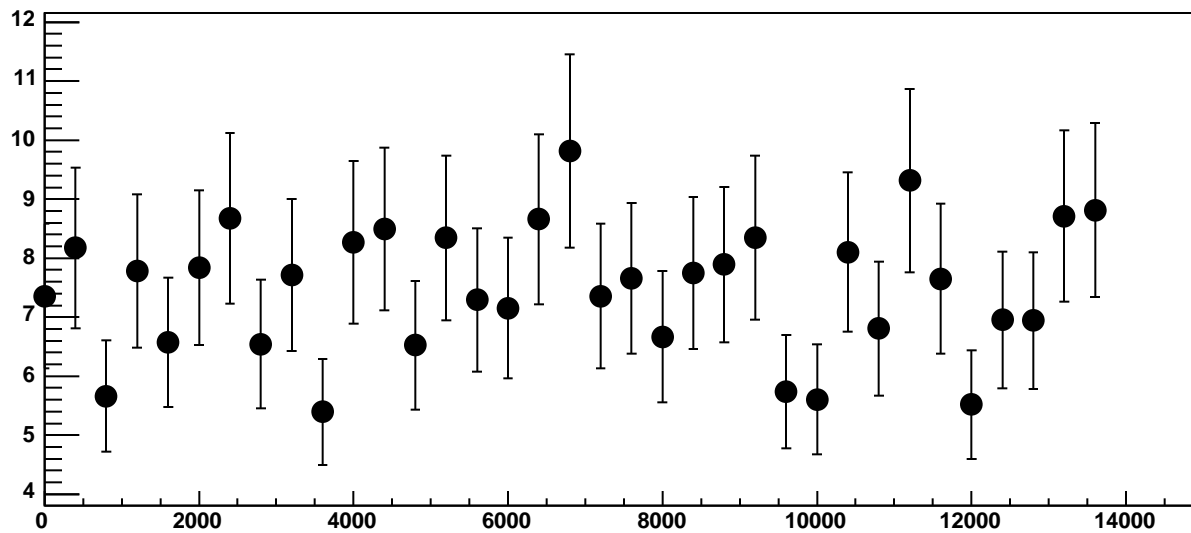
p0

-461.7 ± 0.7594

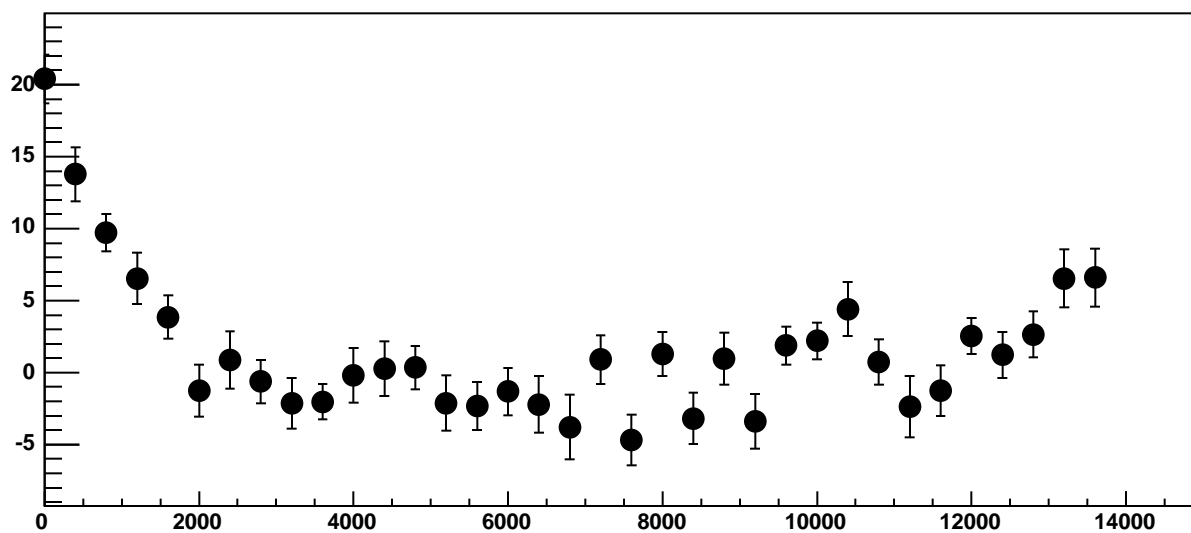
p1

0.01854 ± 0.0001119

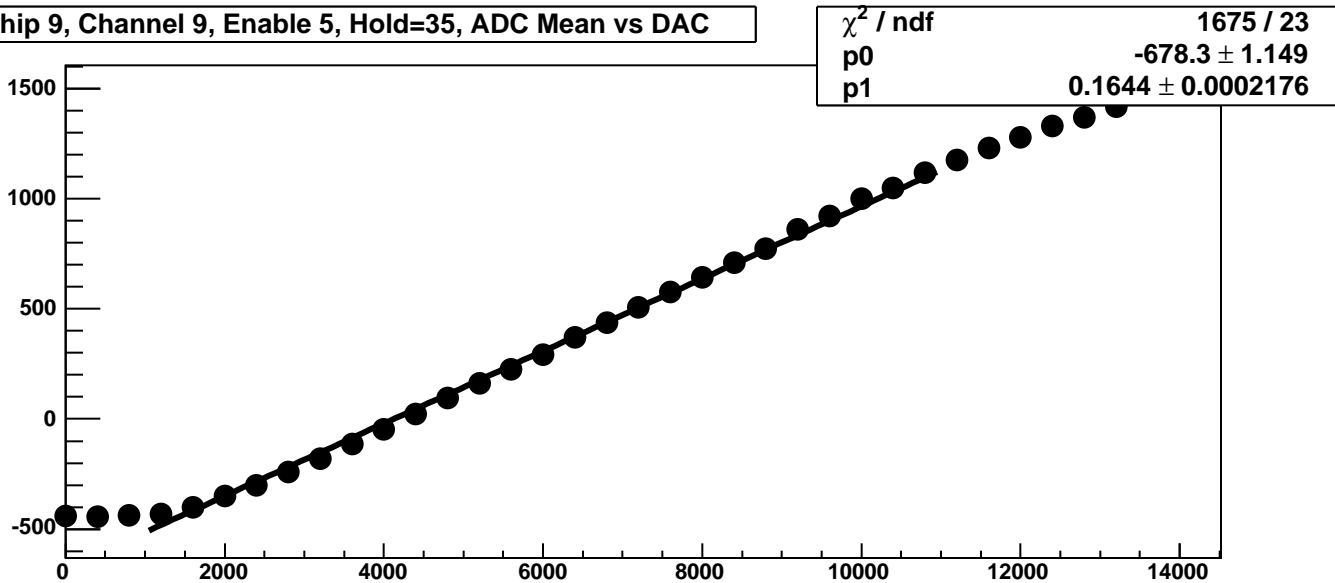
Chip 9, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



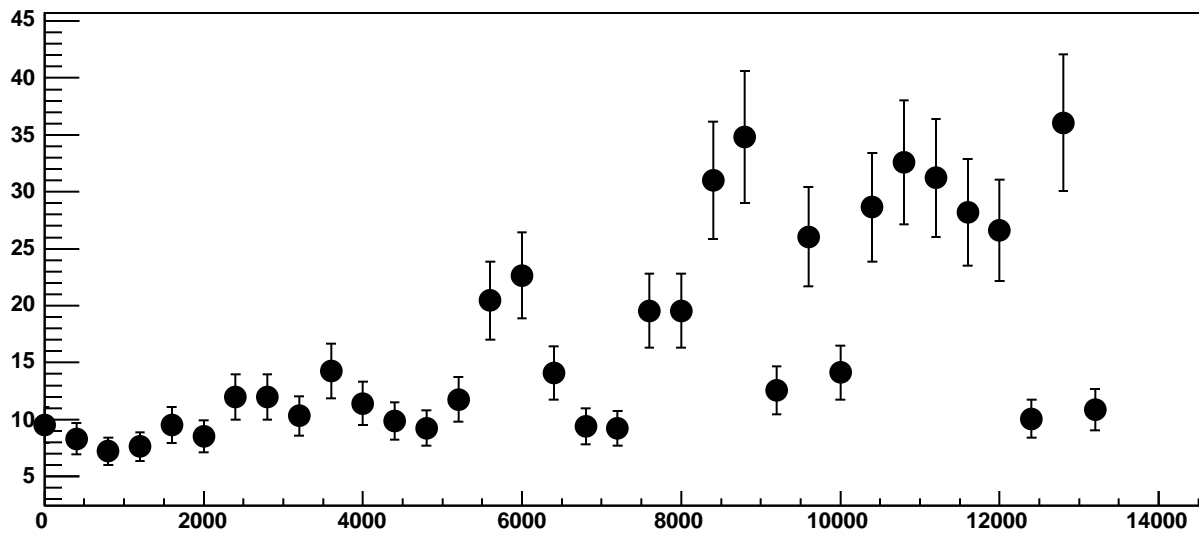
Chip 9, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



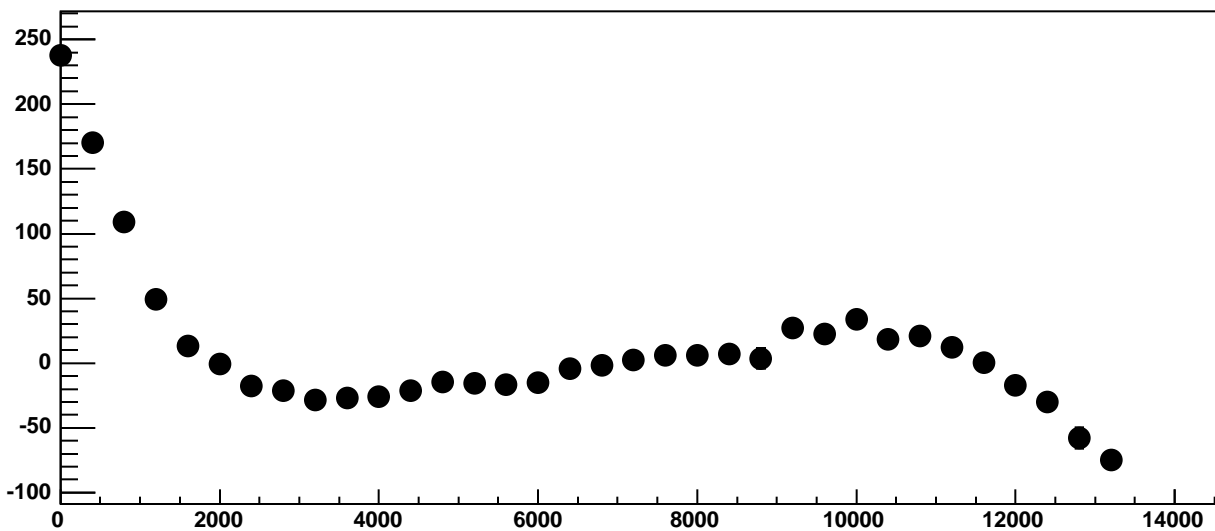
Chip 9, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



Chip 9, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 9, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

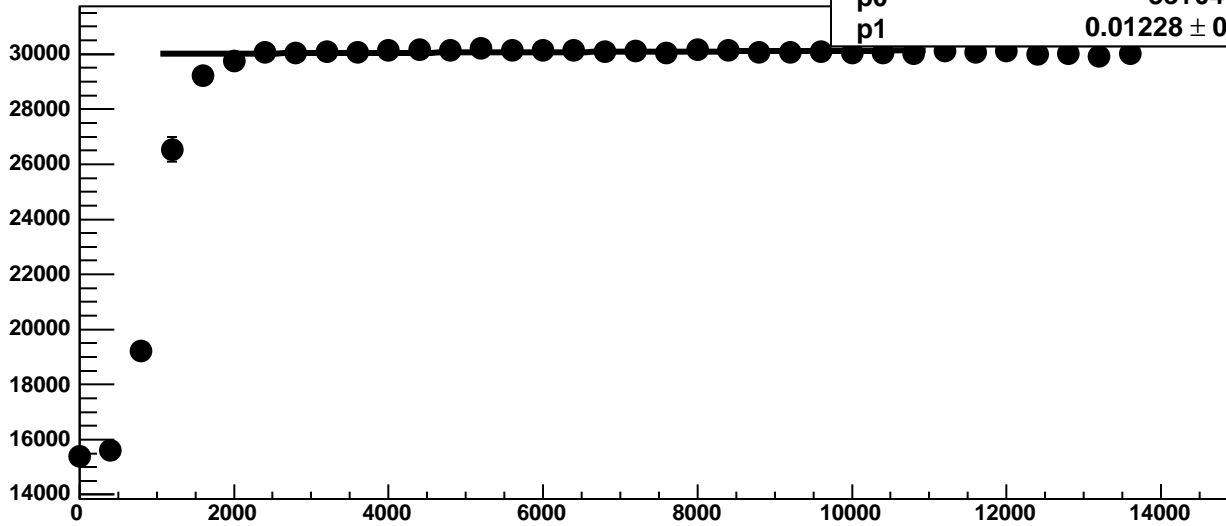
312.7 / 23

p0

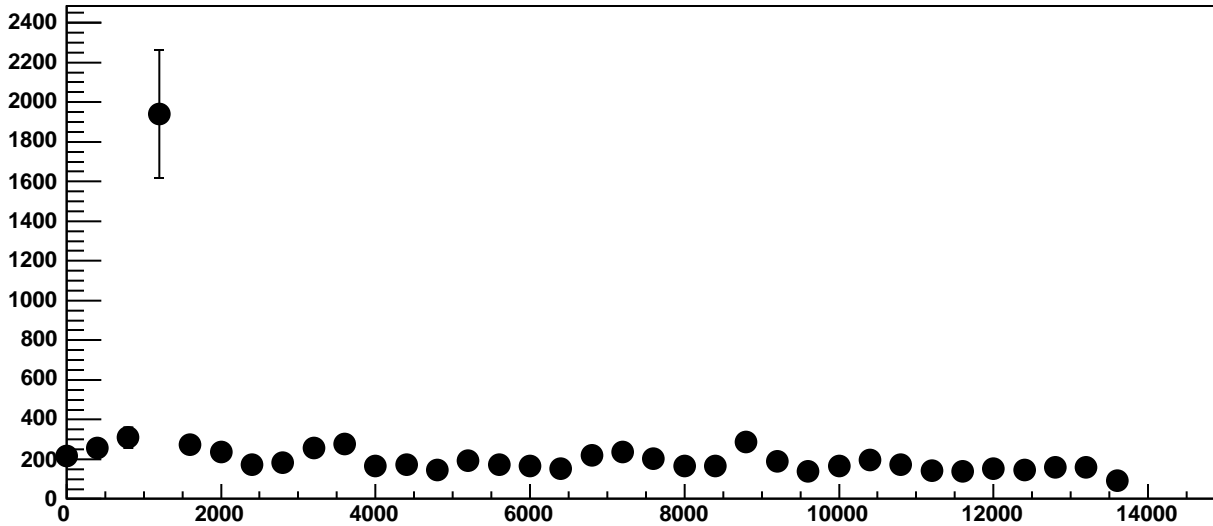
$3e+04 \pm 22.78$

p1

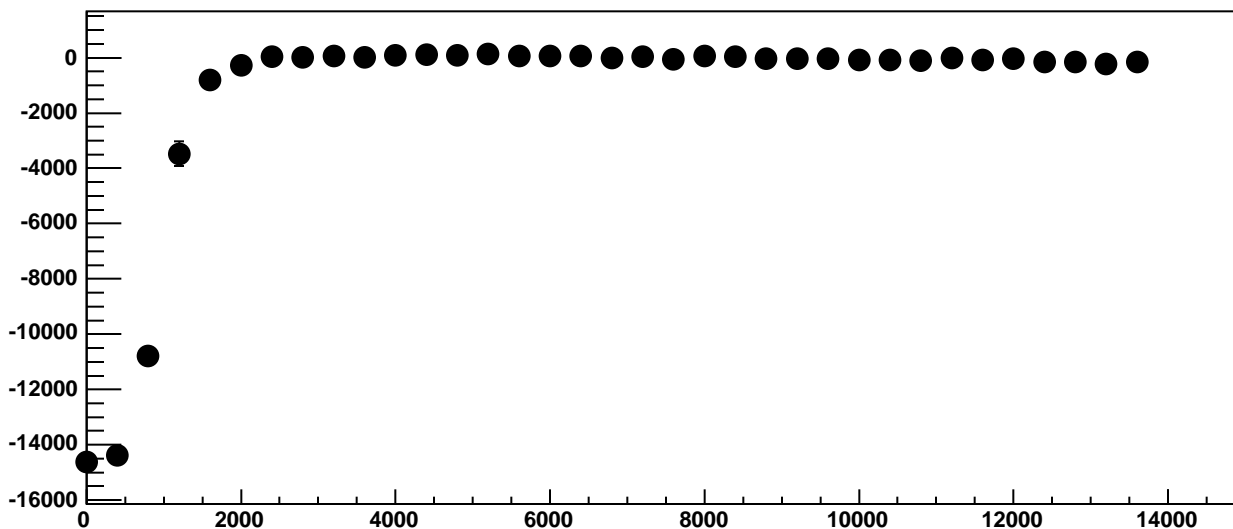
0.01228 ± 0.003251



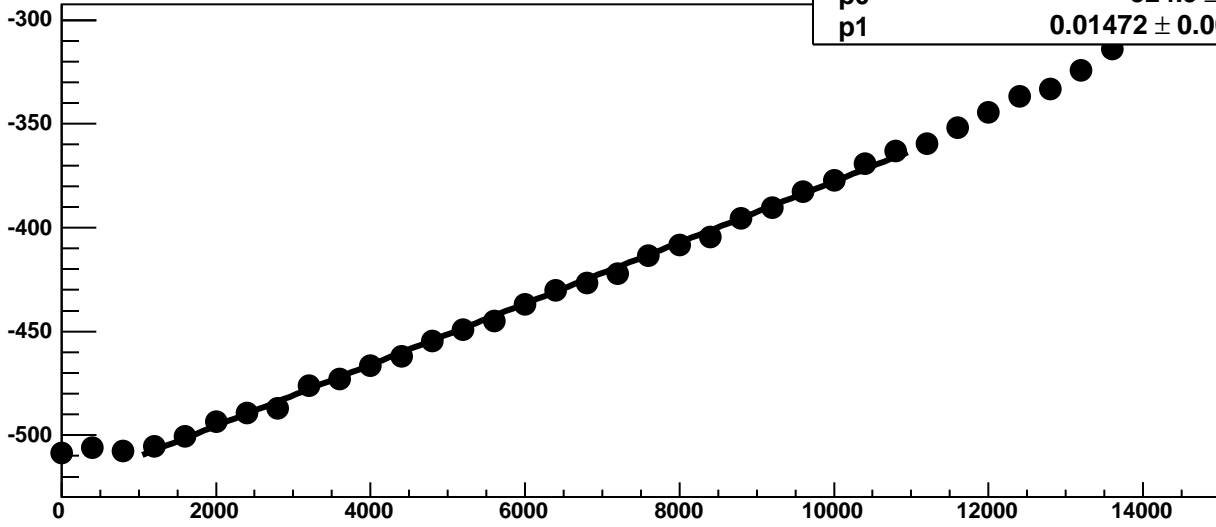
Chip 9, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC

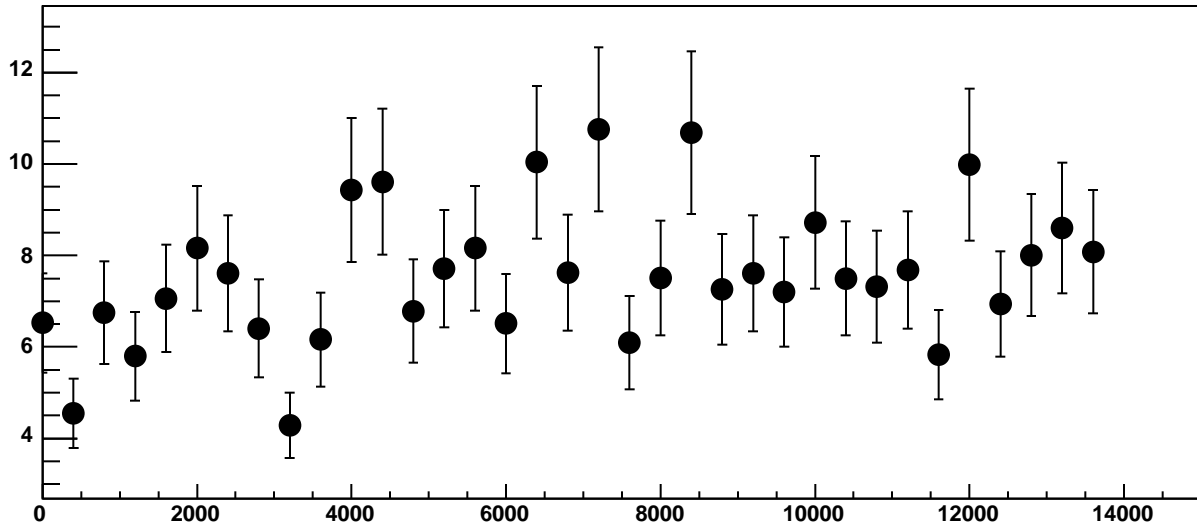


Chip 9, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

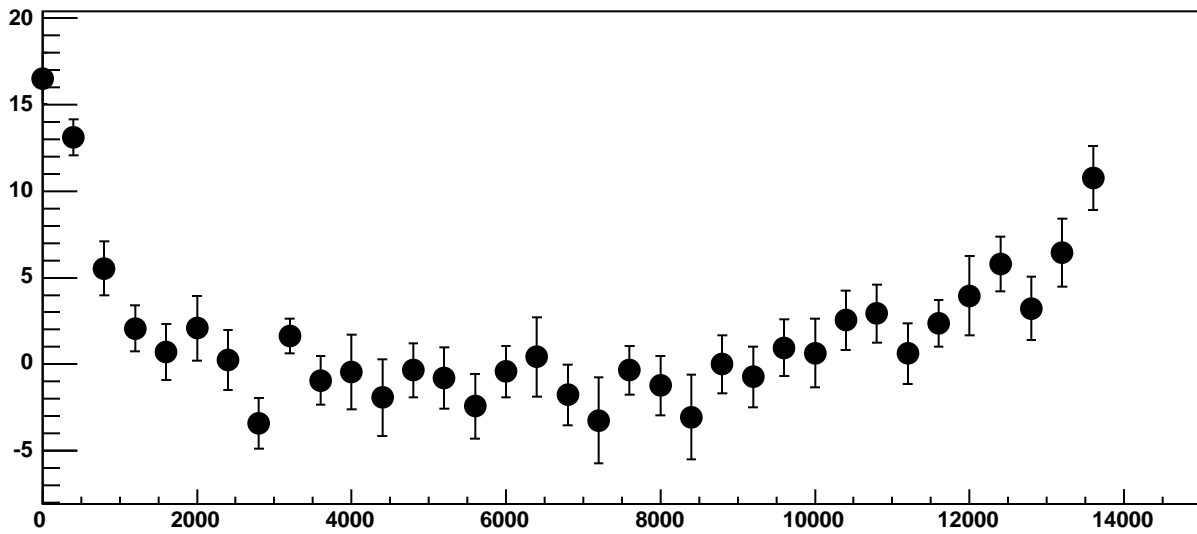


χ^2 / ndf 26.11 / 23
p0 -524.9 ± 0.7081
p1 0.01472 ± 0.0001124

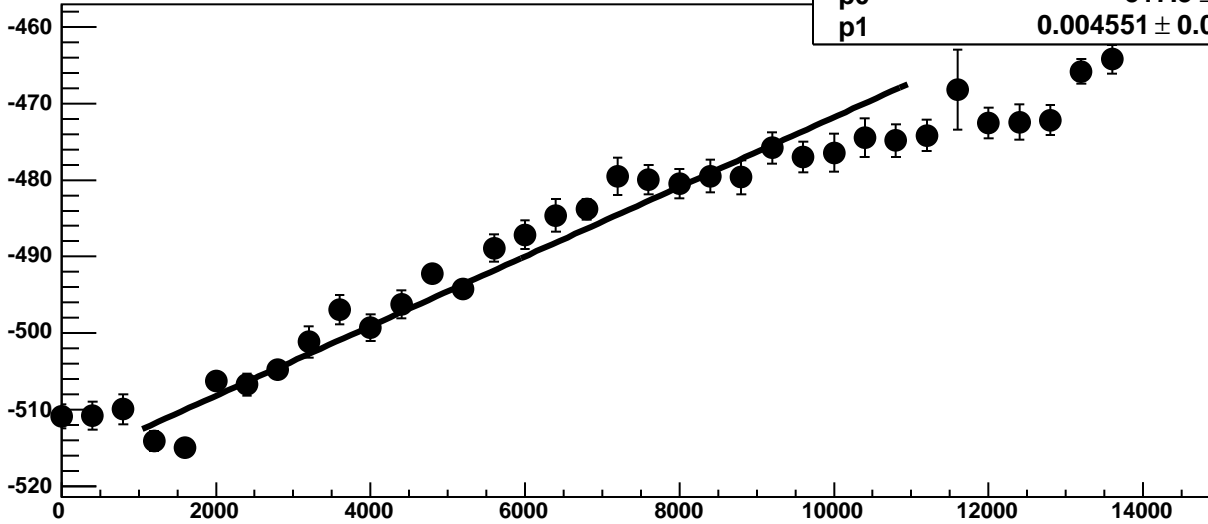
Chip 9, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



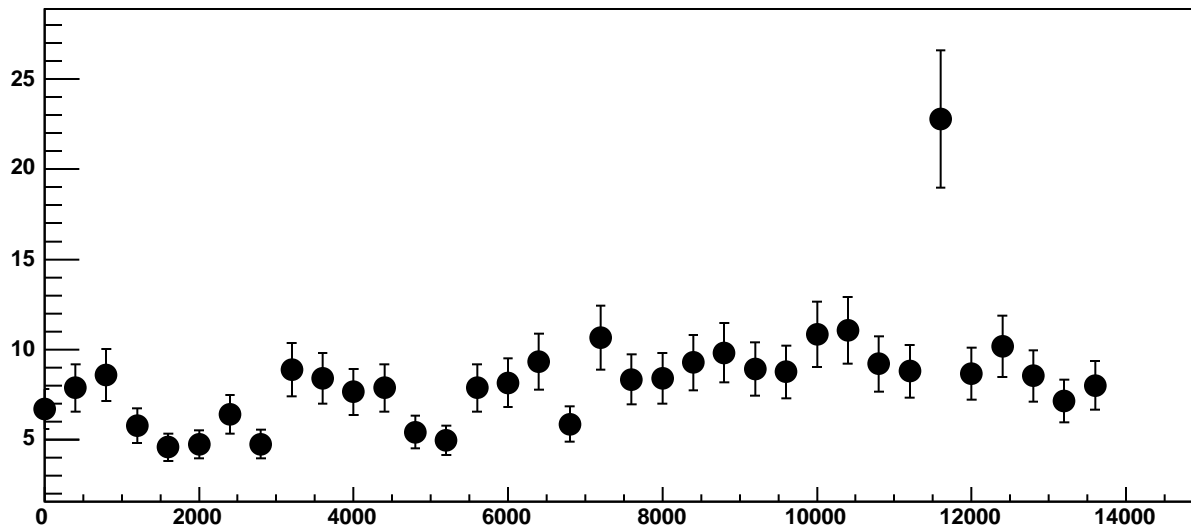
Chip 9, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



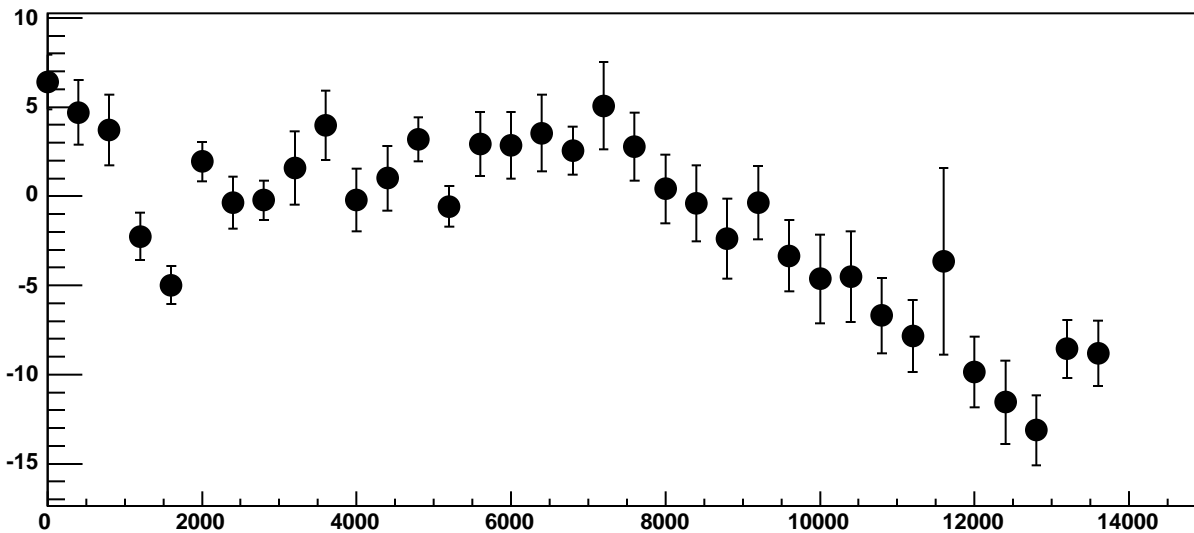
Chip 9, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



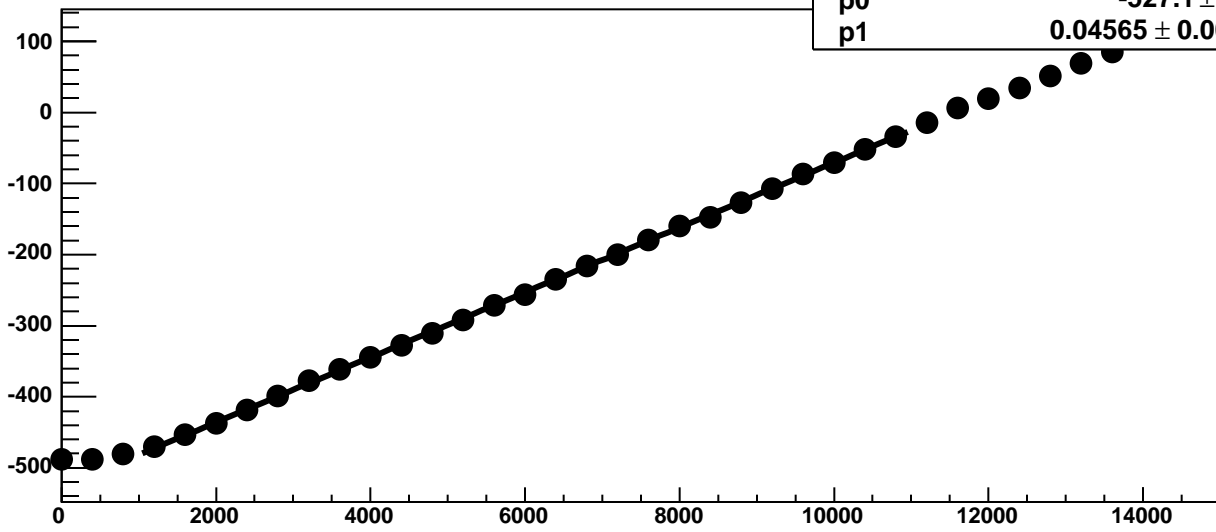
Chip 9, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

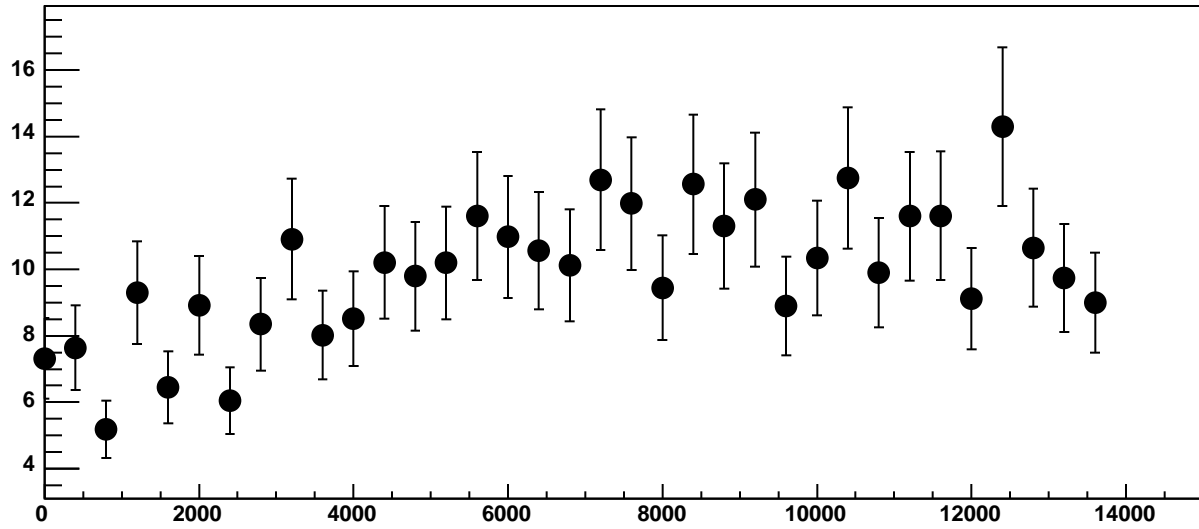


Chip 9, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC

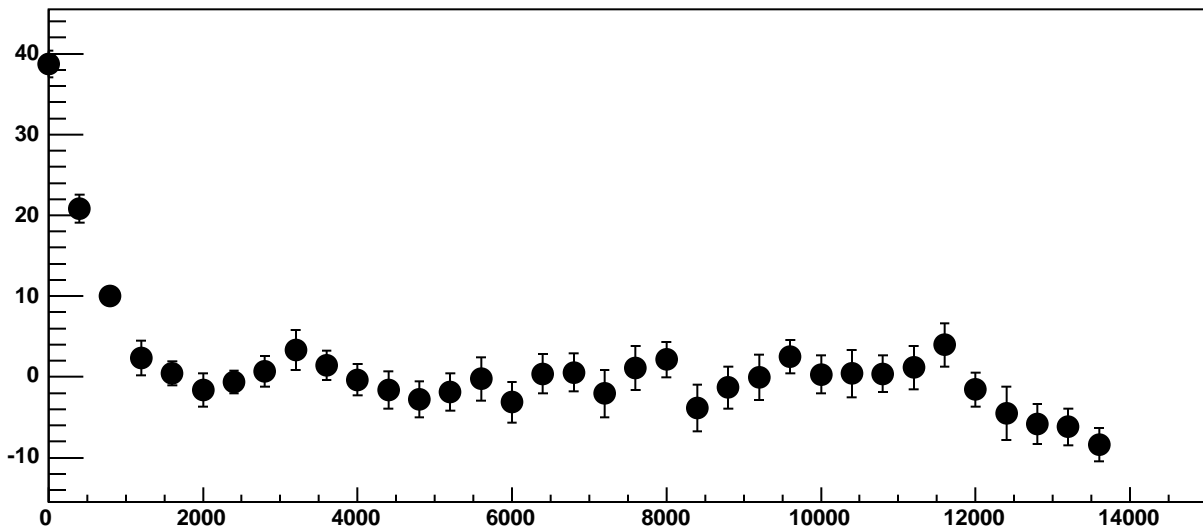


χ^2 / ndf 14.26 / 23
p0 -527.1 ± 0.8832
p1 0.04565 ± 0.0001468

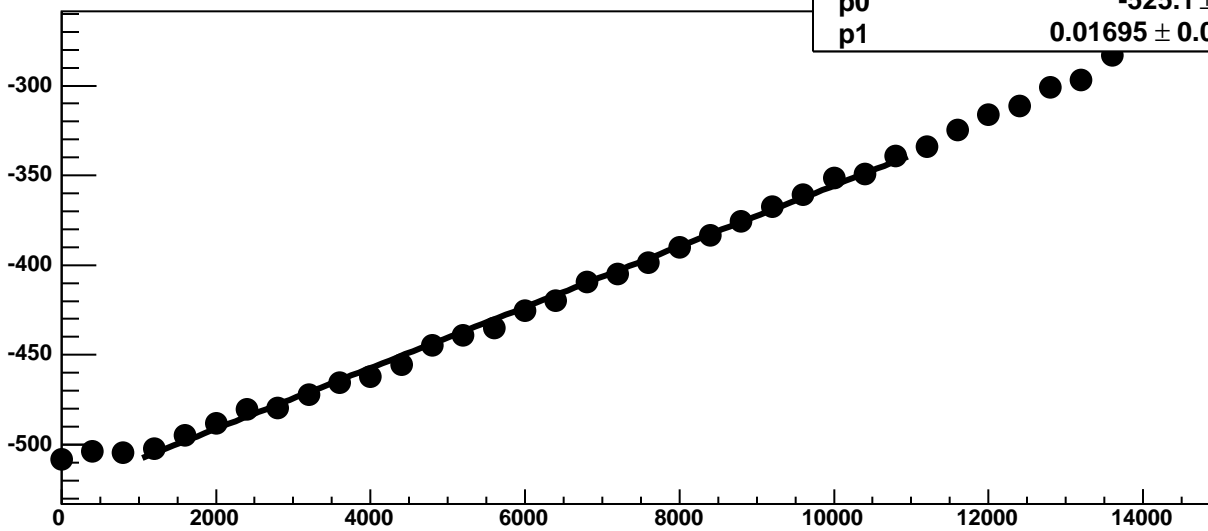
Chip 9, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

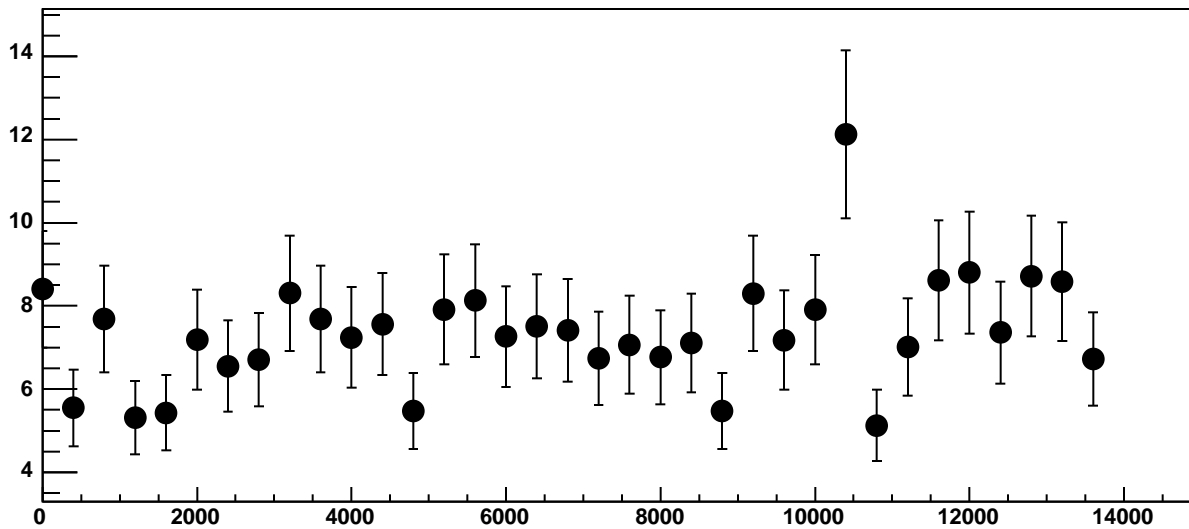


Chip 9, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

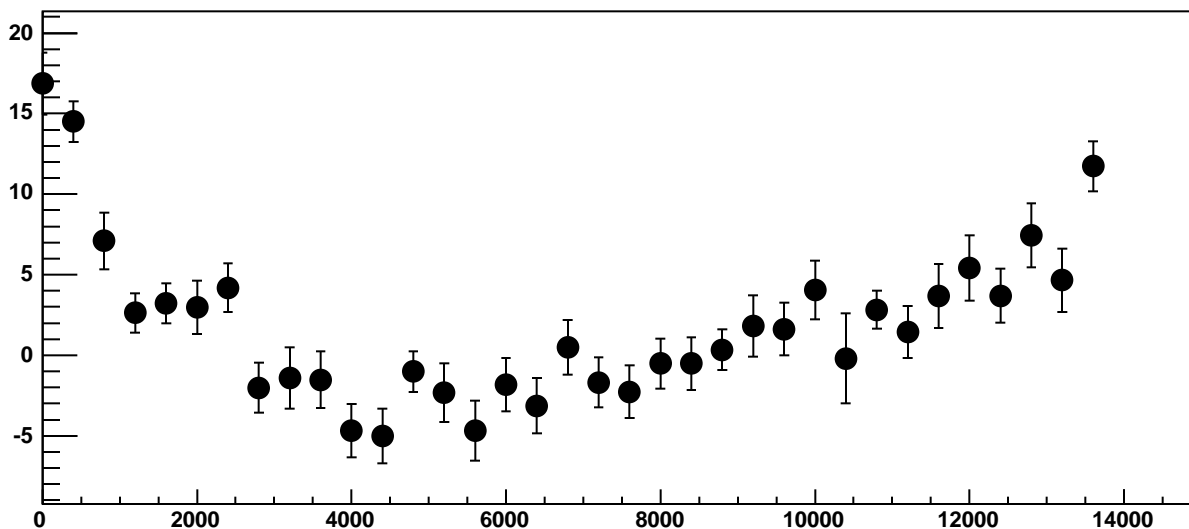


χ^2 / ndf 71.21 / 23
p0 -525.1 ± 0.6785
p1 0.01695 ± 0.0001035

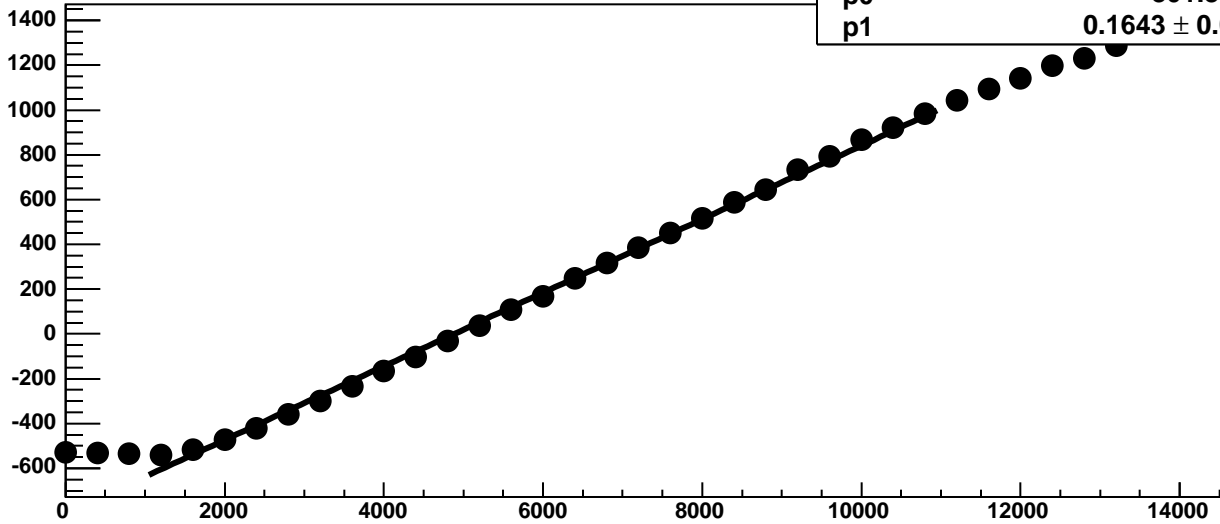
Chip 9, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



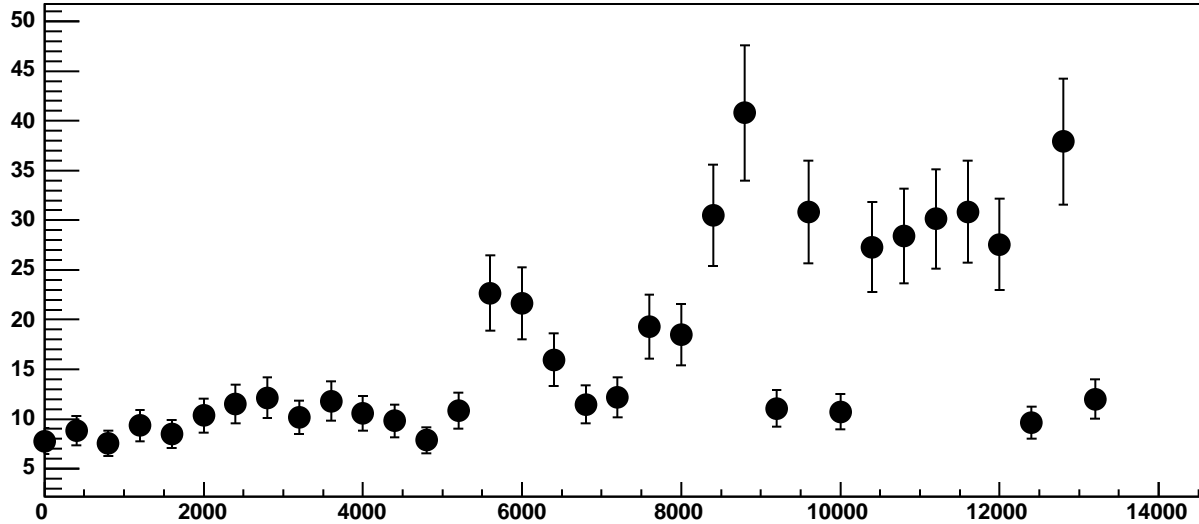
Chip 9, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



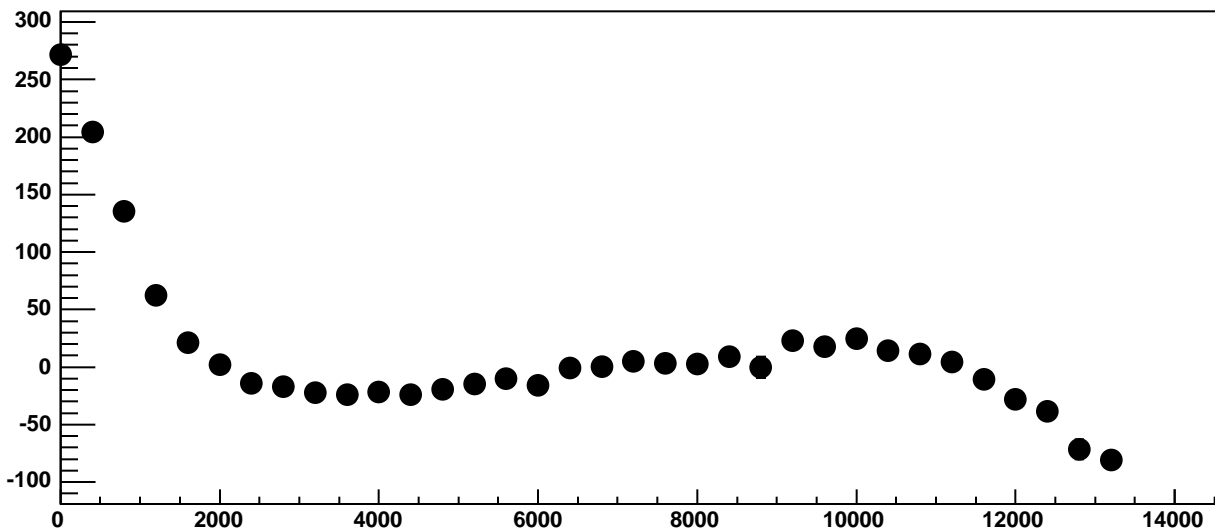
Chip 9, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



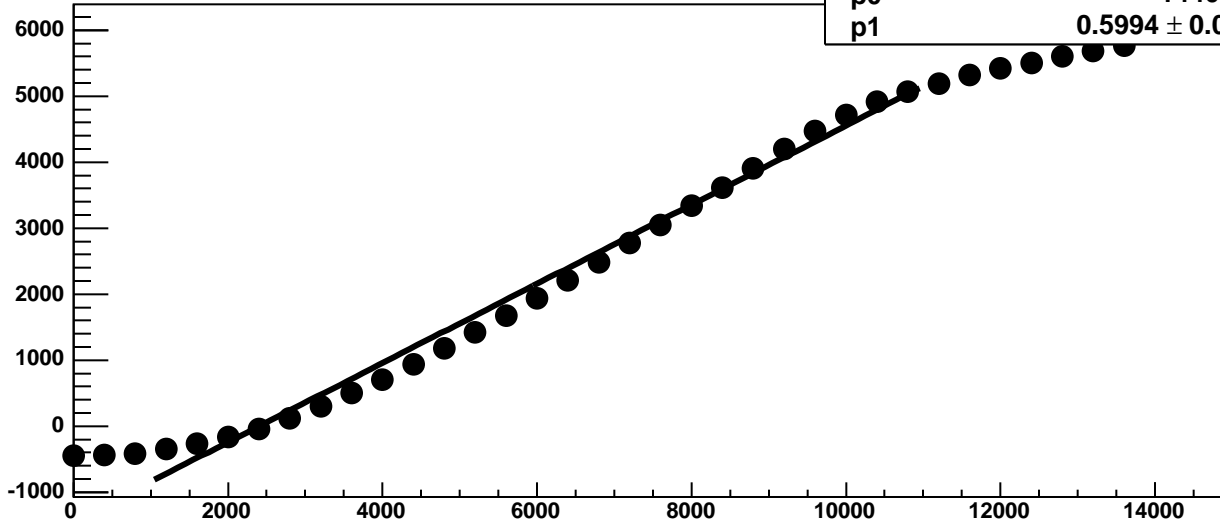
Chip 9, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



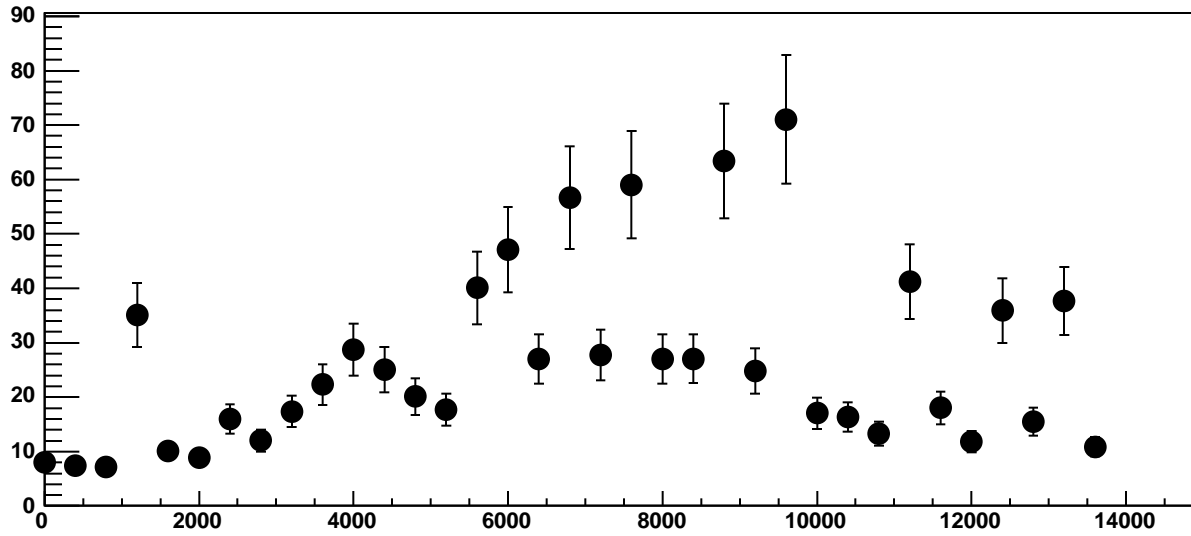
Chip 9, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



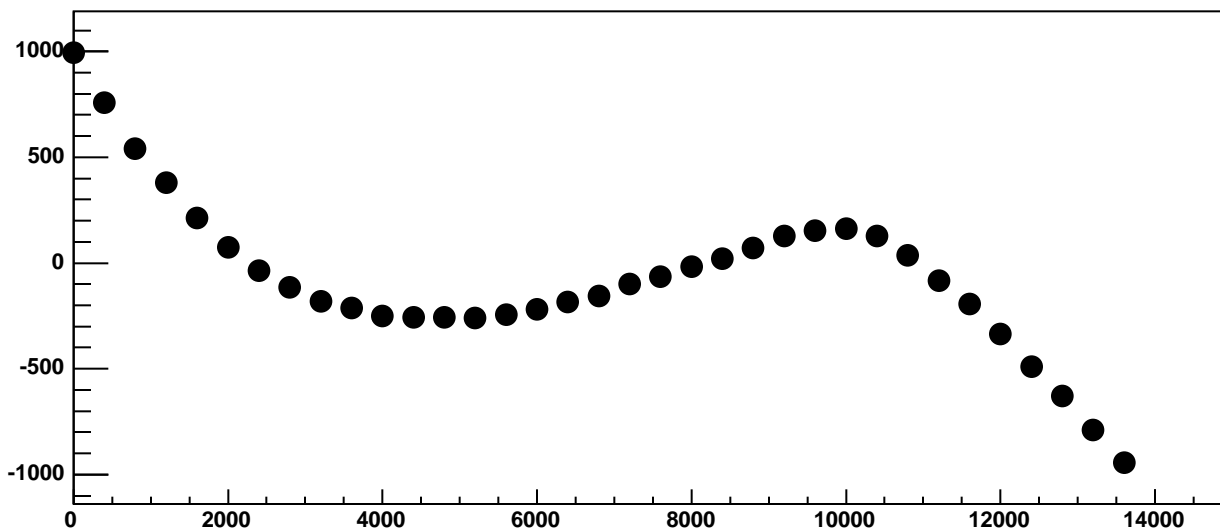
Chip 9, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC



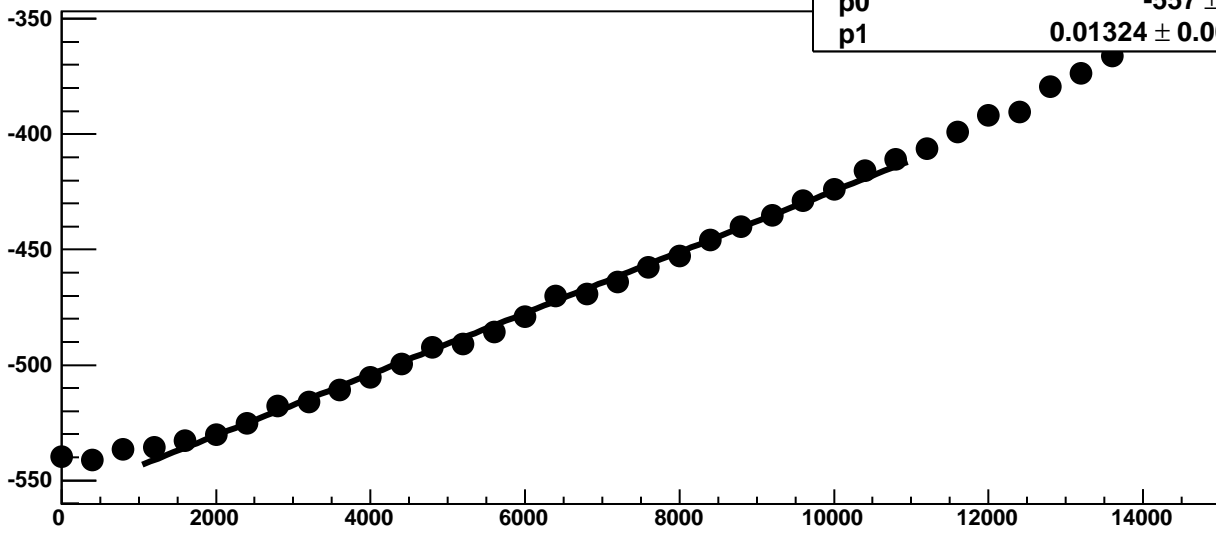
Chip 9, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC

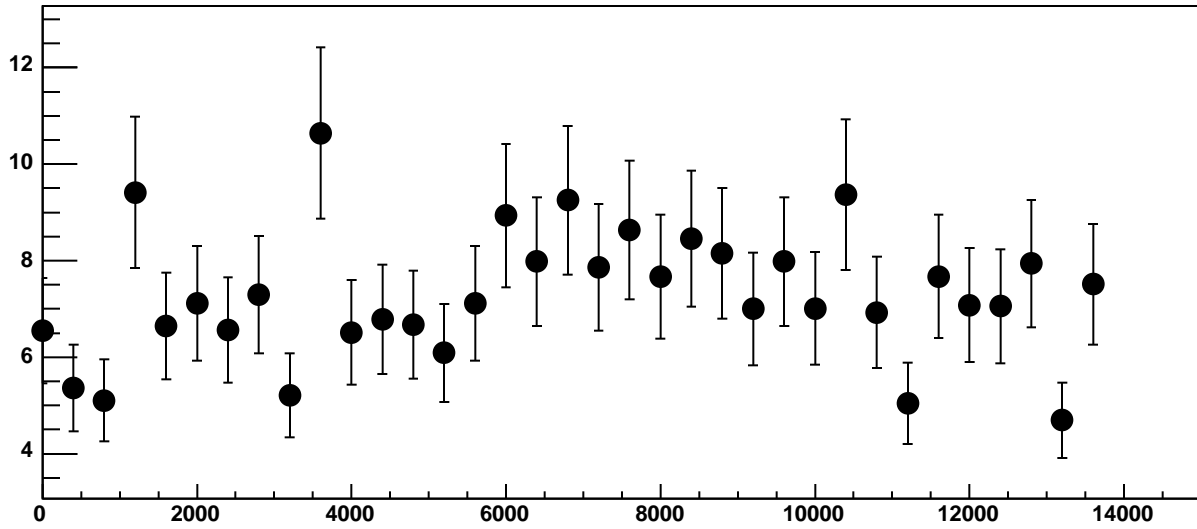


Chip 9, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

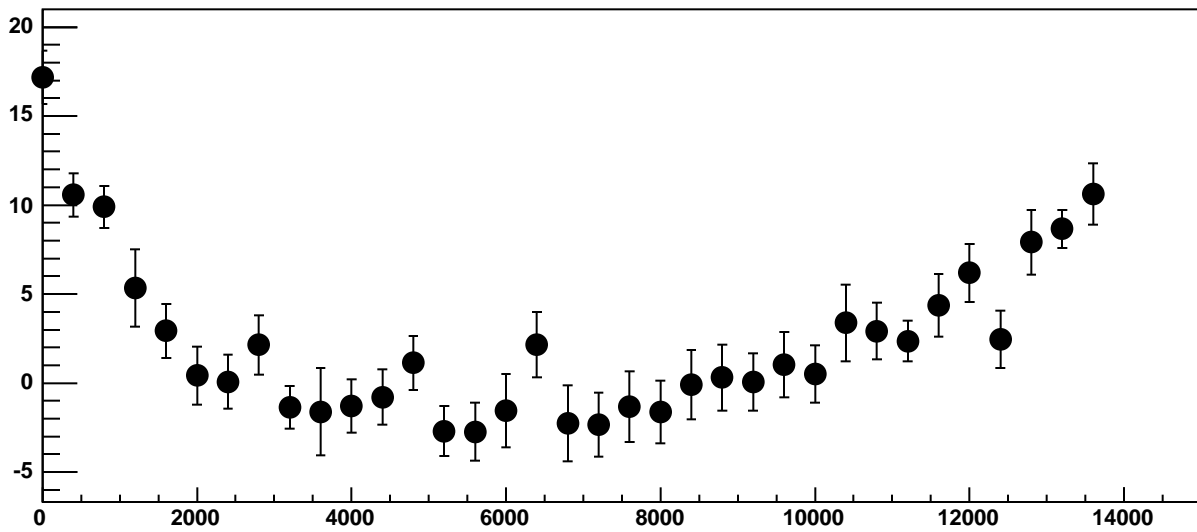


χ^2 / ndf 33.68 / 23
p0 -557 ± 0.7561
p1 0.01324 ± 0.0001178

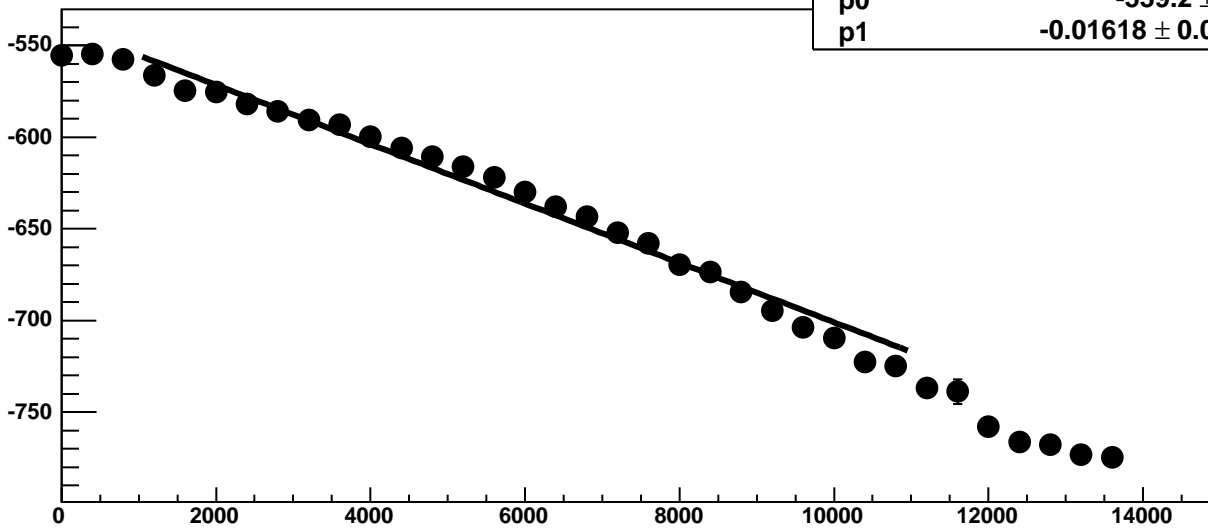
Chip 9, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 9, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

240.1 / 23

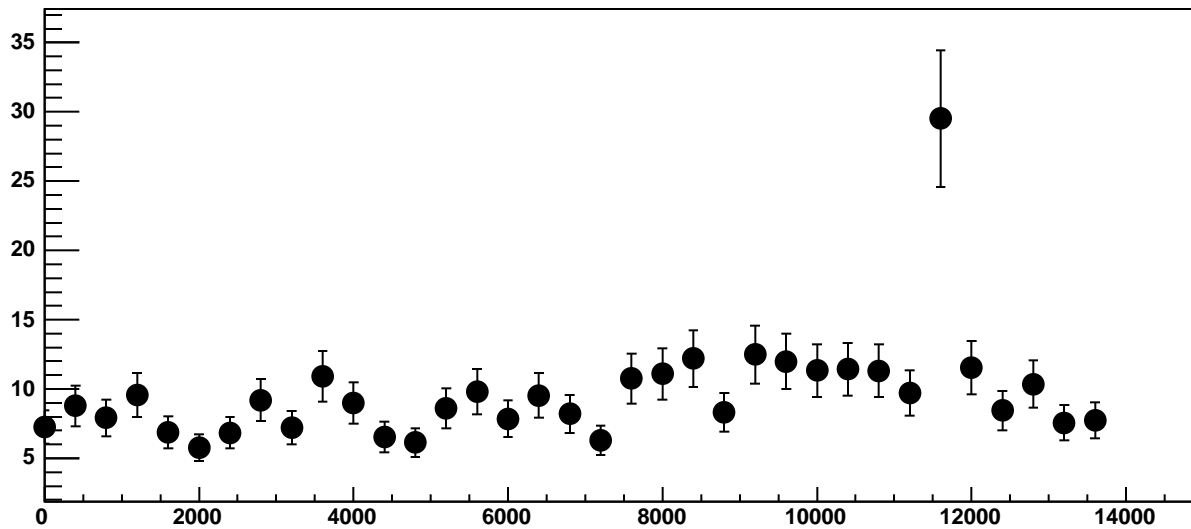
p0

-539.2 ± 0.8437

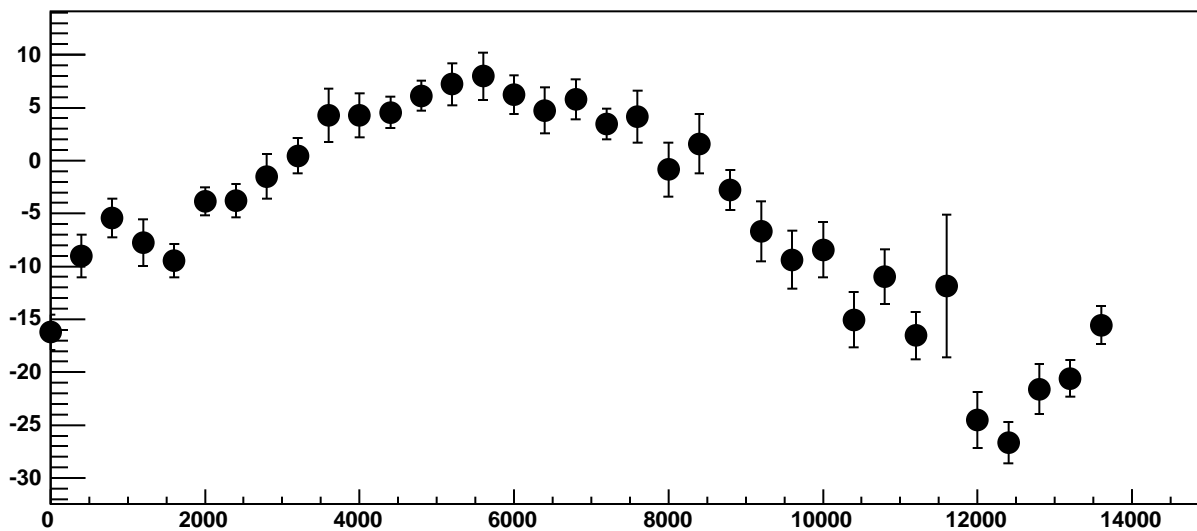
p1

-0.01618 ± 0.0001442

Chip 9, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

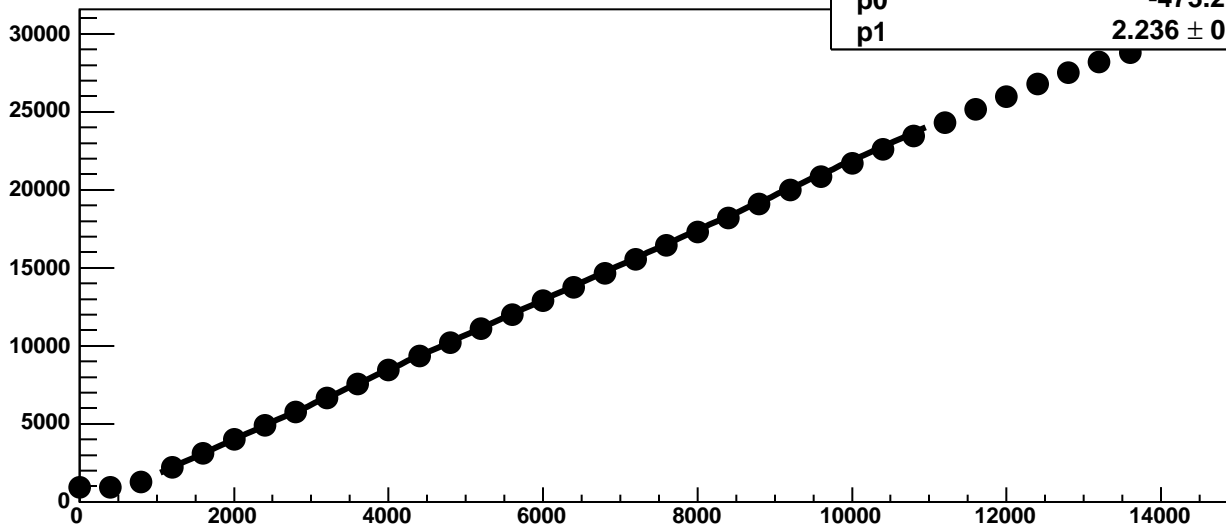


Chip 9, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

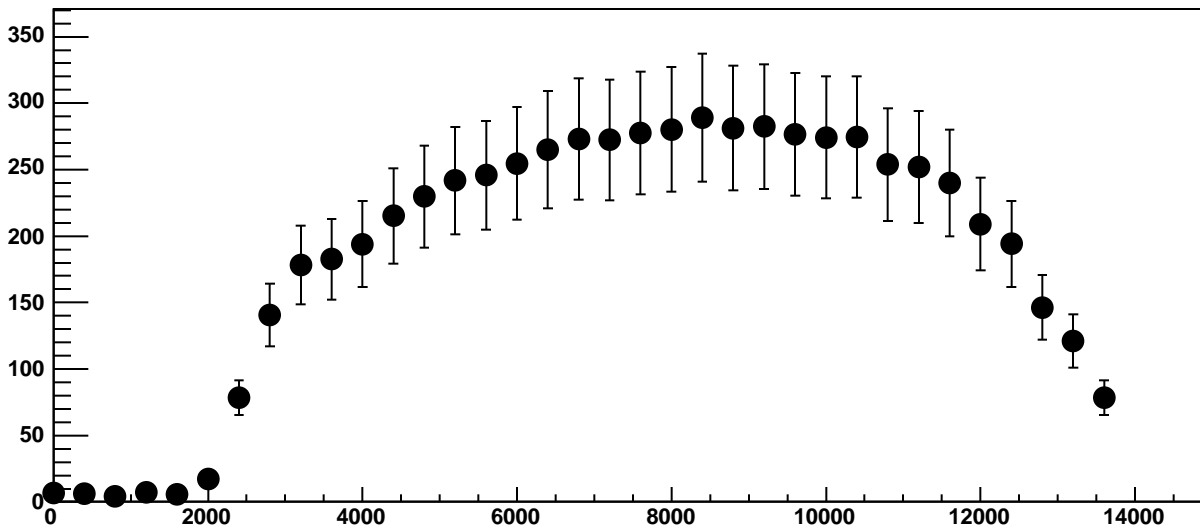


Chip 9, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC

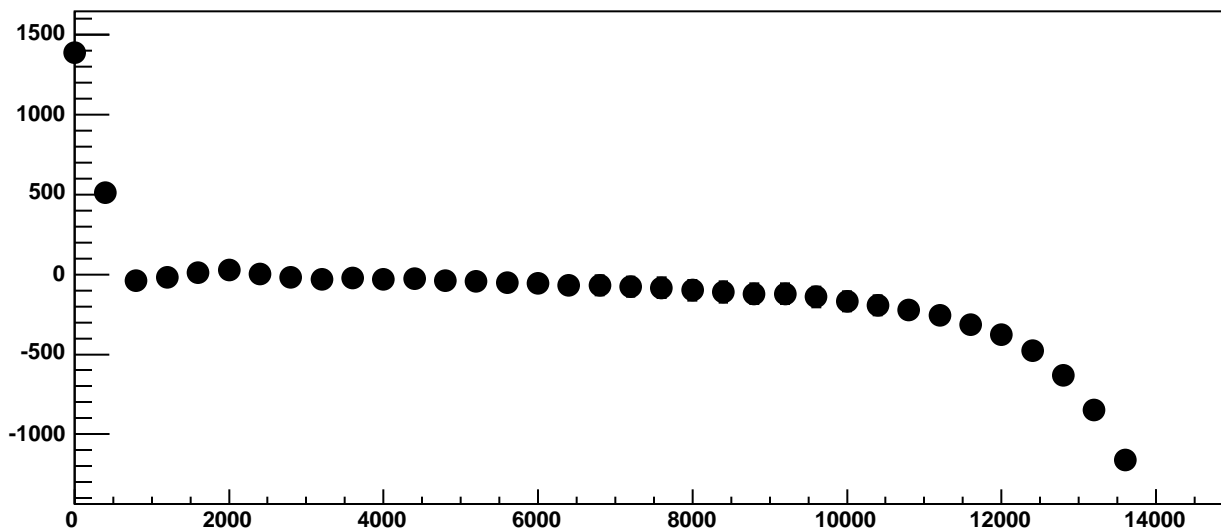
χ^2 / ndf 255 / 23
p0 -473.2 ± 3.185
p1 2.236 ± 0.002003



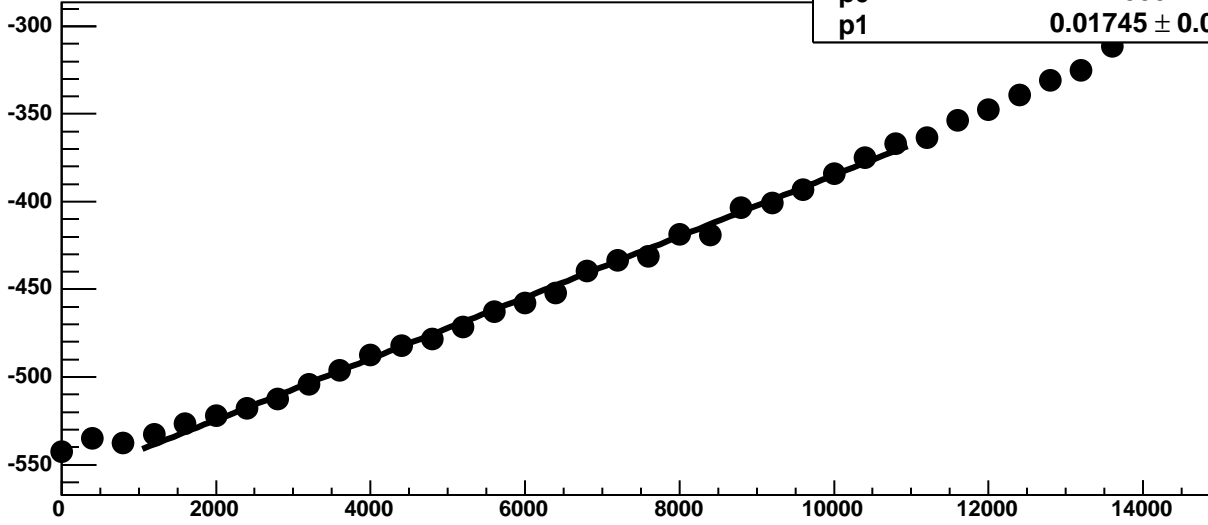
Chip 9, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC

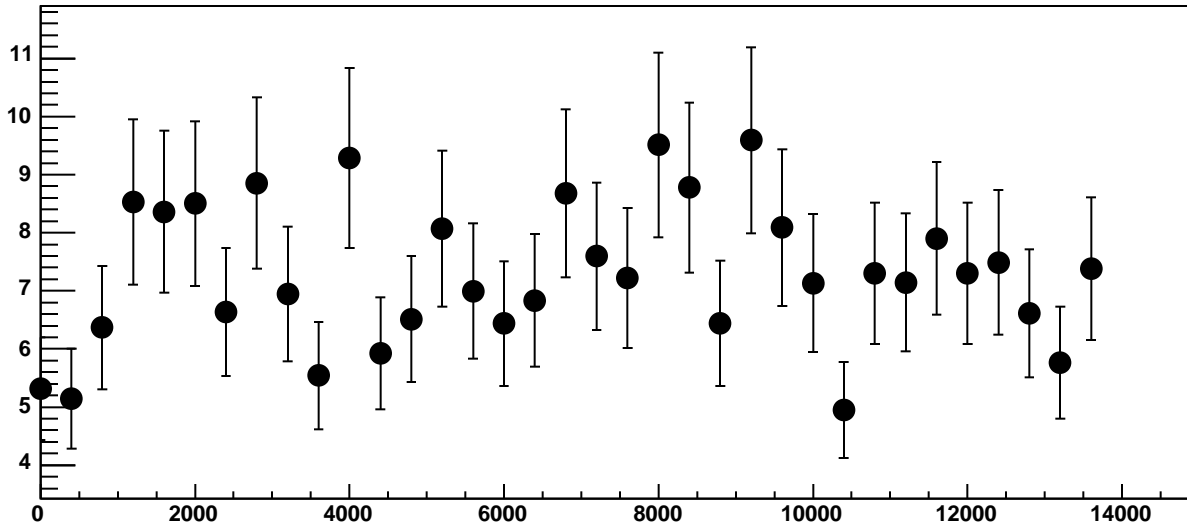


Chip 9, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

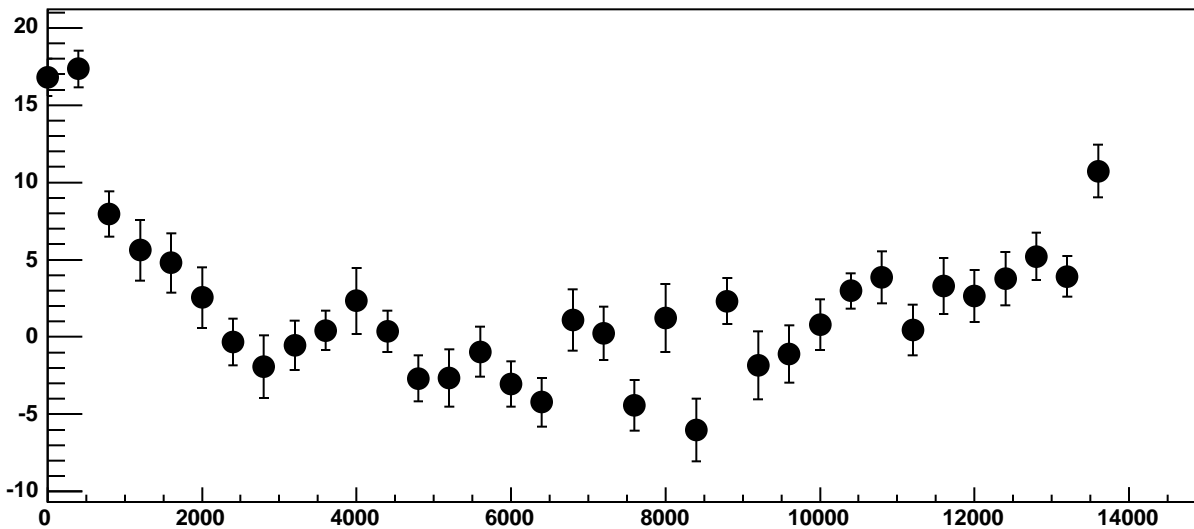


χ^2 / ndf 68.34 / 23
p0 -559.4 ± 0.7788
p1 0.01745 ± 0.0001148

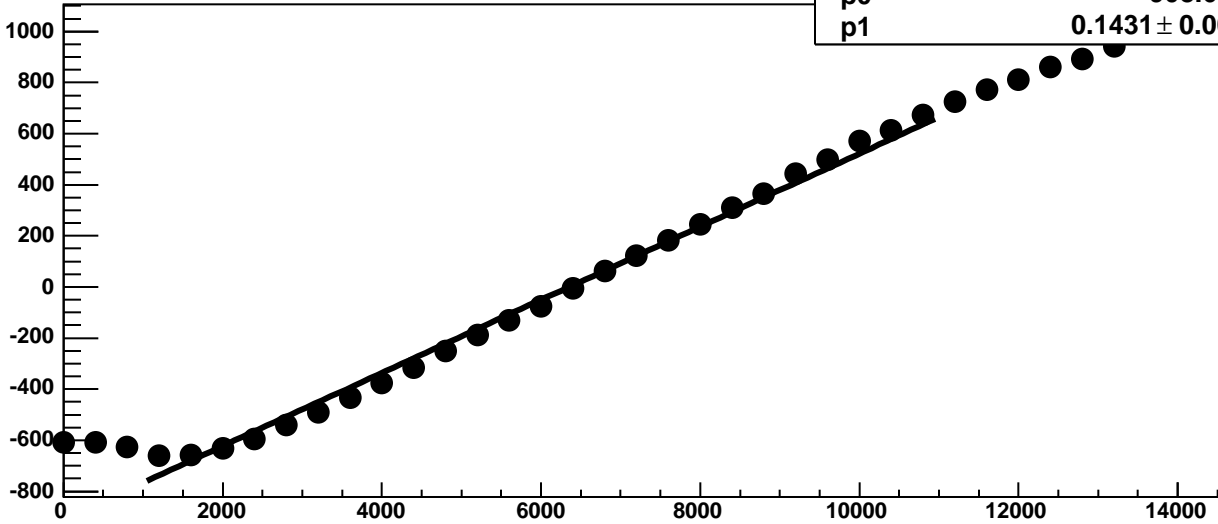
Chip 9, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



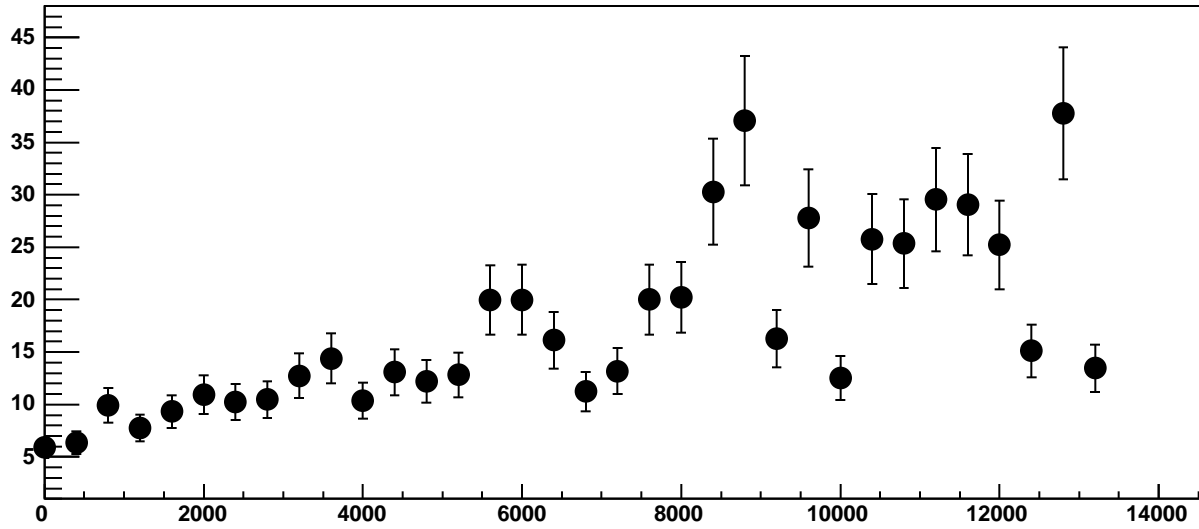
Chip 9, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



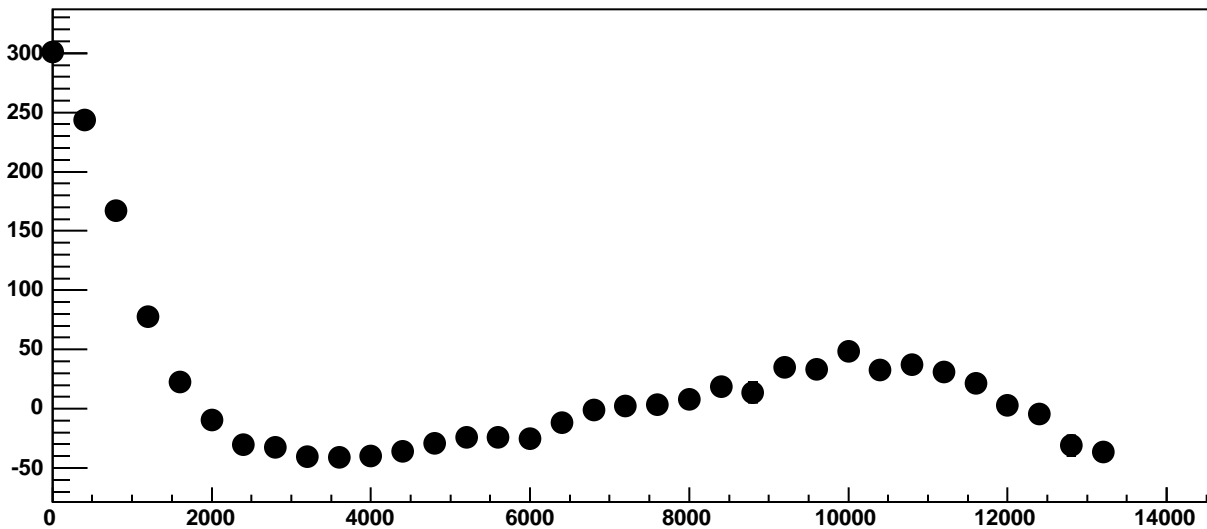
Chip 9, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



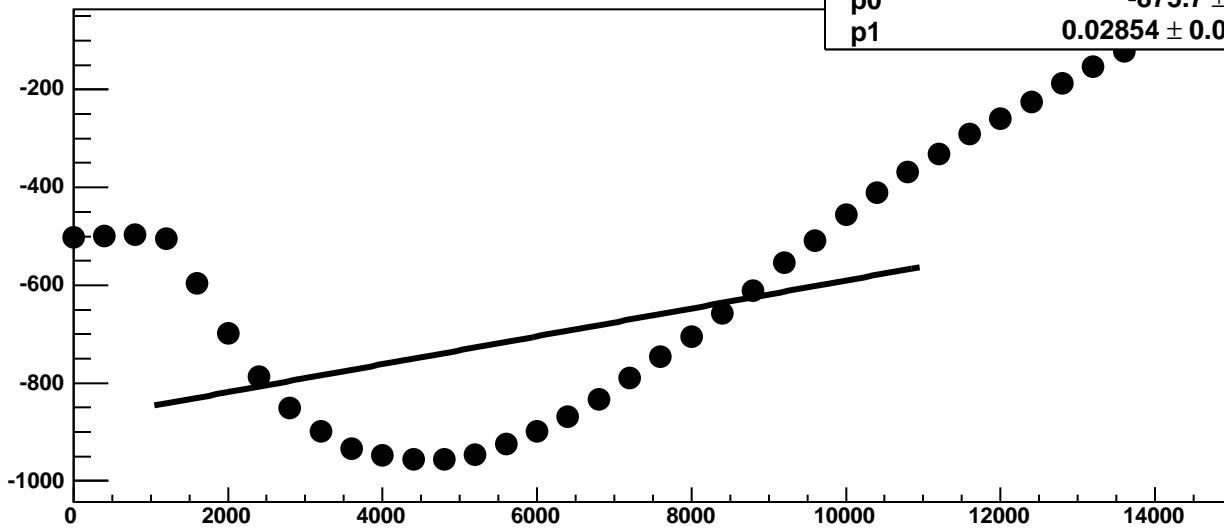
Chip 9, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



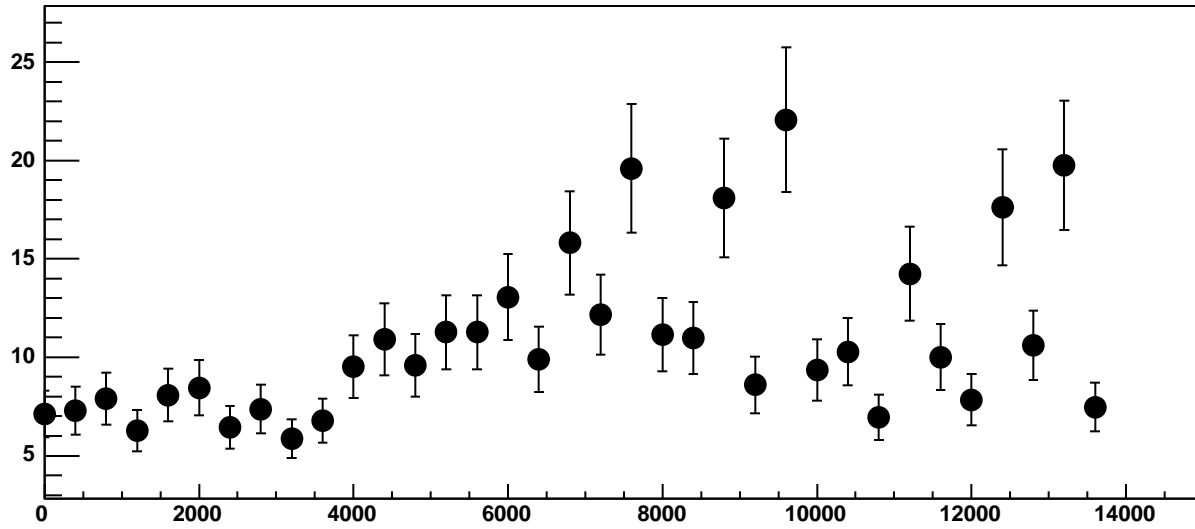
Chip 9, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



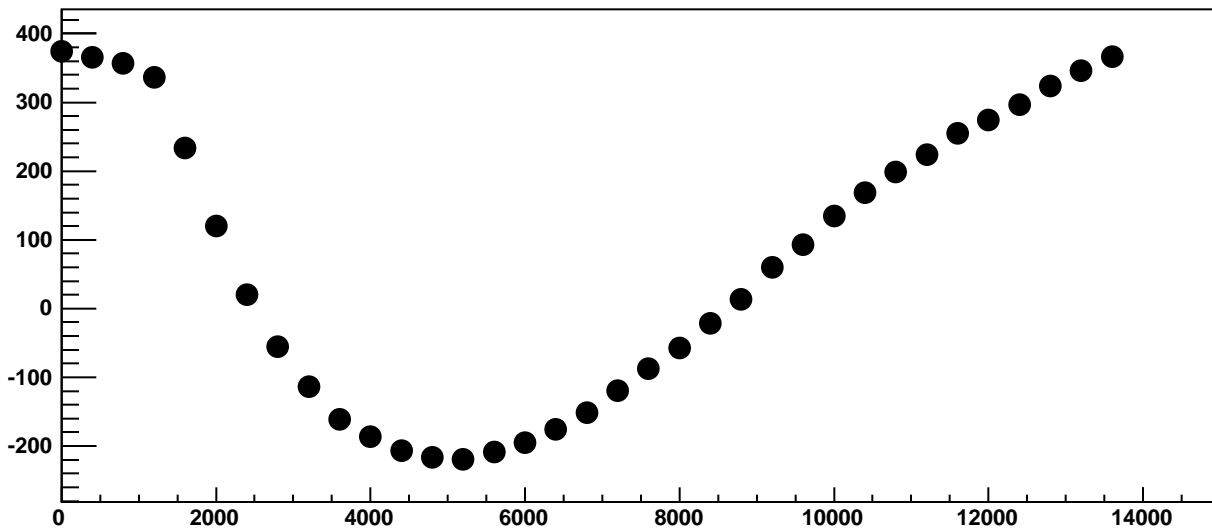
Chip 9, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



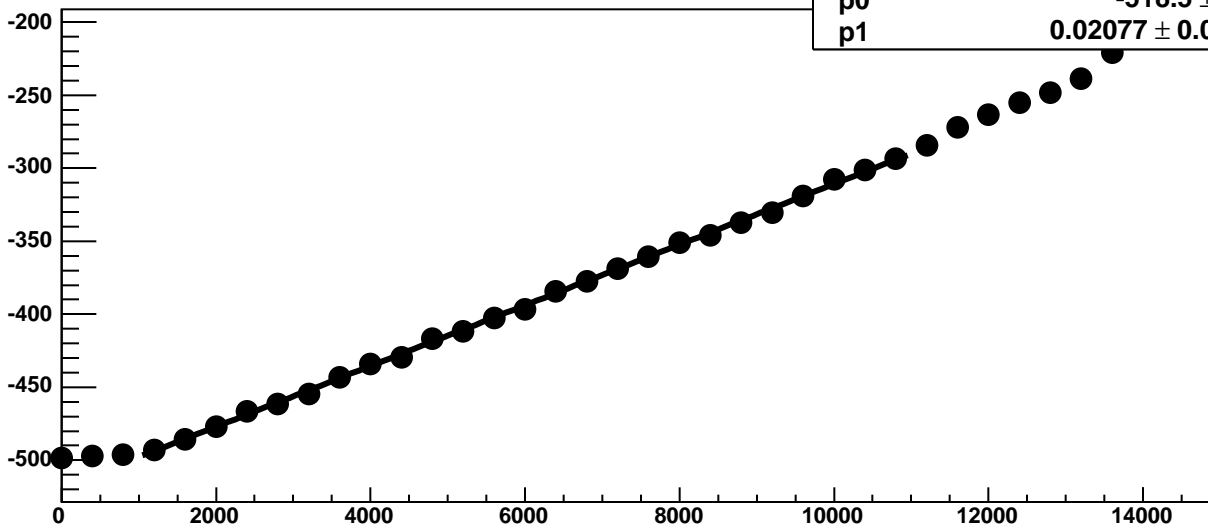
Chip 9, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

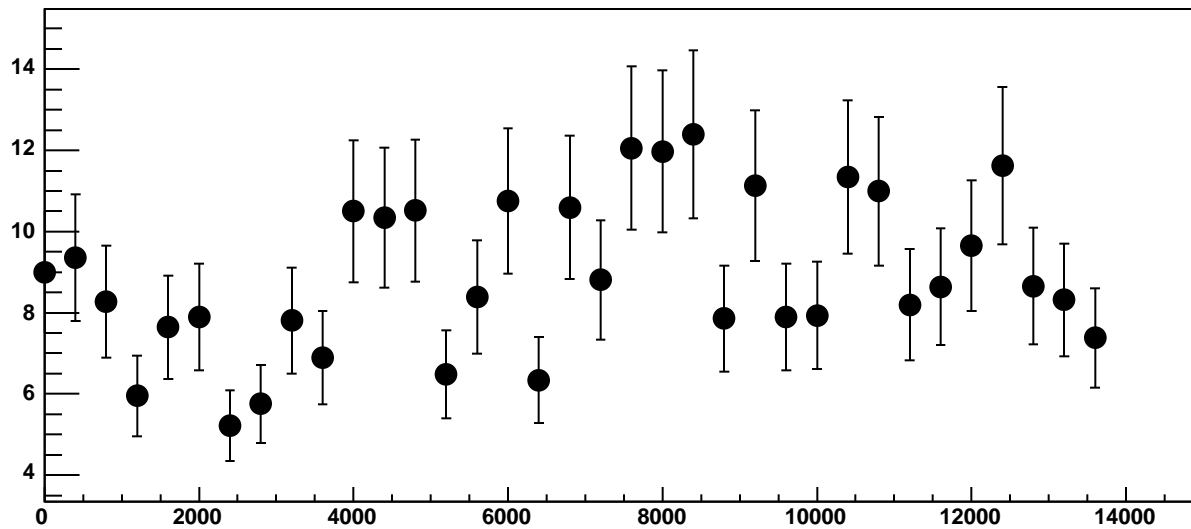


Chip 9, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

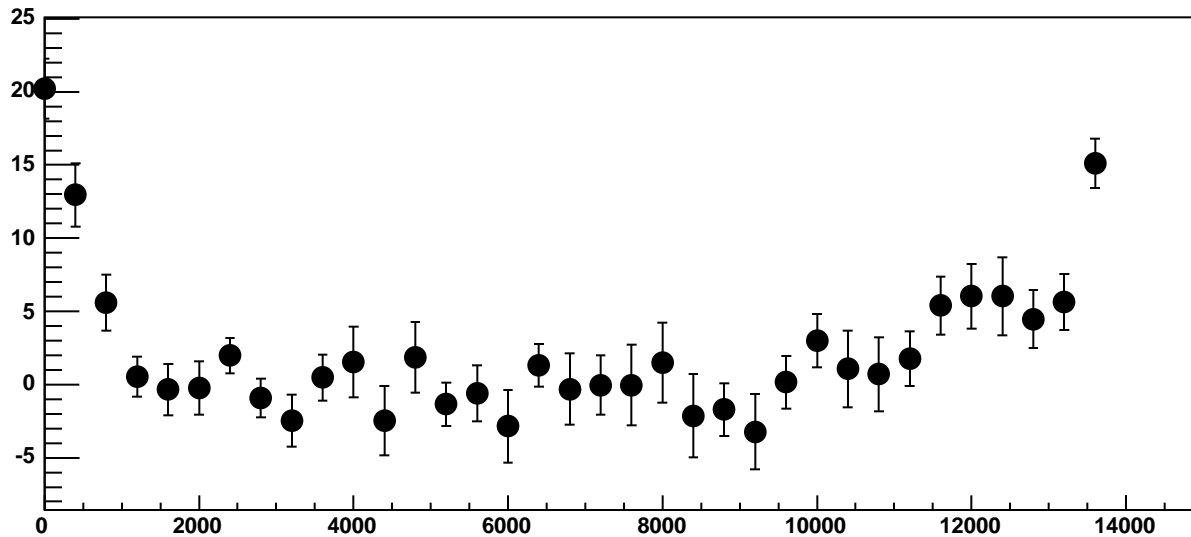


χ^2 / ndf 16.9 / 23
p0 -518.5 ± 0.7559
p1 0.02077 ± 0.0001282

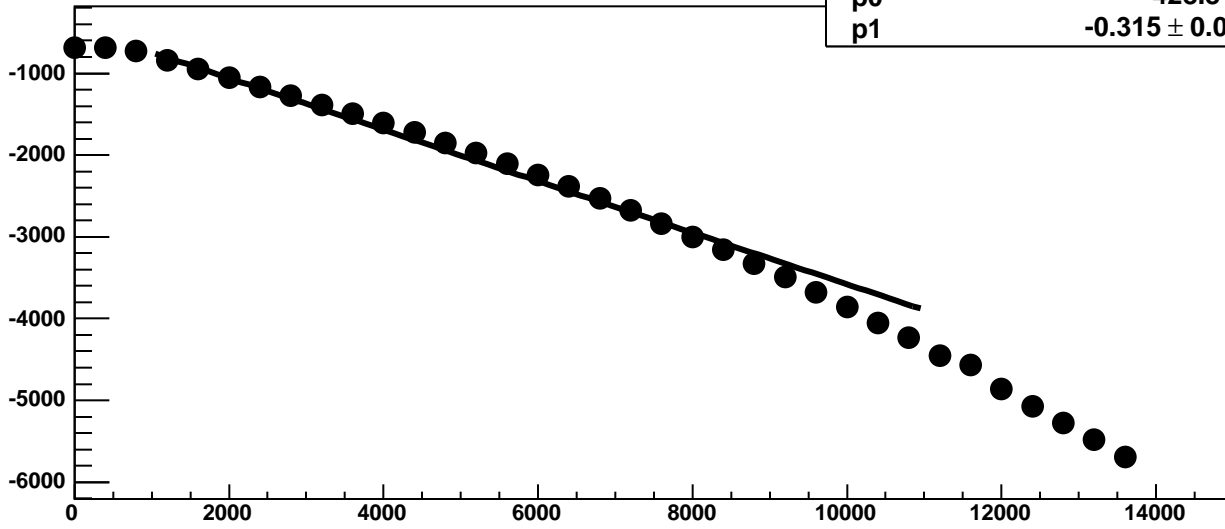
Chip 9, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 9, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

4059 / 23

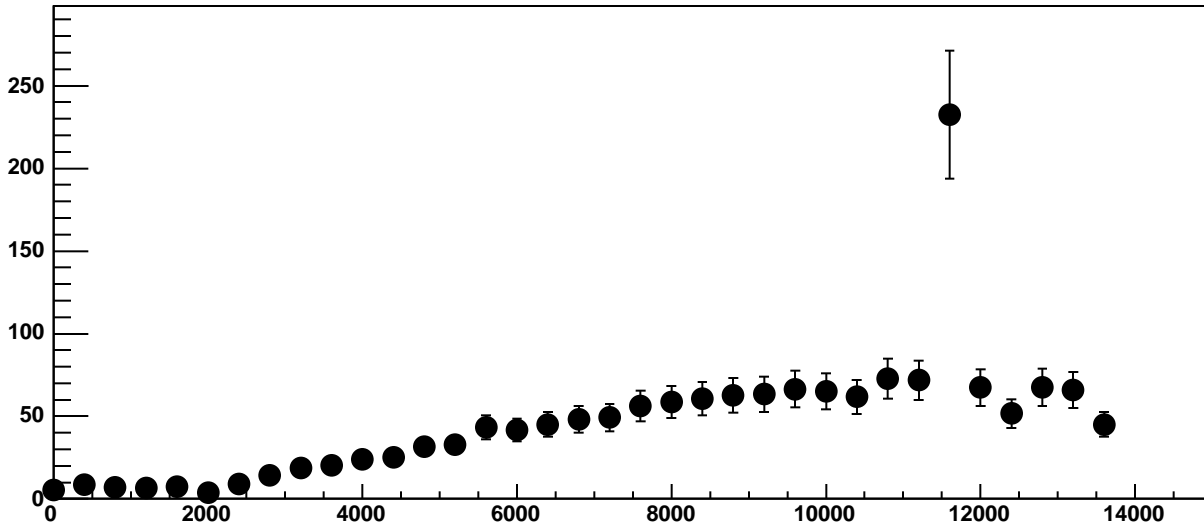
p0

-428.8 ± 1.234

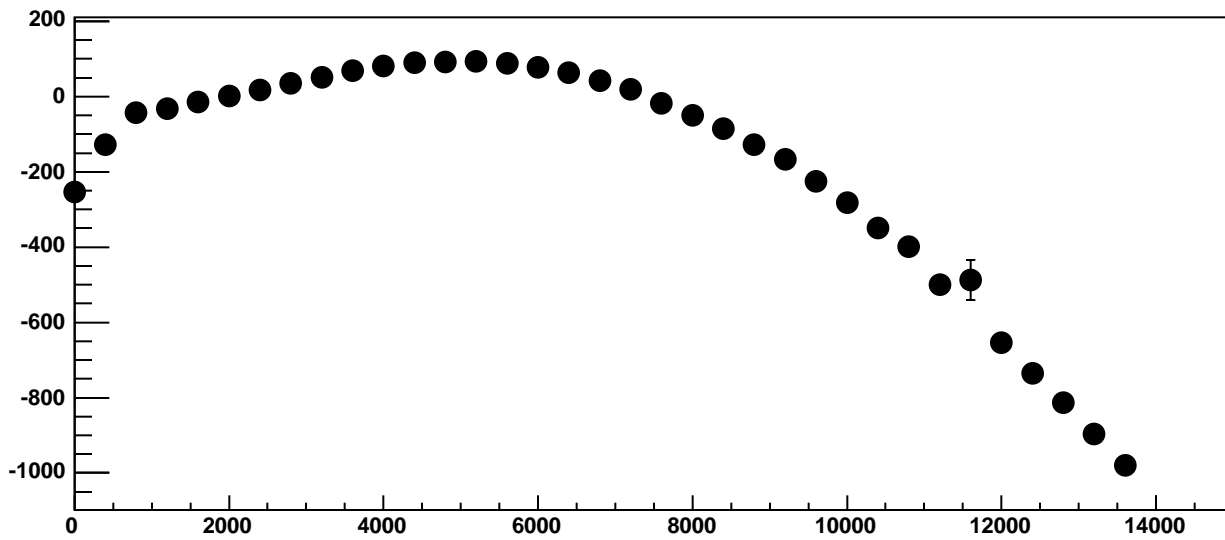
p1

-0.315 ± 0.0004839

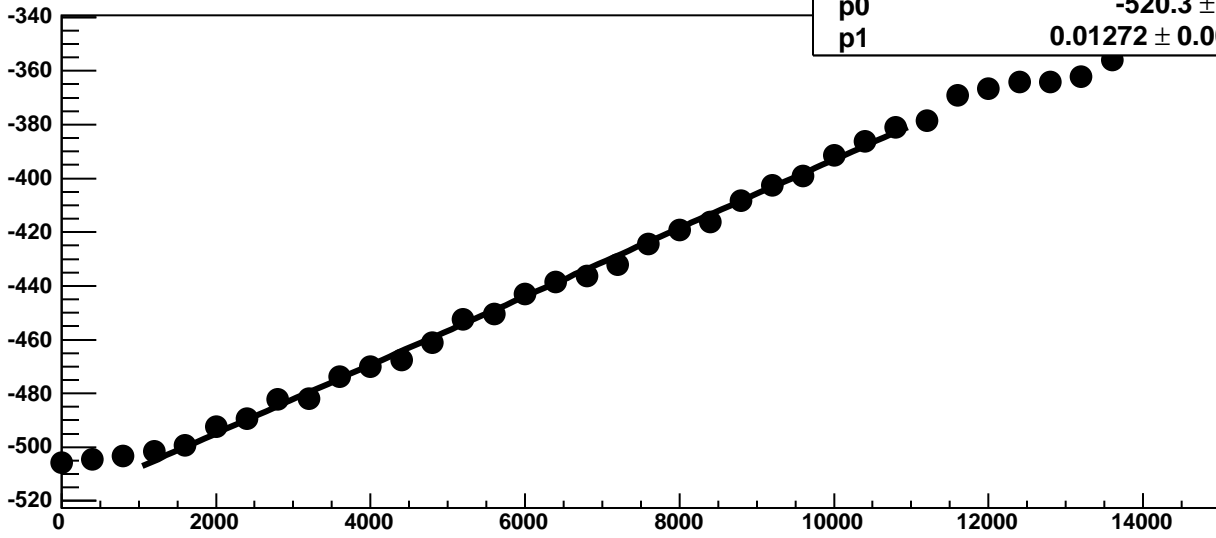
Chip 9, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

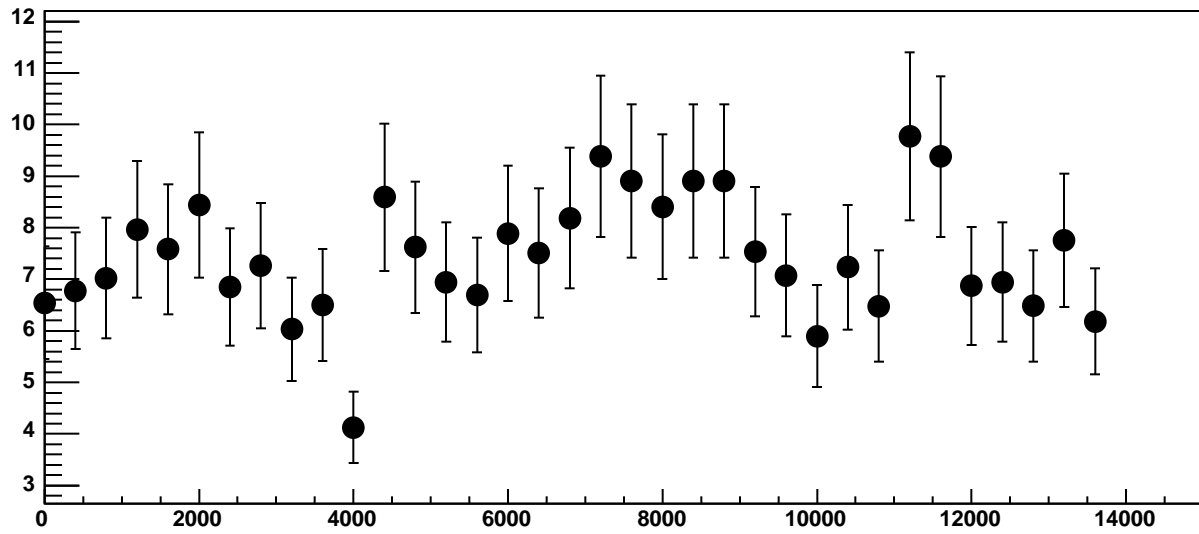


Chip 9, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

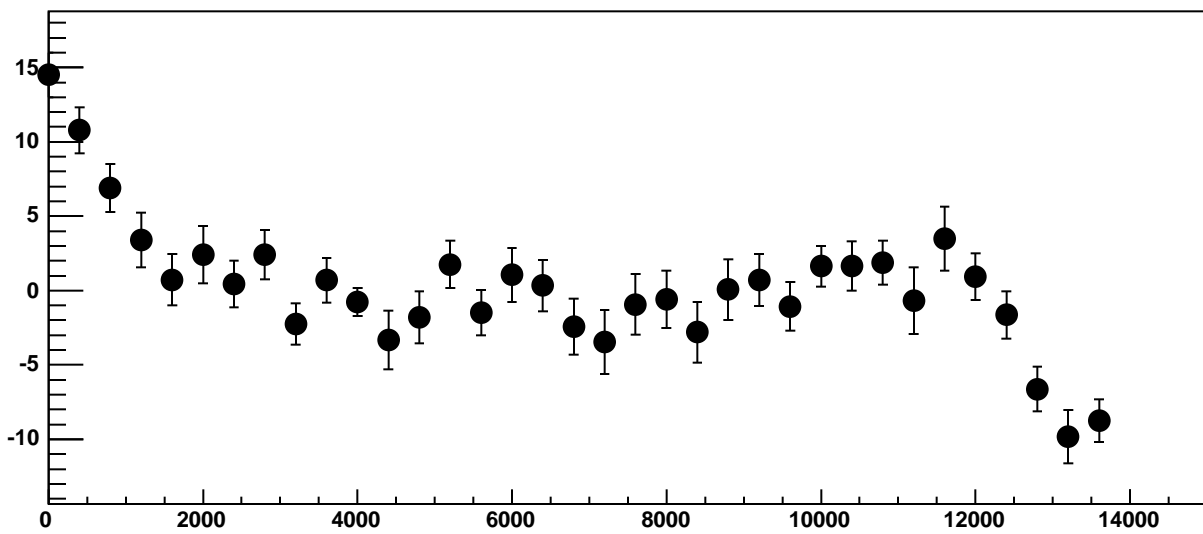


χ^2 / ndf 28.42 / 23
p0 -520.3 ± 0.7348
p1 0.01272 ± 0.0001126

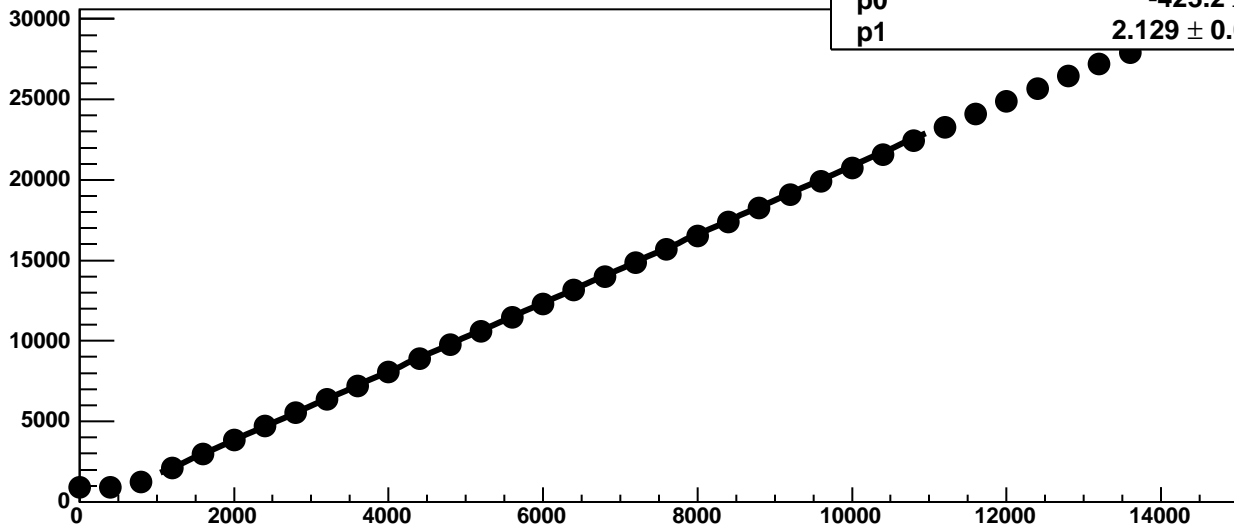
Chip 9, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



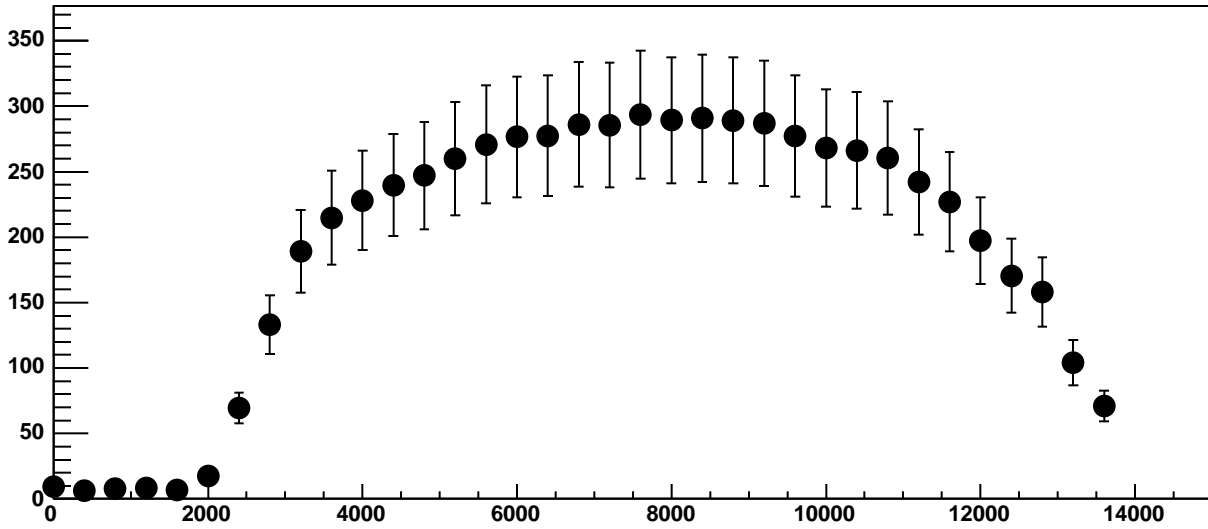
Chip 9, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



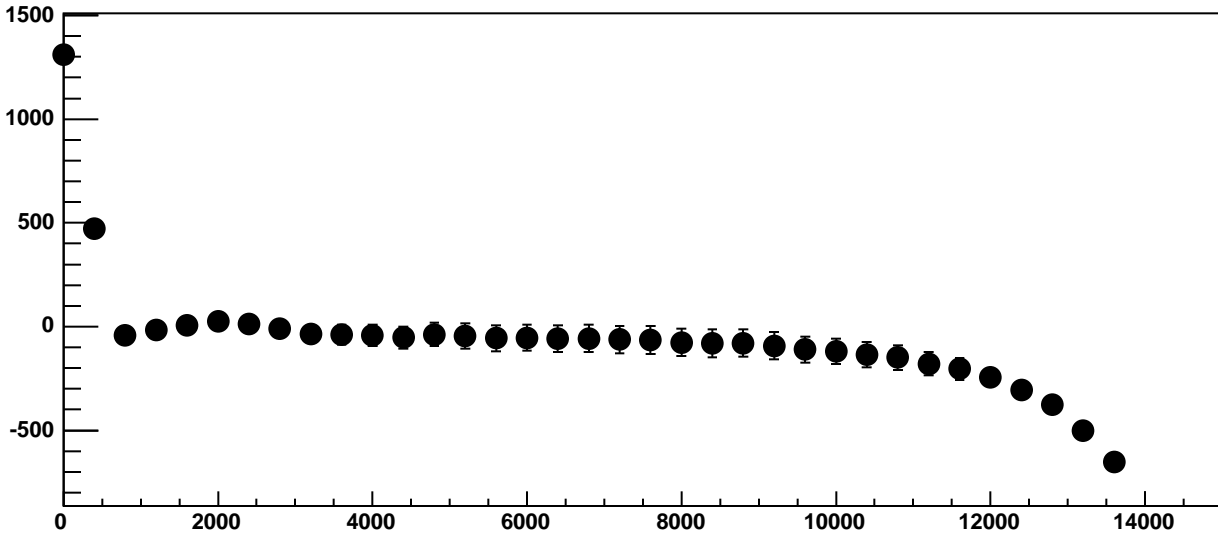
Chip 9, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC



Chip 9, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC

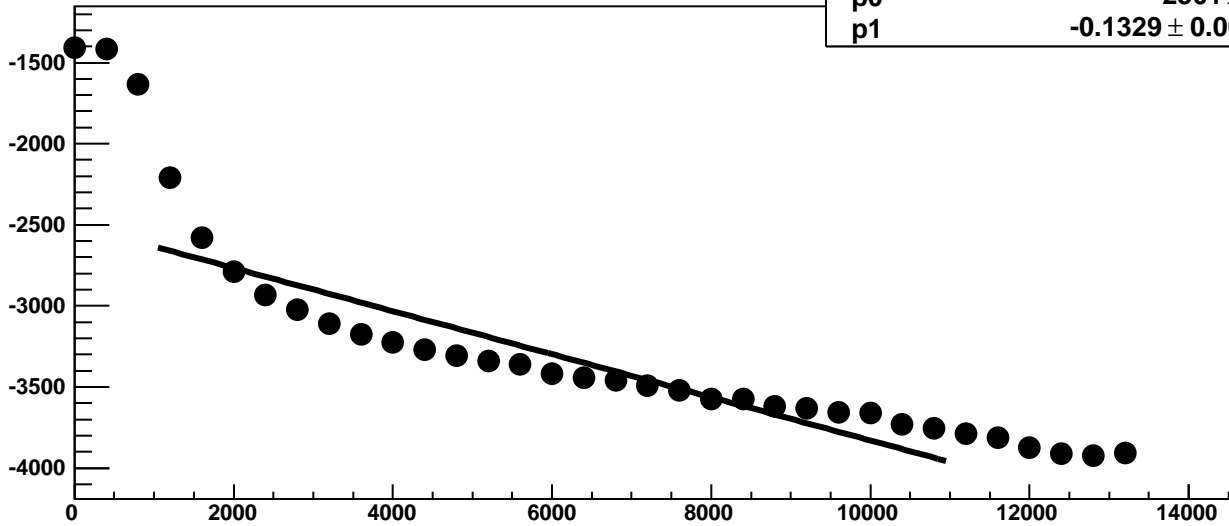


Chip 9, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC

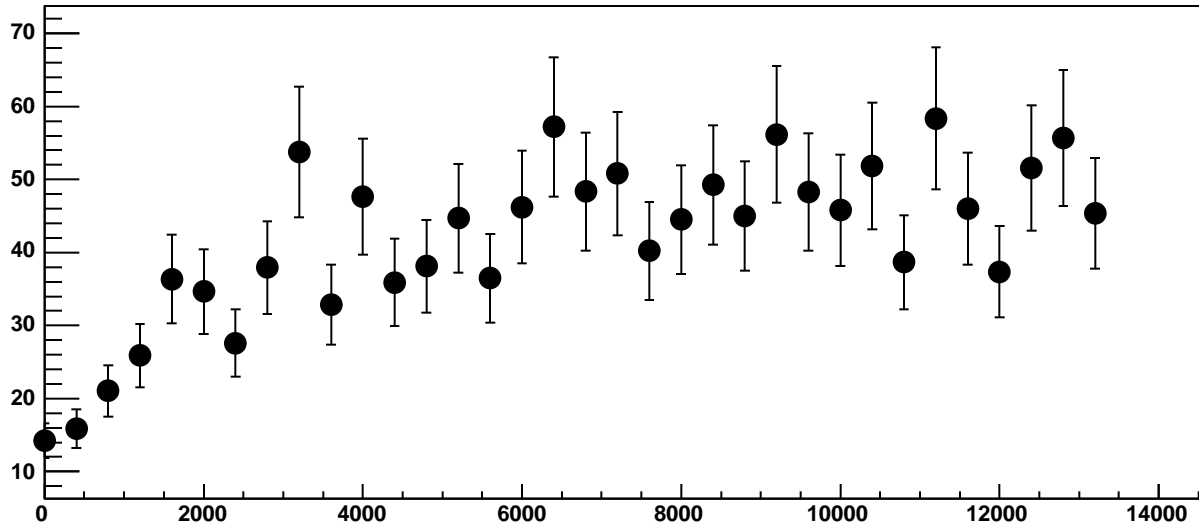


Chip 9, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

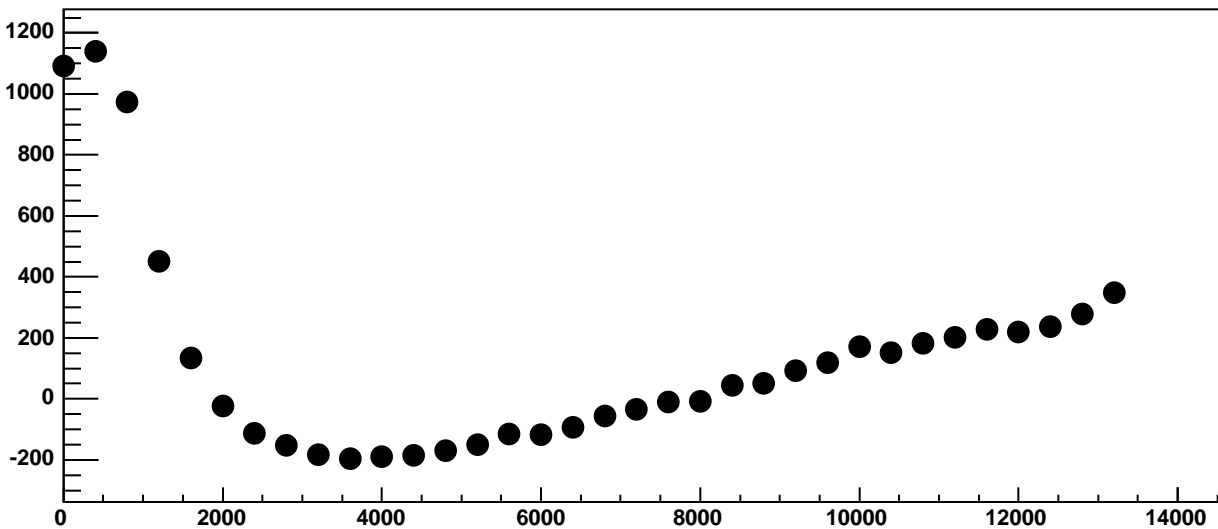
χ^2 / ndf 1.041e+04 / 23
p0 -2501 ± 3.677
p1 -0.1329 ± 0.0006164



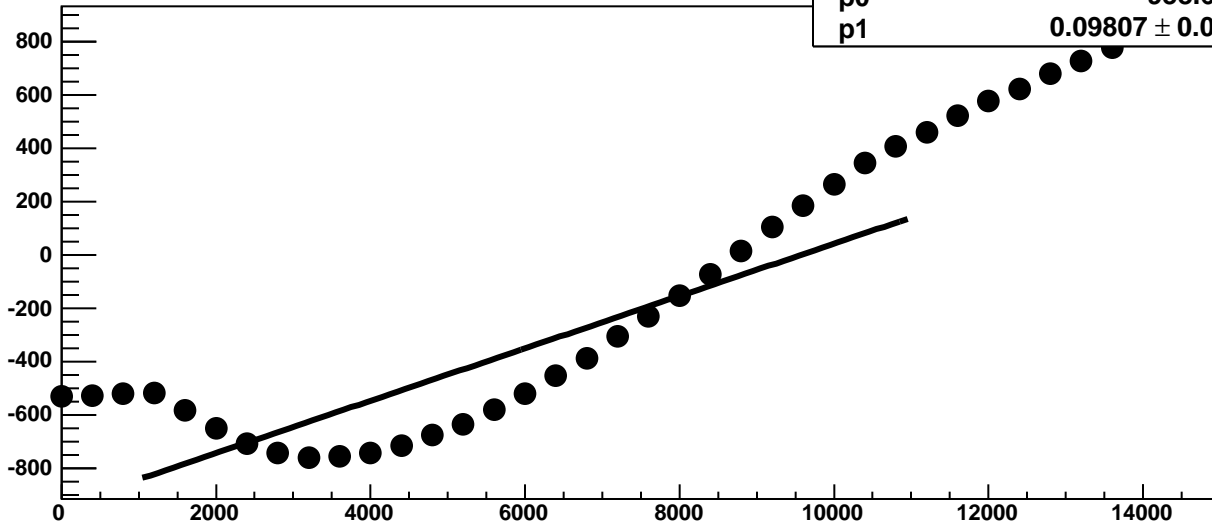
Chip 9, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

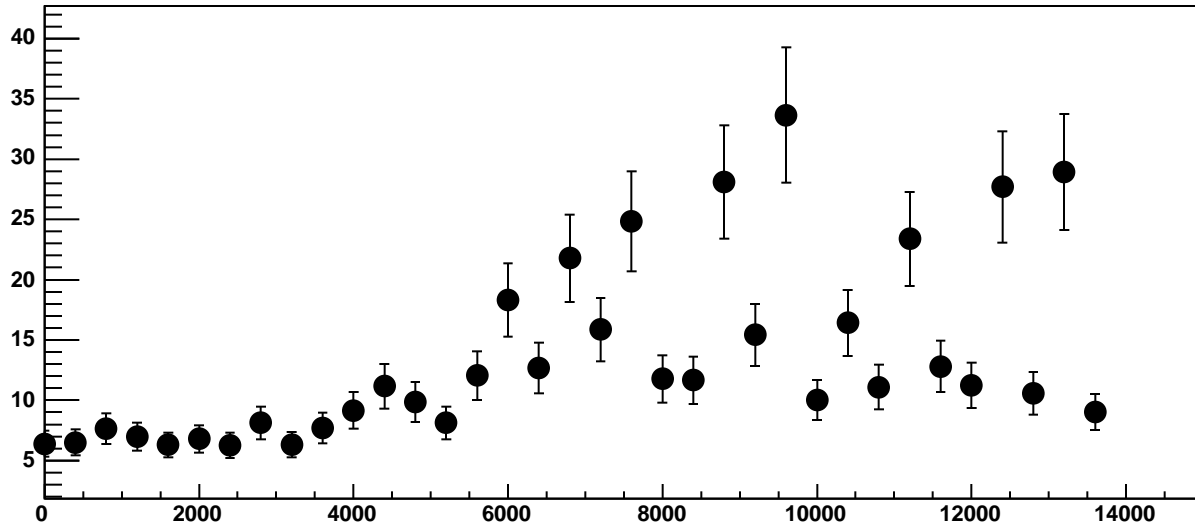


Chip 9, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

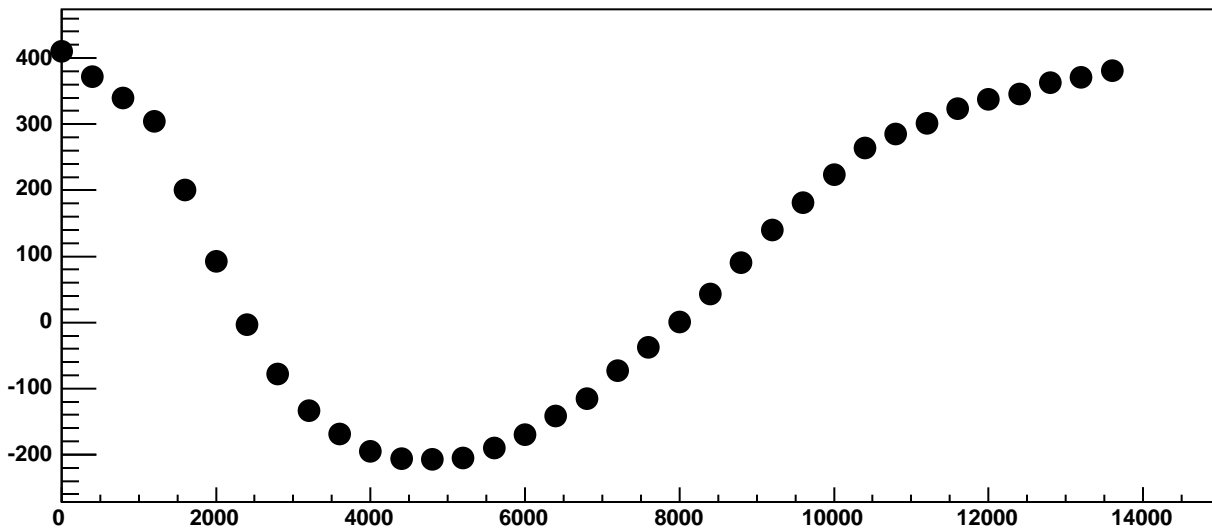


χ^2 / ndf 1.532e+05 / 23
p0 -938.6 ± 0.83
p1 0.09807 ± 0.0001635

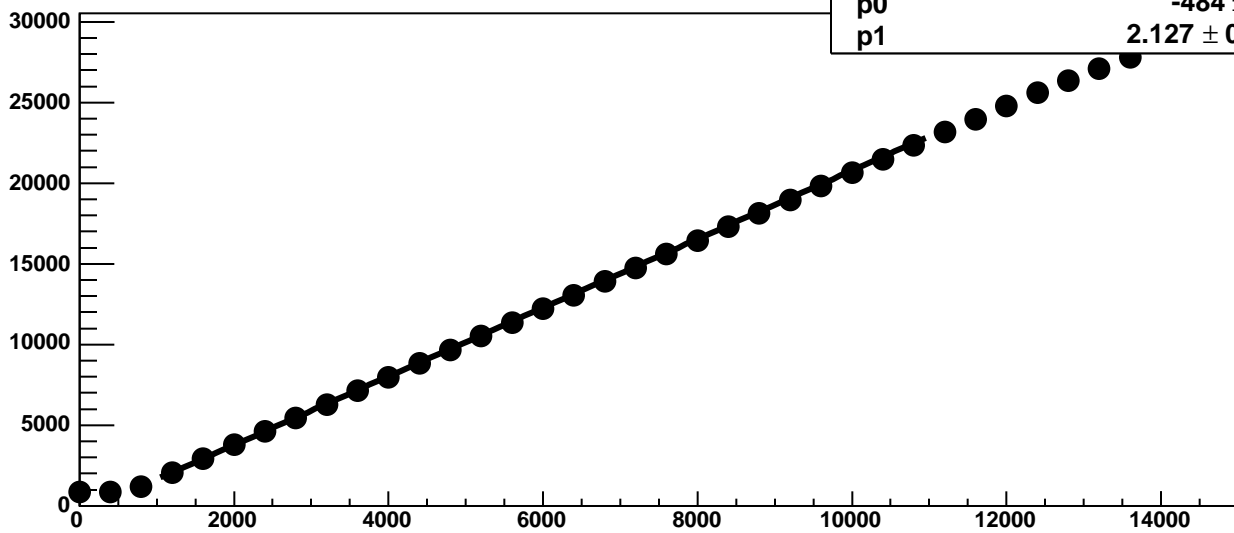
Chip 9, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

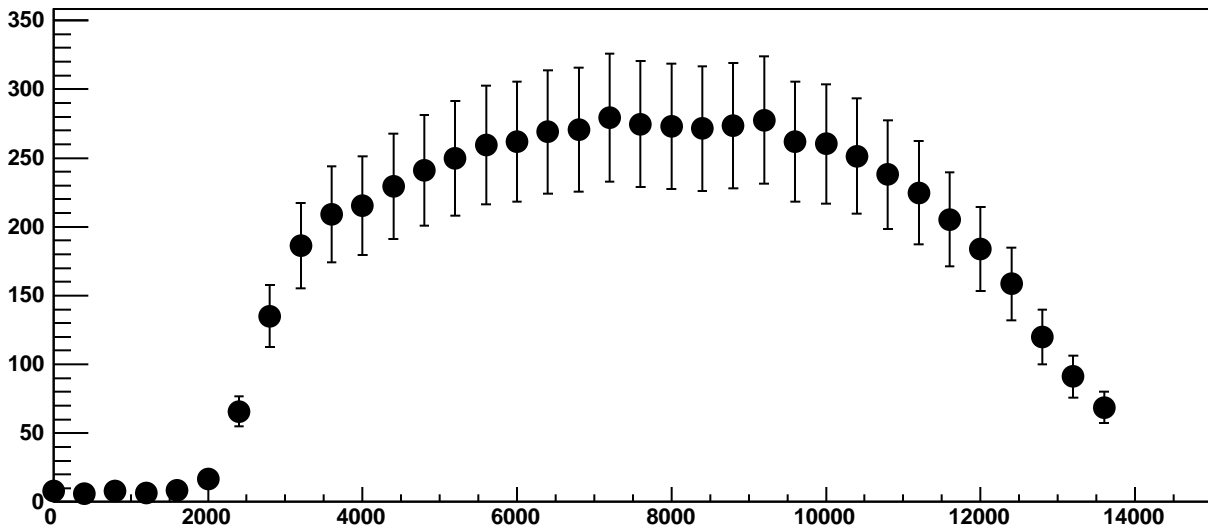


Chip 9, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC

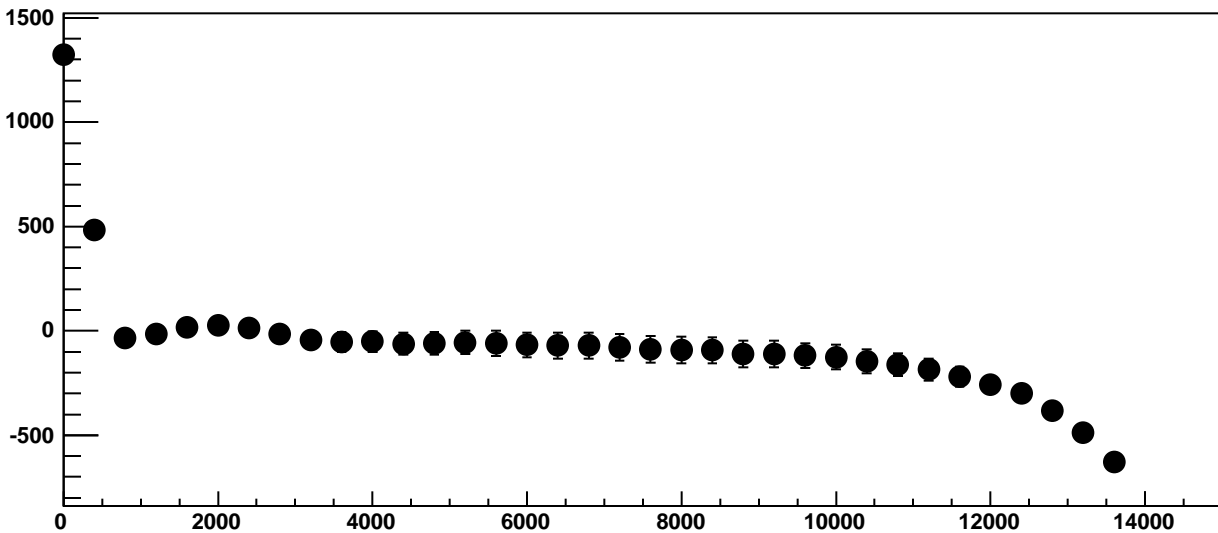


χ^2 / ndf 248.1 / 23
p0 -484 ± 3.066
p1 2.127 ± 0.00195

Chip 9, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC

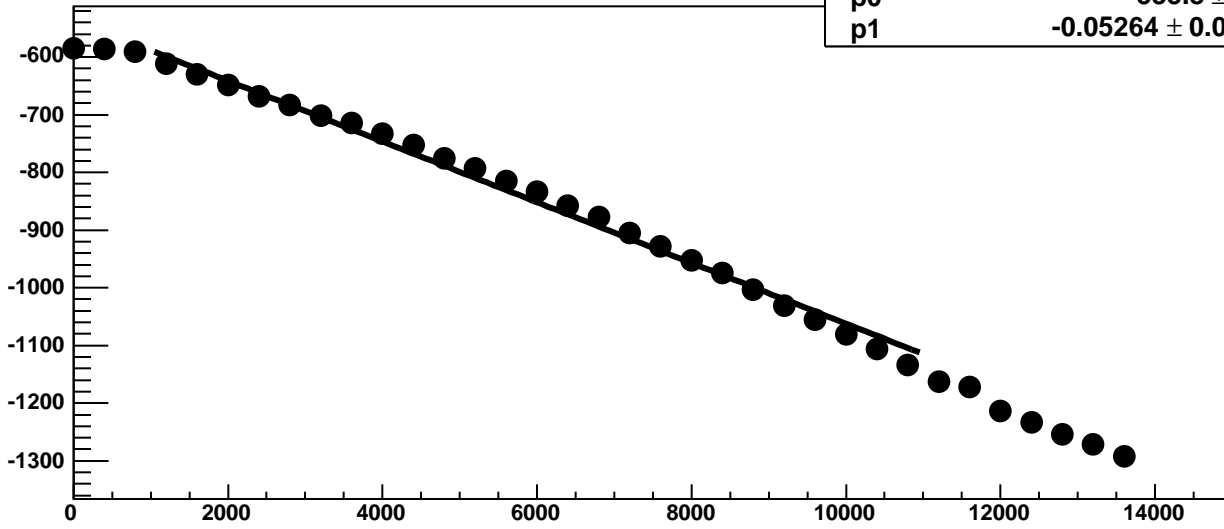


Chip 9, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC

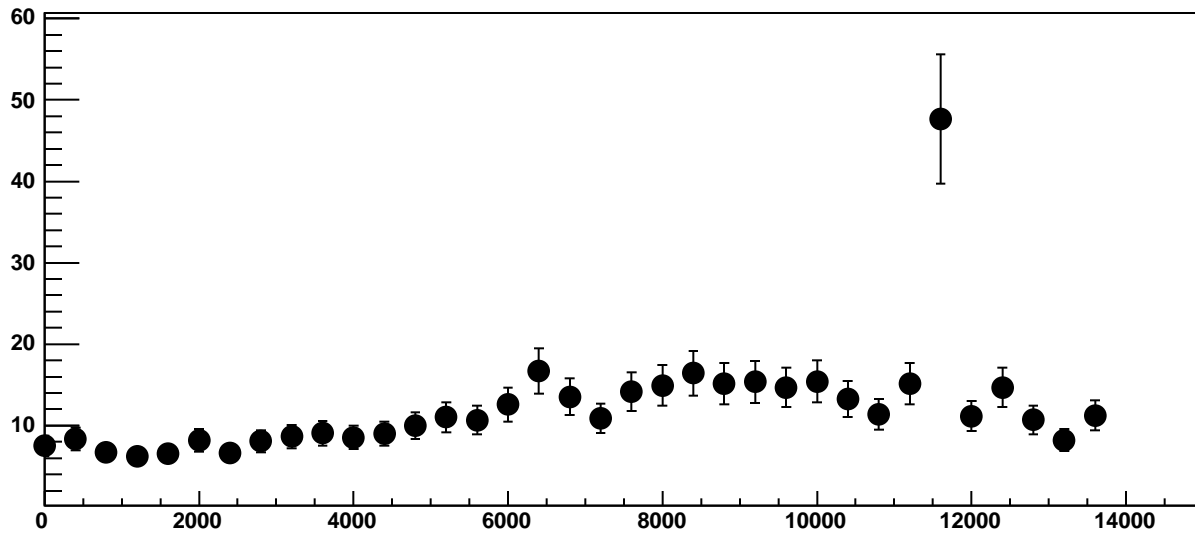


Chip 9, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

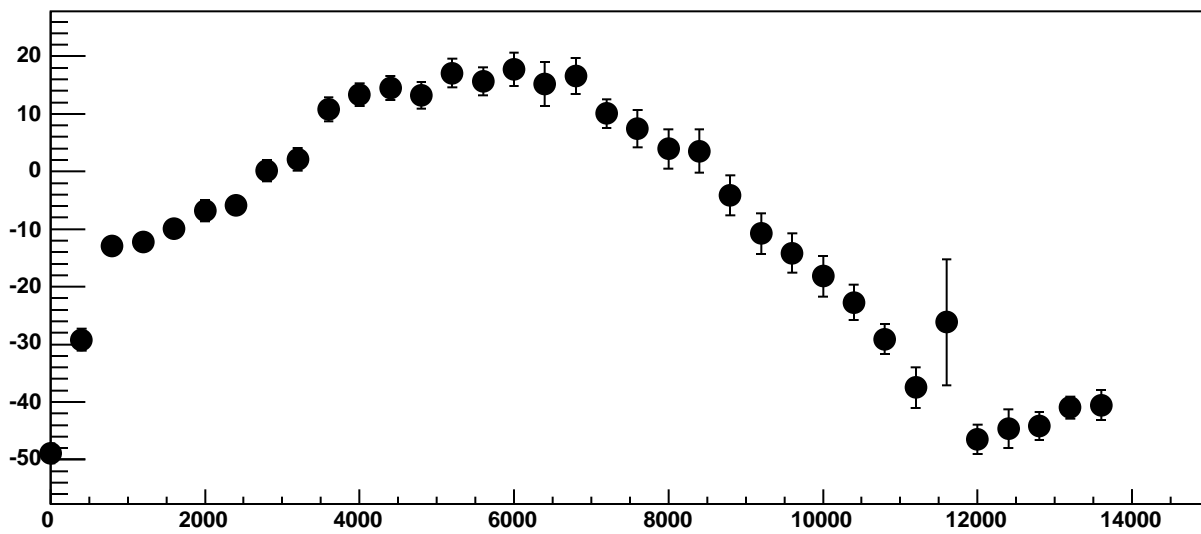
χ^2 / ndf 730.3 / 23
p0 -535.8 ± 0.8566
p1 -0.05264 ± 0.0001634



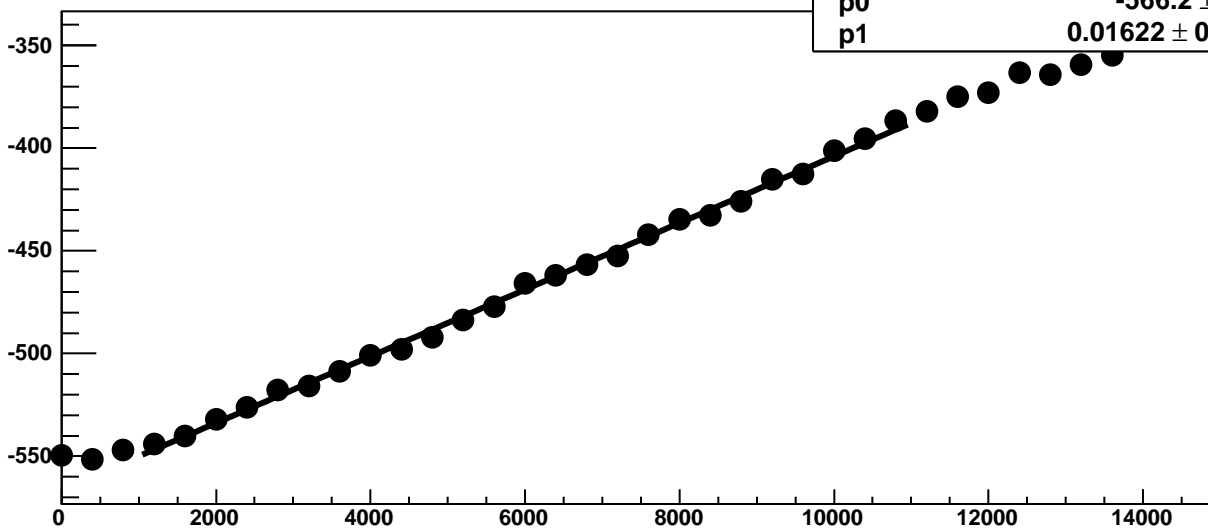
Chip 9, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

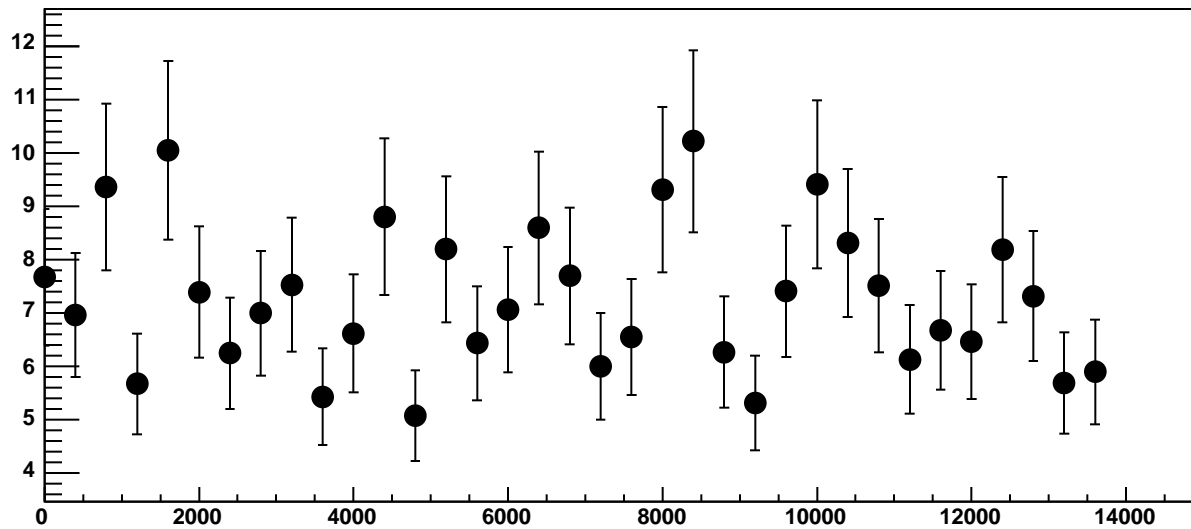


Chip 9, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

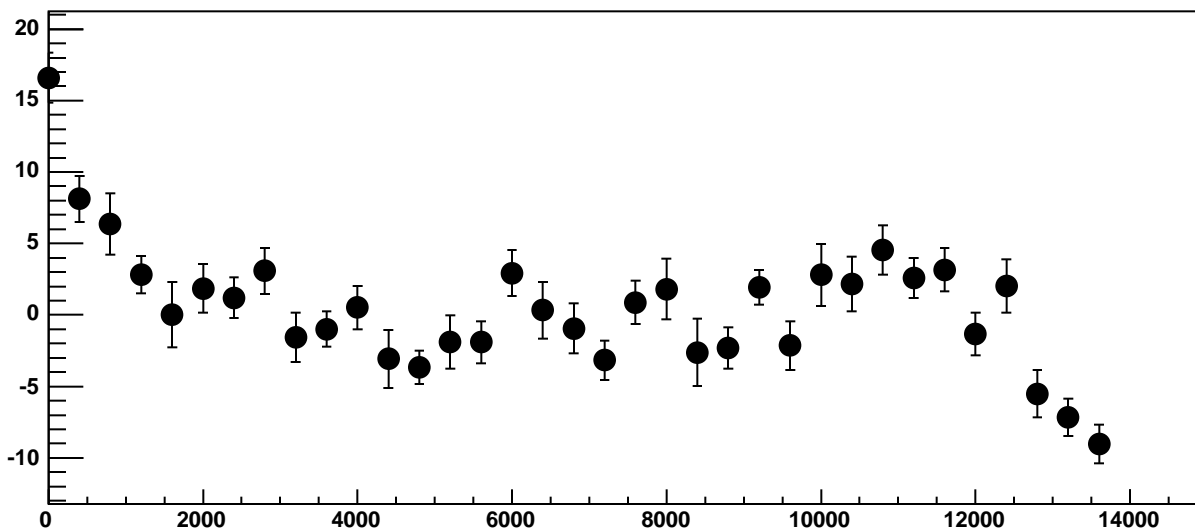


χ^2 / ndf 54.52 / 23
p0 -566.2 ± 0.7278
p1 0.01622 ± 0.000113

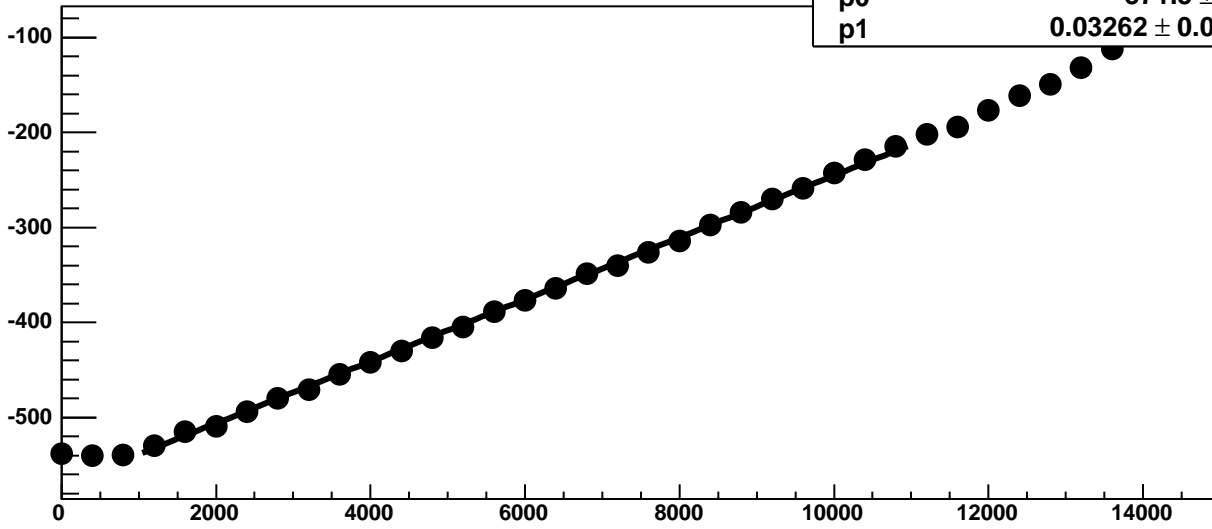
Chip 9, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

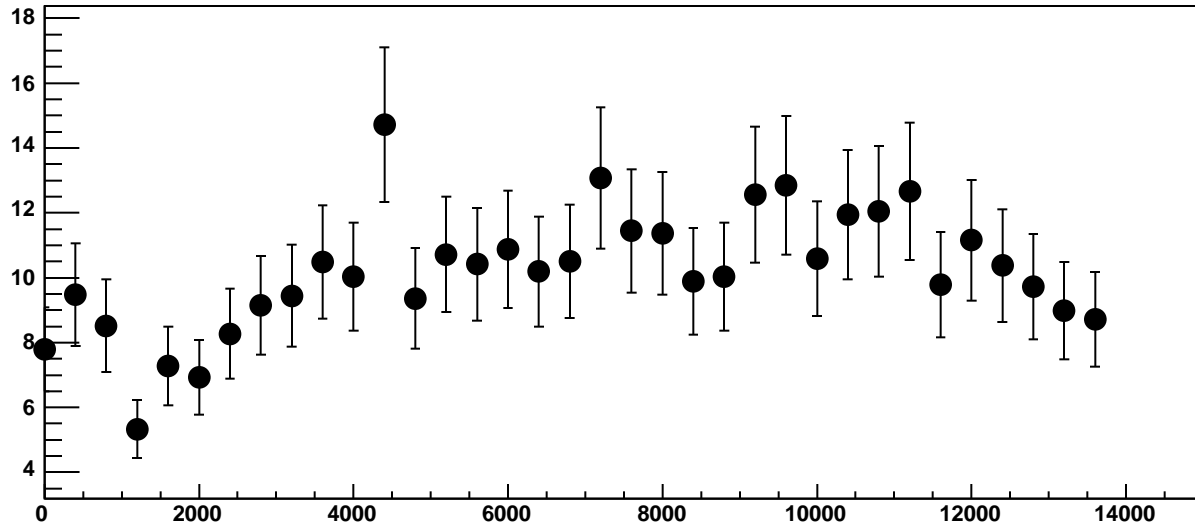


Chip 9, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC

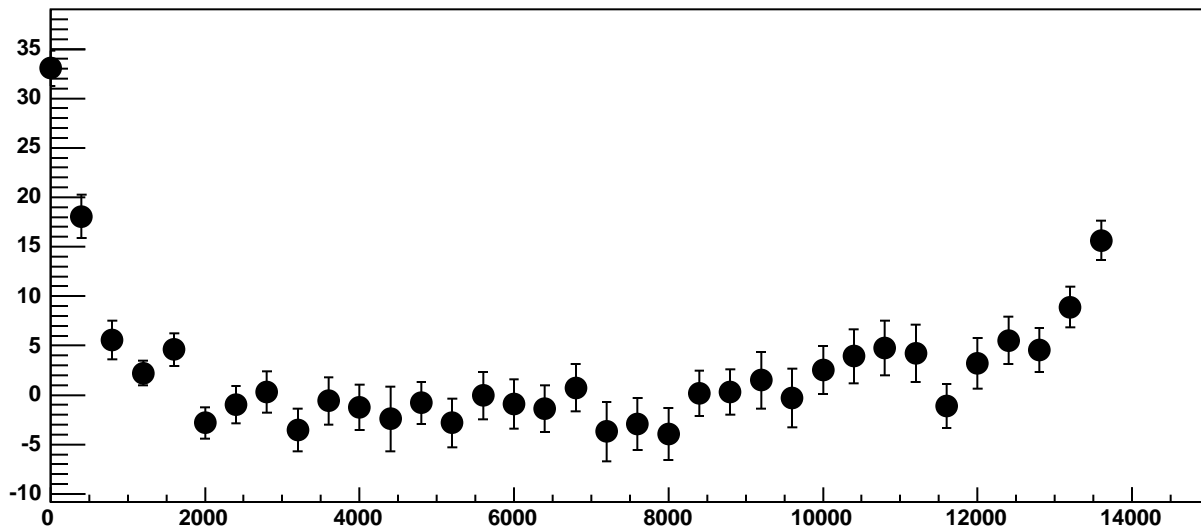


χ^2 / ndf 31.36 / 23
p0 -571.5 ± 0.8387
p1 0.03262 ± 0.0001449

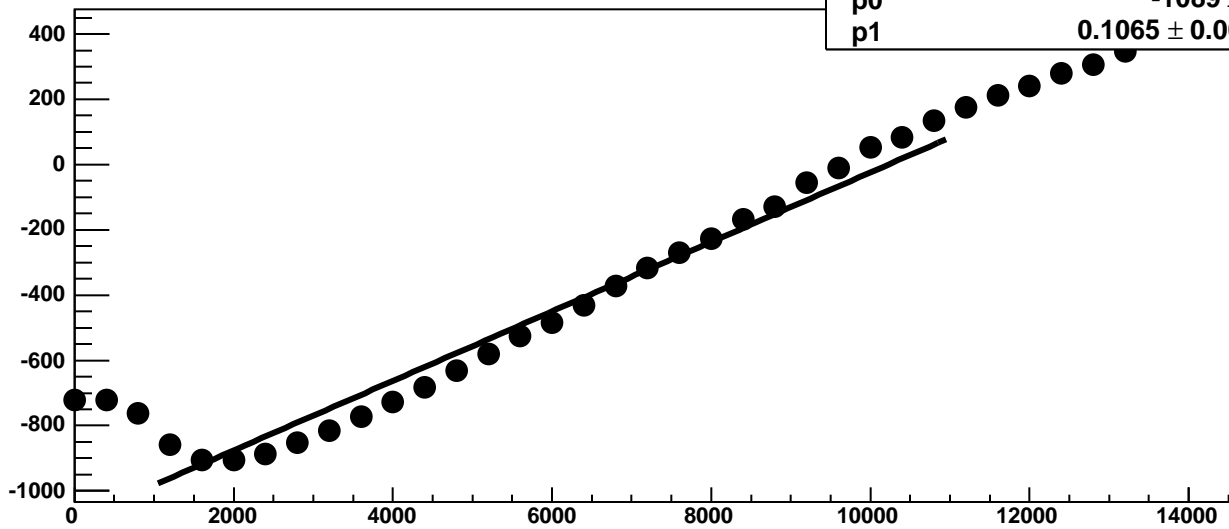
Chip 9, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



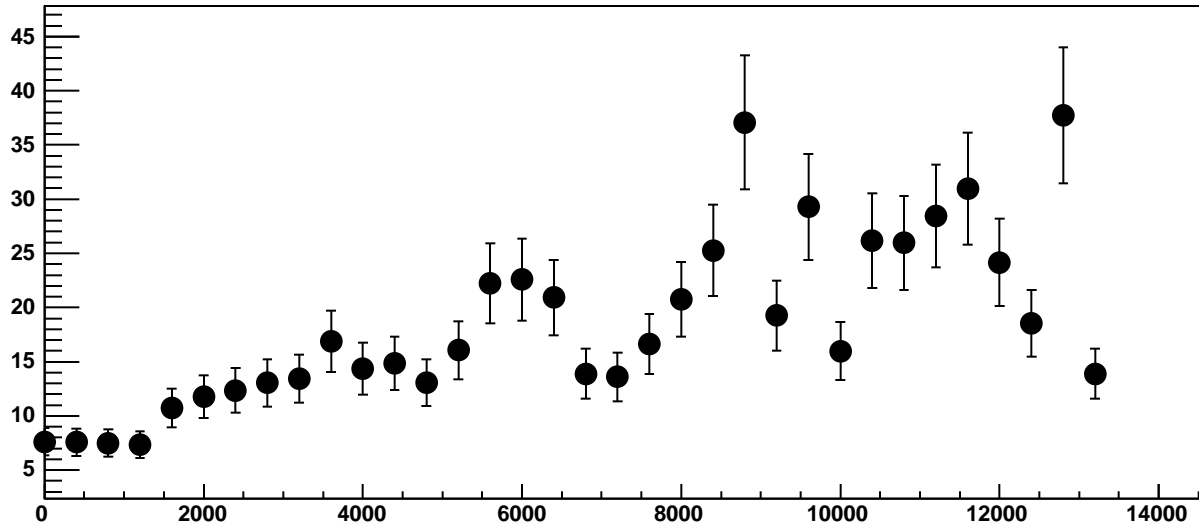
Chip 9, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC



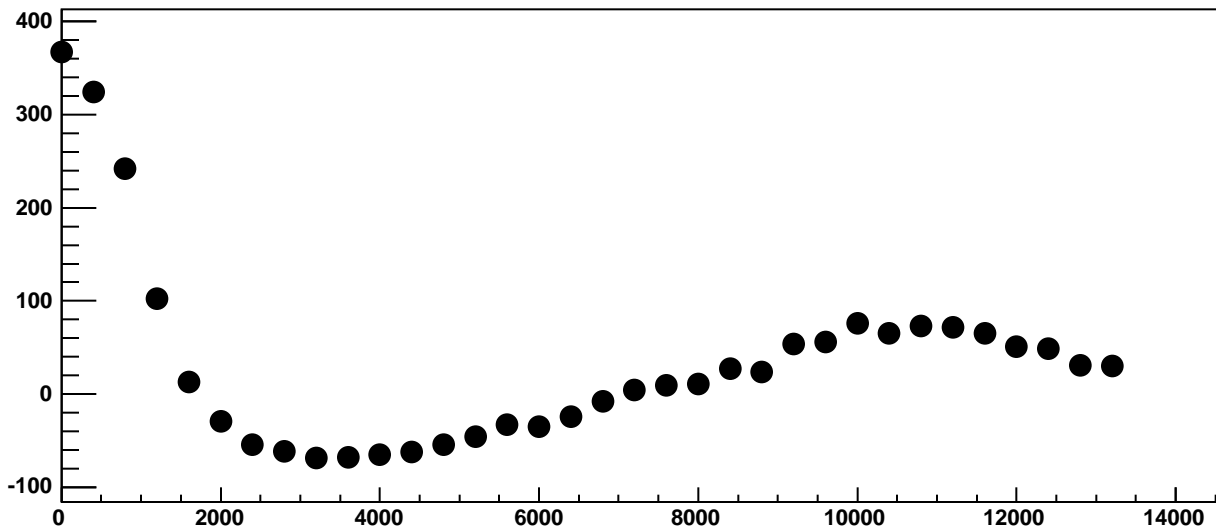
Chip 9, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



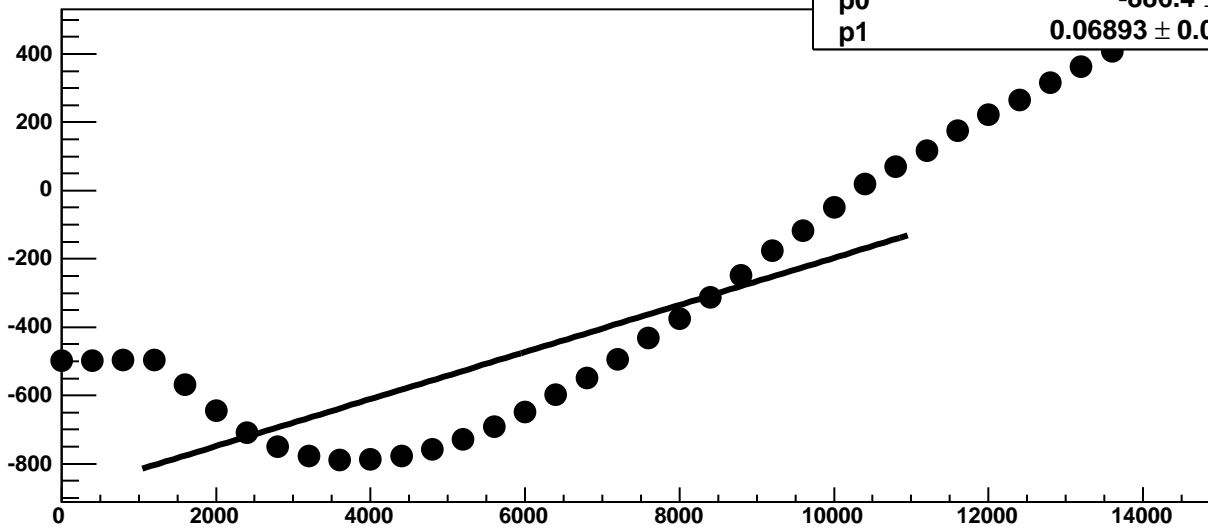
Chip 9, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

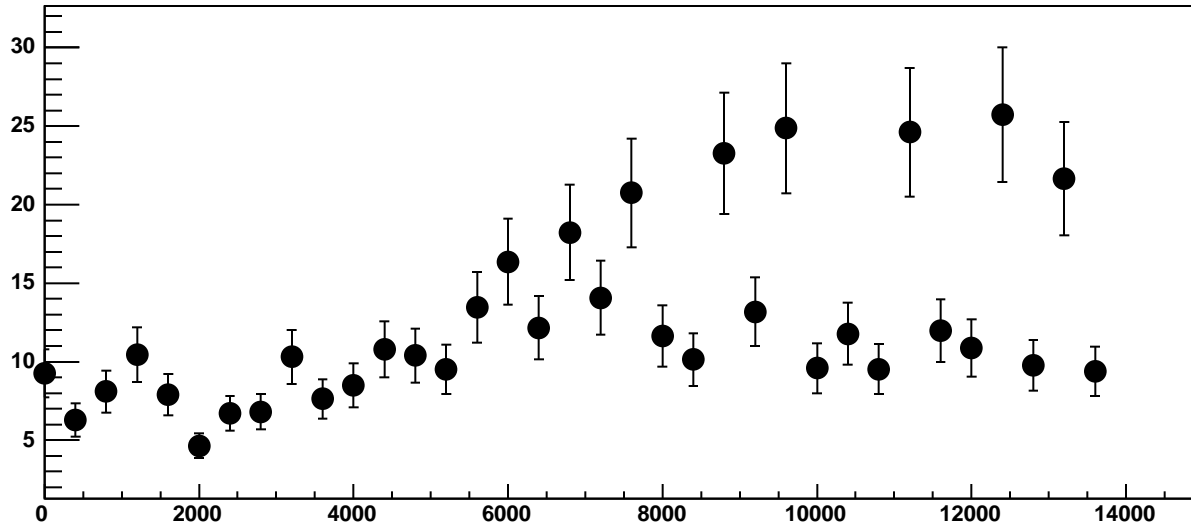


Chip 9, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

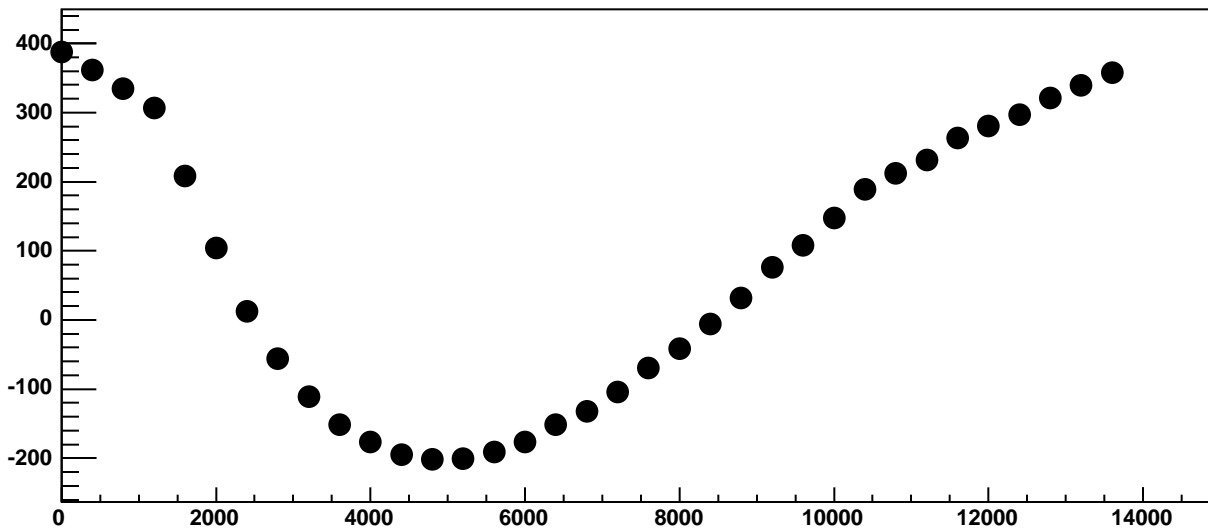


χ^2 / ndf 1.115e+05 / 23
p0 -886.4 ± 0.8281
p1 0.06893 ± 0.0001515

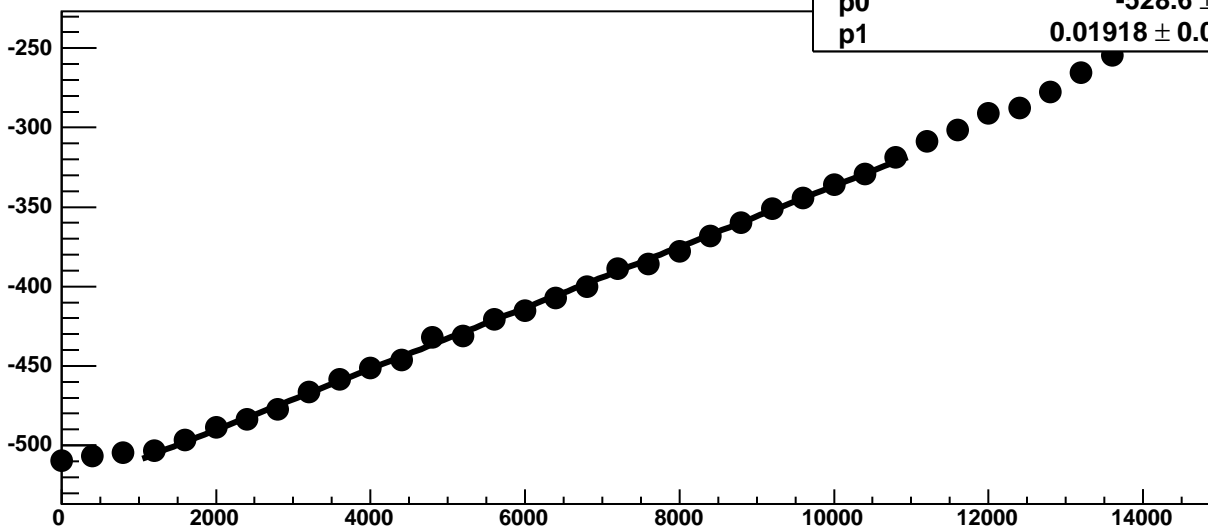
Chip 9, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

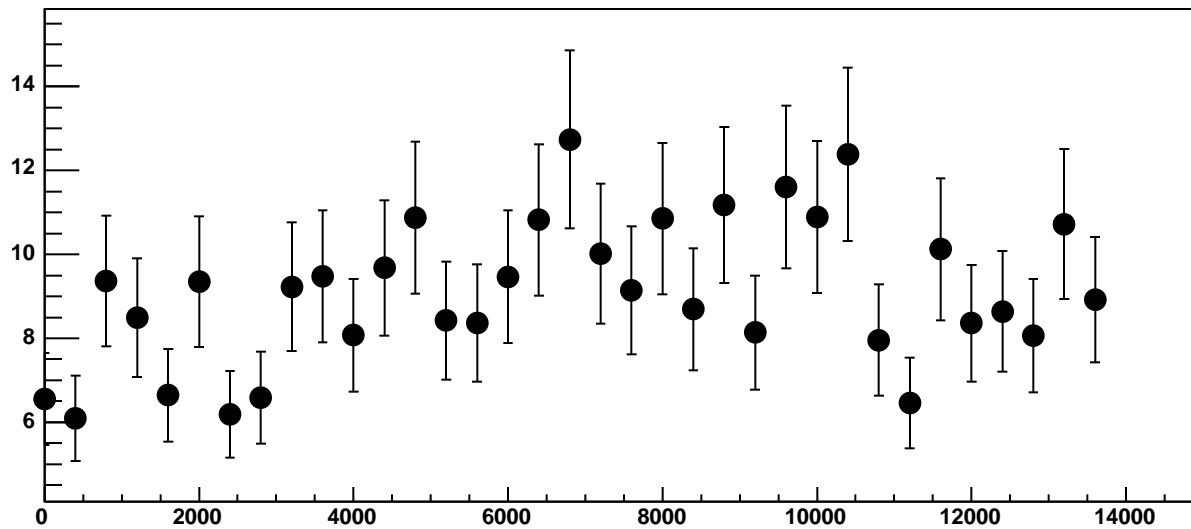


Chip 9, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC

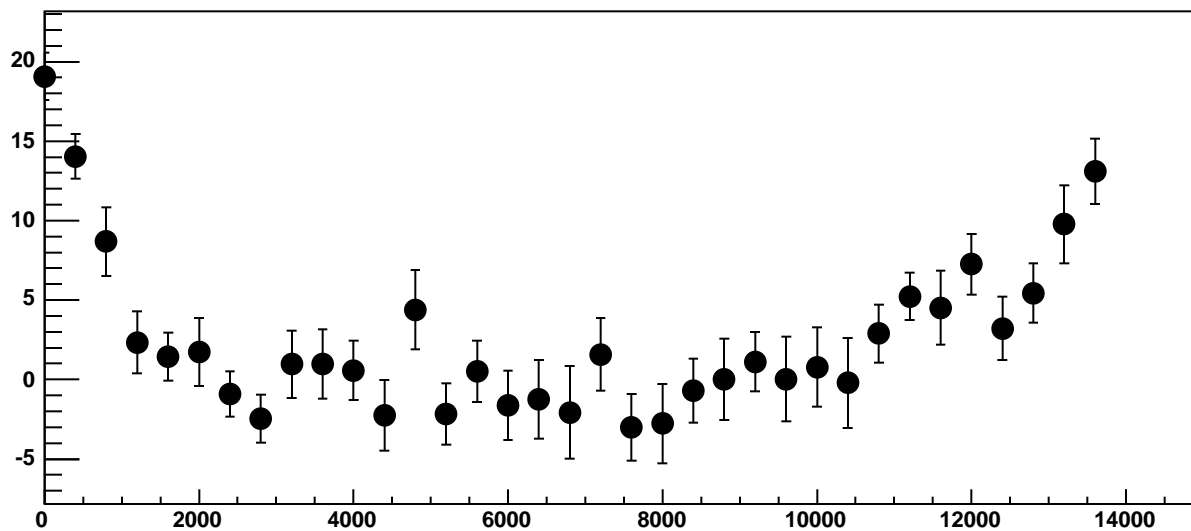


χ^2 / ndf 20.16 / 23
p0 -528.6 ± 0.8493
p1 0.01918 ± 0.0001379

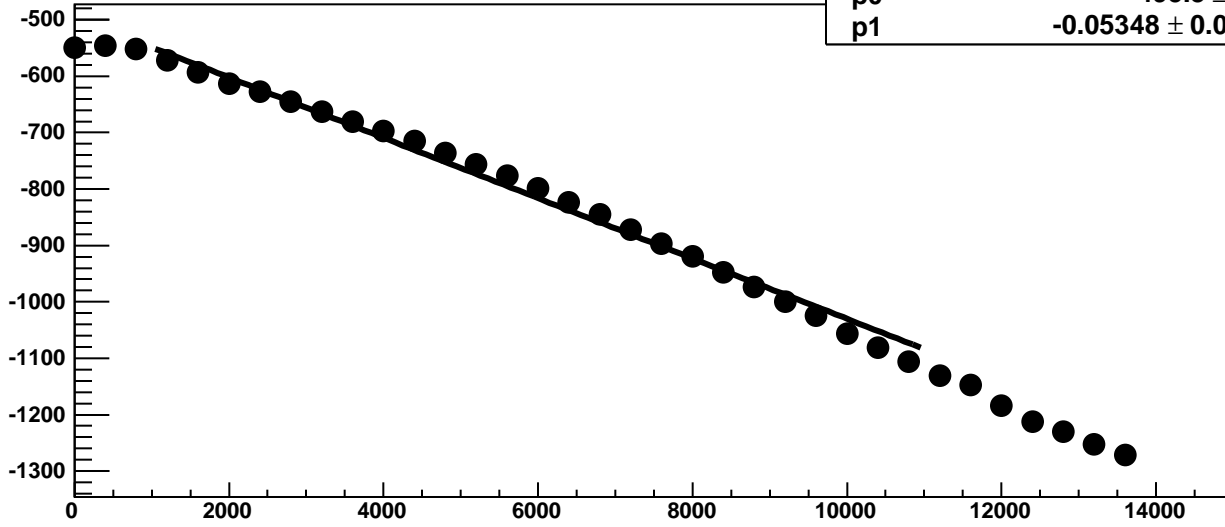
Chip 9, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC

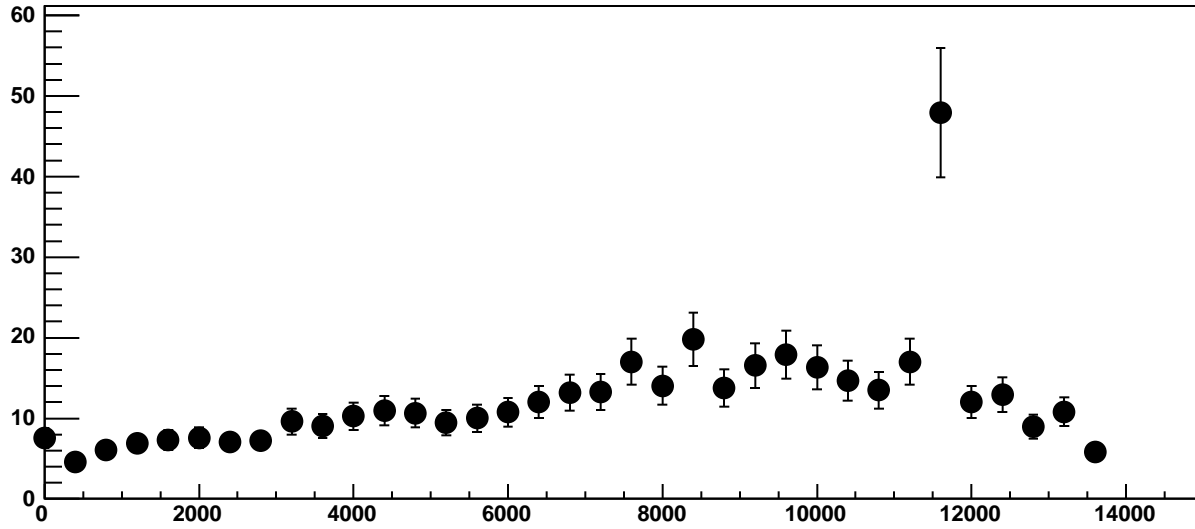


Chip 9, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

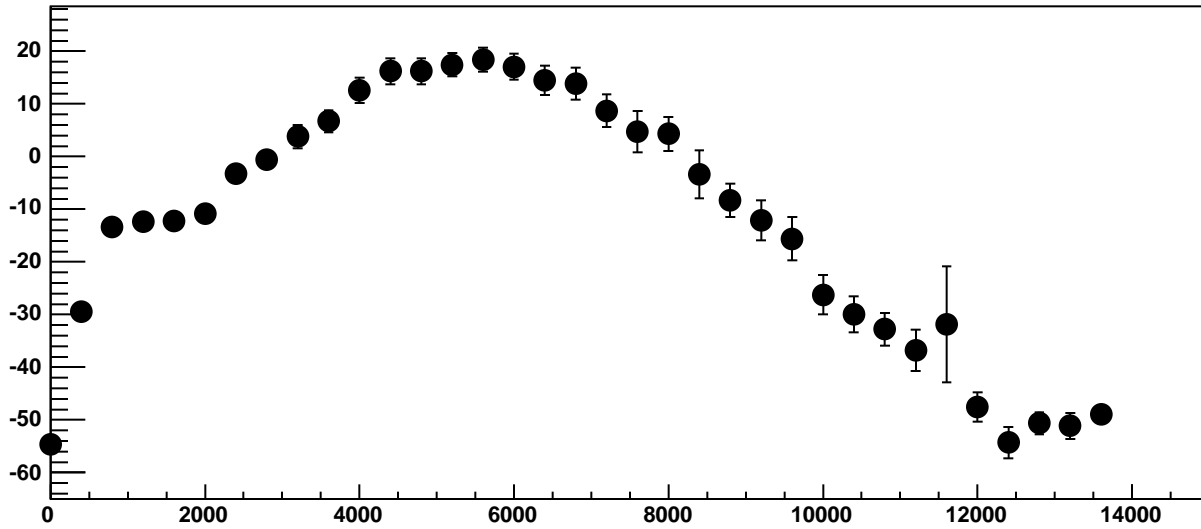


χ^2 / ndf	792.9 / 23
p0	-495.3 ± 0.9154
p1	-0.05348 ± 0.0001766

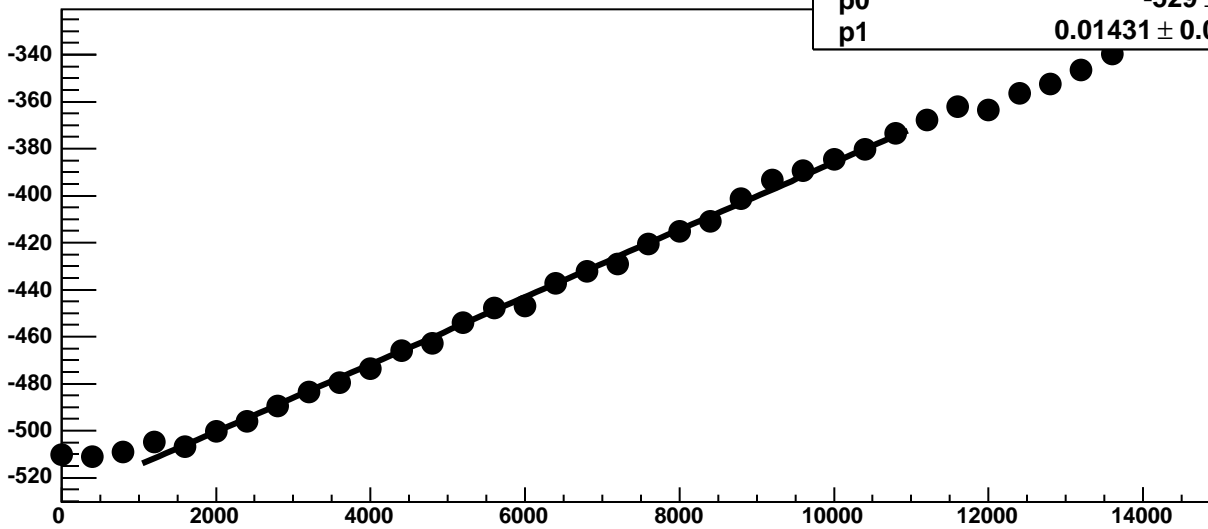
Chip 9, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

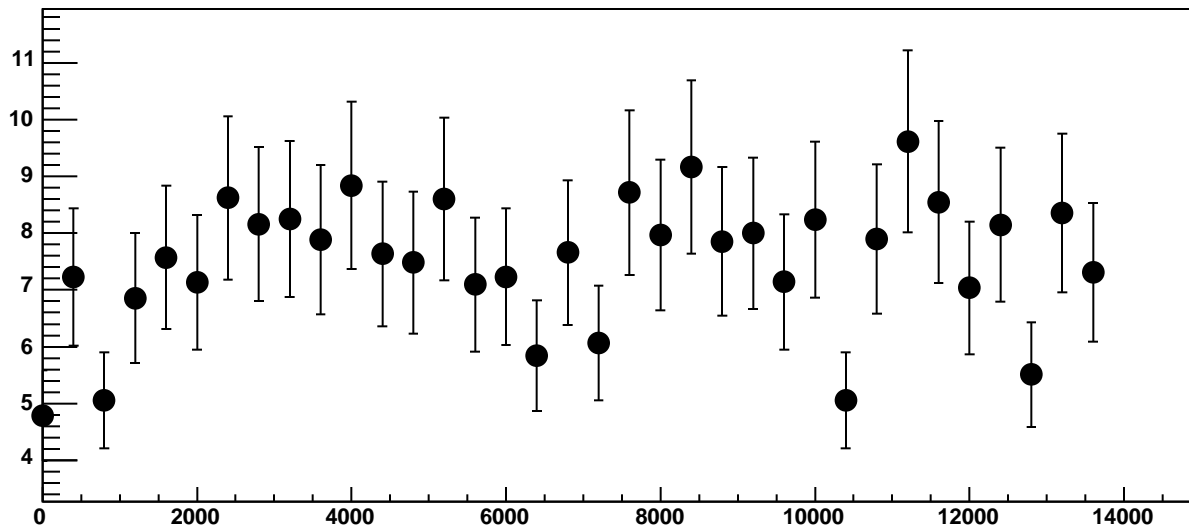


Chip 9, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

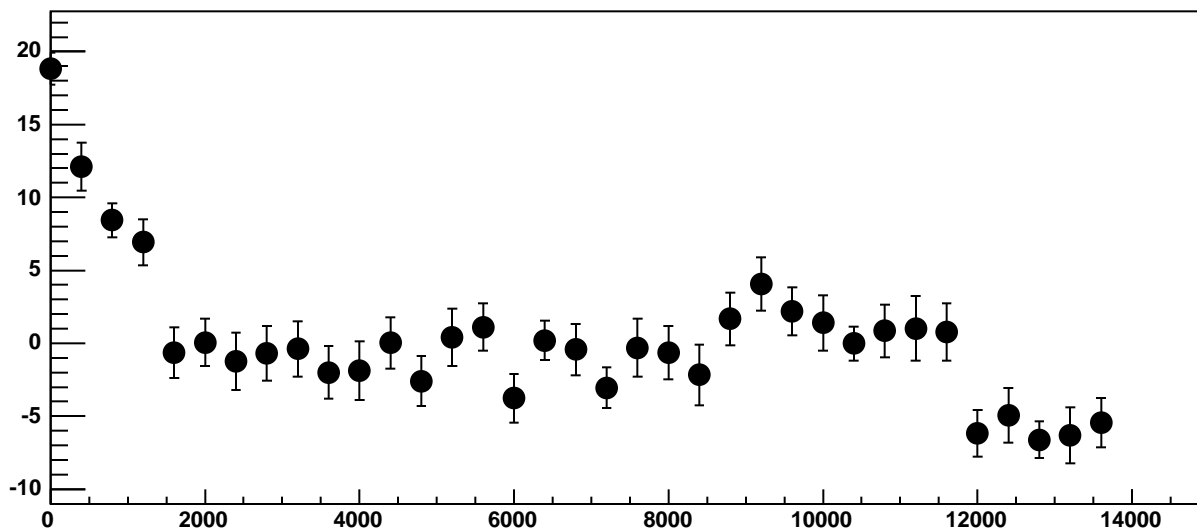


χ^2 / ndf 44.59 / 23
p0 -529 ± 0.7941
p1 0.01431 ± 0.0001159

Chip 9, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC

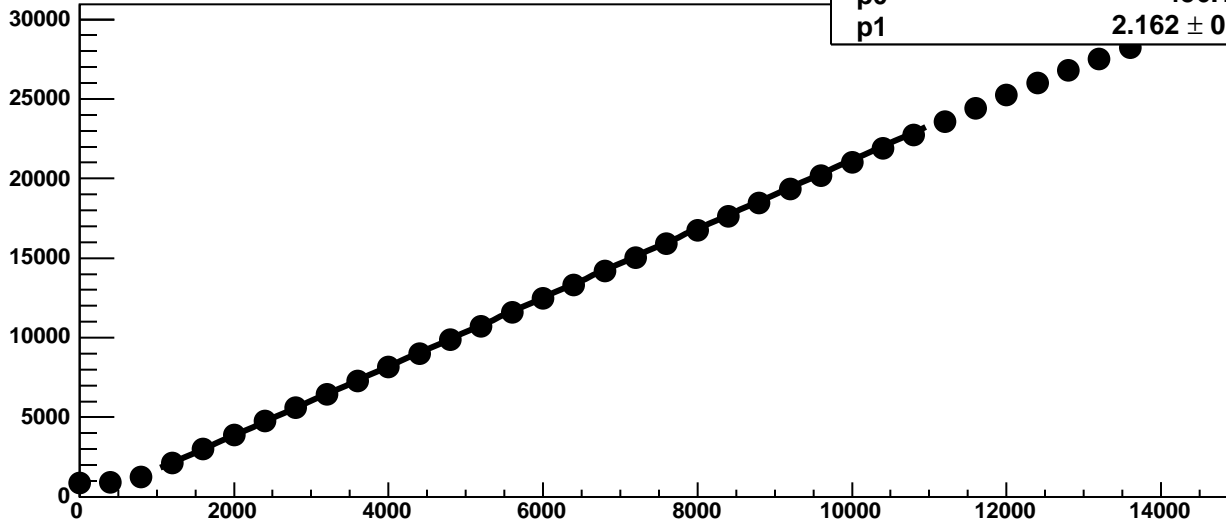


Chip 9, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

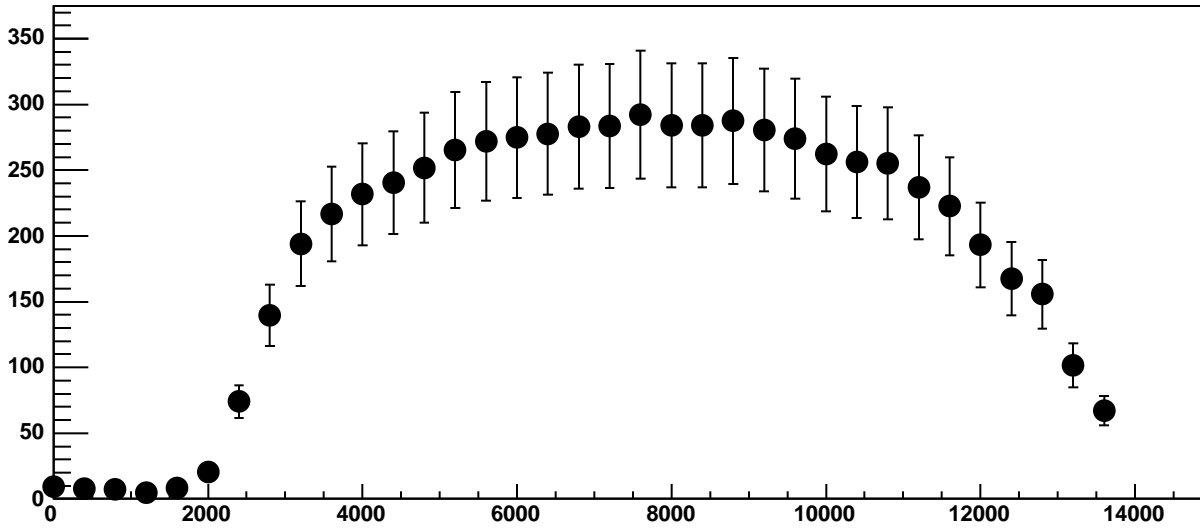


Chip 9, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC

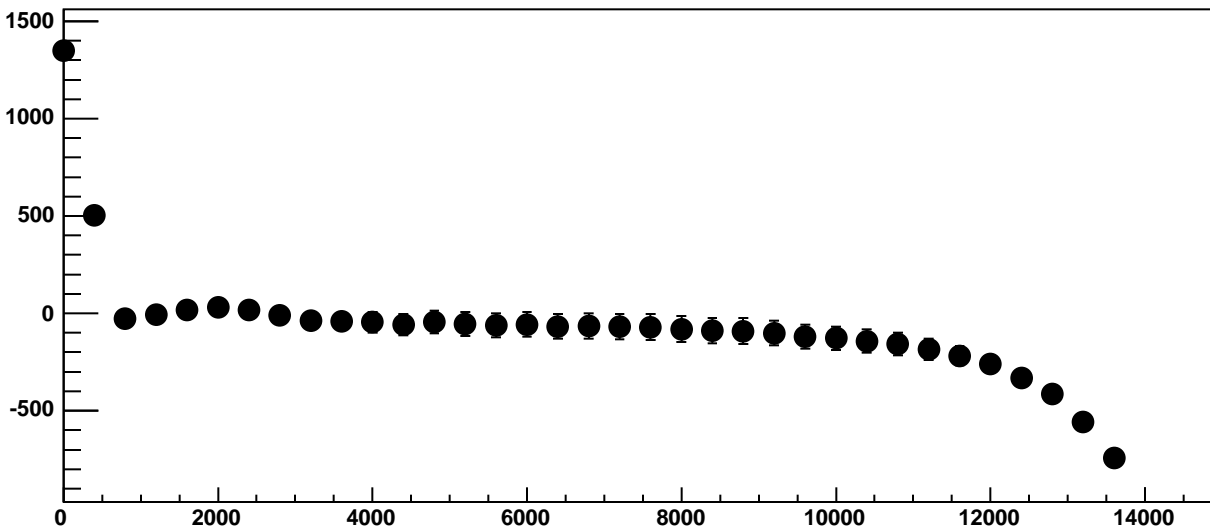
χ^2 / ndf 181.2 / 23
p0 -456.1 ± 2.821
p1 2.162 ± 0.001979



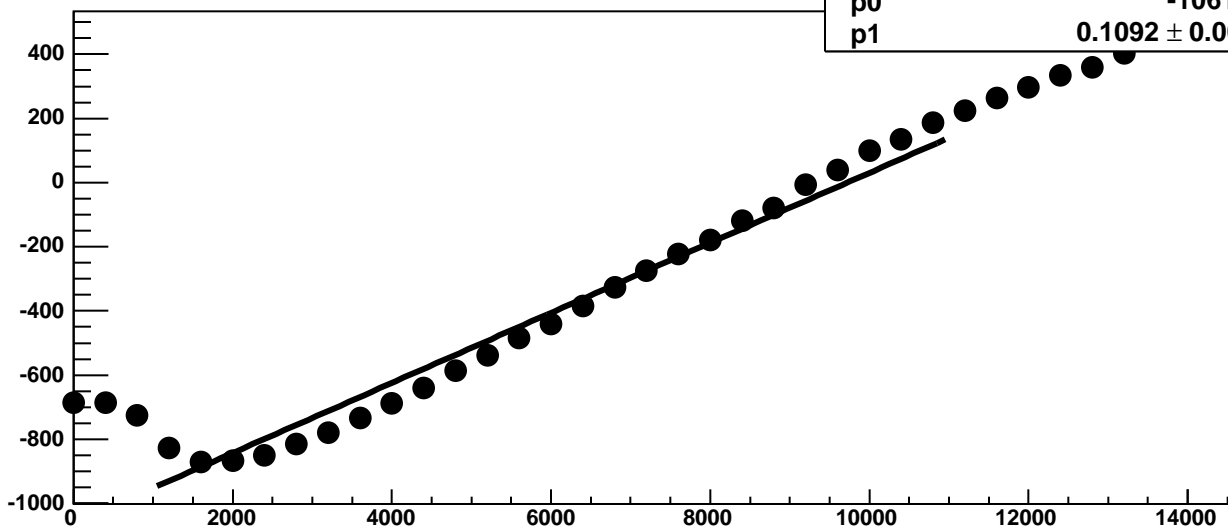
Chip 9, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



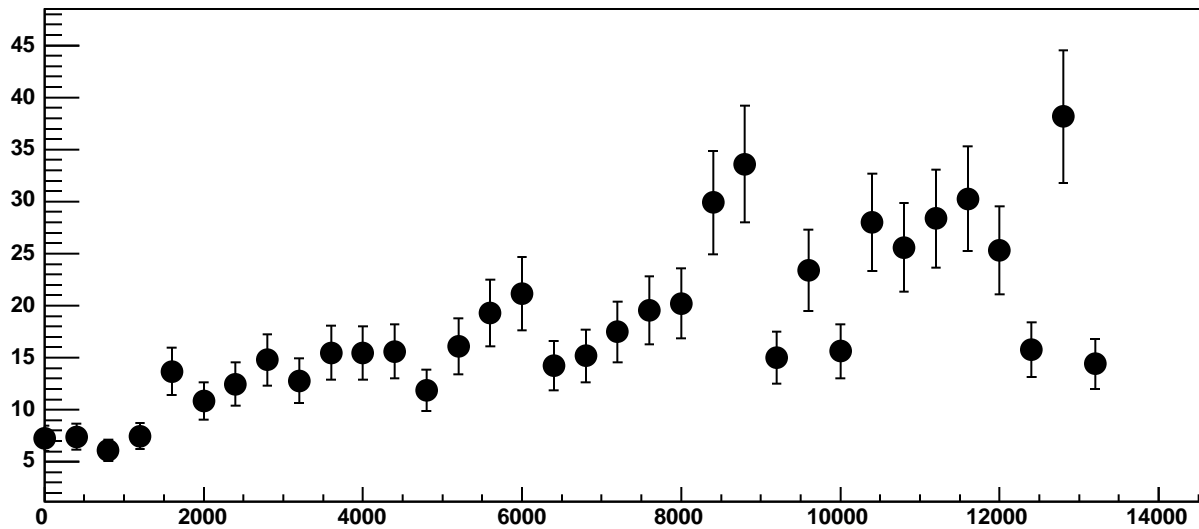
Chip 9, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC



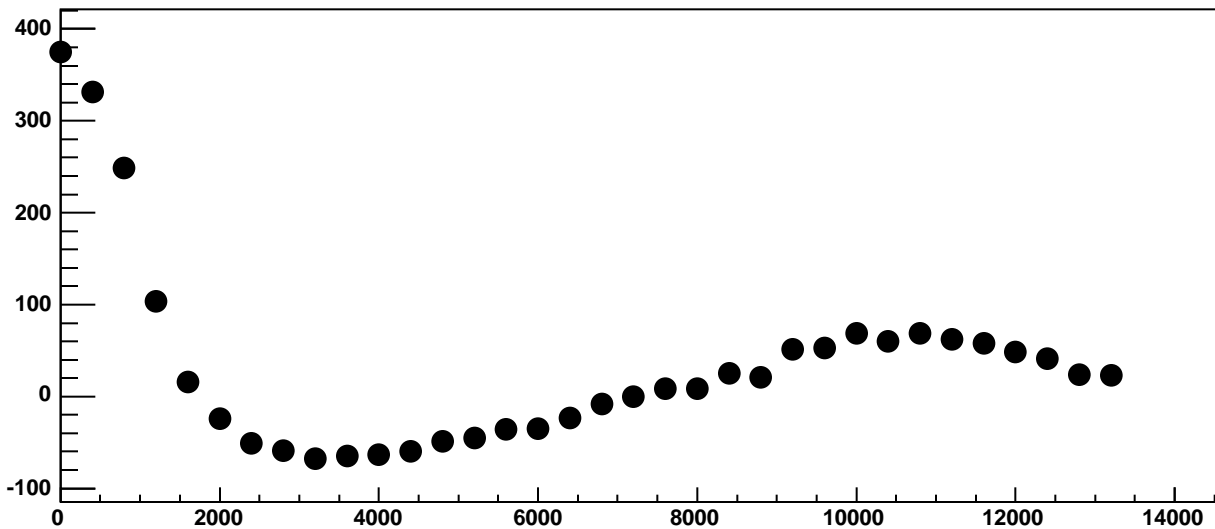
Chip 9, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



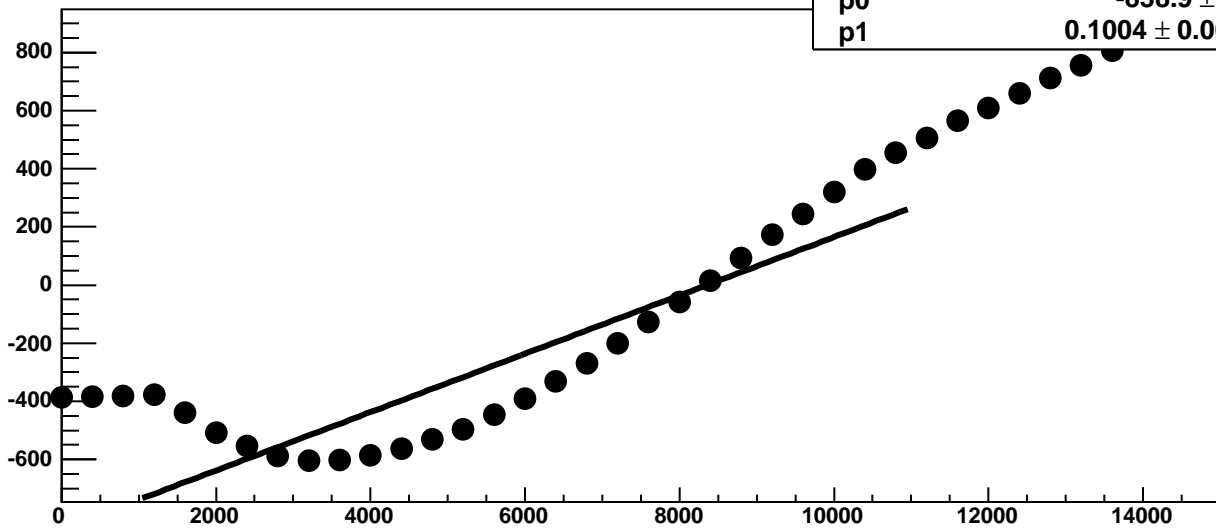
Chip 9, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

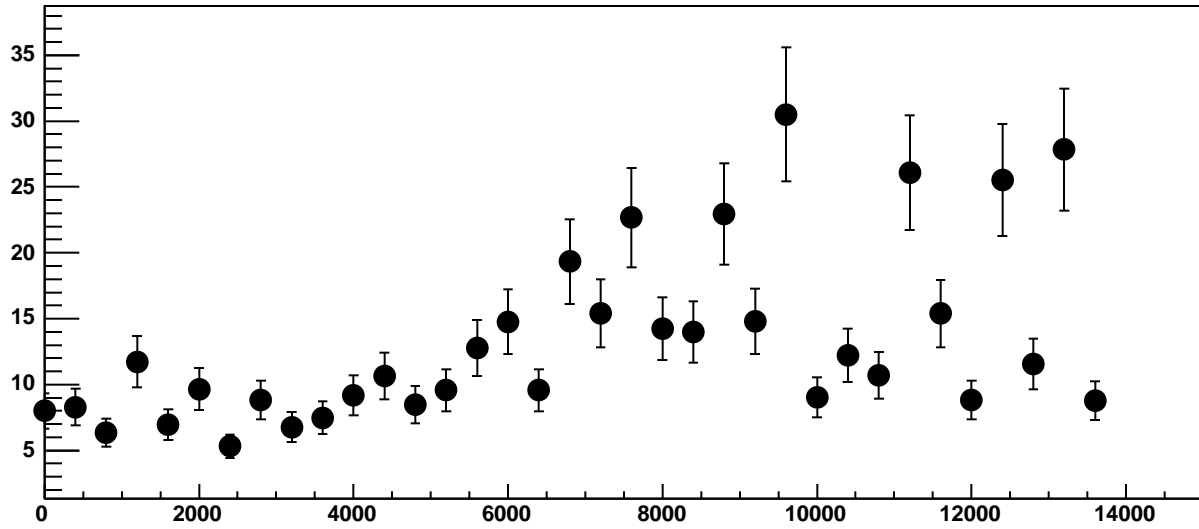


Chip 9, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

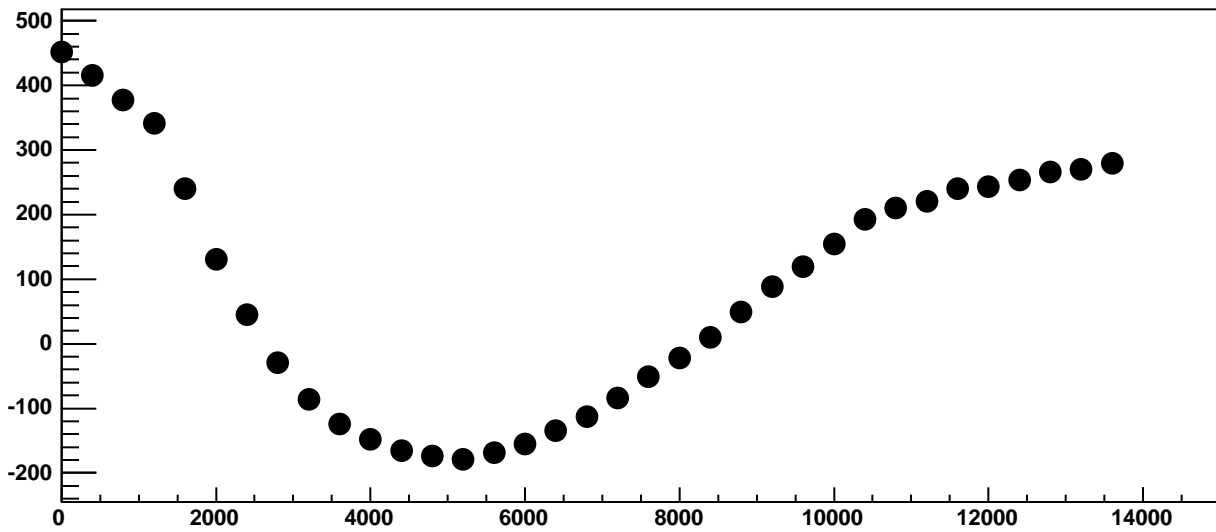


χ^2 / ndf 1.054e+05 / 23
p0 -838.9 ± 0.8992
p1 0.1004 ± 0.0001648

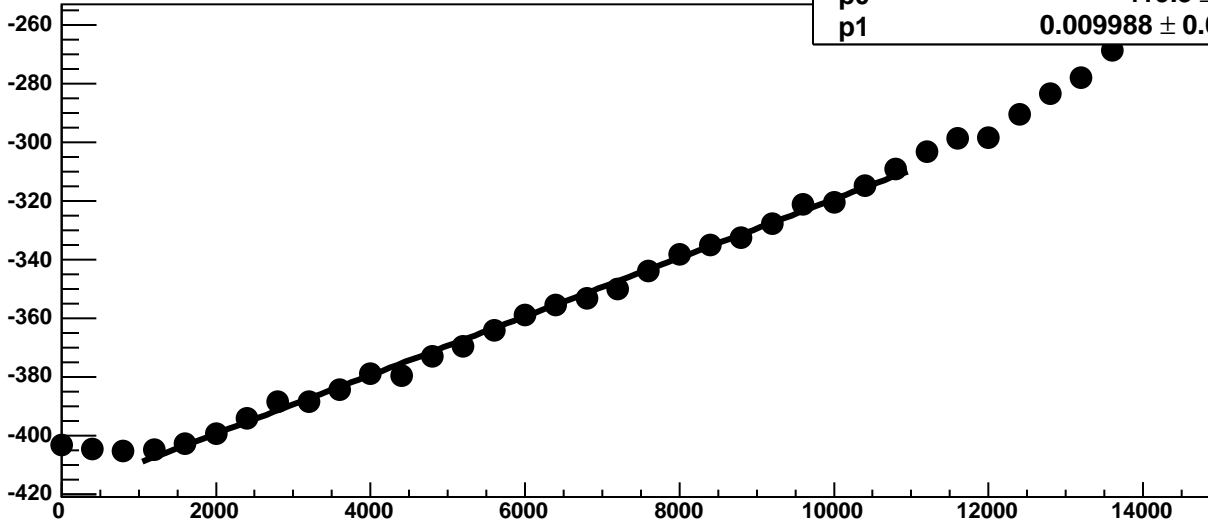
Chip 9, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

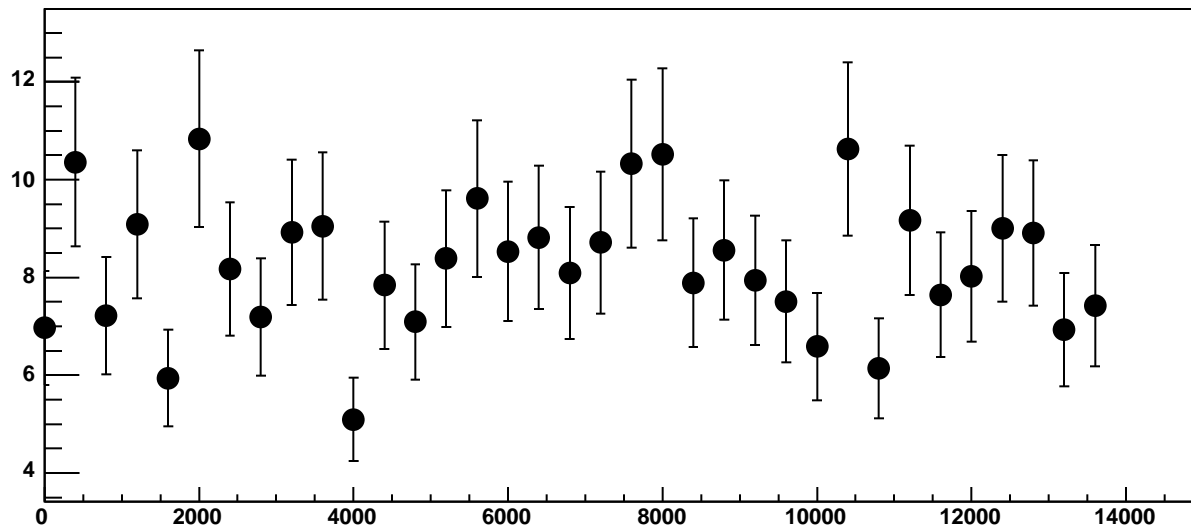


Chip 9, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

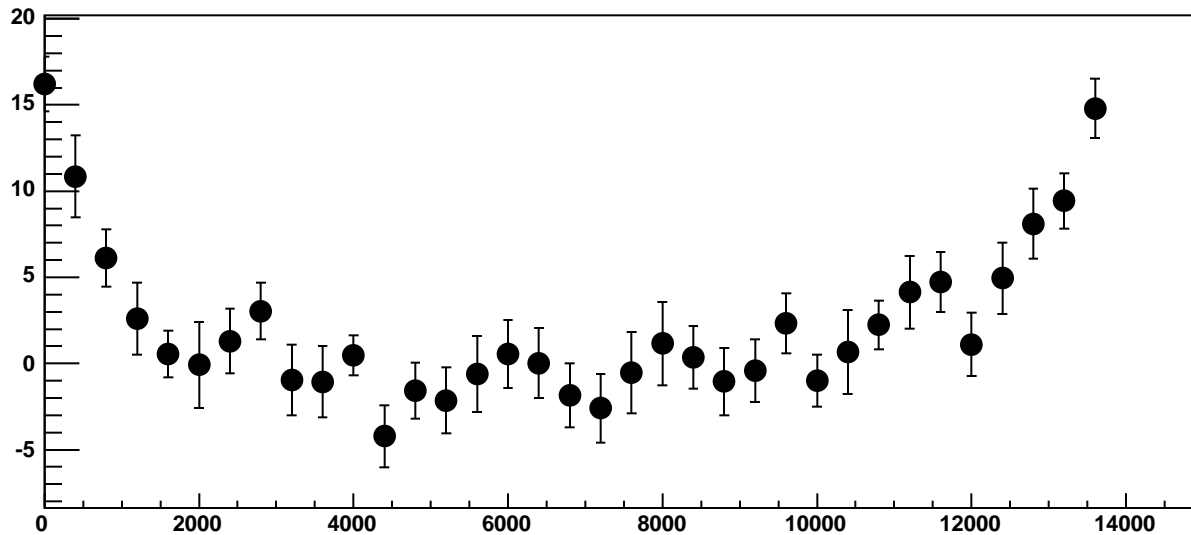


χ^2 / ndf 22.22 / 23
p0 -419.3 ± 0.8049
p1 0.009988 ± 0.0001211

Chip 9, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

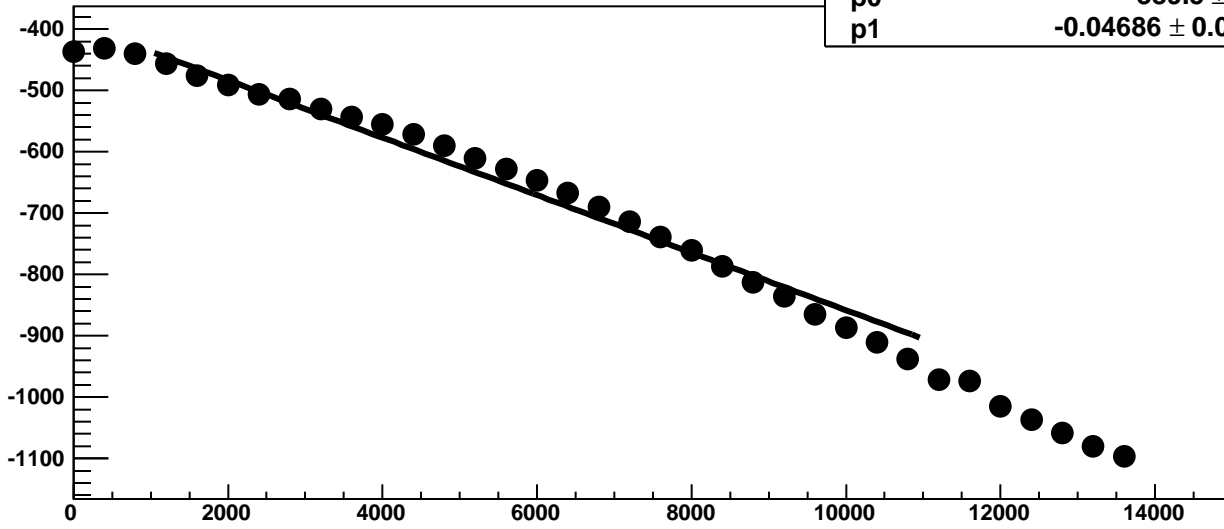


Chip 9, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

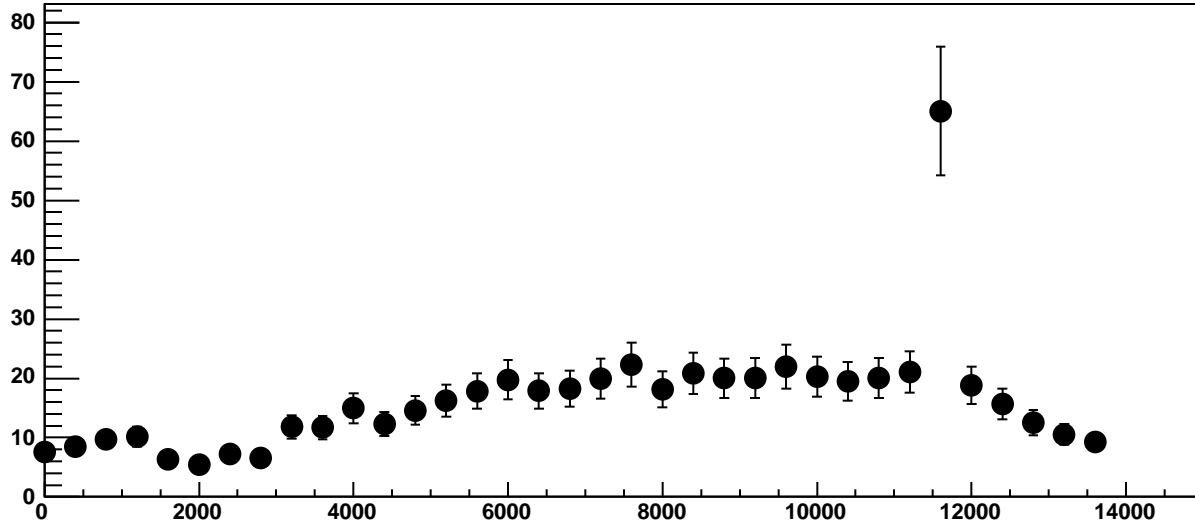


Chip 9, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

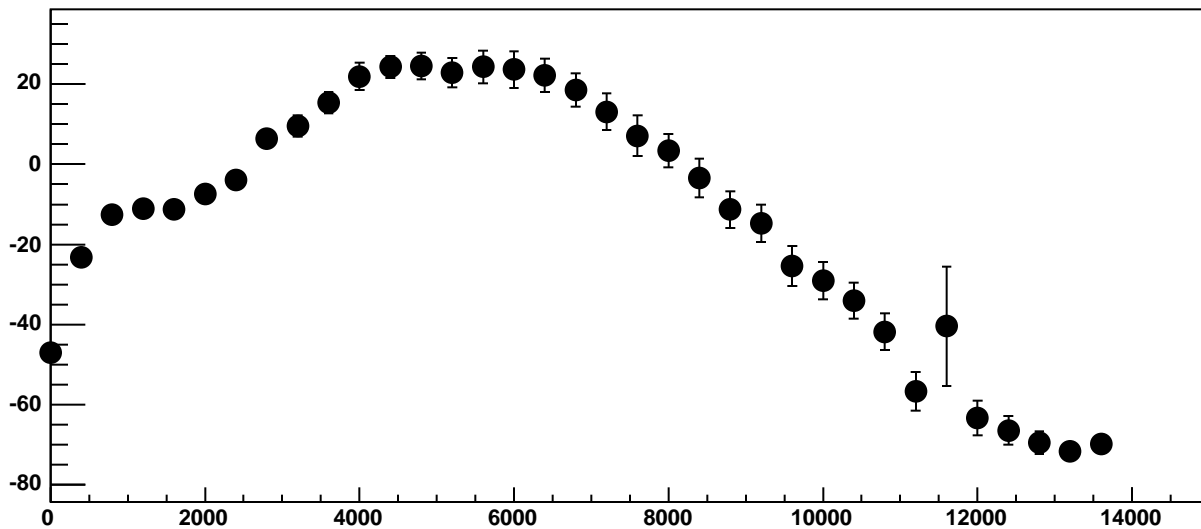
χ^2 / ndf 739.7 / 23
p0 -389.5 ± 0.9522
p1 -0.04686 ± 0.0002151



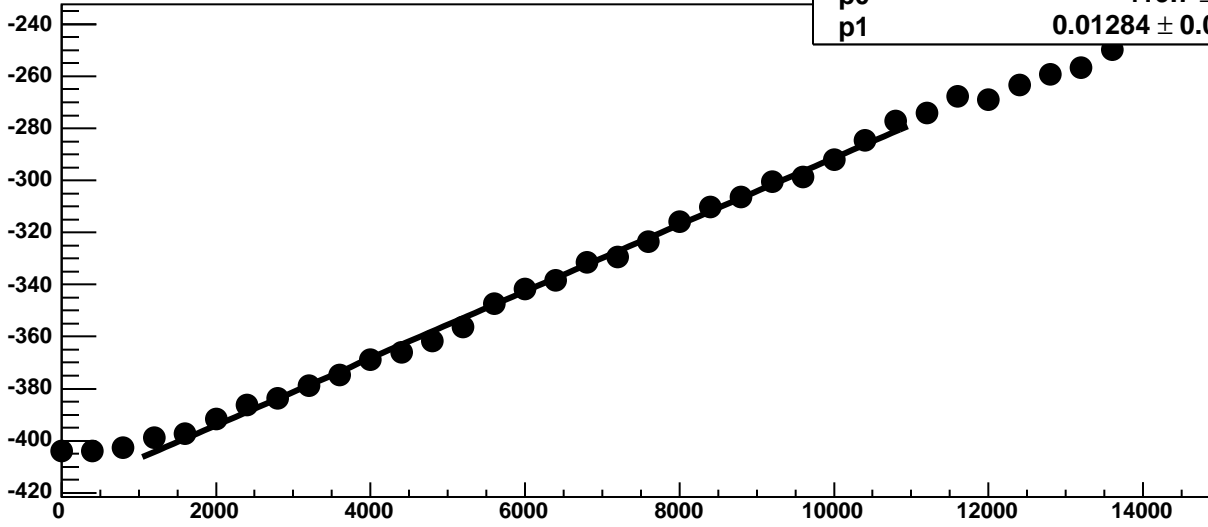
Chip 9, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



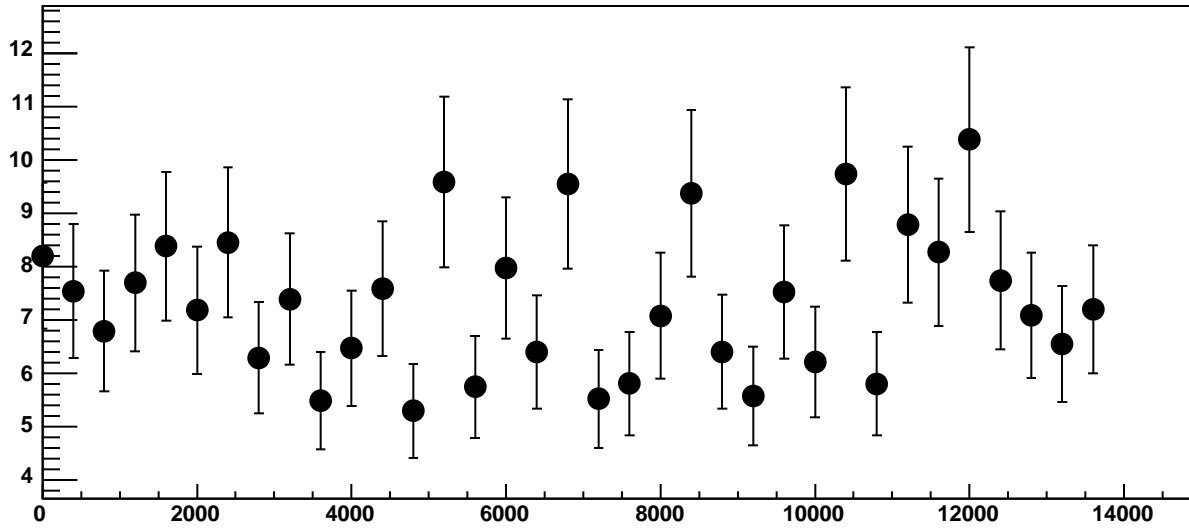
Chip 9, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC



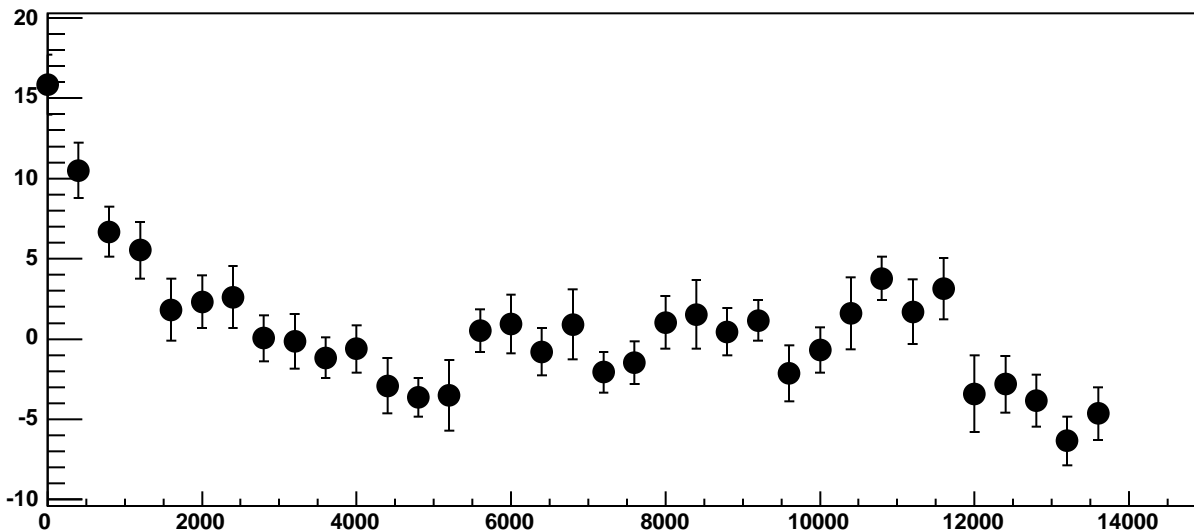
Chip 9, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



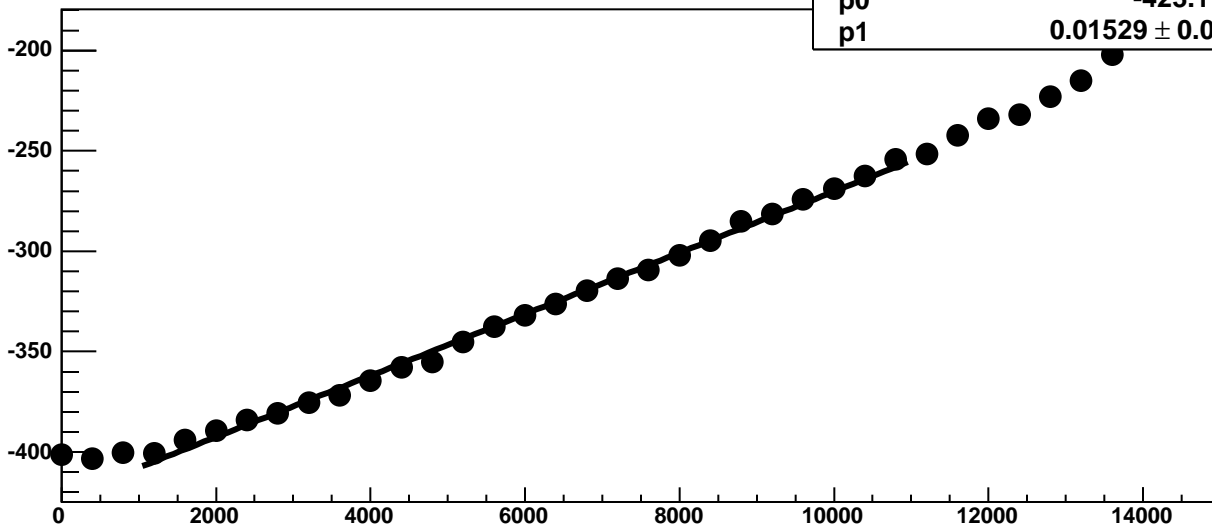
Chip 9, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

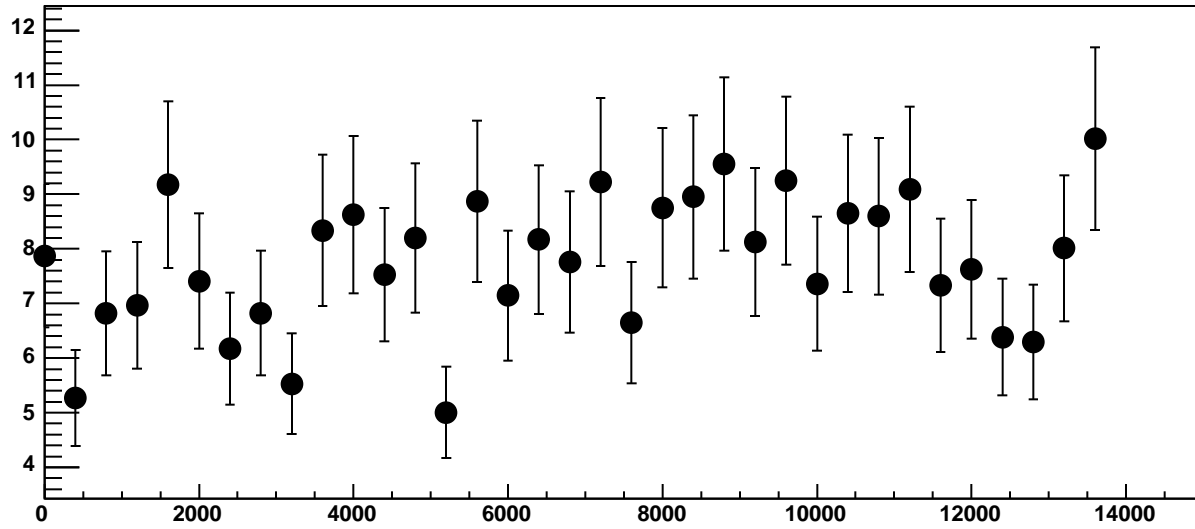


Chip 9, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

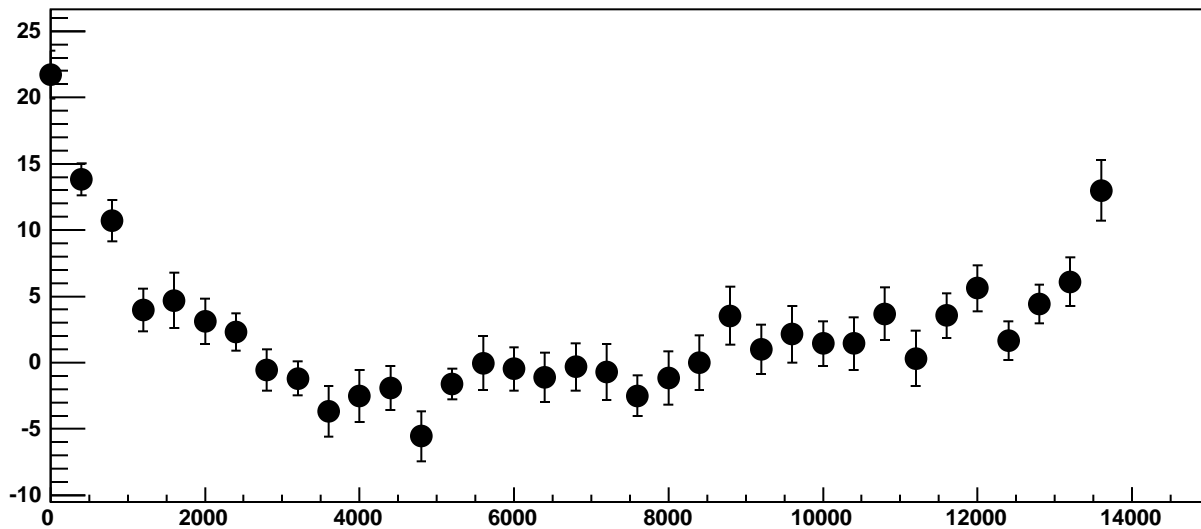


χ^2 / ndf 47.52 / 23
p0 -423.1 ± 0.773
p1 0.01529 ± 0.0001237

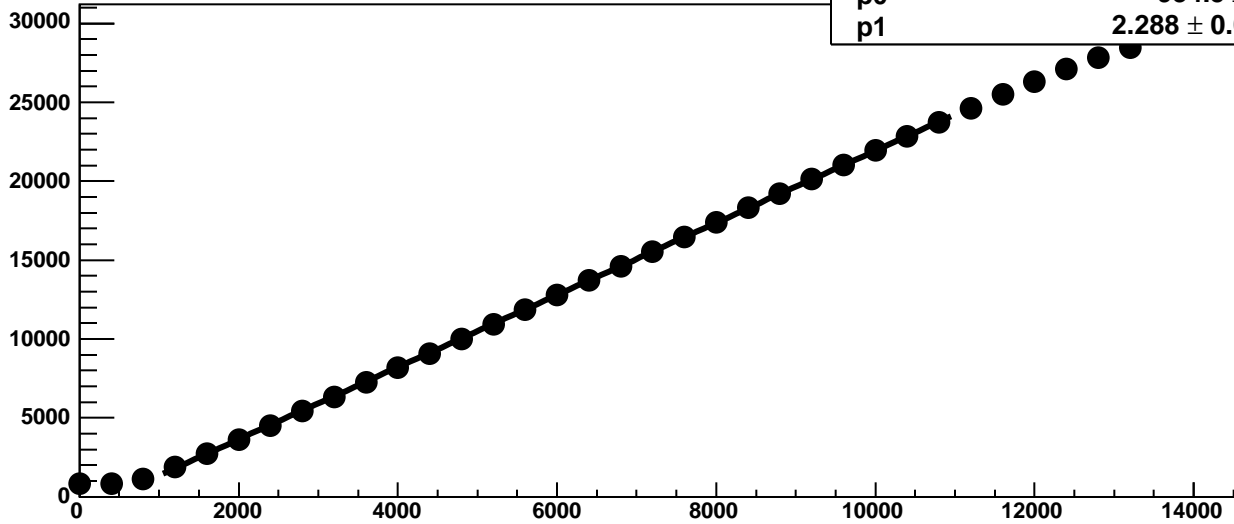
Chip 9, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 9, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

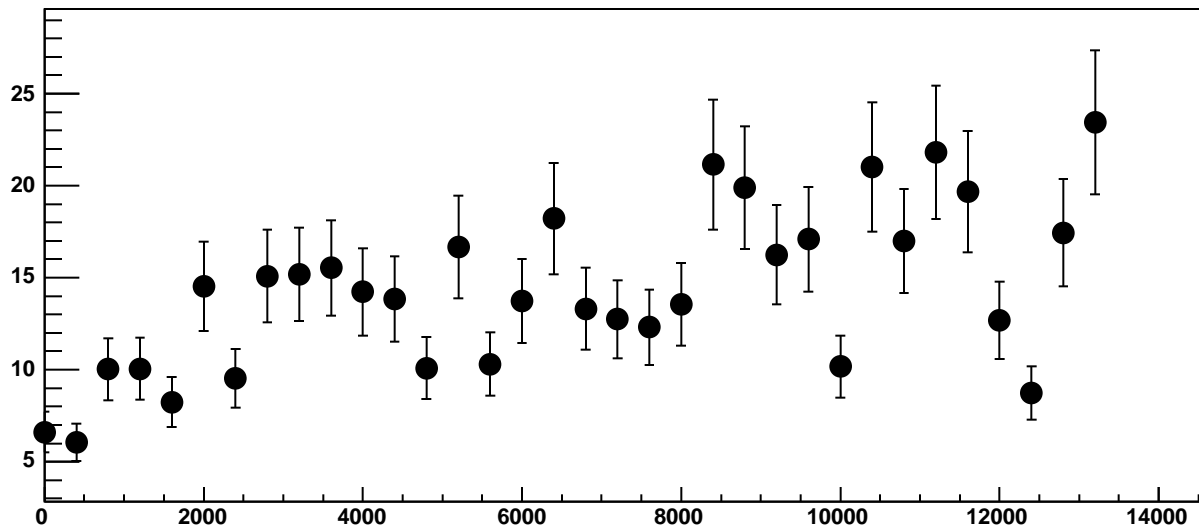


Chip 9, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC

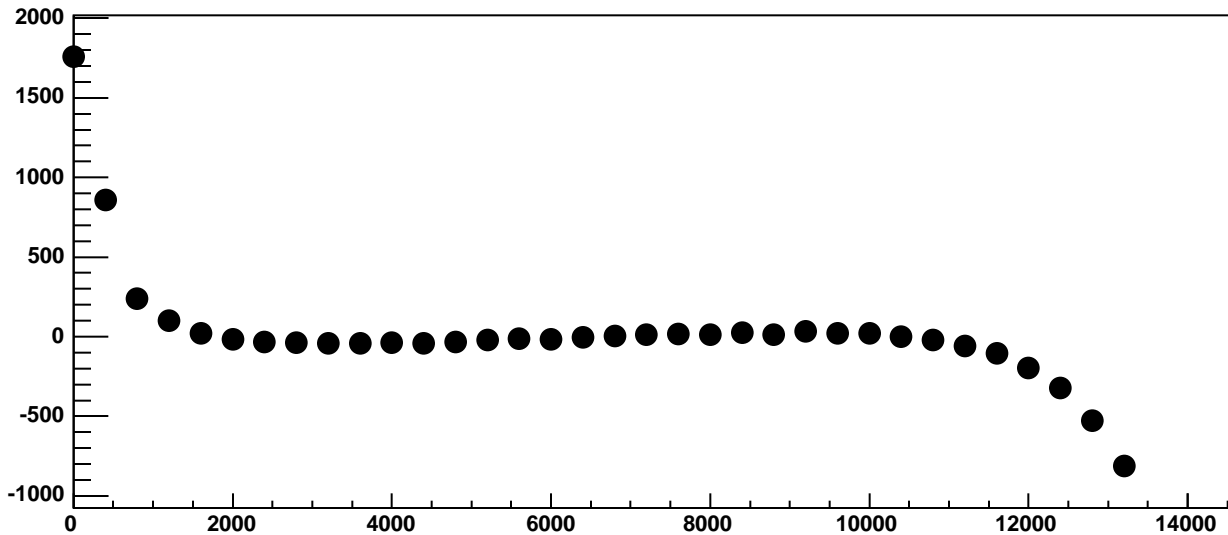


χ^2 / ndf 3584 / 23
p0 -934.5 ± 1.236
p1 2.288 ± 0.000205

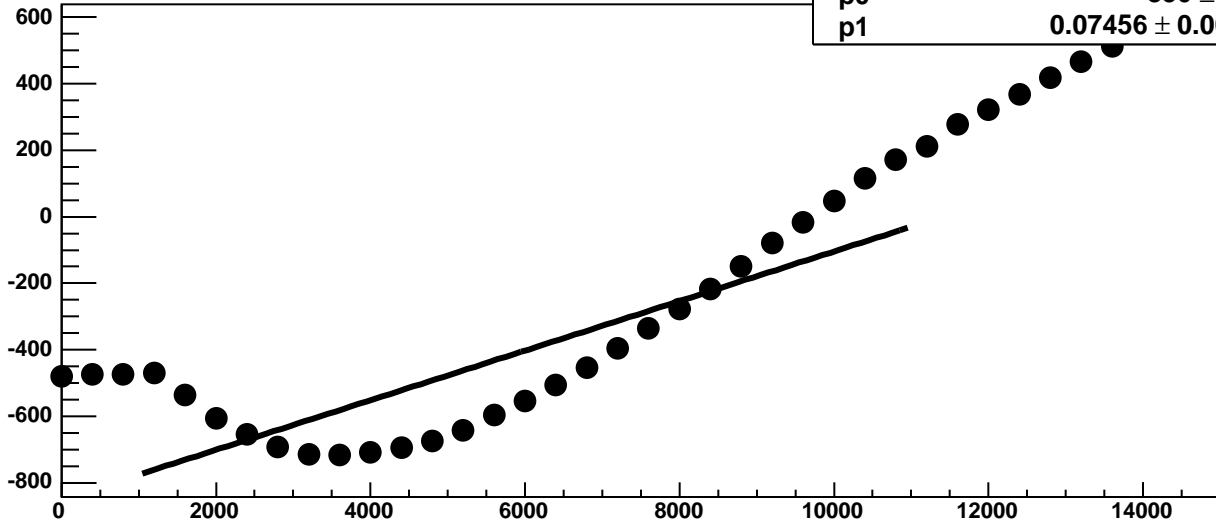
Chip 9, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC



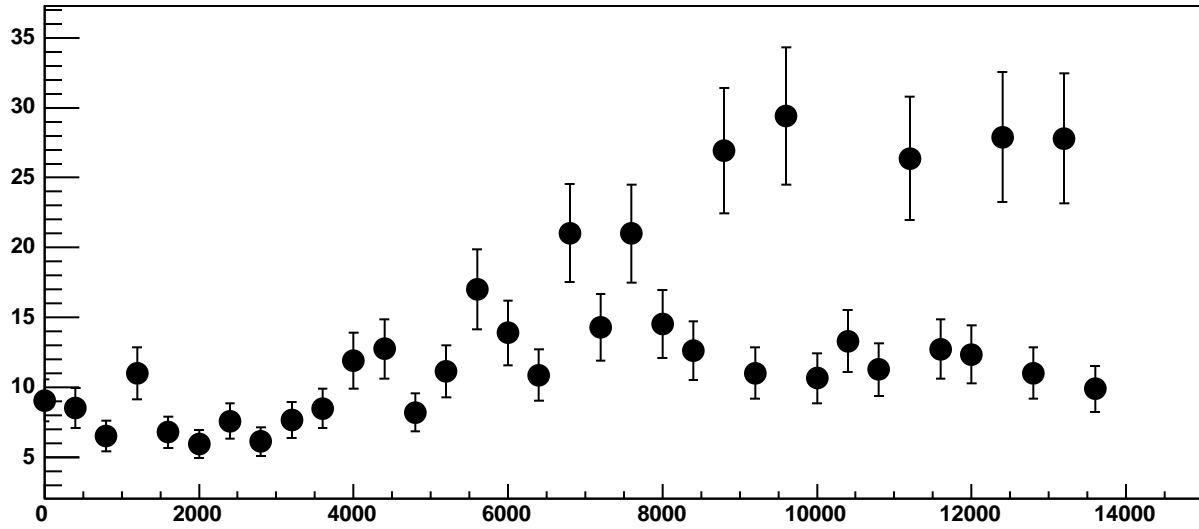
Chip 9, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC



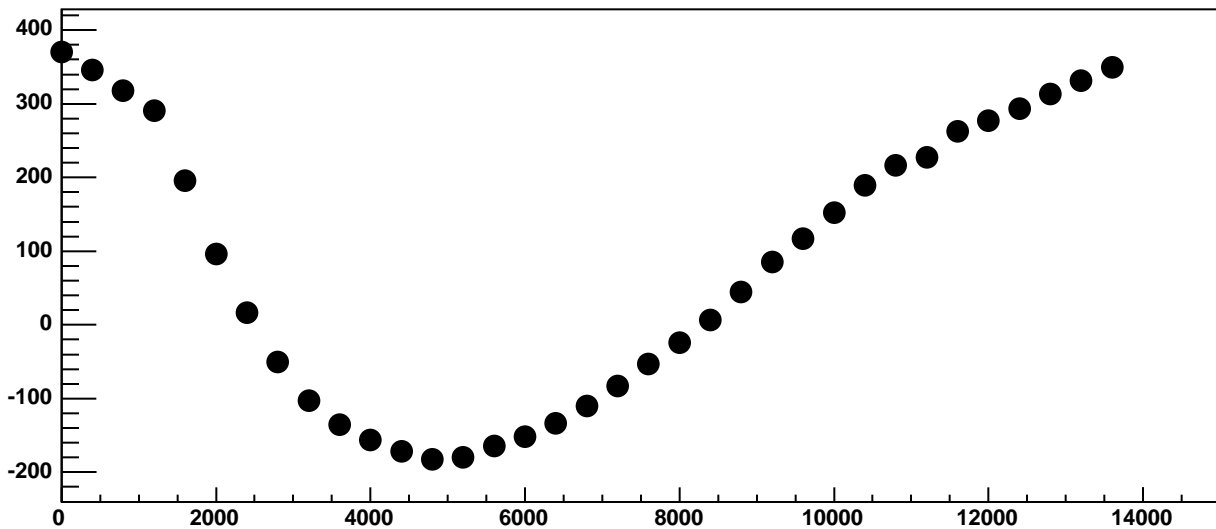
Chip 9, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



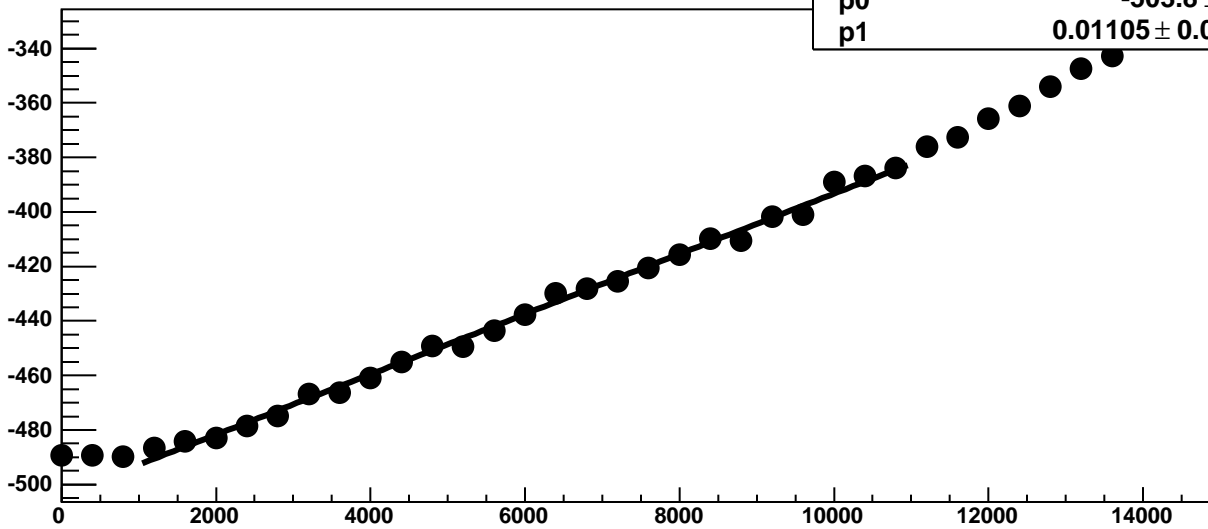
Chip 9, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

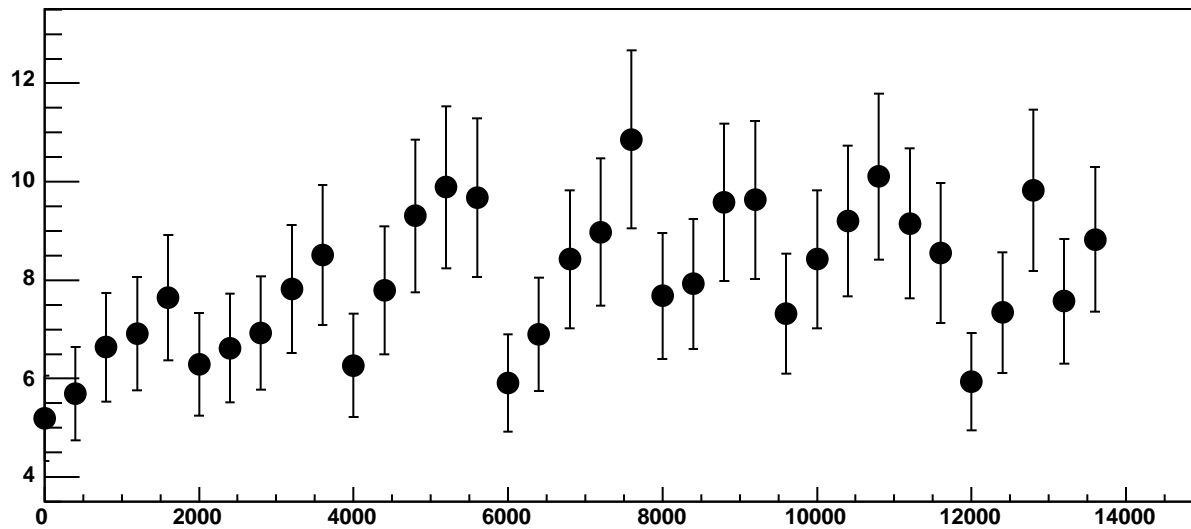


Chip 9, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

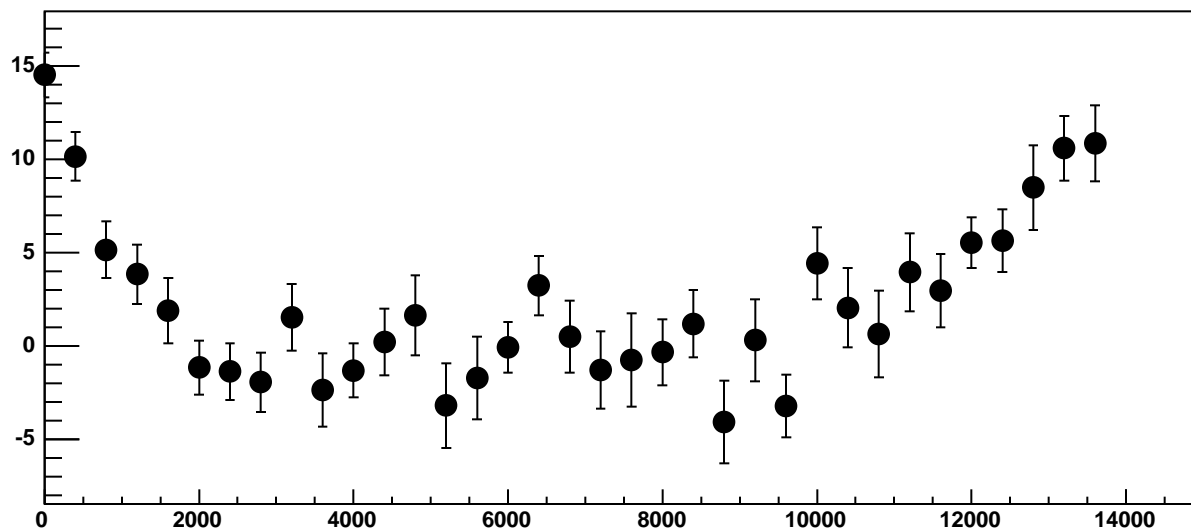


χ^2 / ndf 34.72 / 23
p0 -503.8 ± 0.7801
p1 0.01105 ± 0.0001257

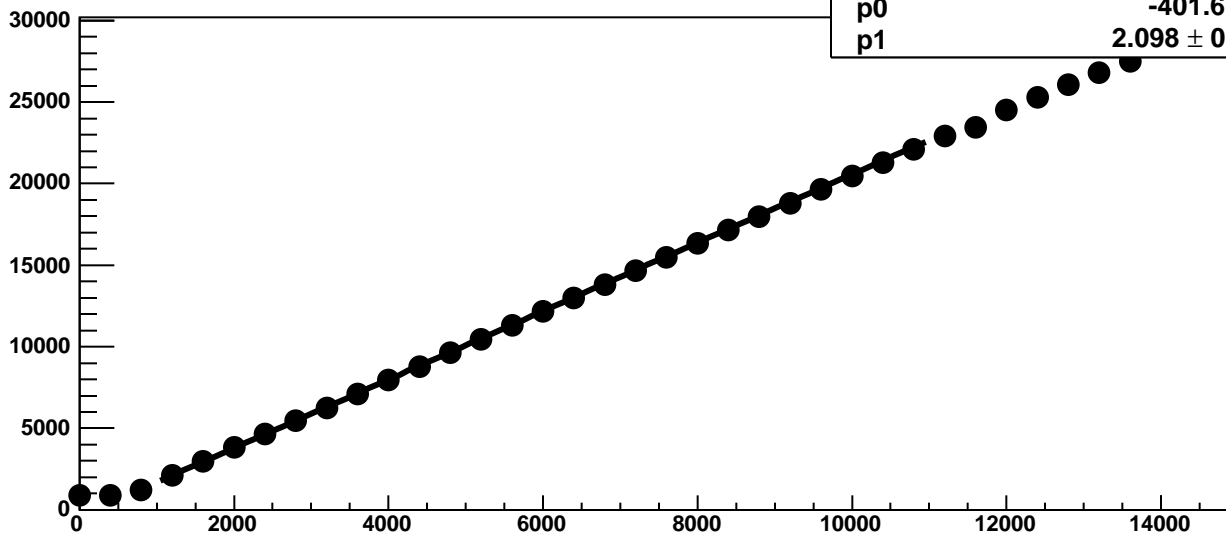
Chip 9, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



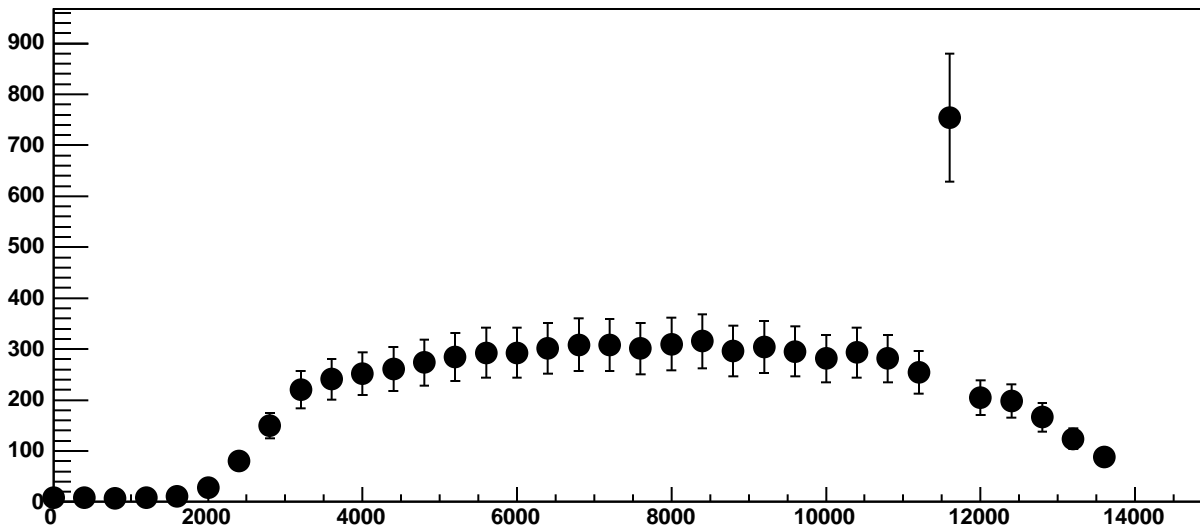
Chip 9, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



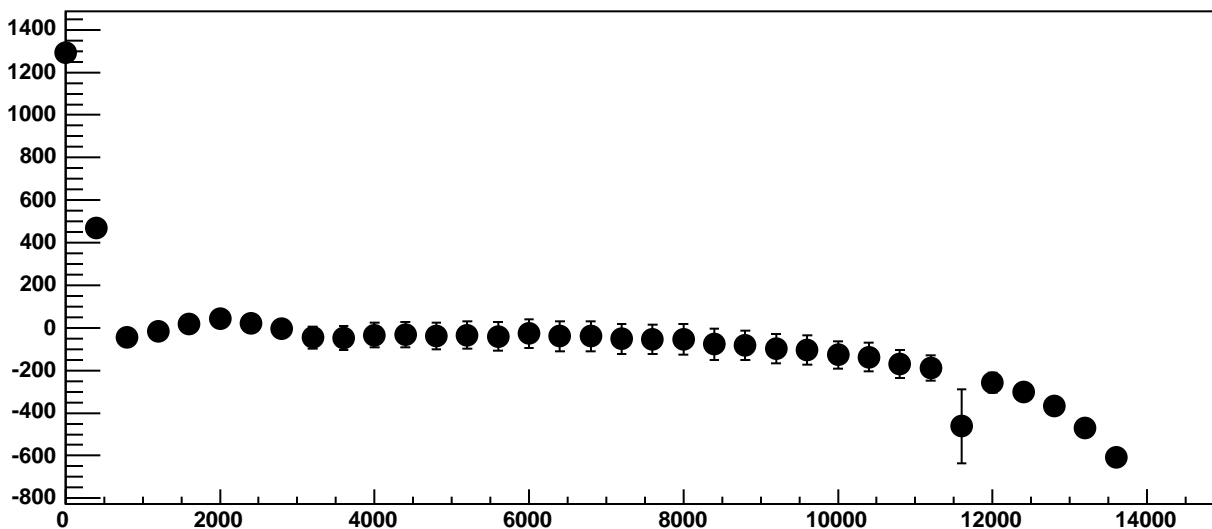
Chip 9, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC



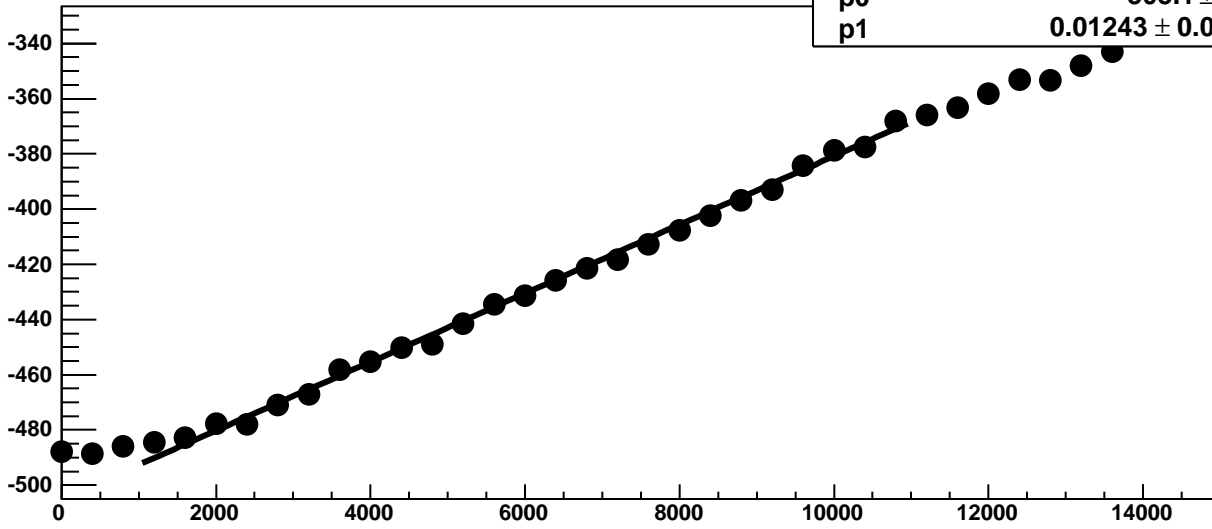
Chip 9, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC



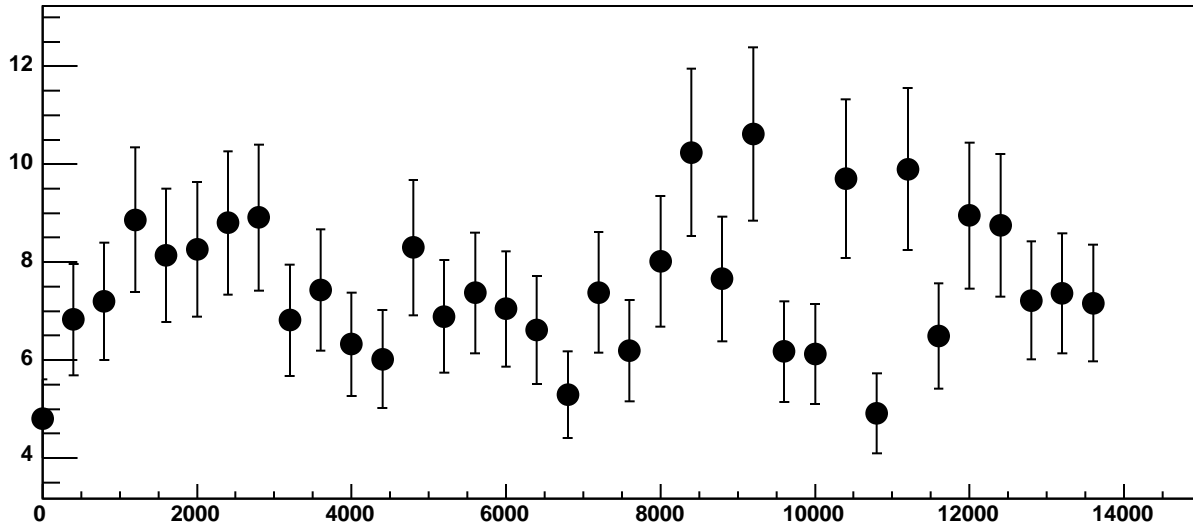
Chip 9, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC



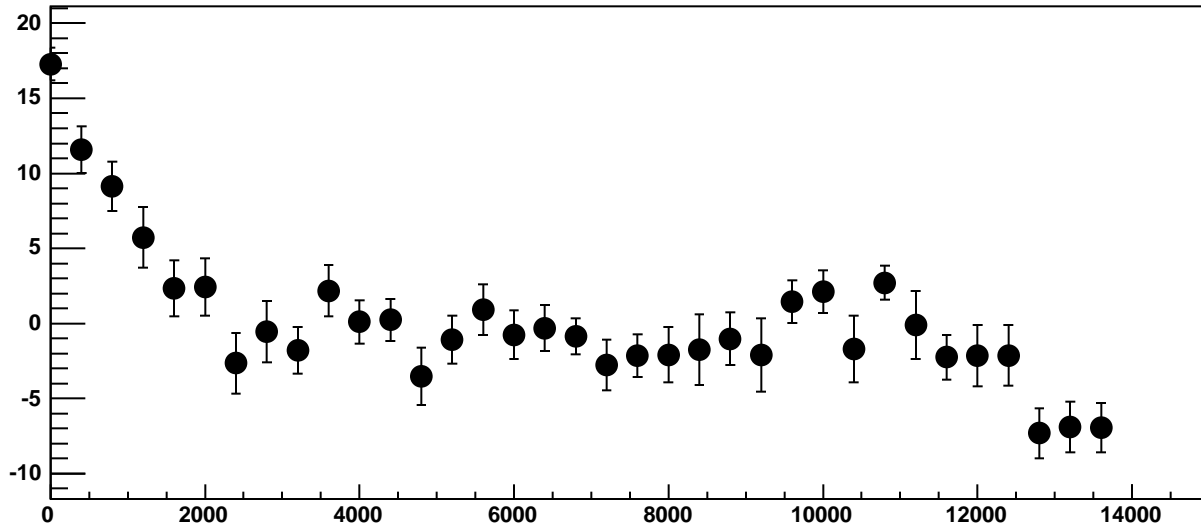
Chip 9, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



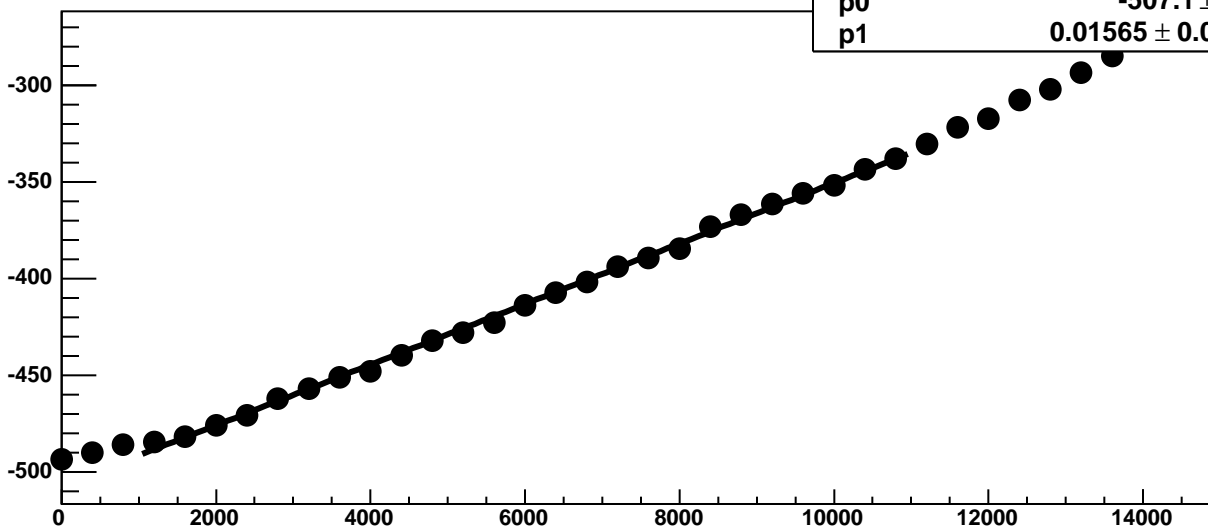
Chip 9, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

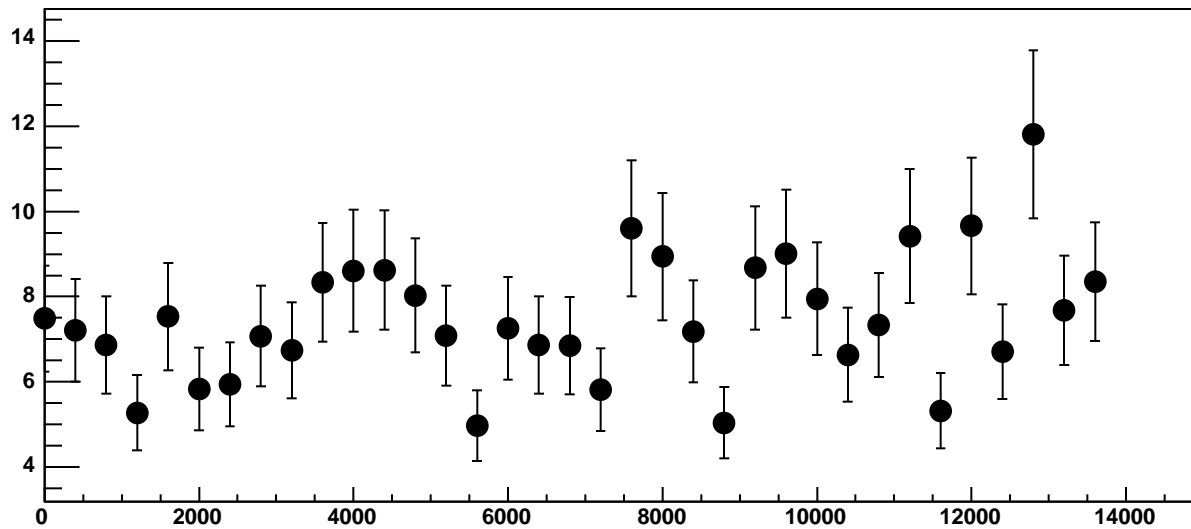


Chip 9, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

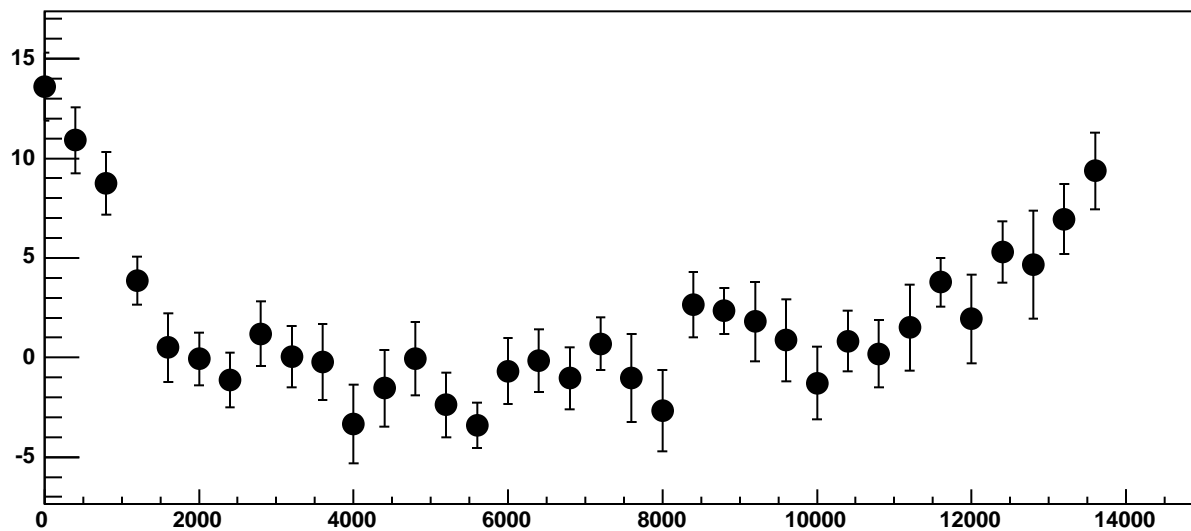


χ^2 / ndf 37.45 / 23
p0 -507.1 ± 0.6949
p1 0.01565 ± 0.0001076

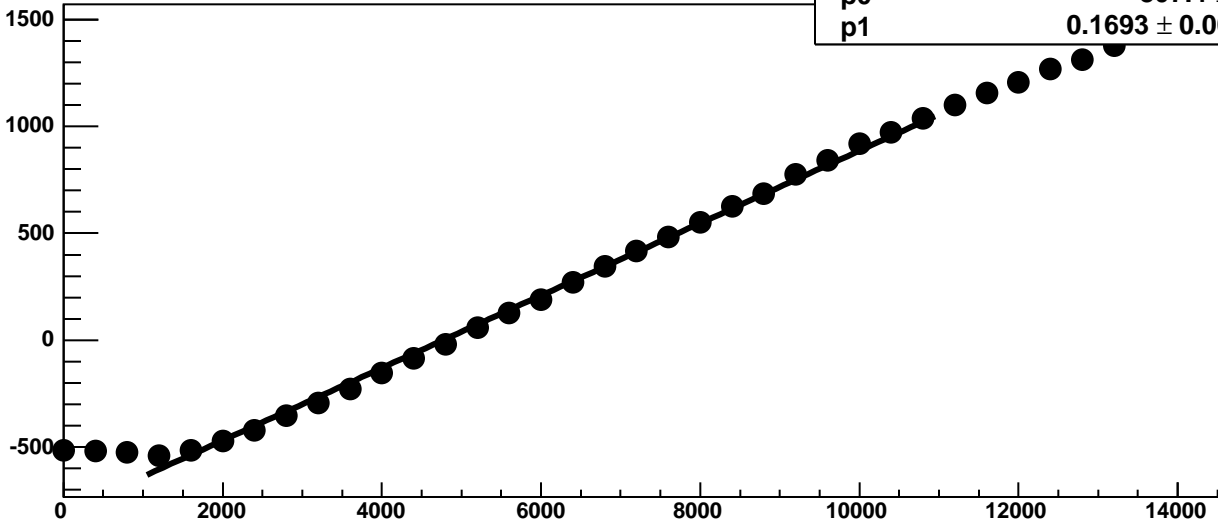
Chip 9, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



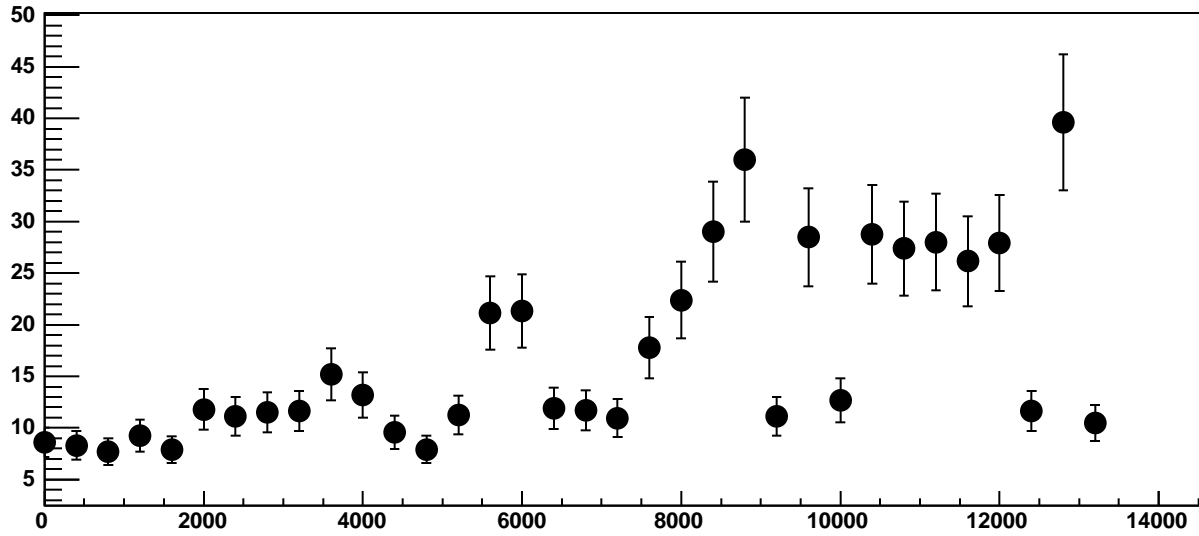
Chip 9, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



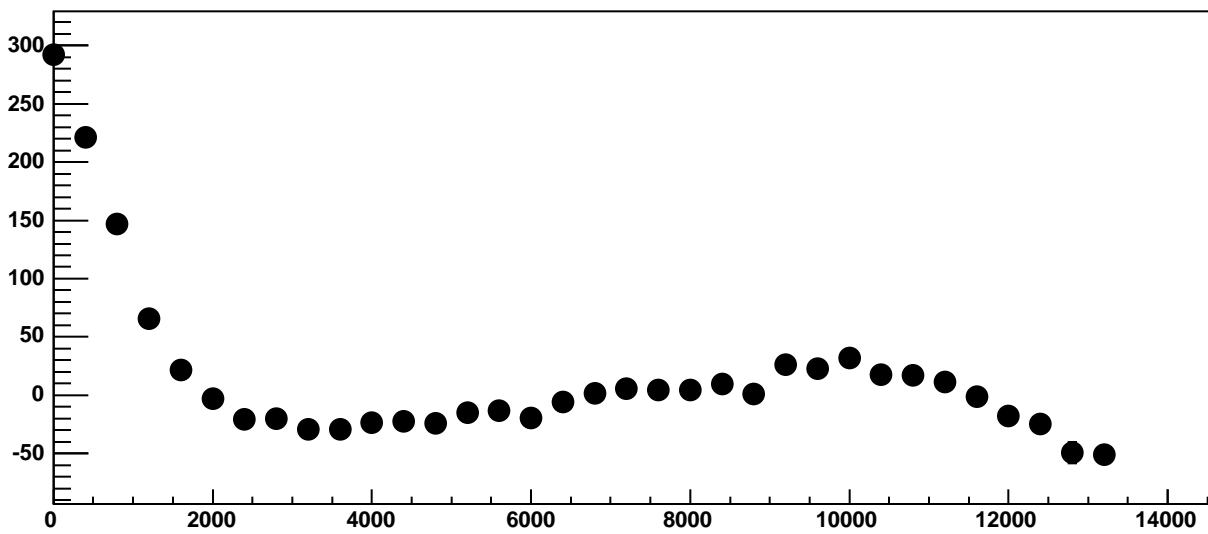
Chip 9, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC



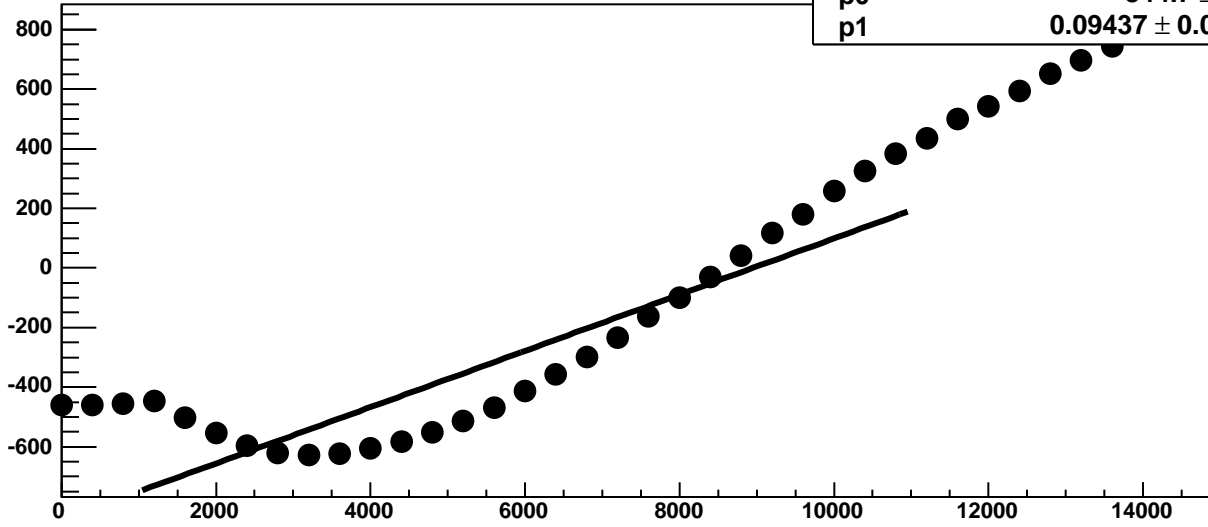
Chip 9, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC

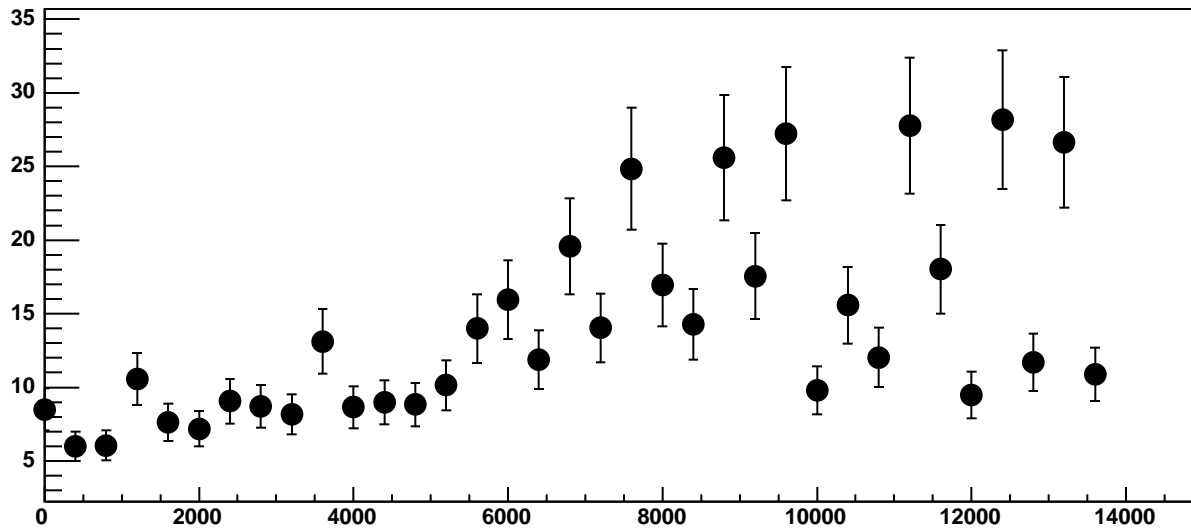


Chip 9, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

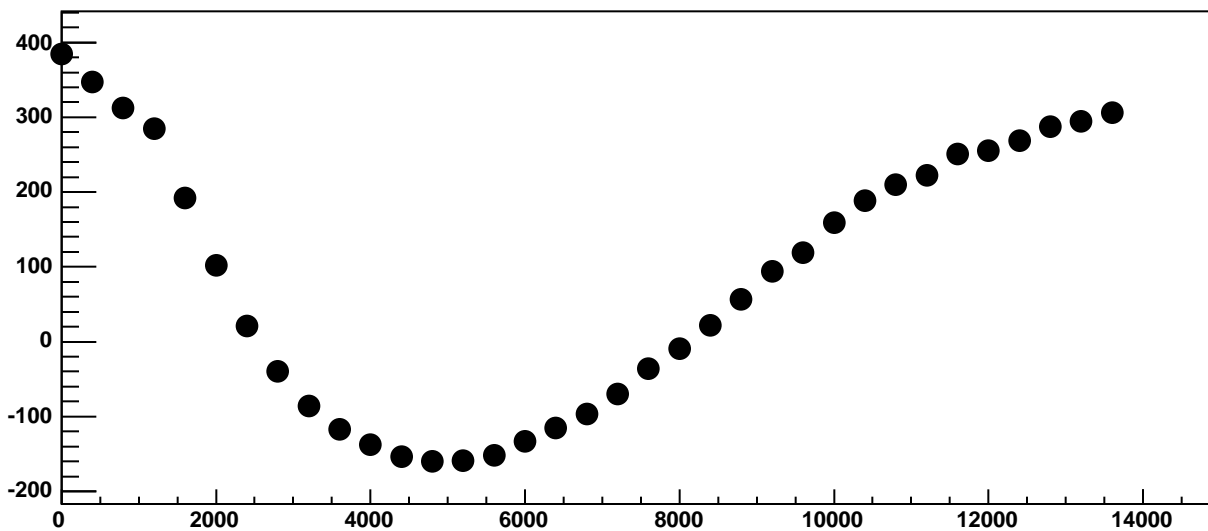


χ^2 / ndf 7.599e+04 / 23
p0 -844.7 ± 0.9962
p1 0.09437 ± 0.0001825

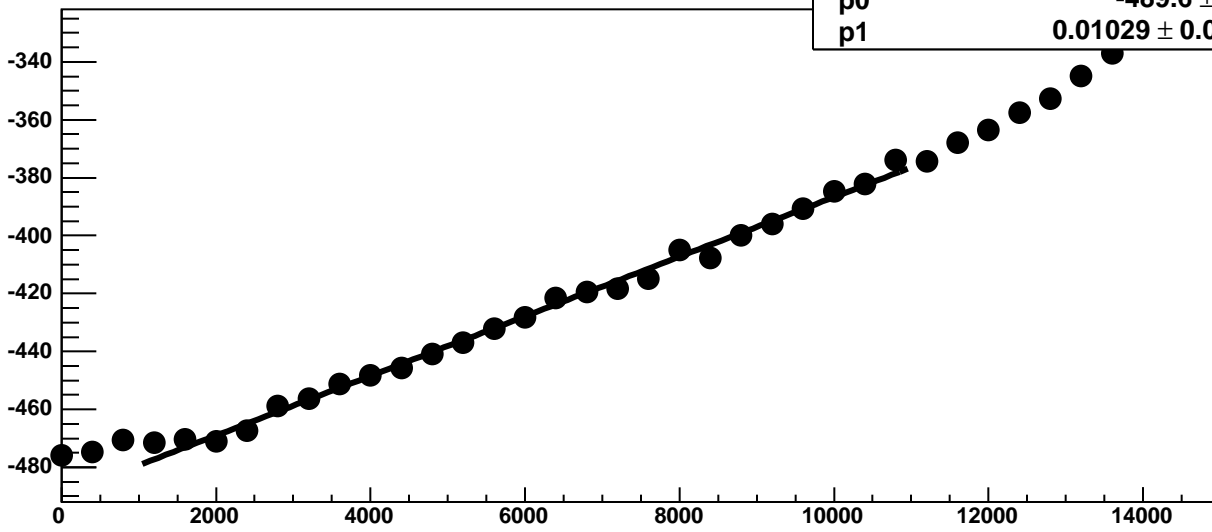
Chip 9, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



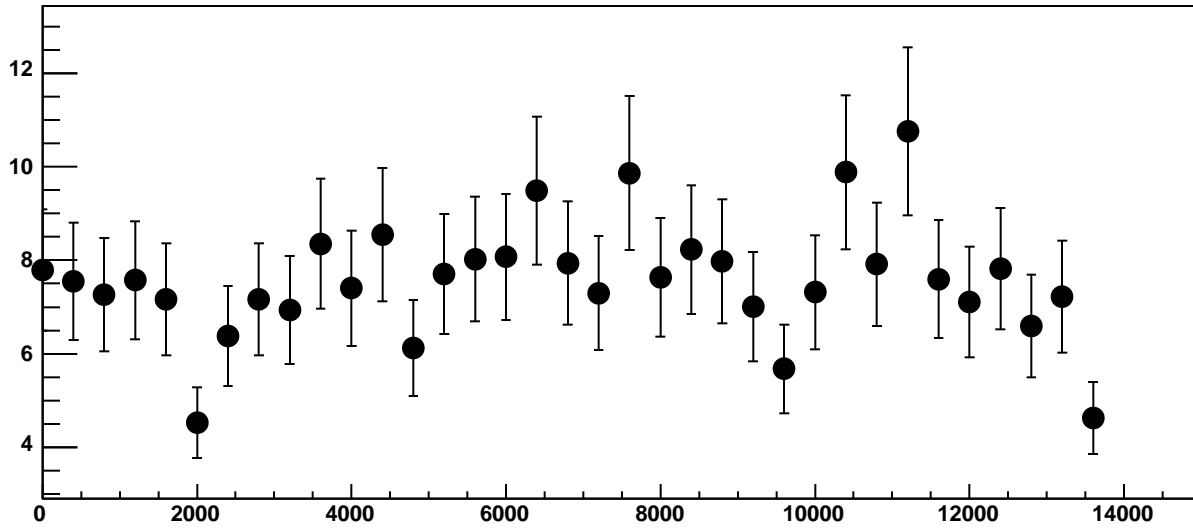
Chip 9, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



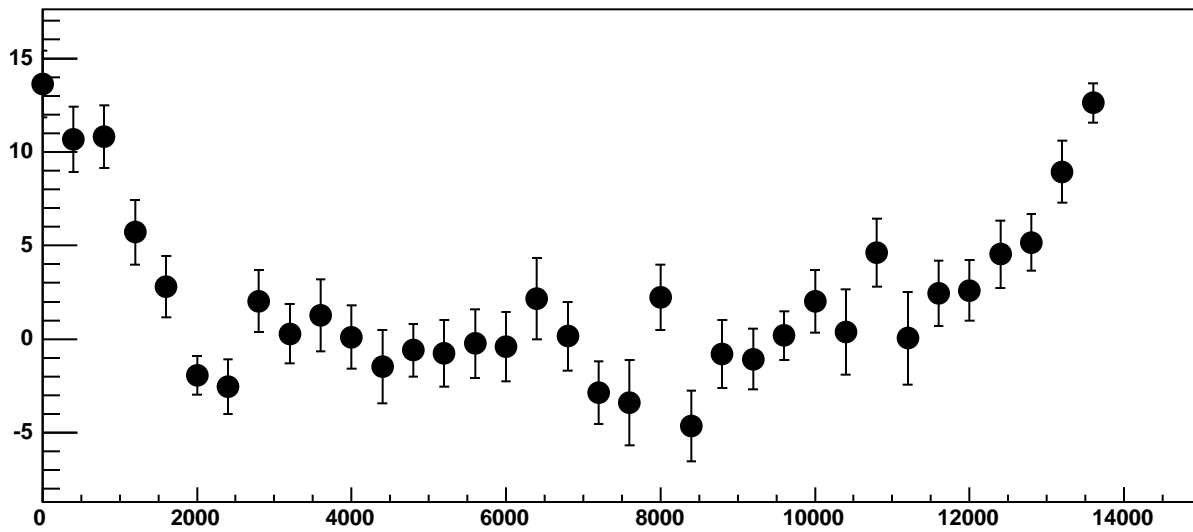
Chip 9, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



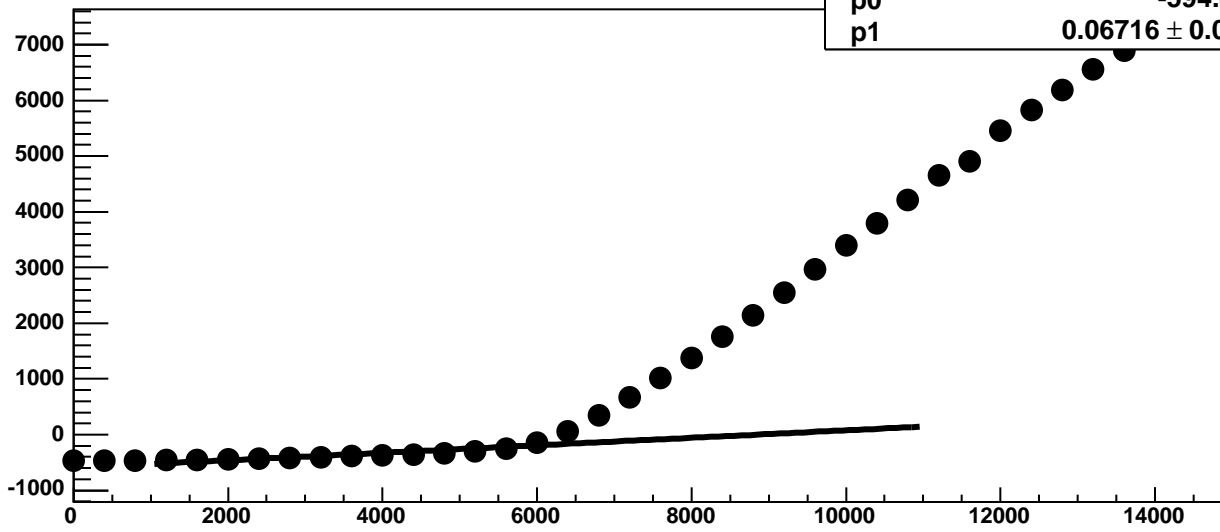
Chip 9, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 9, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

8.724e+04 / 23

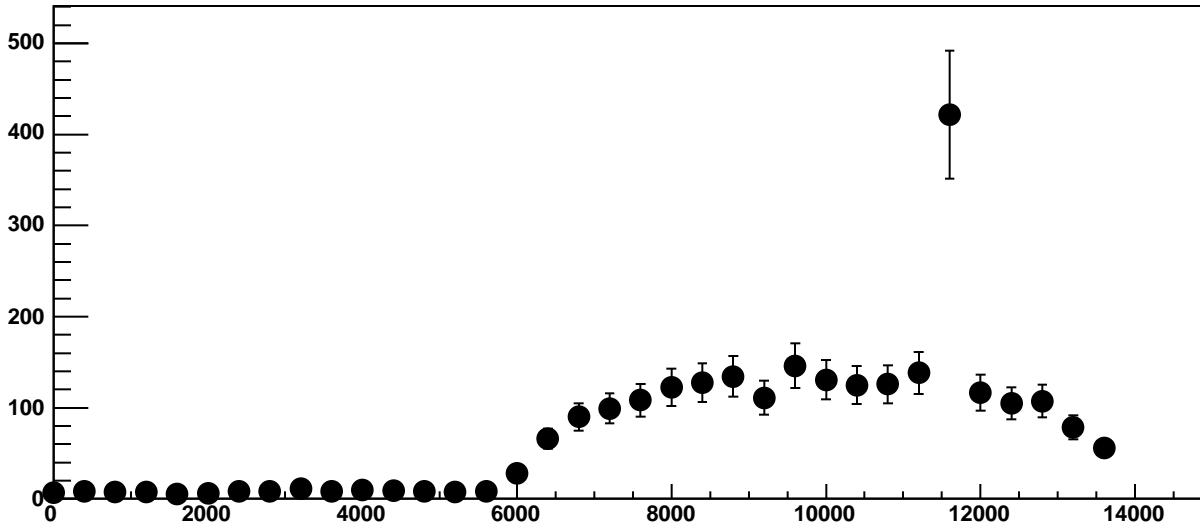
p0

-594.4 ± 1.2

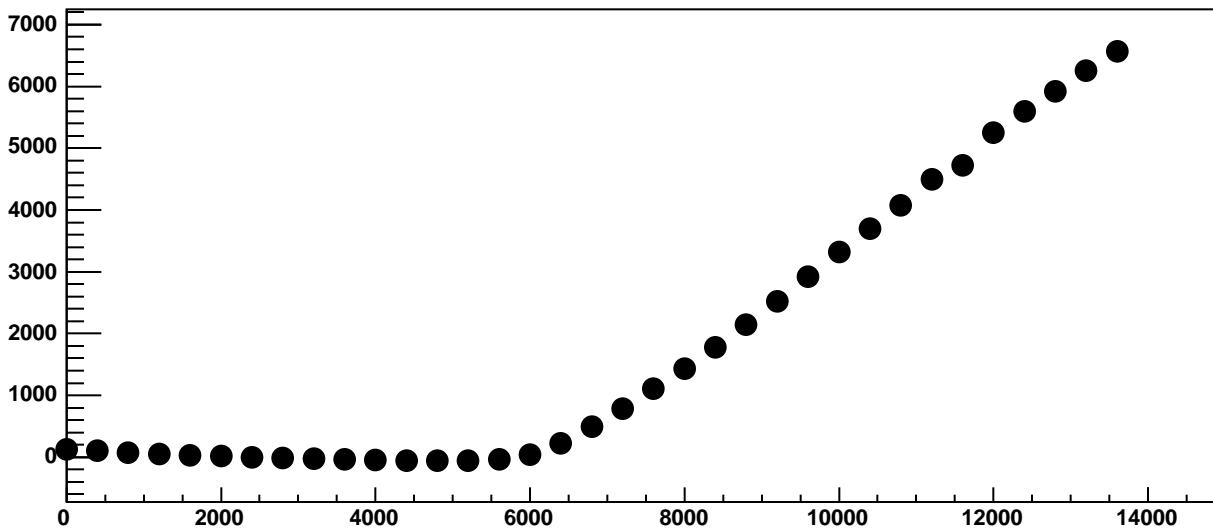
p1

0.06716 ± 0.0003432

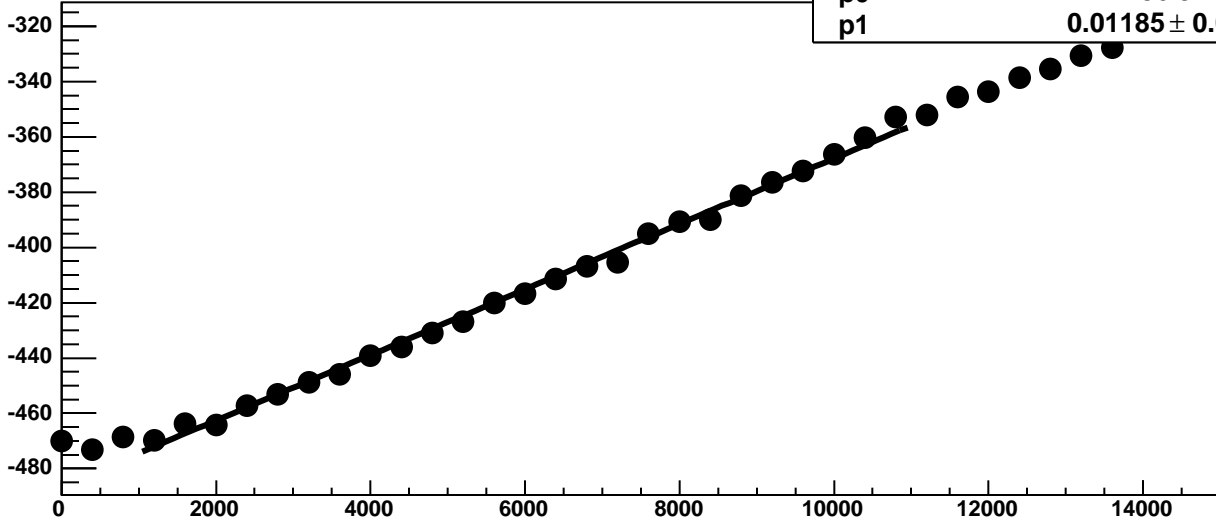
Chip 9, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



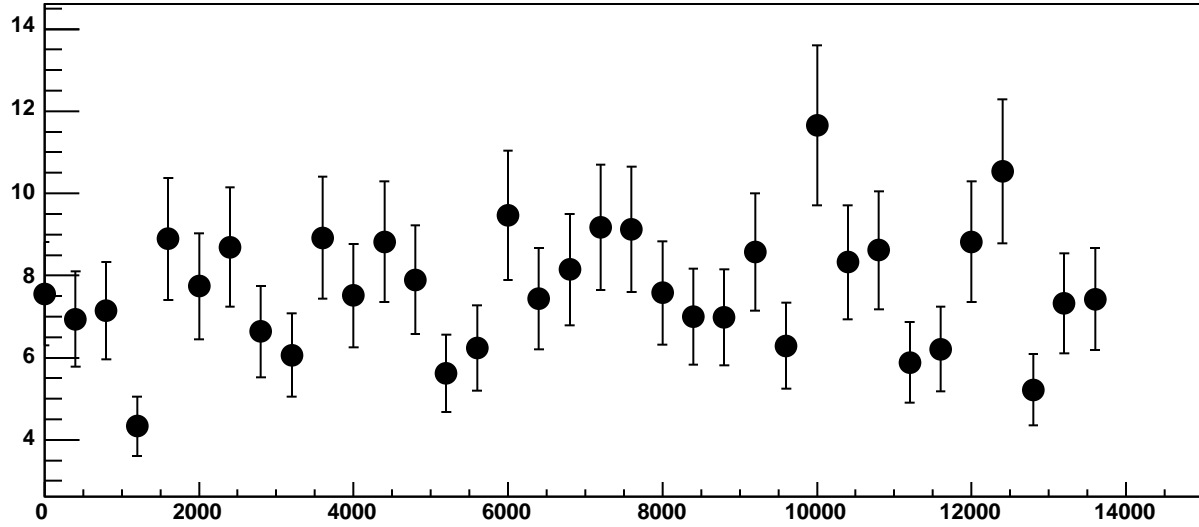
Chip 9, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC



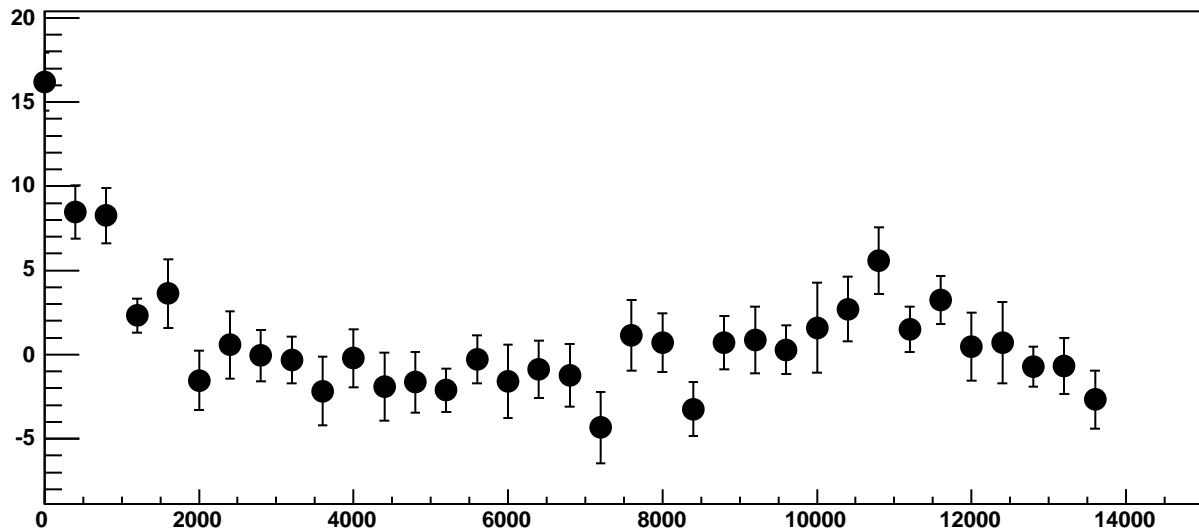
Chip 9, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



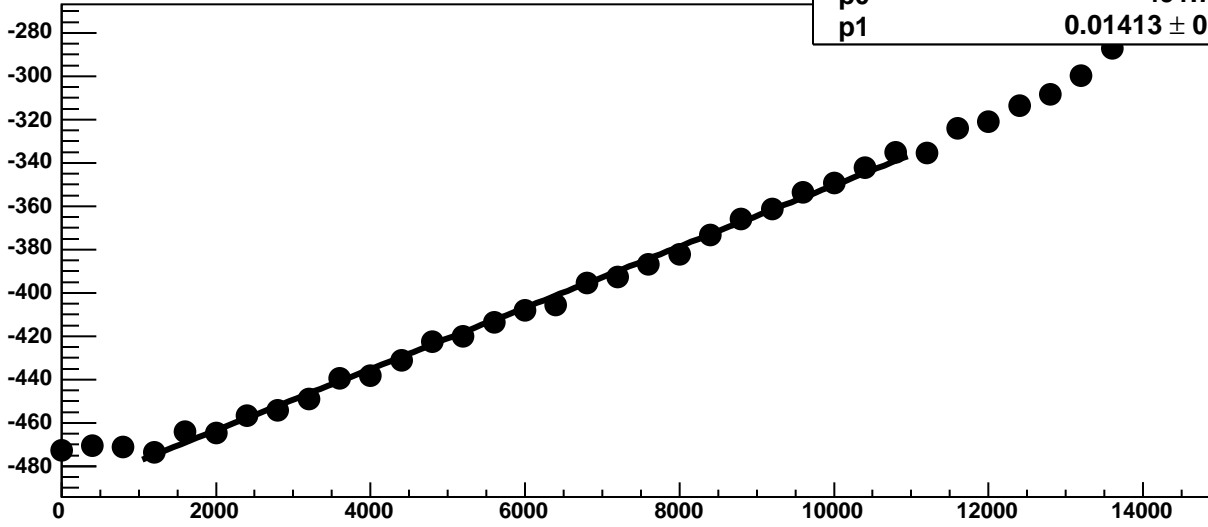
Chip 9, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

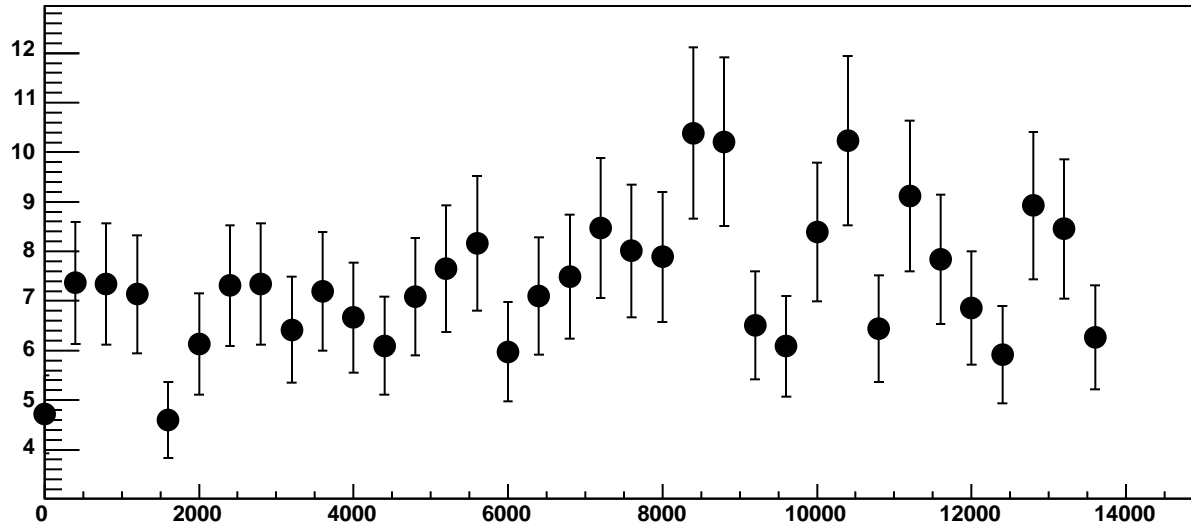


Chip 9, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

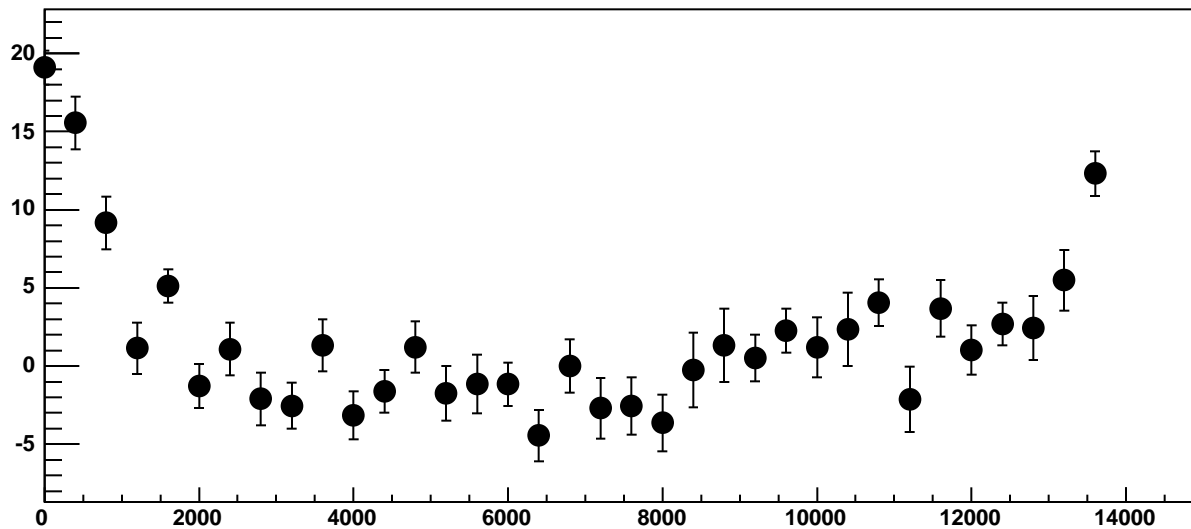


χ^2 / ndf 66.15 / 23
p0 -491.7 ± 0.681
p1 0.01413 ± 0.000109

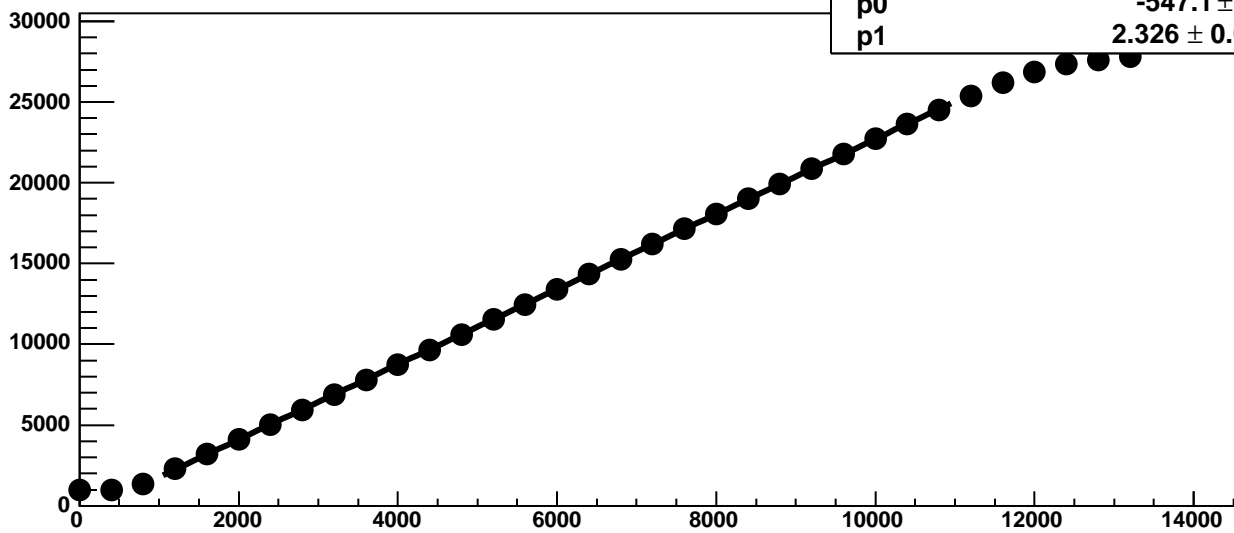
Chip 9, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



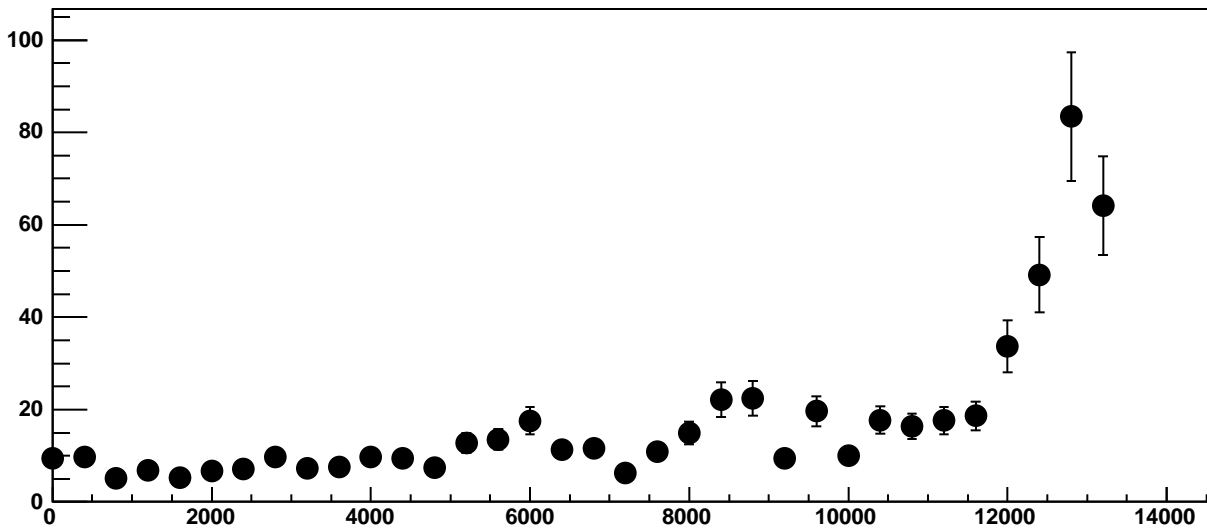
Chip 9, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



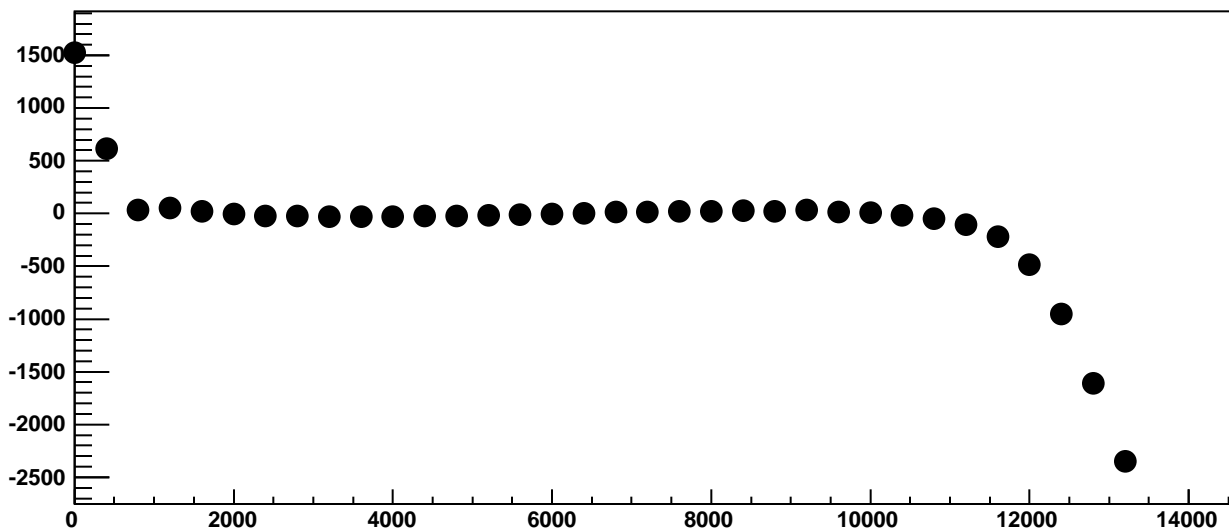
Chip 9, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC



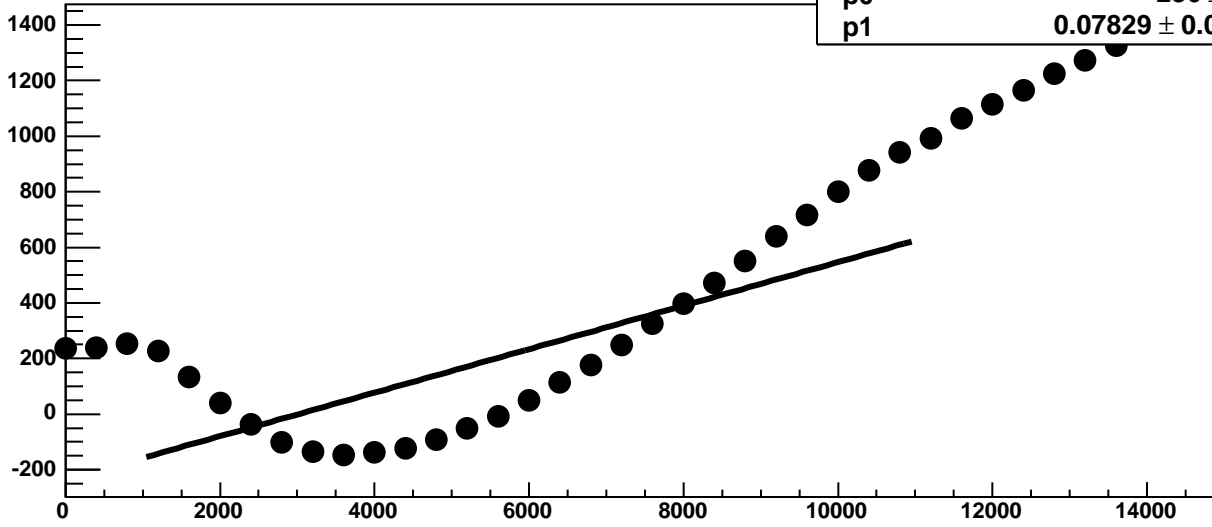
Chip 9, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC



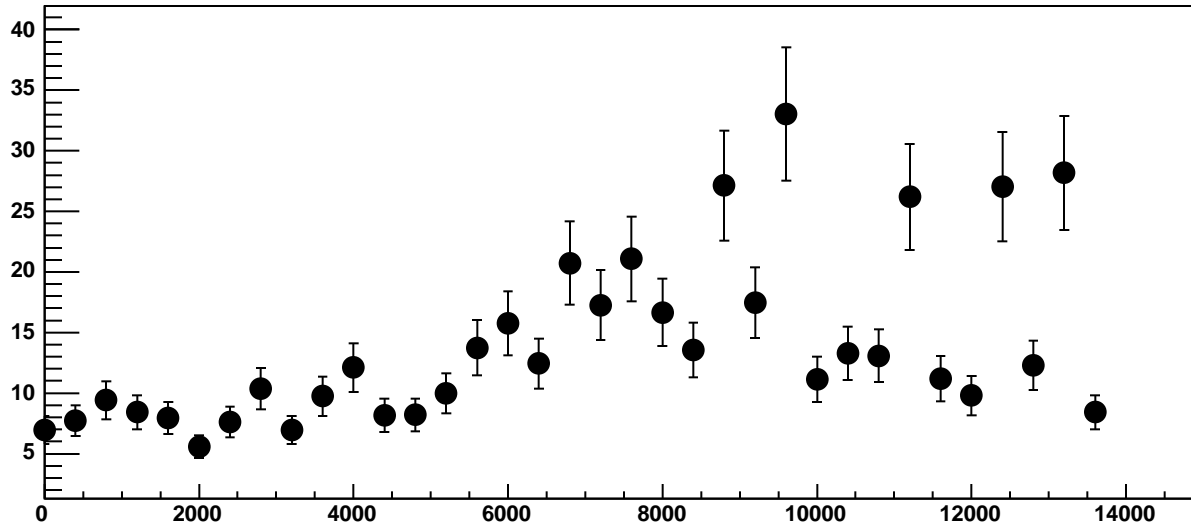
Chip 9, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC



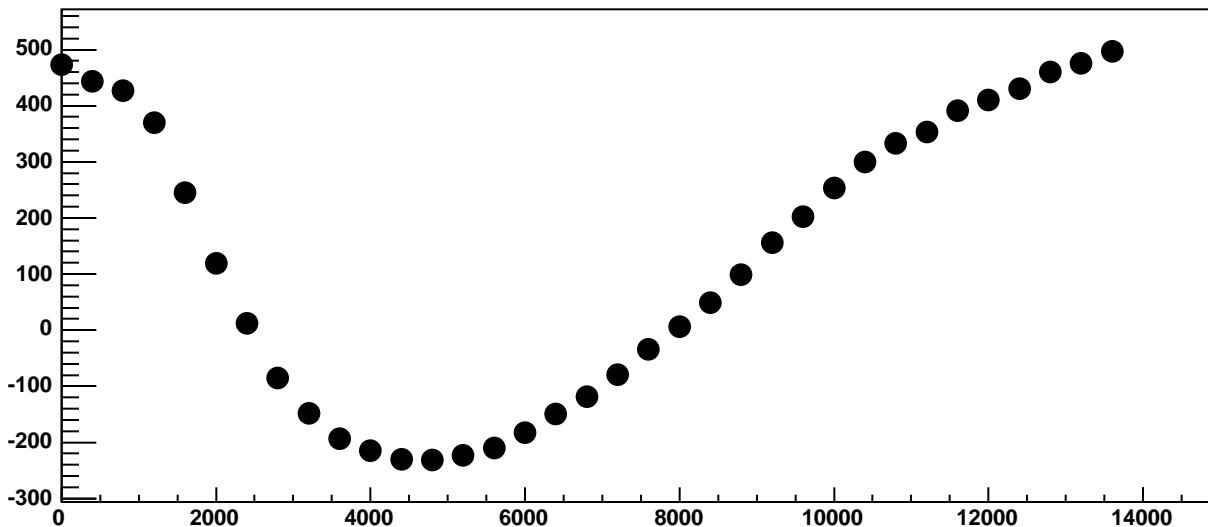
Chip 10, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



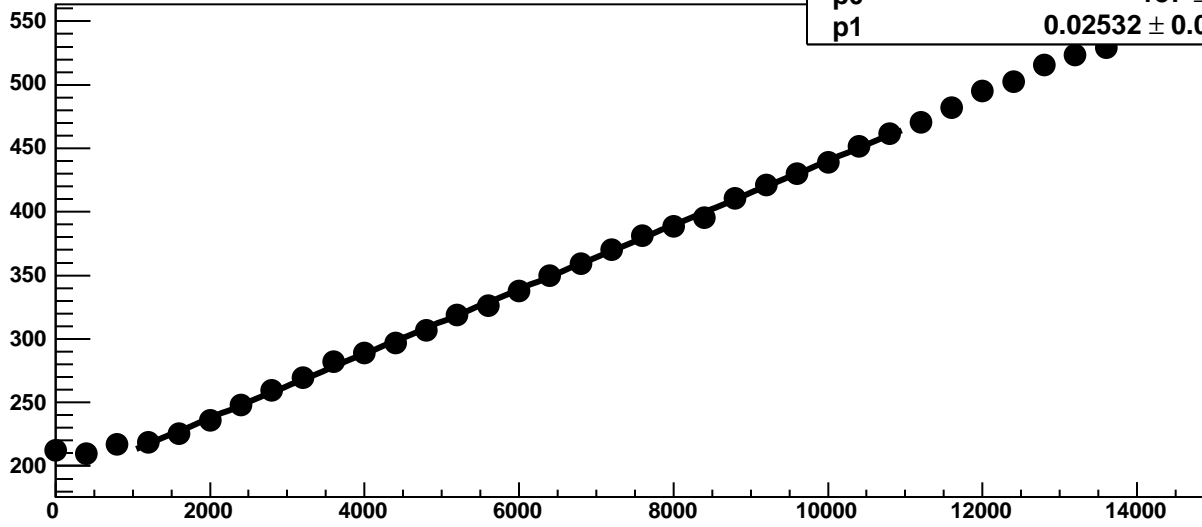
Chip 10, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

24.08 / 23

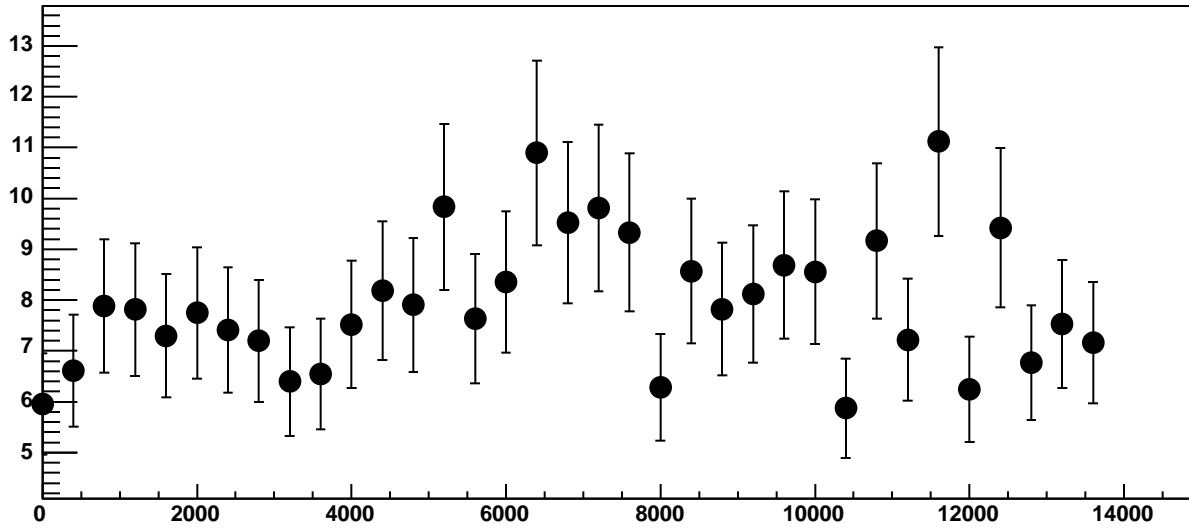
p0

187 ± 0.7888

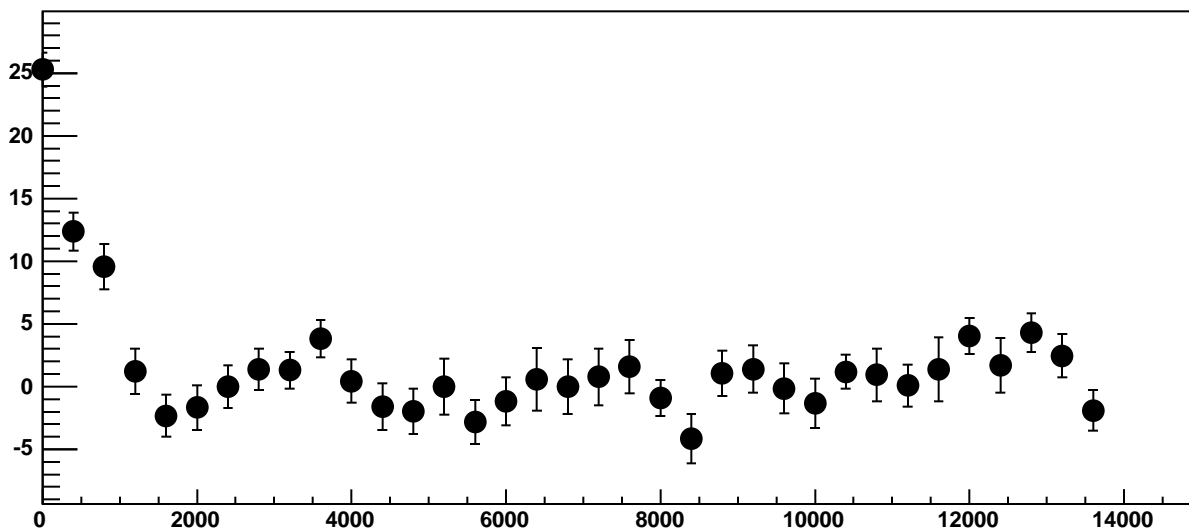
p1

0.02532 ± 0.0001202

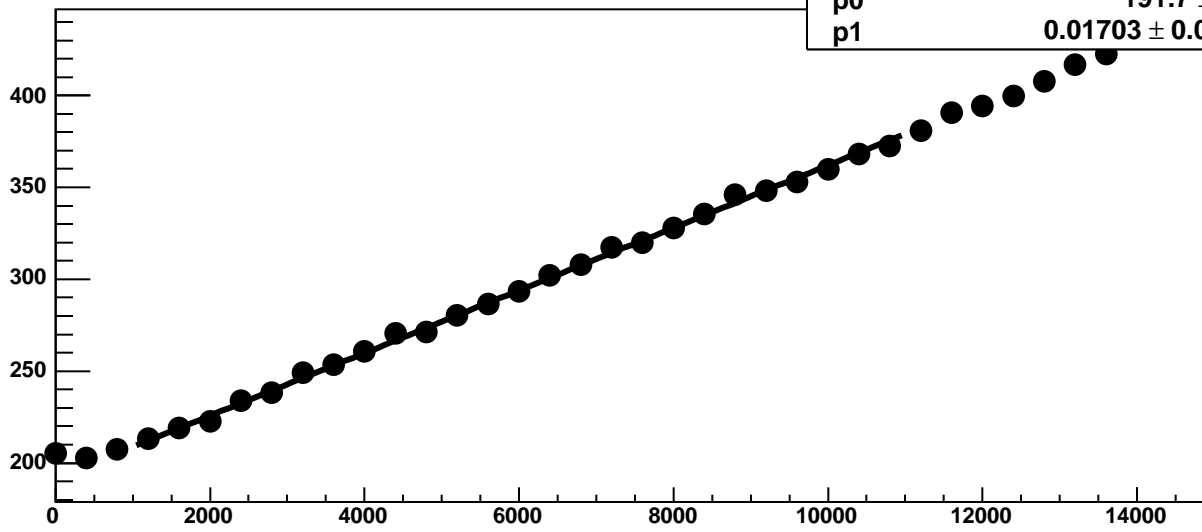
Chip 10, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

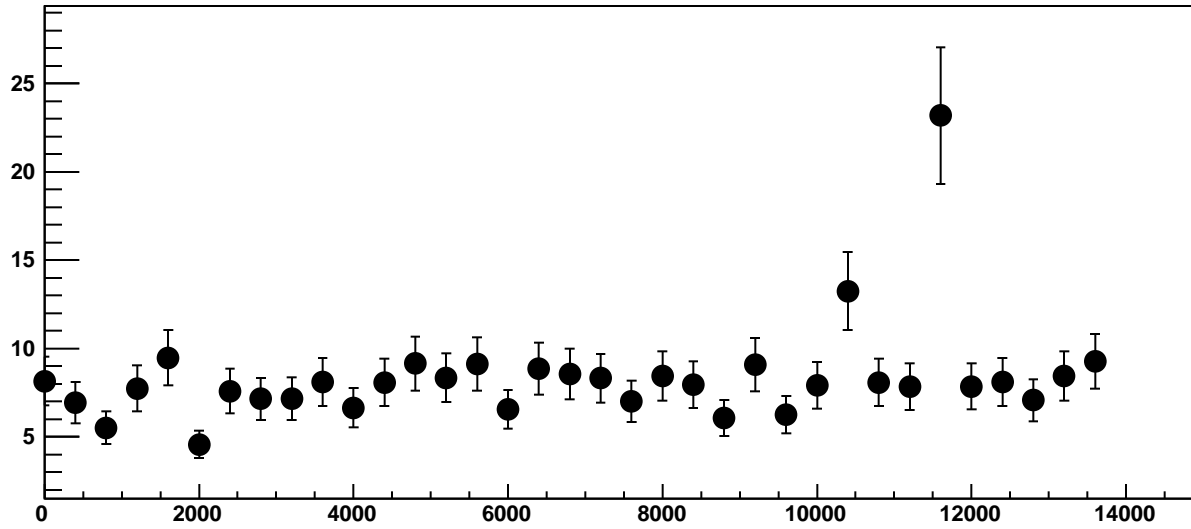


Chip 10, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

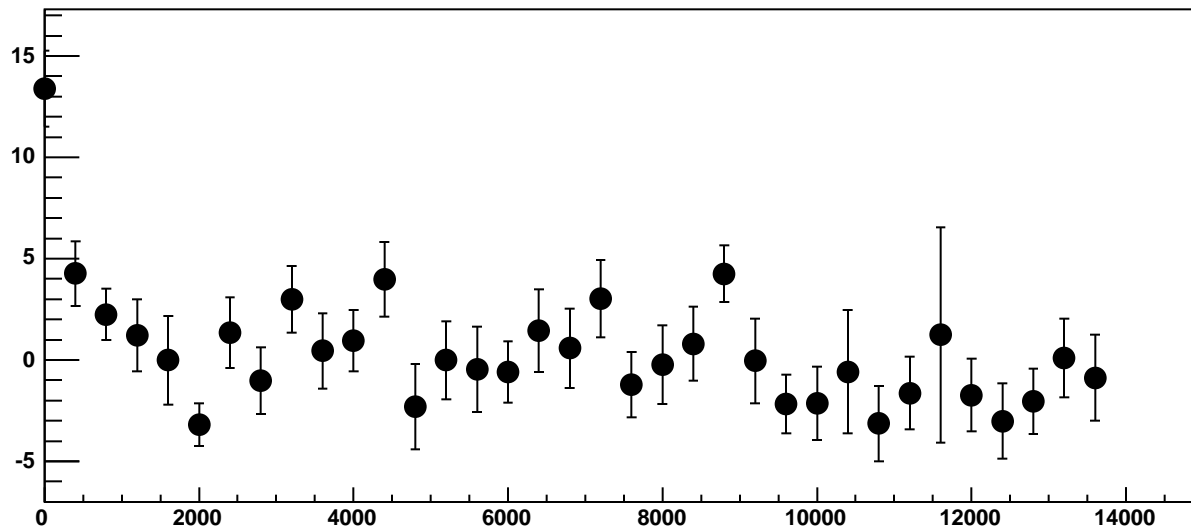


χ^2 / ndf 40.2 / 23
p0 191.7 ± 0.7571
p1 0.01703 ± 0.0001184

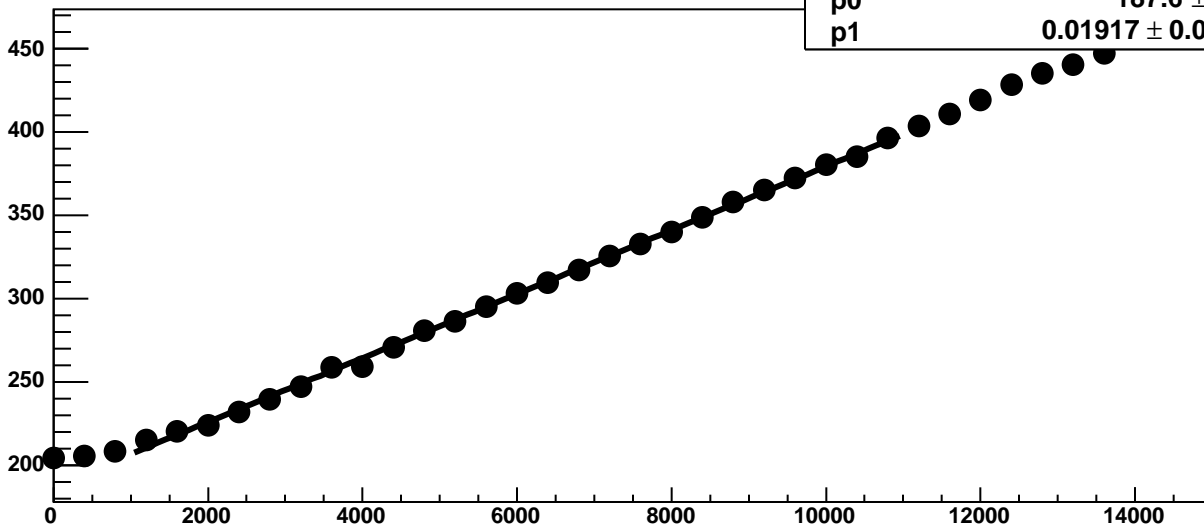
Chip 10, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

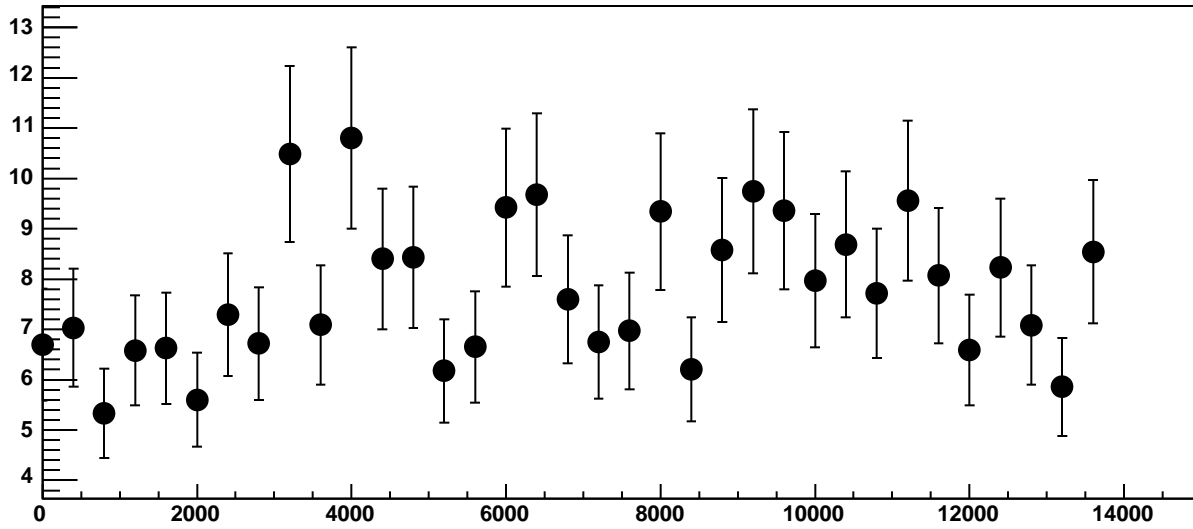


Chip 10, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

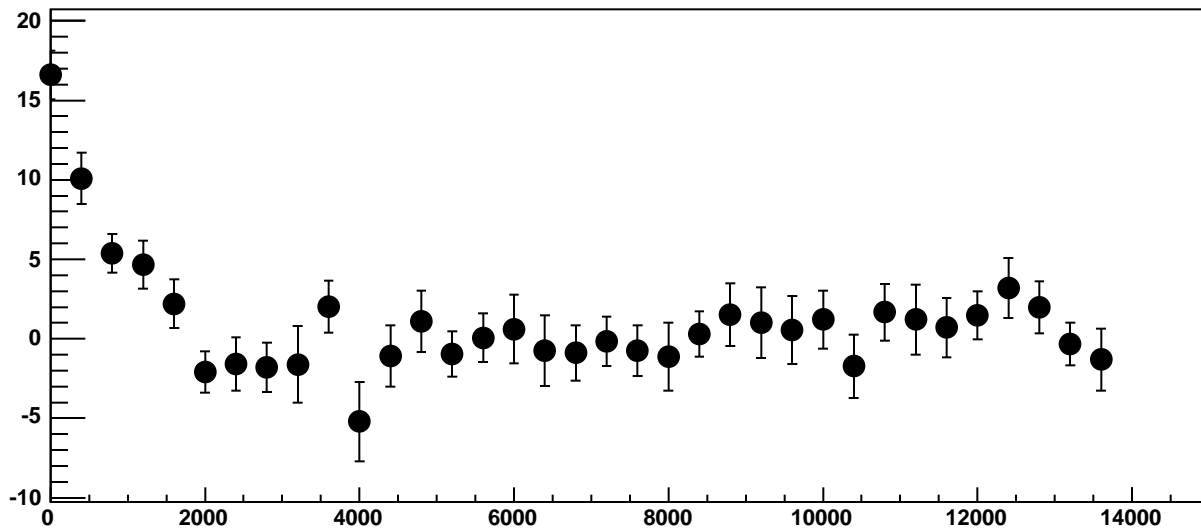


χ^2 / ndf 28.03 / 23
p0 187.6 ± 0.7533
p1 0.01917 ± 0.0001186

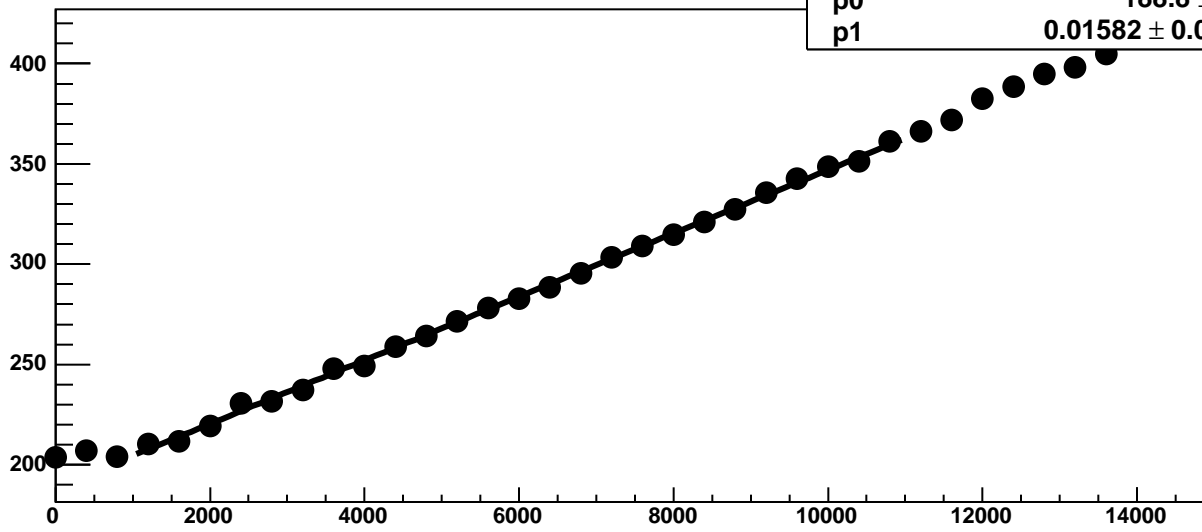
Chip 10, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC

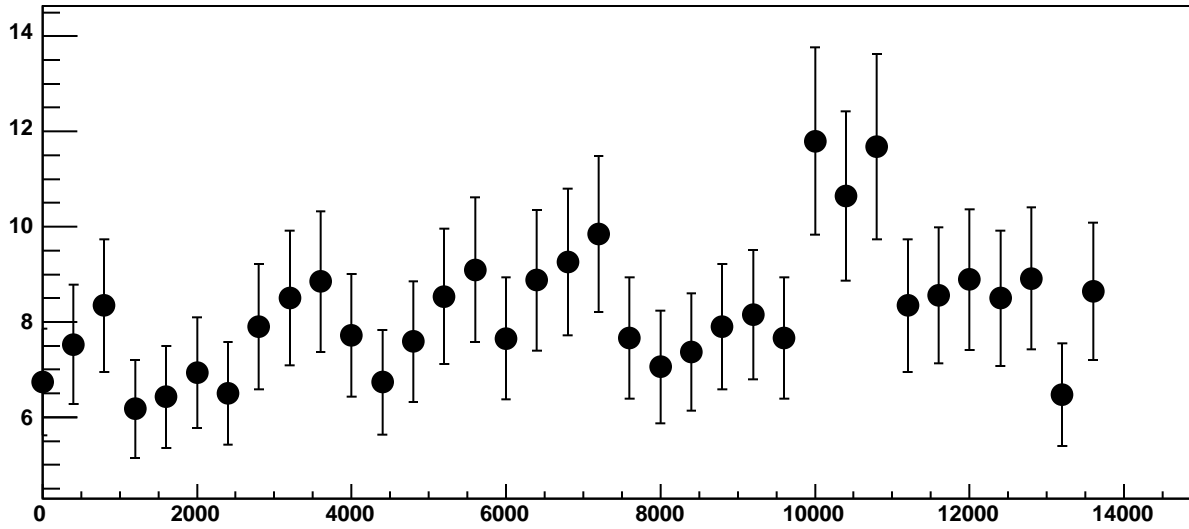


Chip 10, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

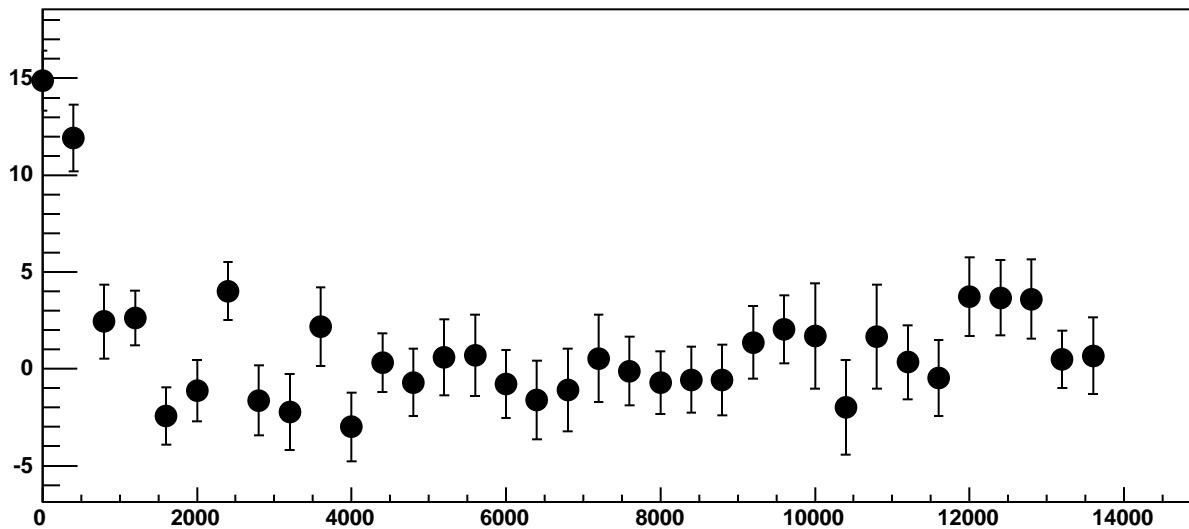


χ^2 / ndf 25.3 / 23
p0 188.8 ± 0.7751
p1 0.01582 ± 0.0001264

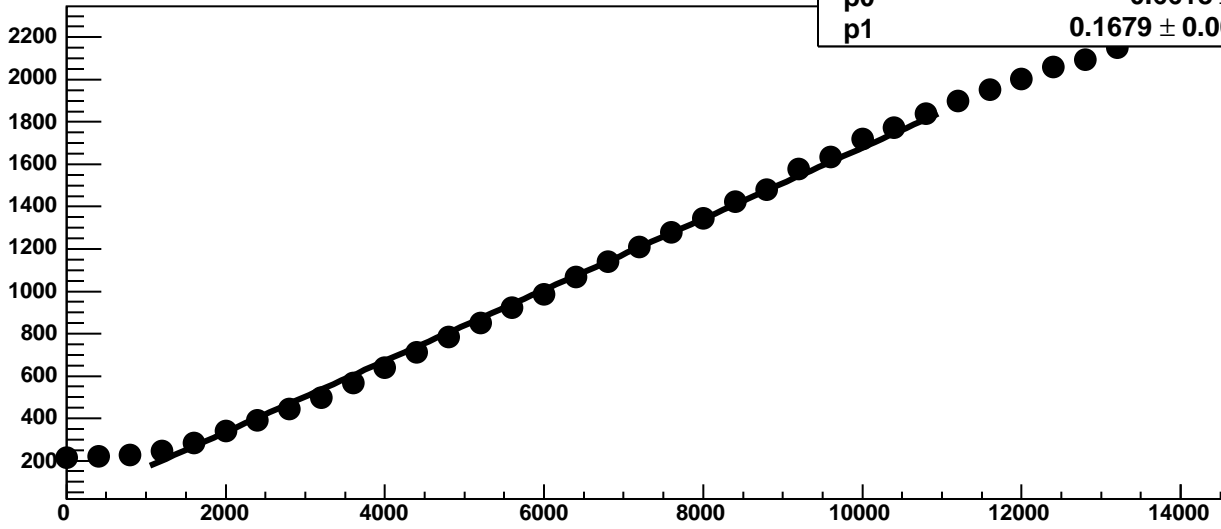
Chip 10, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 10, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

2508 / 23

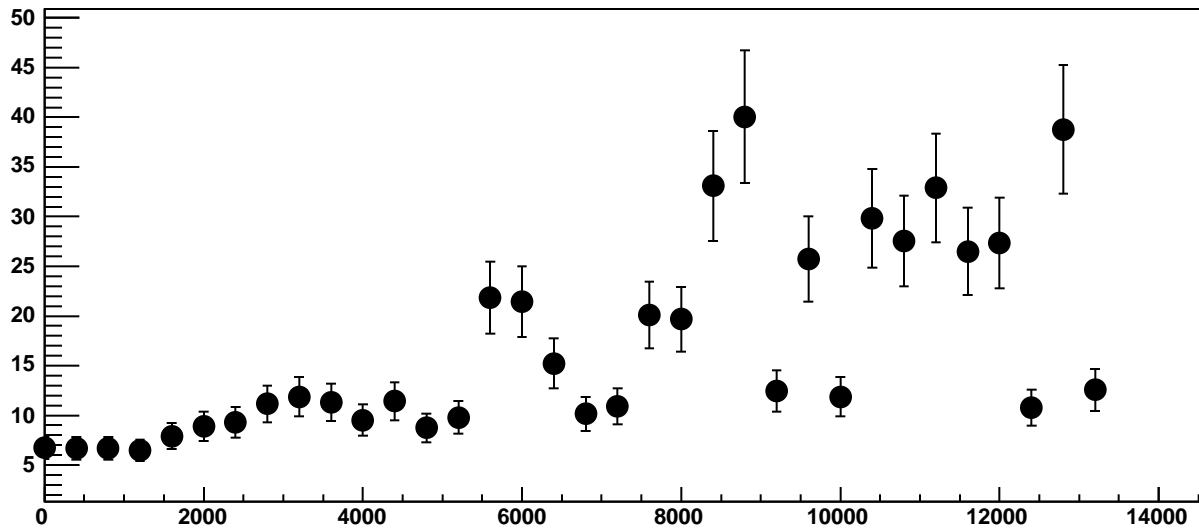
p0

-0.6618 ± 1.033

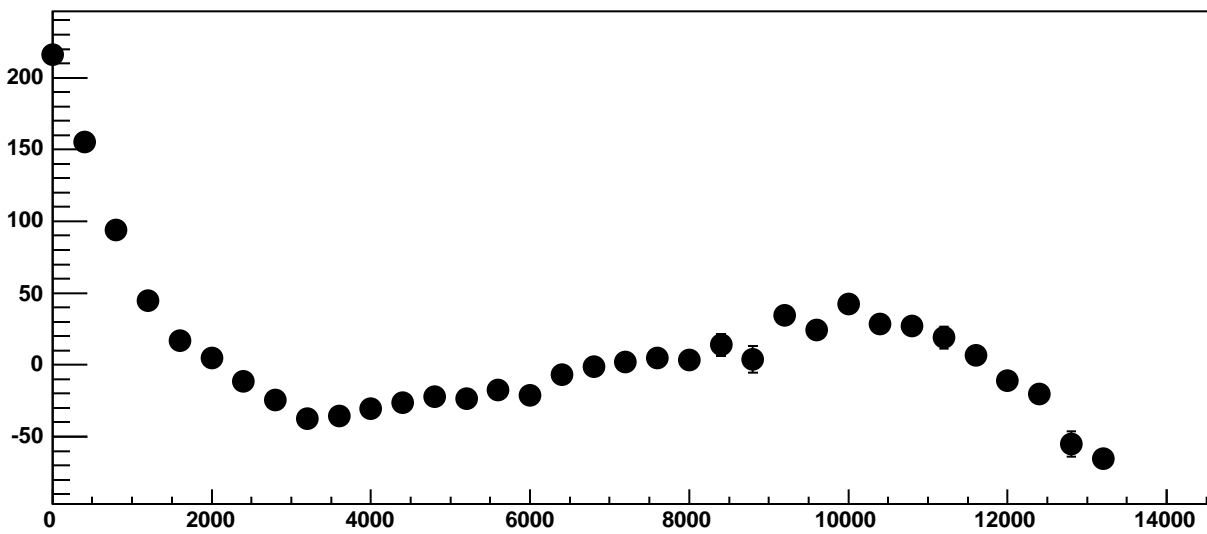
p1

0.1679 ± 0.0002034

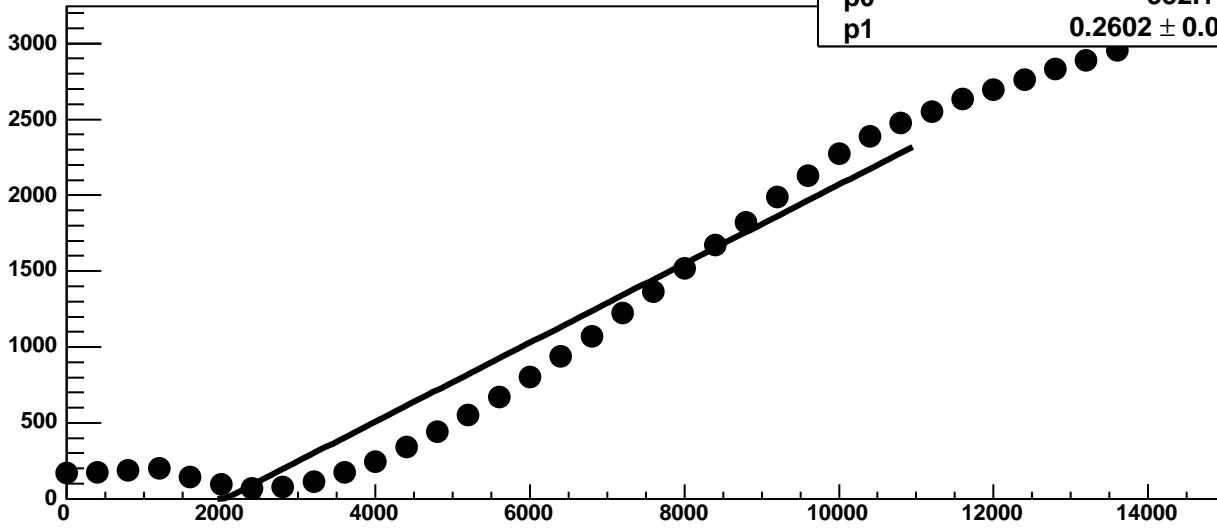
Chip 10, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

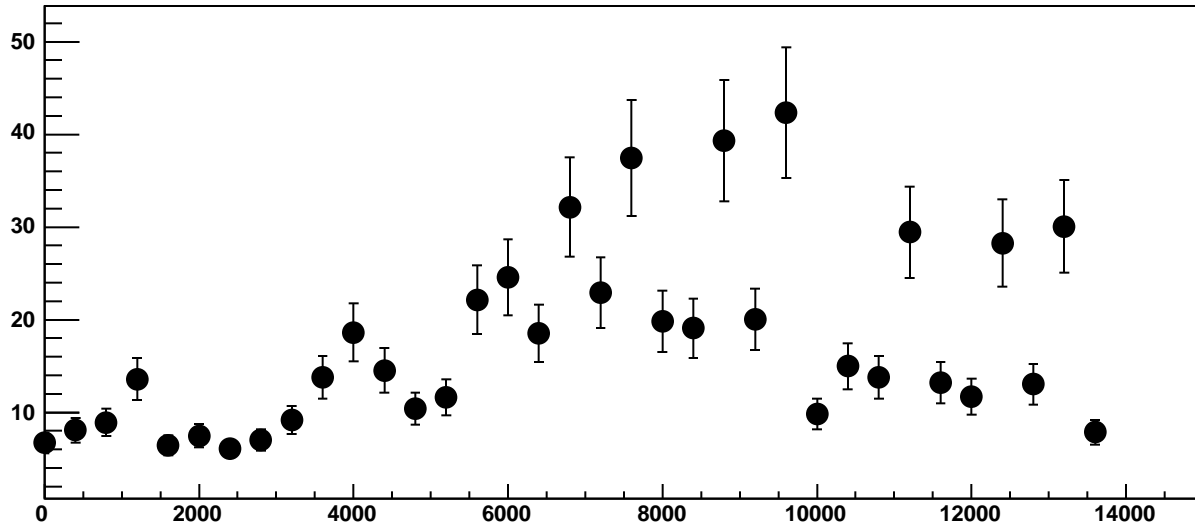


Chip 10, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC

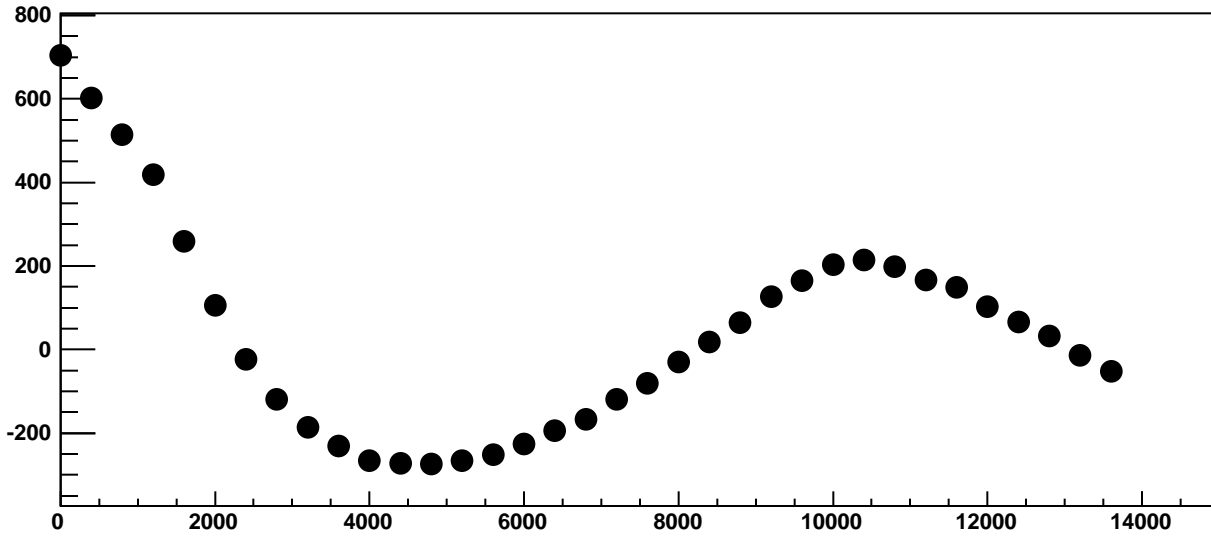


χ^2 / ndf 1.296e+05 / 23
p0 -532.1 ± 0.963
p1 0.2602 ± 0.0001917

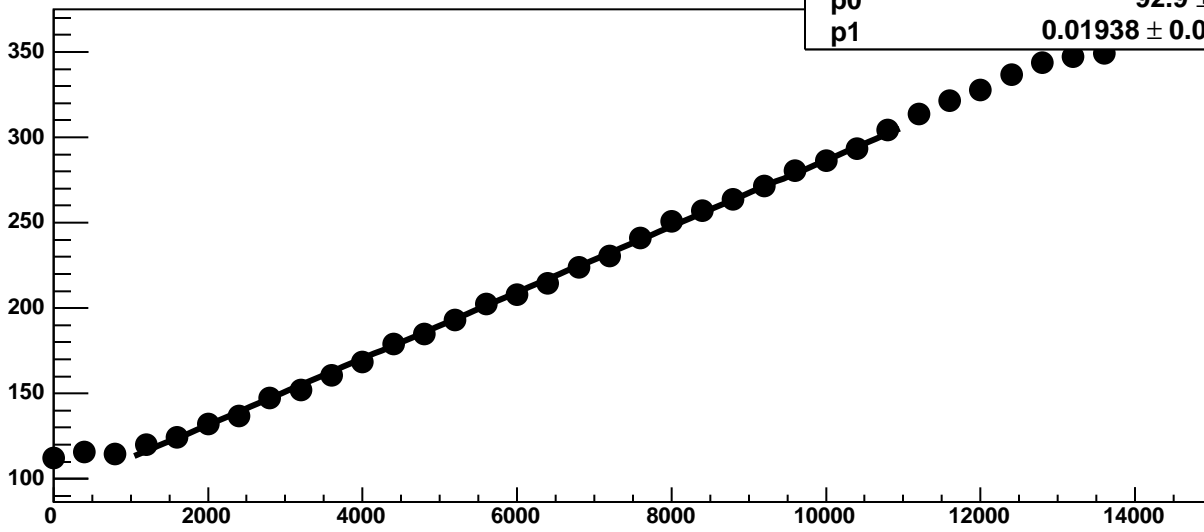
Chip 10, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC

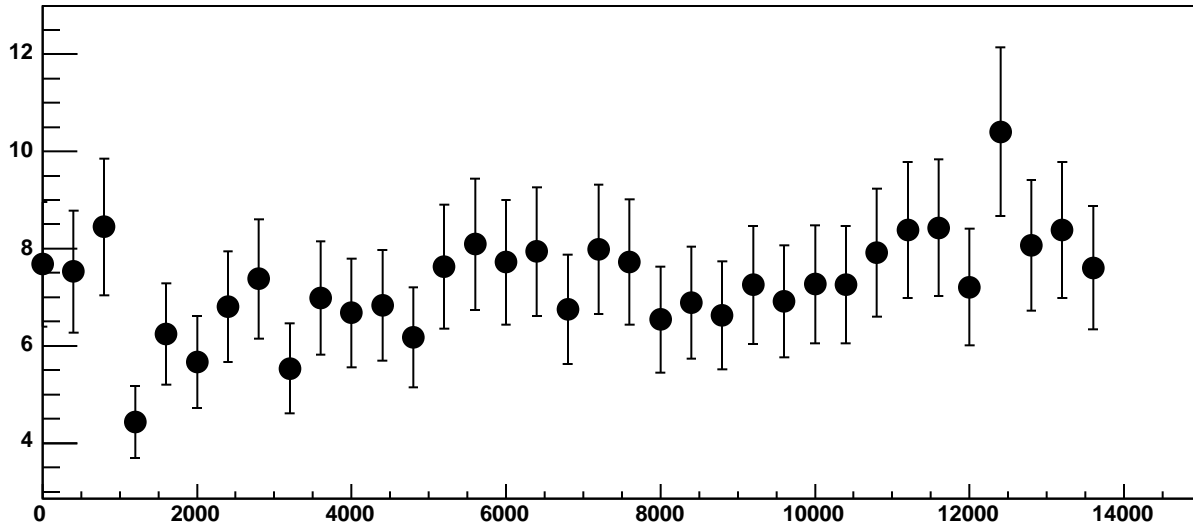


Chip 10, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

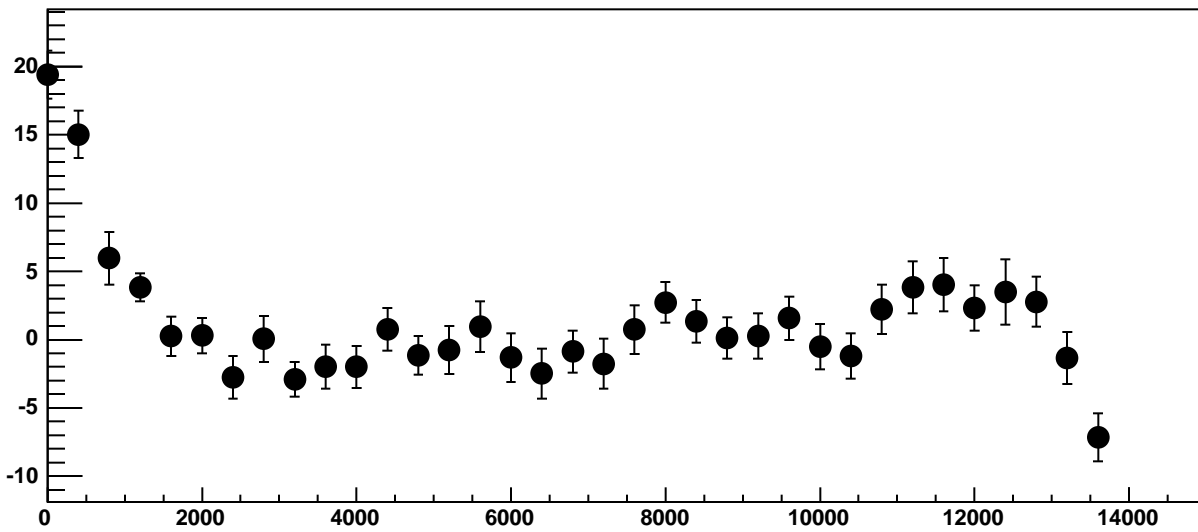


χ^2 / ndf 38.25 / 23
p0 92.9 ± 0.6401
p1 0.01938 ± 0.0001023

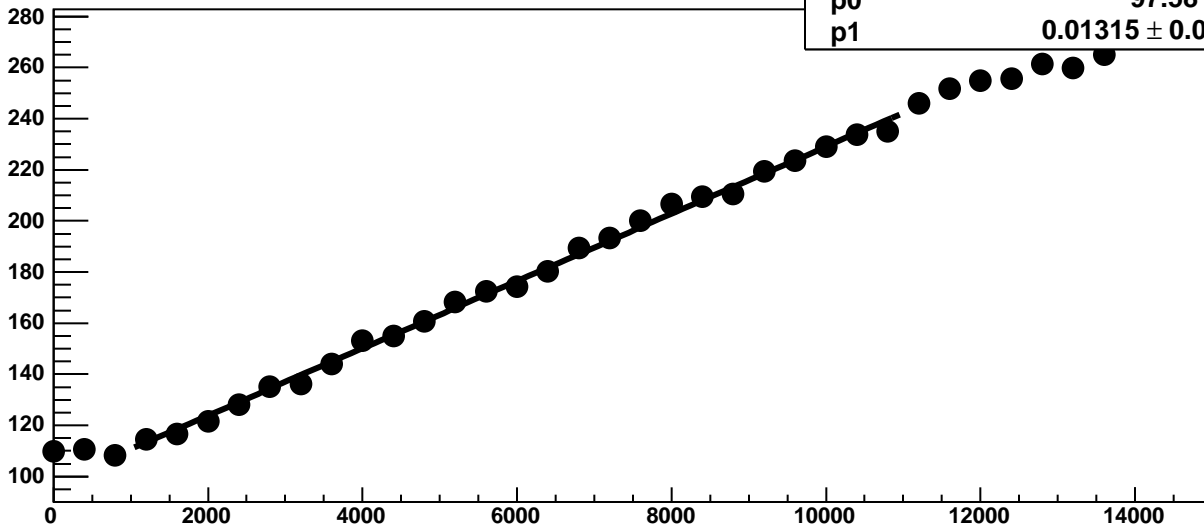
Chip 10, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

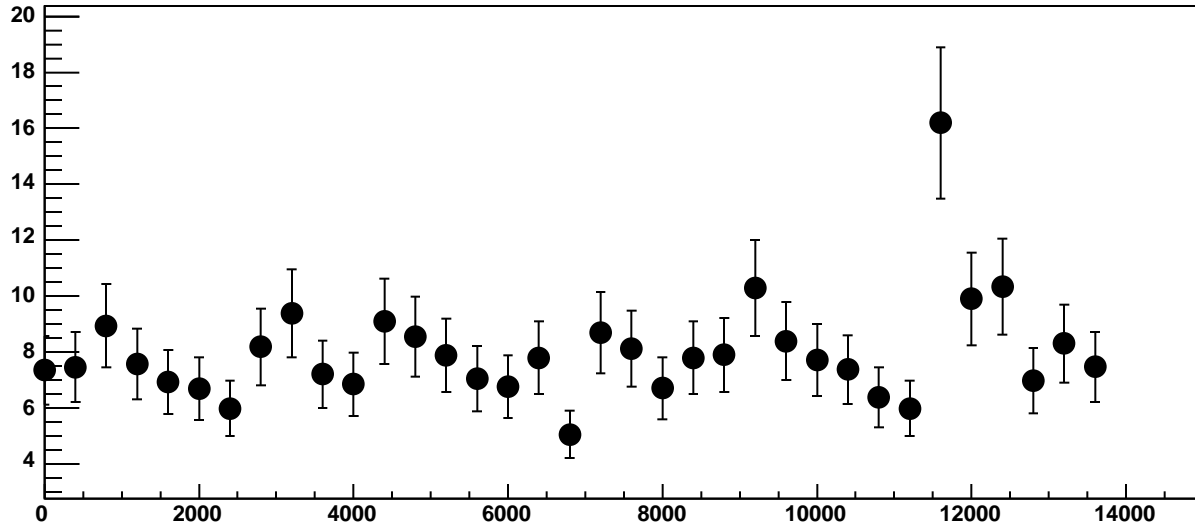


Chip 10, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

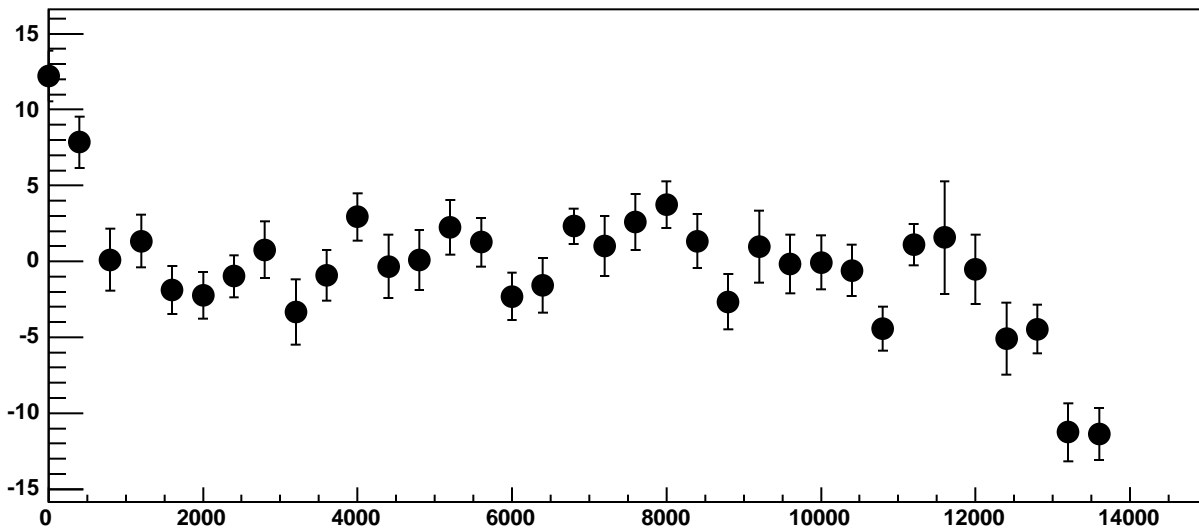


χ^2 / ndf 40.54 / 23
p0 97.58 ± 0.767
p1 0.01315 ± 0.0001163

Chip 10, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

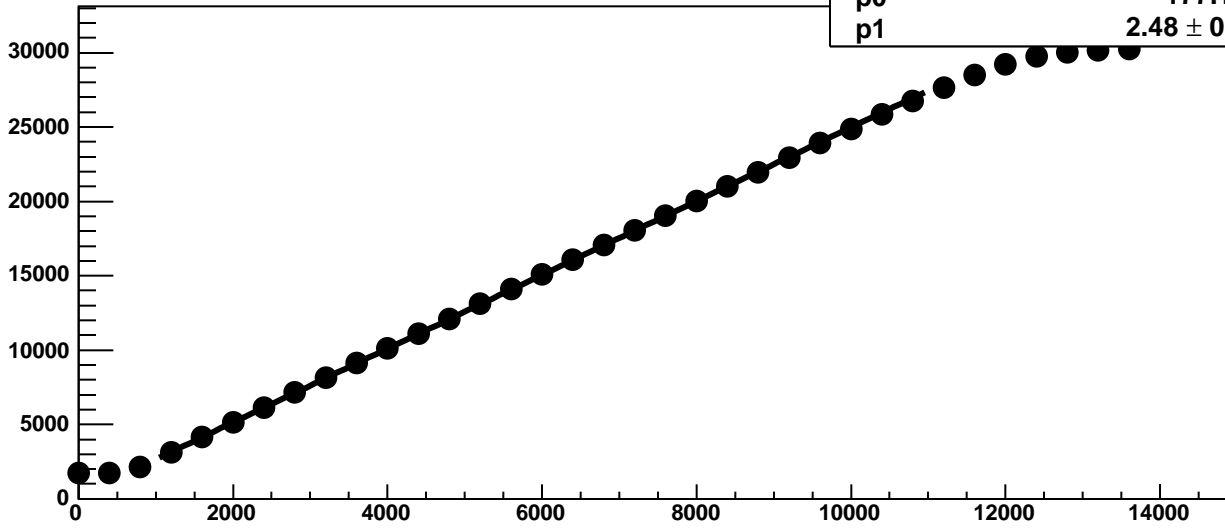


Chip 10, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

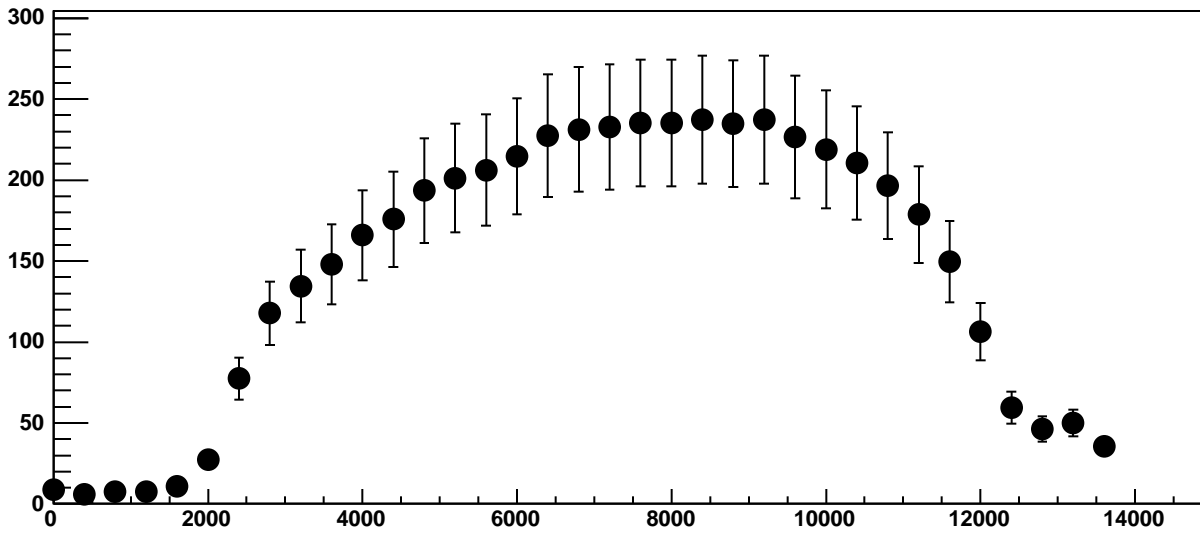


Chip 10, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC

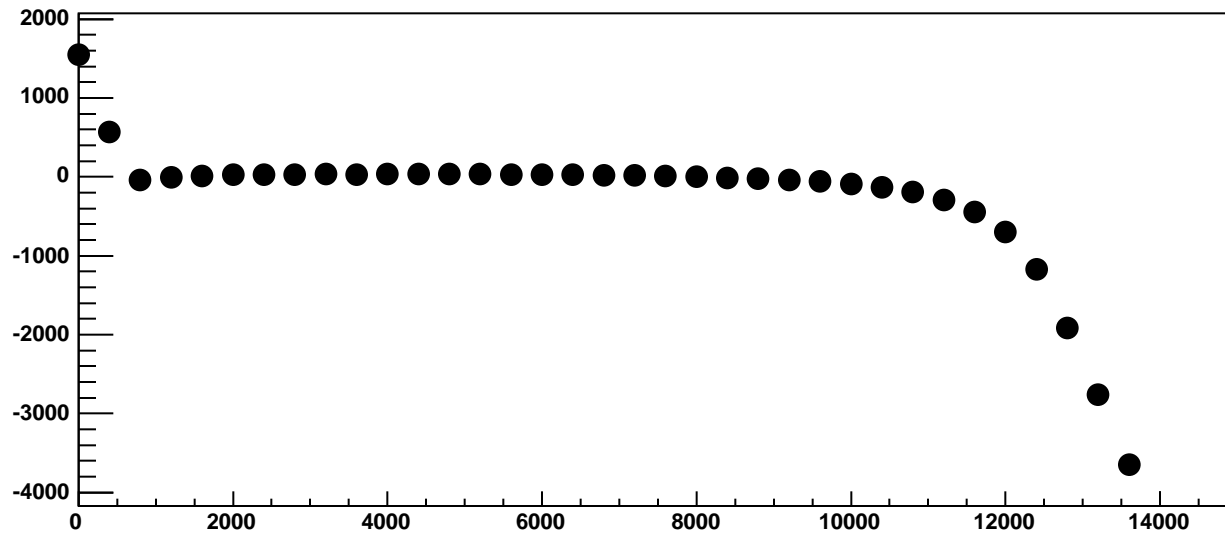
χ^2 / ndf 105.6 / 23
p0 177.1 ± 2.881
p1 2.48 ± 0.001745



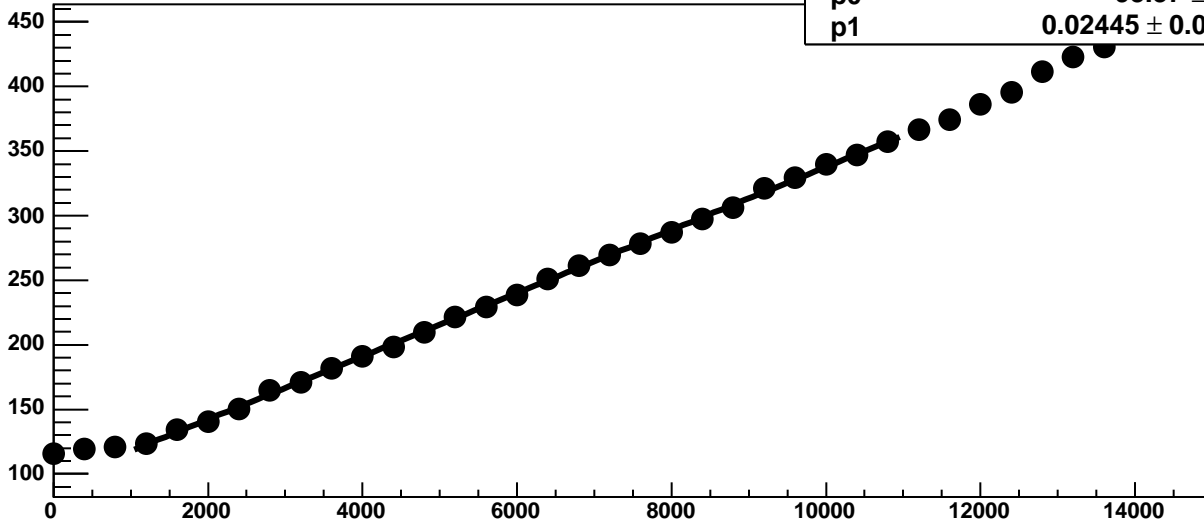
Chip 10, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC

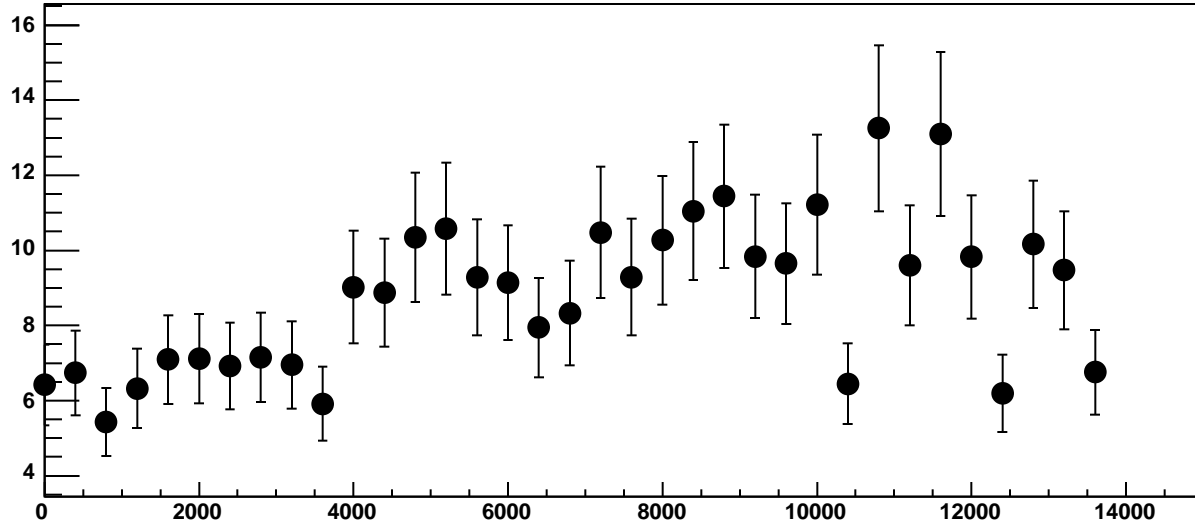


Chip 10, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

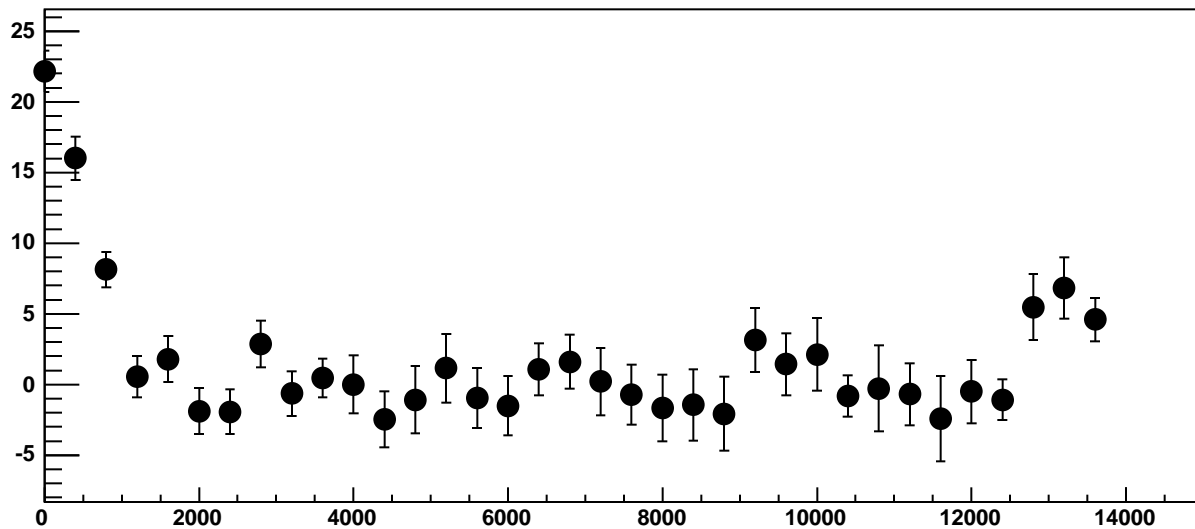


χ^2 / ndf 16.22 / 23
p0 93.37 ± 0.7785
p1 0.02445 ± 0.0001295

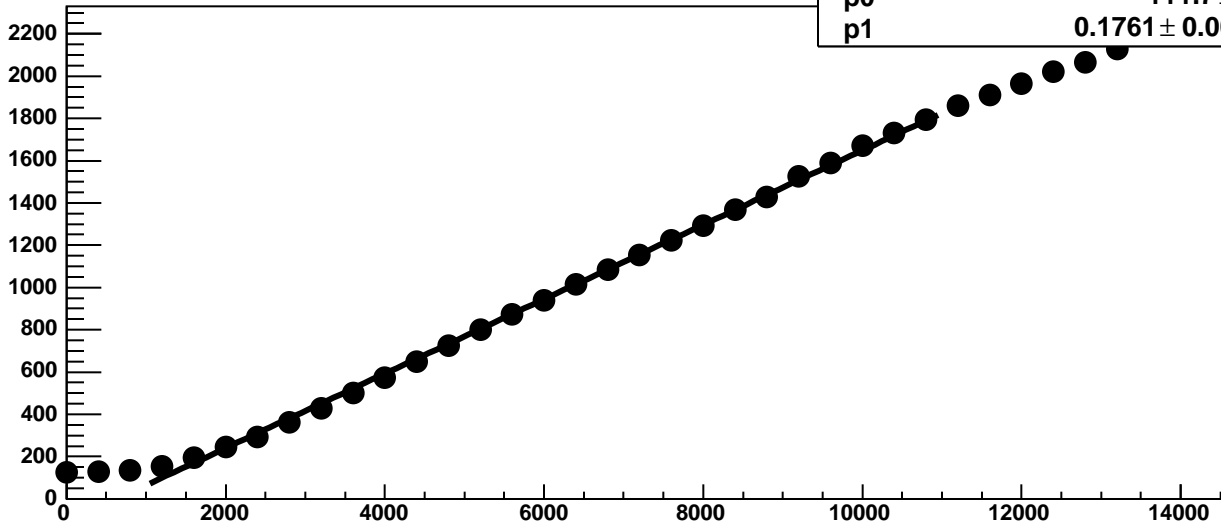
Chip 10, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



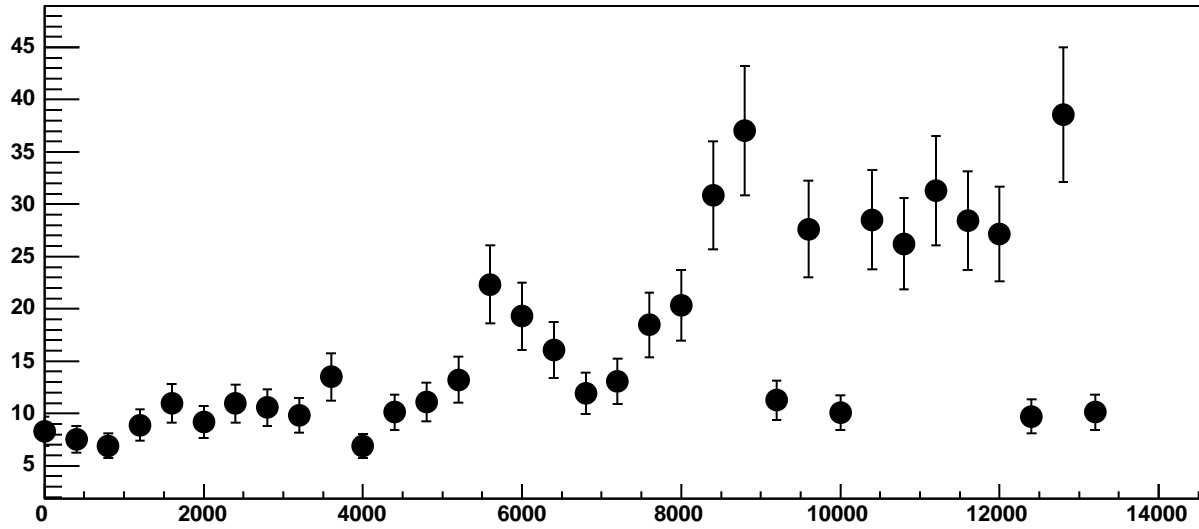
Chip 10, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



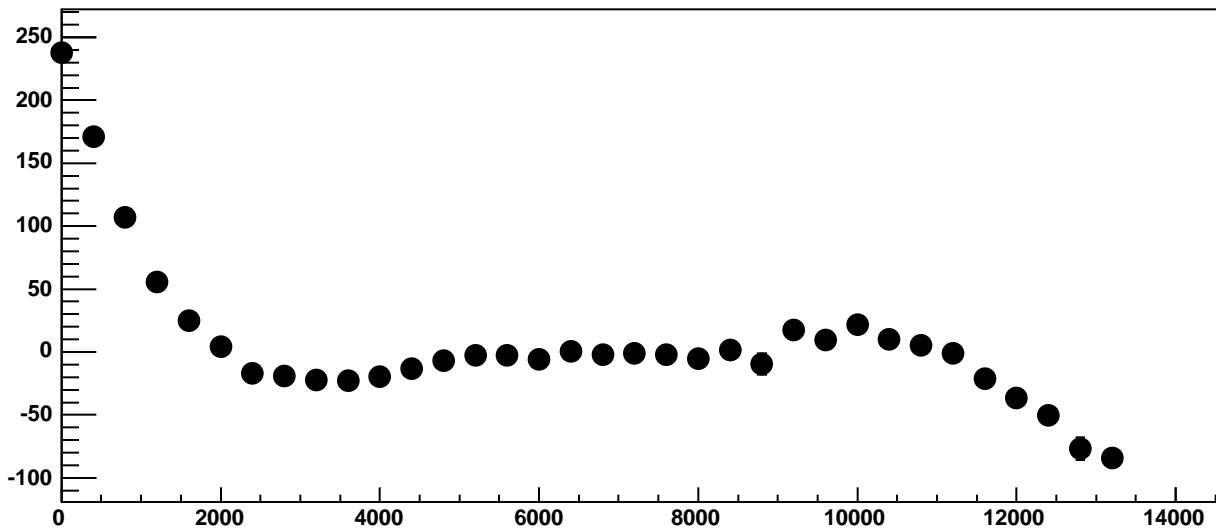
Chip 10, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



Chip 10, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

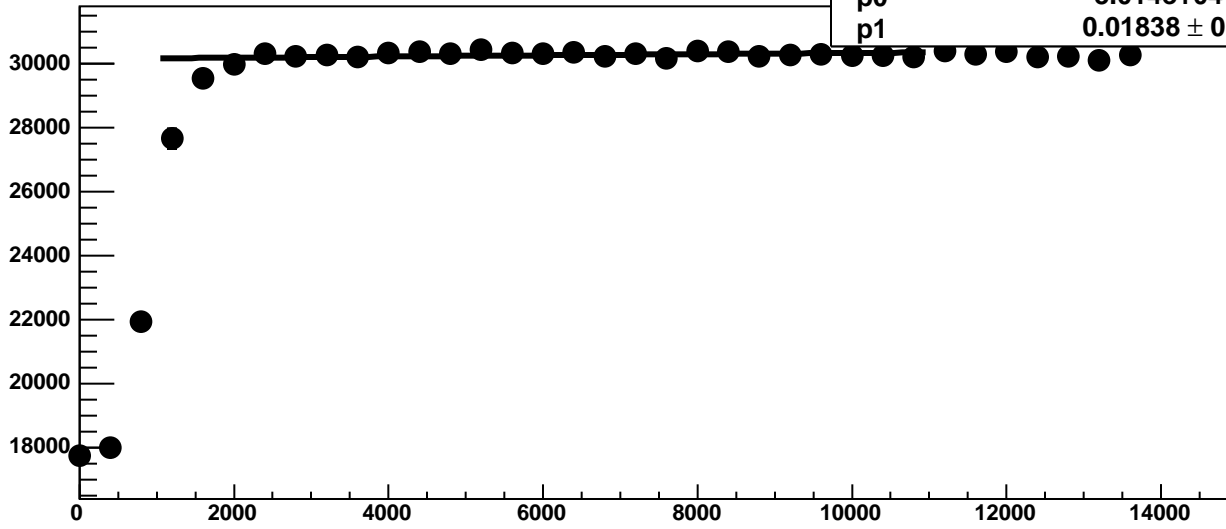


Chip 10, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

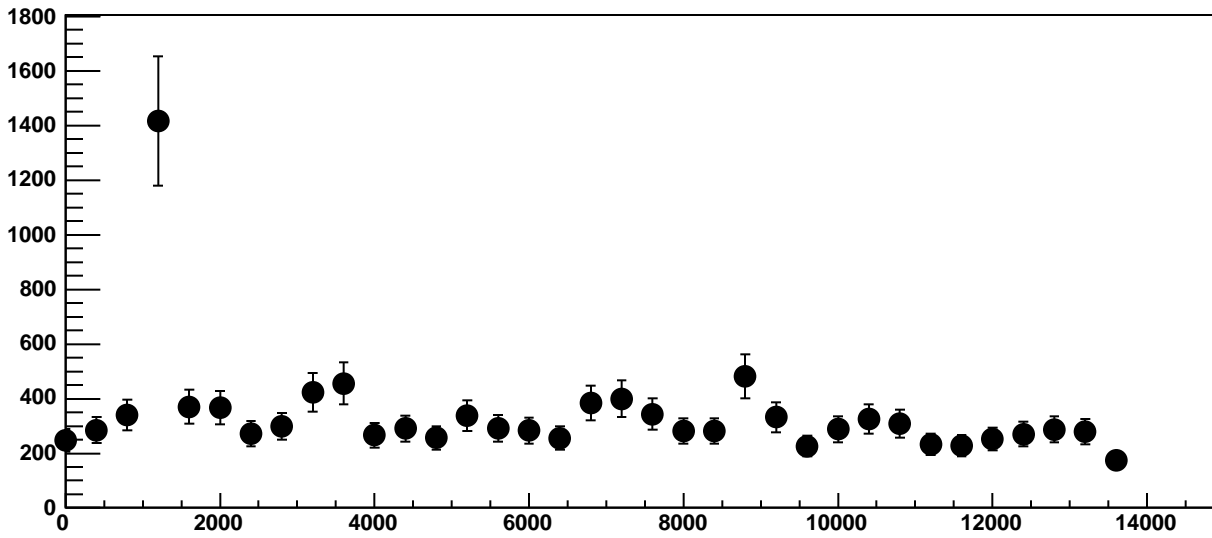


Chip 10, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC

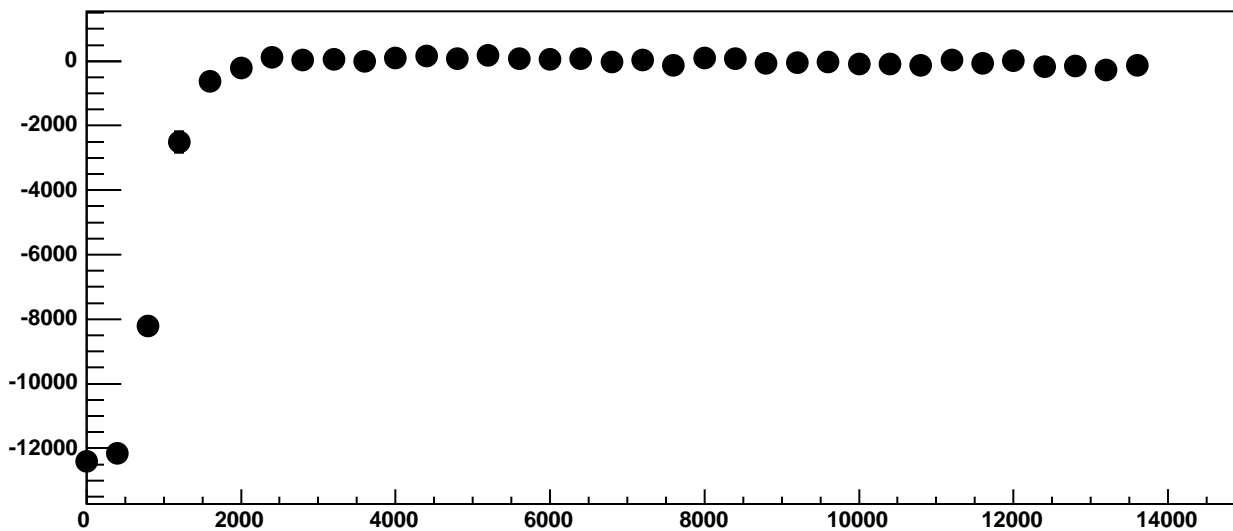
χ^2 / ndf 160.8 / 23
p0 $3.014\text{e}+04 \pm 36.59$
p1 0.01838 ± 0.005294



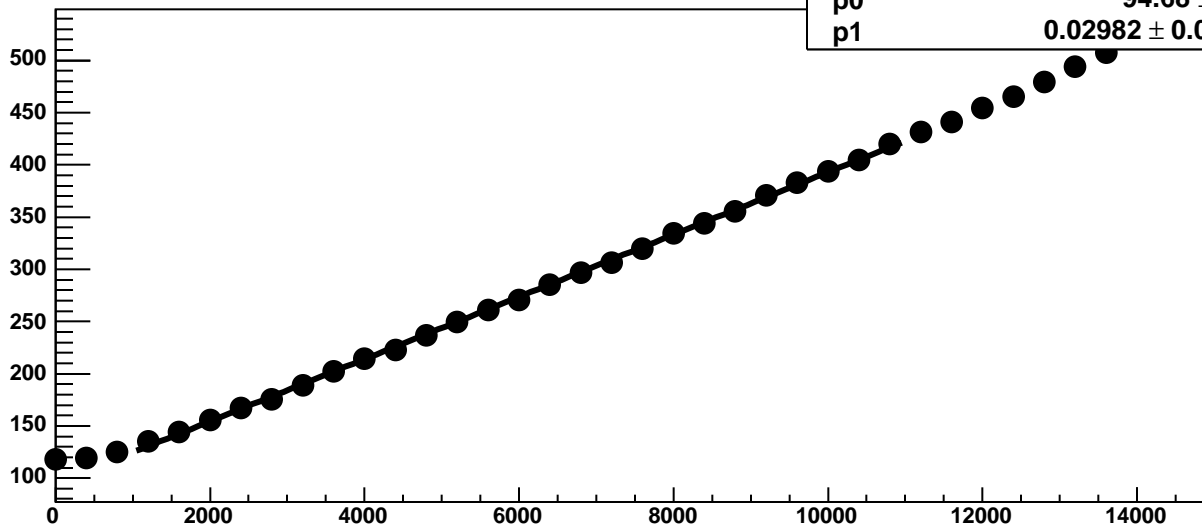
Chip 10, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC

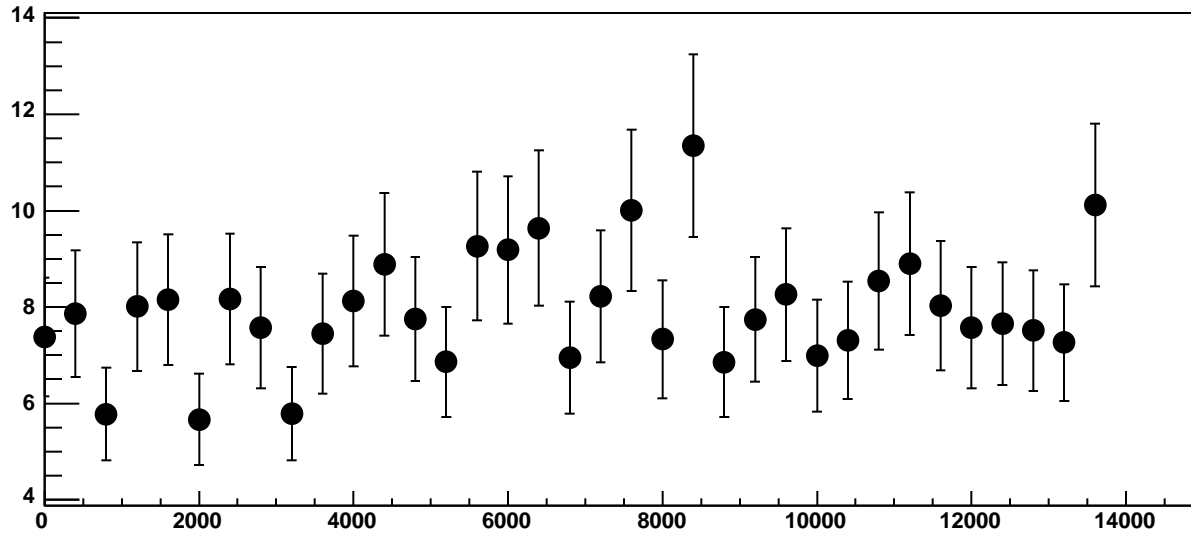


Chip 10, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

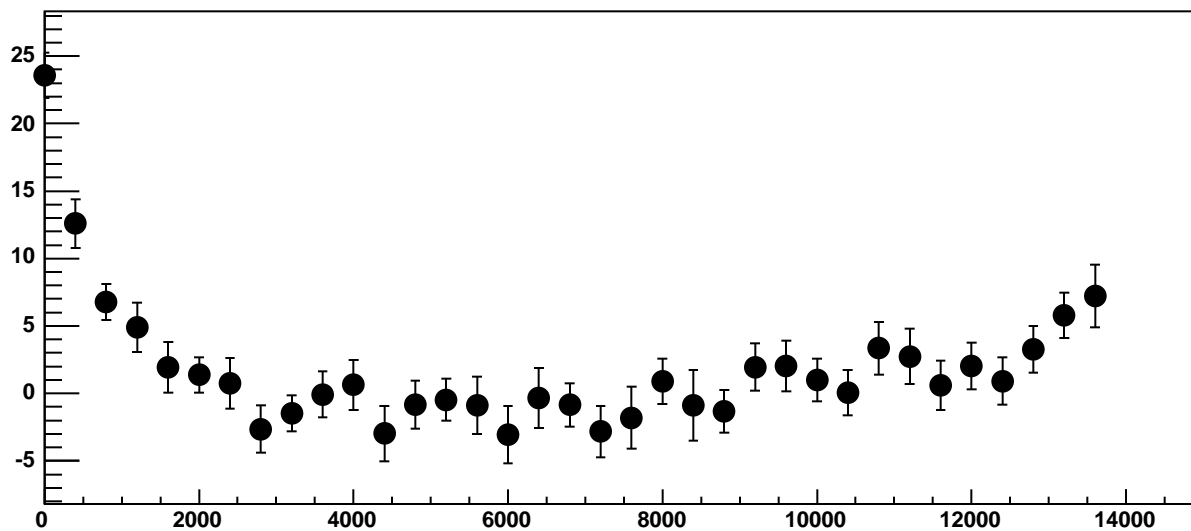


χ^2 / ndf 27.8 / 23
p0 94.68 ± 0.7771
p1 0.02982 ± 0.0001195

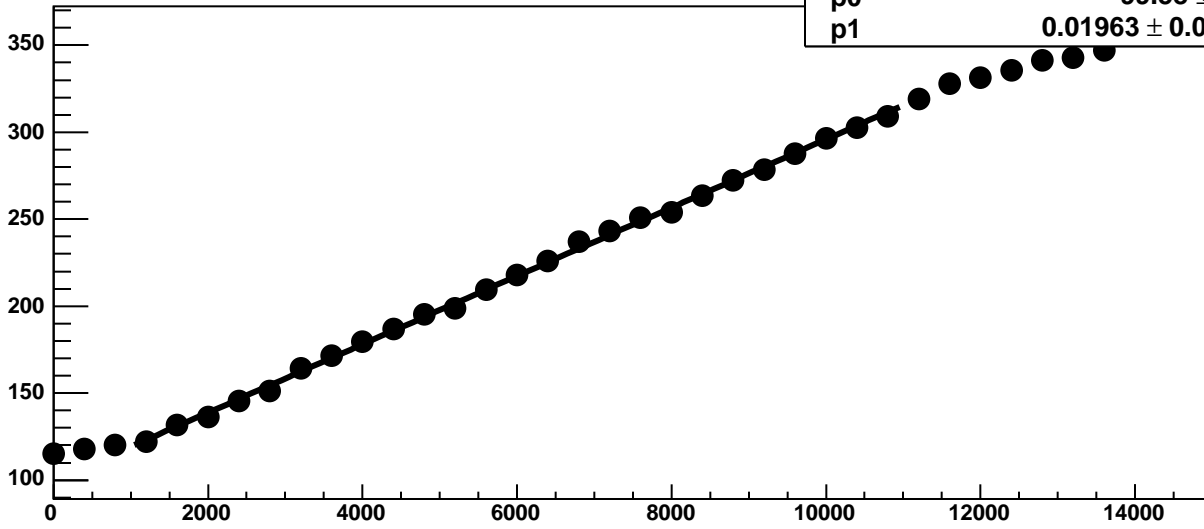
Chip 10, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



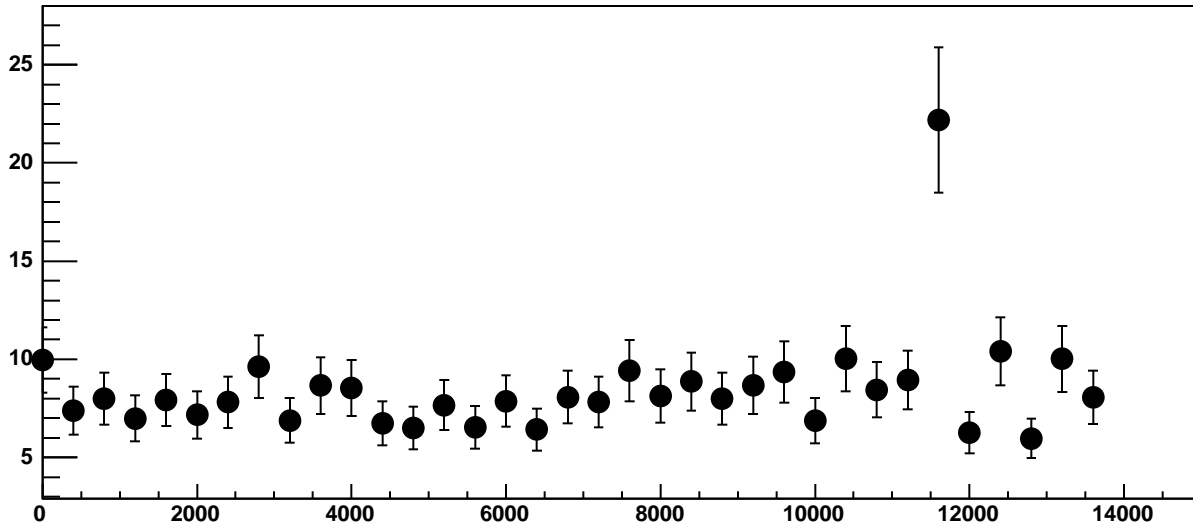
Chip 10, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



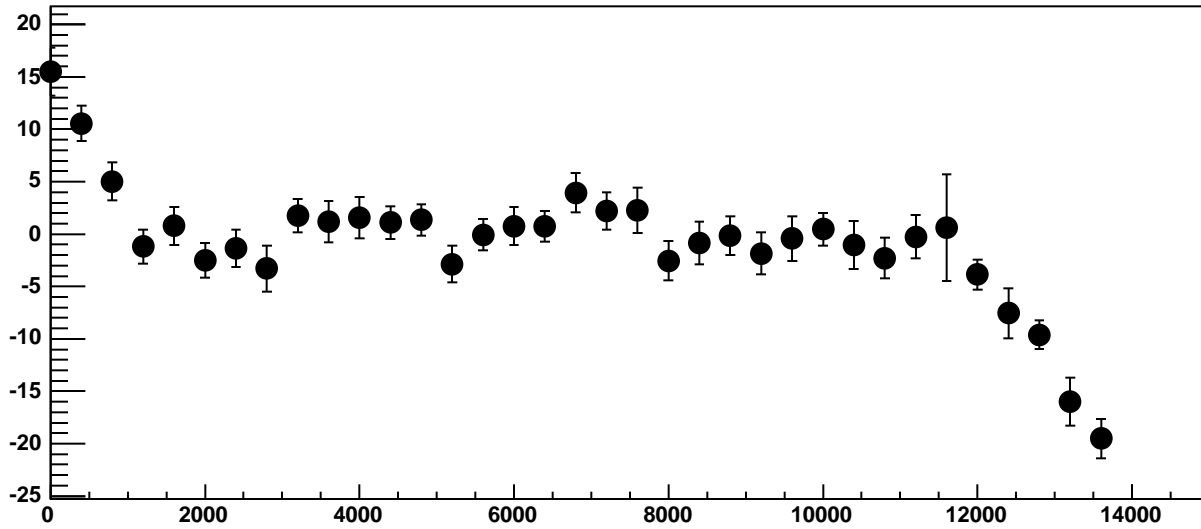
Chip 10, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



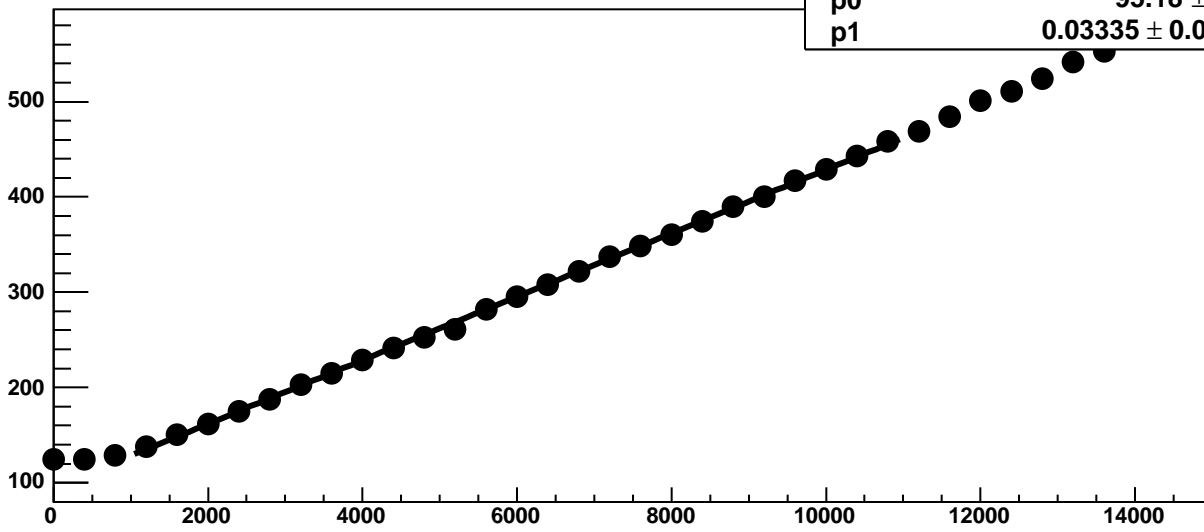
Chip 10, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

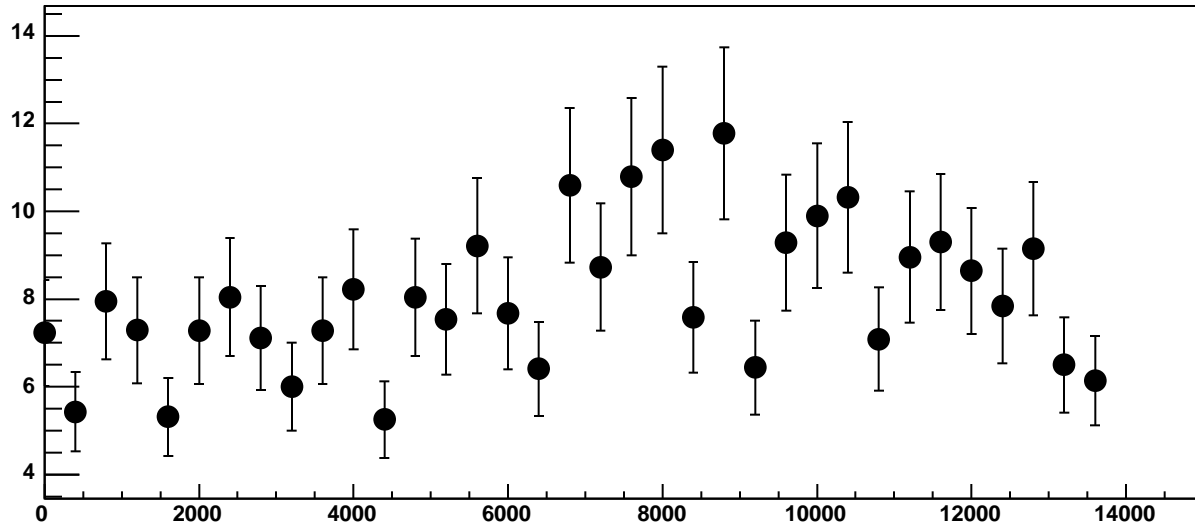


Chip 10, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

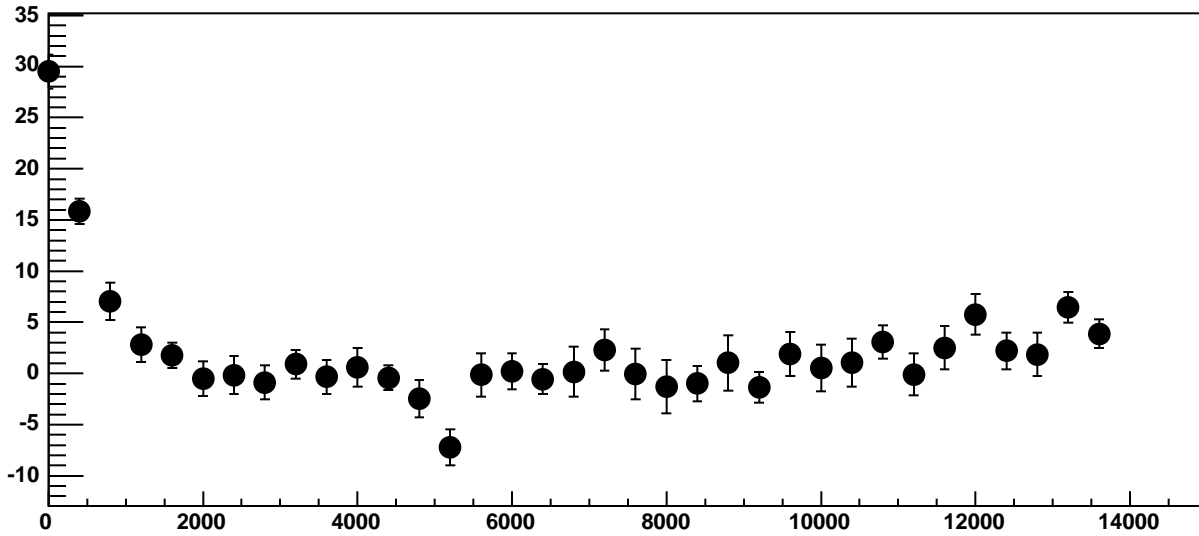


χ^2 / ndf 32.9 / 23
p0 95.18 ± 0.7375
p1 0.03335 ± 0.0001208

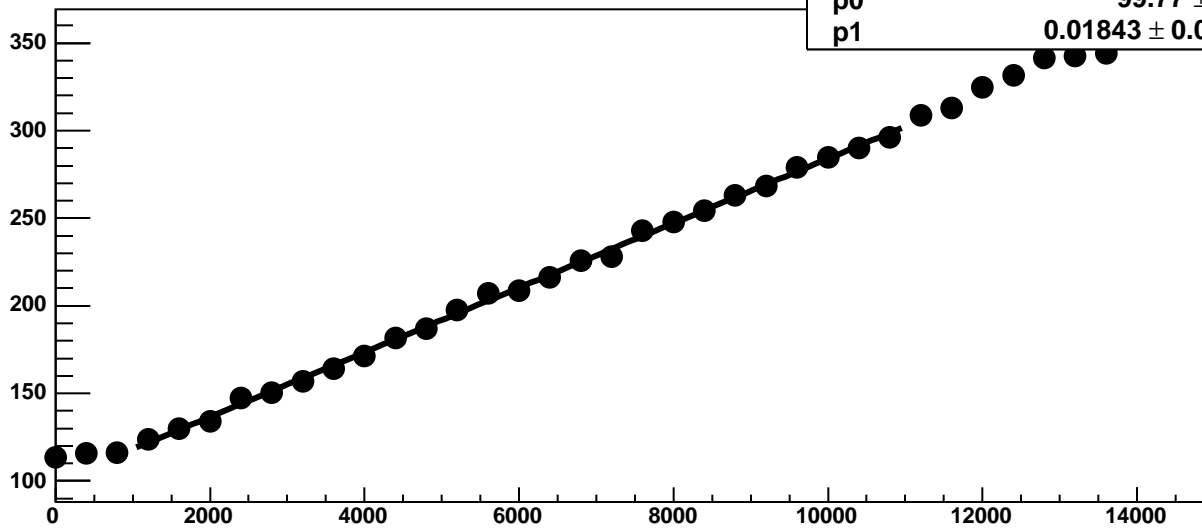
Chip 10, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

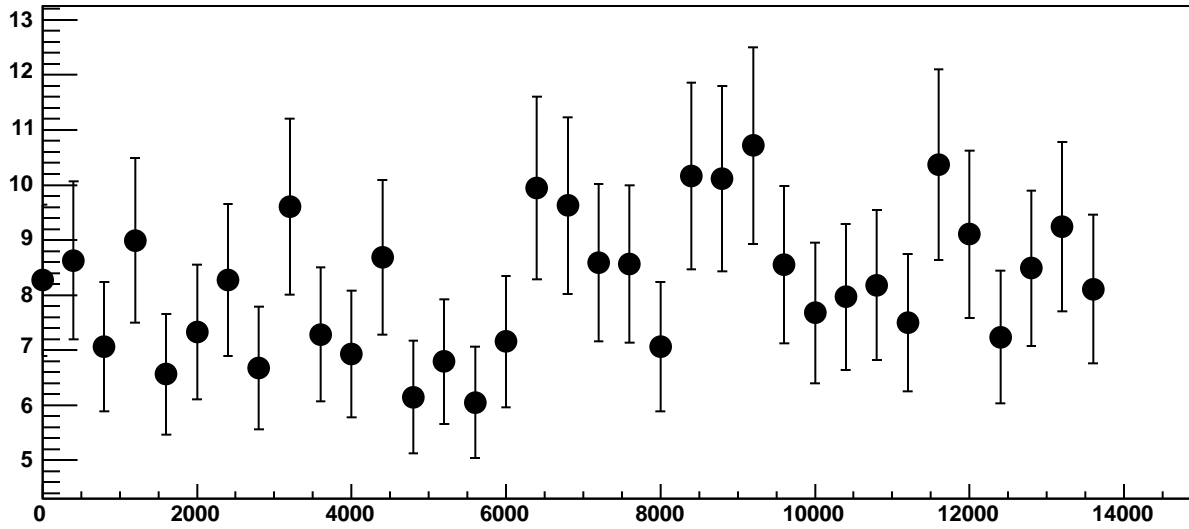


Chip 10, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

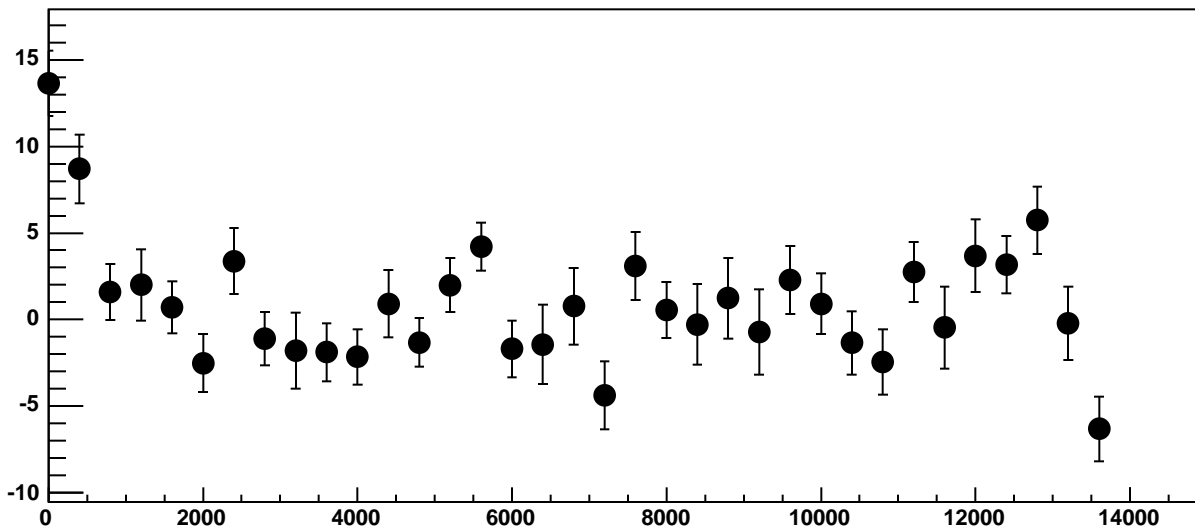


χ^2 / ndf 36.16 / 23
p0 99.77 ± 0.8076
p1 0.01843 ± 0.0001275

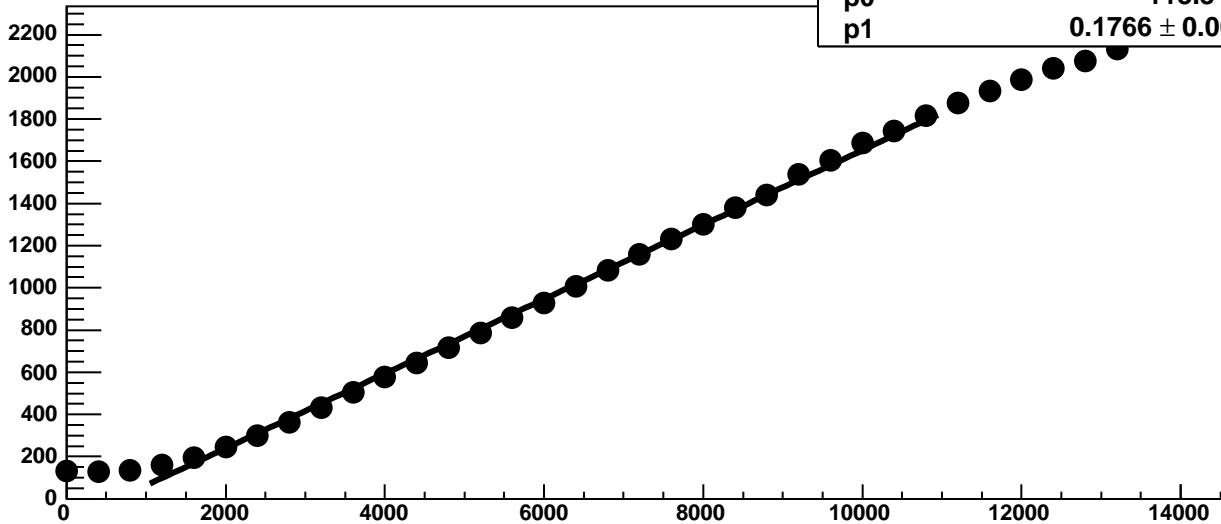
Chip 10, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 10, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

2118 / 23

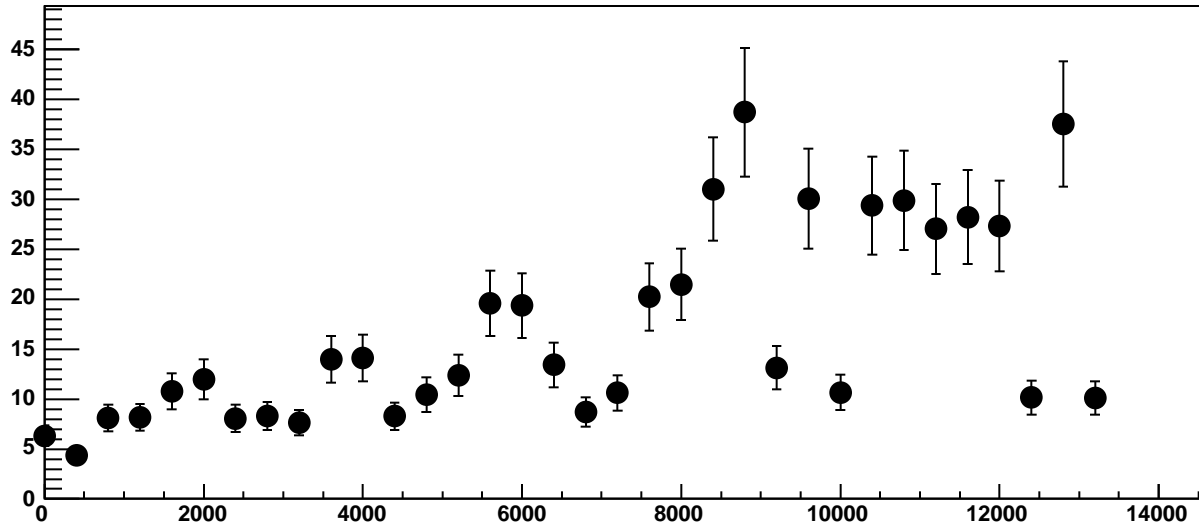
p0

-113.3 ± 1.117

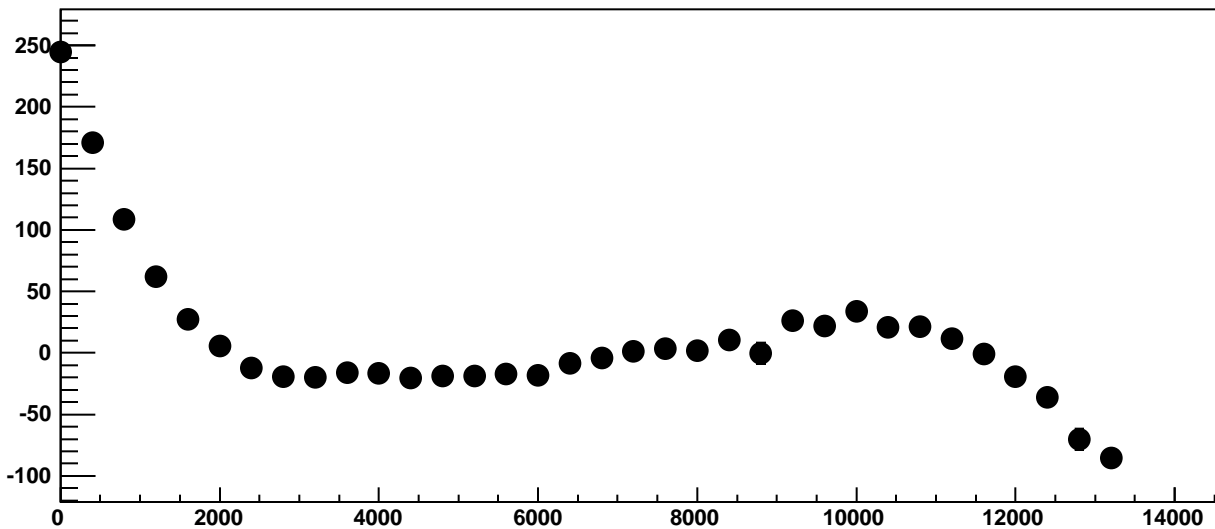
p1

0.1766 ± 0.0002117

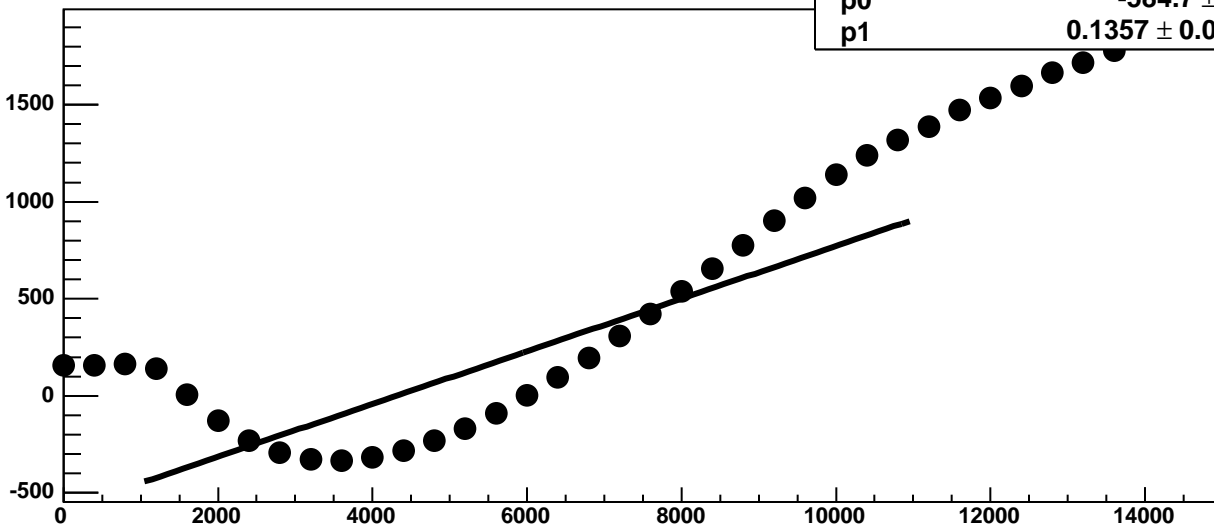
Chip 10, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

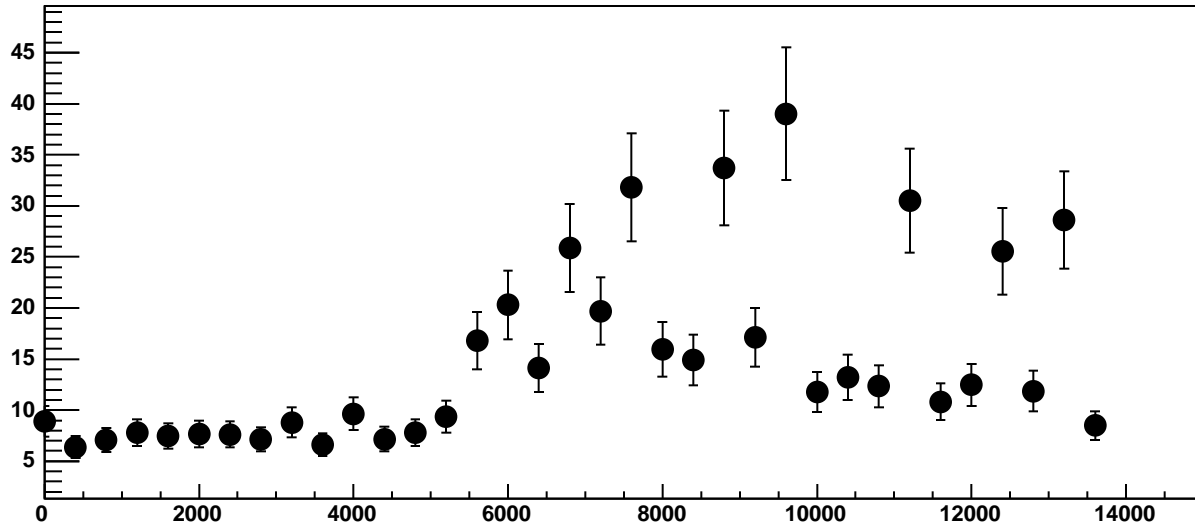


Chip 10, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

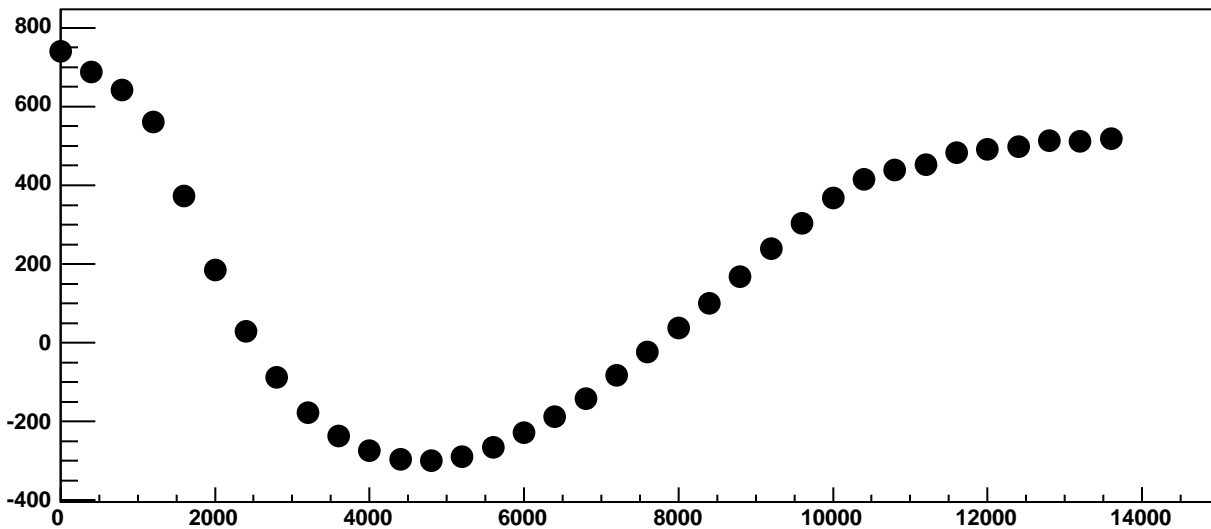


χ^2 / ndf 3.642e+05 / 23
p0 -584.7 ± 0.9168
p1 0.1357 ± 0.0001829

Chip 10, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

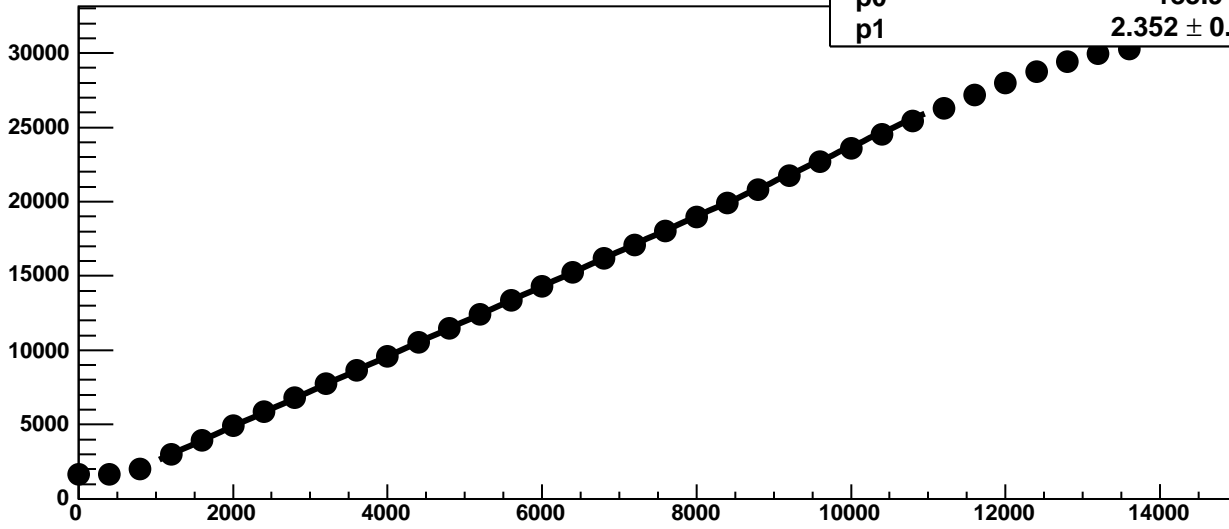


Chip 10, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

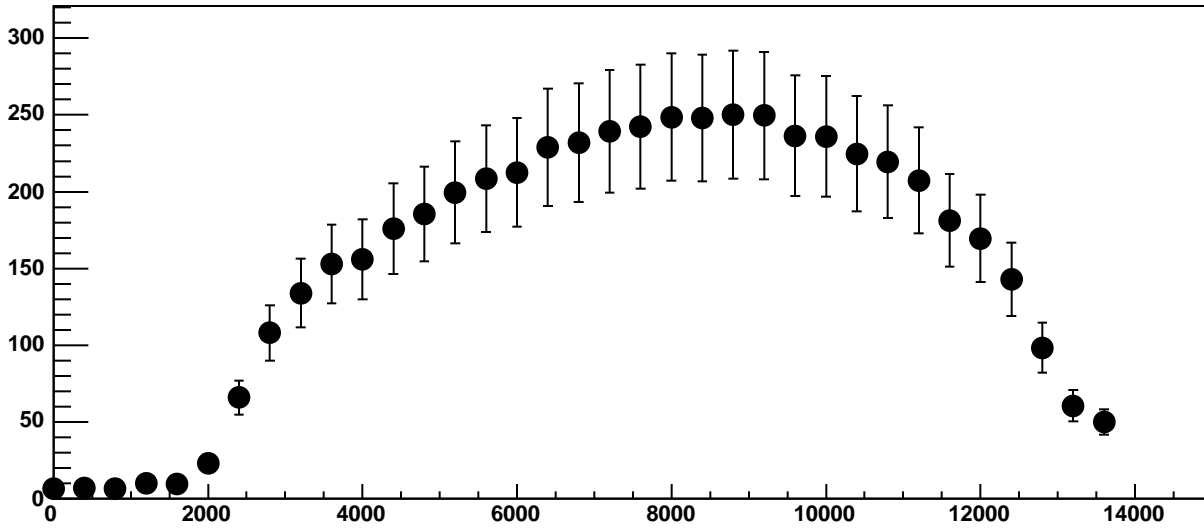


Chip 10, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC

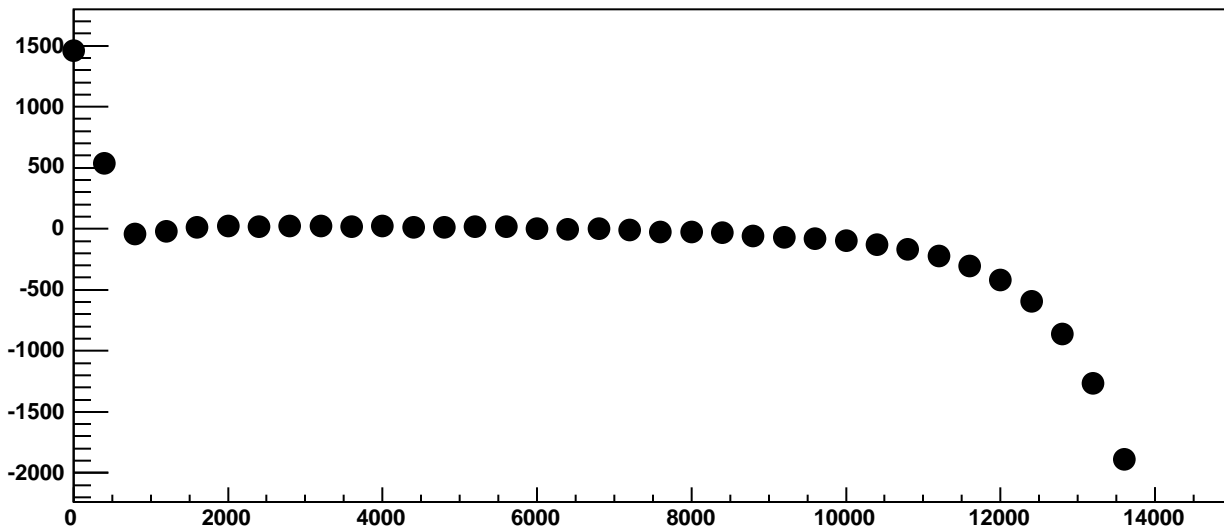
χ^2 / ndf 144.5 / 23
p0 183.9 ± 3.274
p1 2.352 ± 0.001853



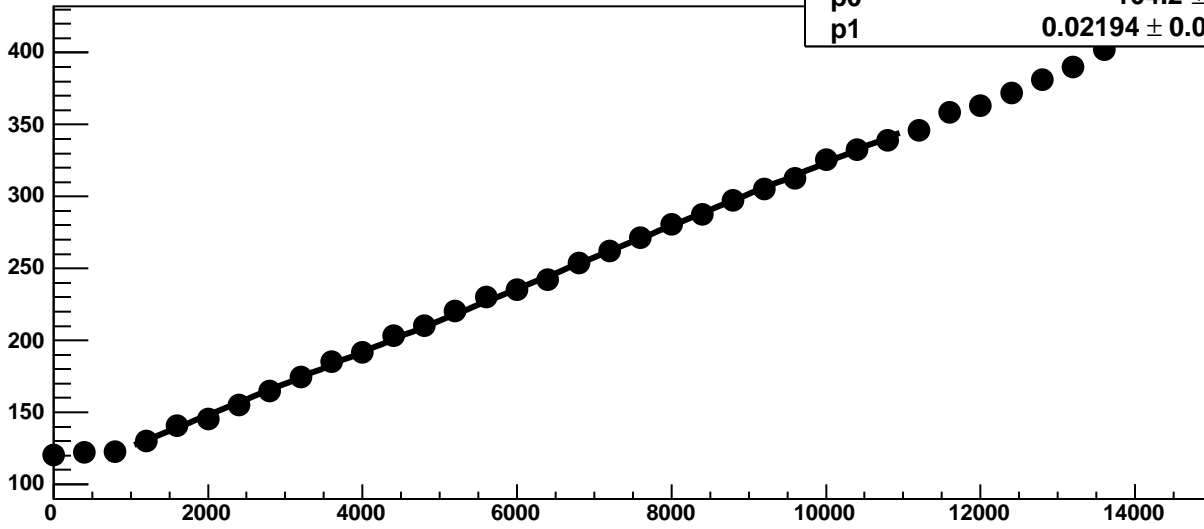
Chip 10, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC

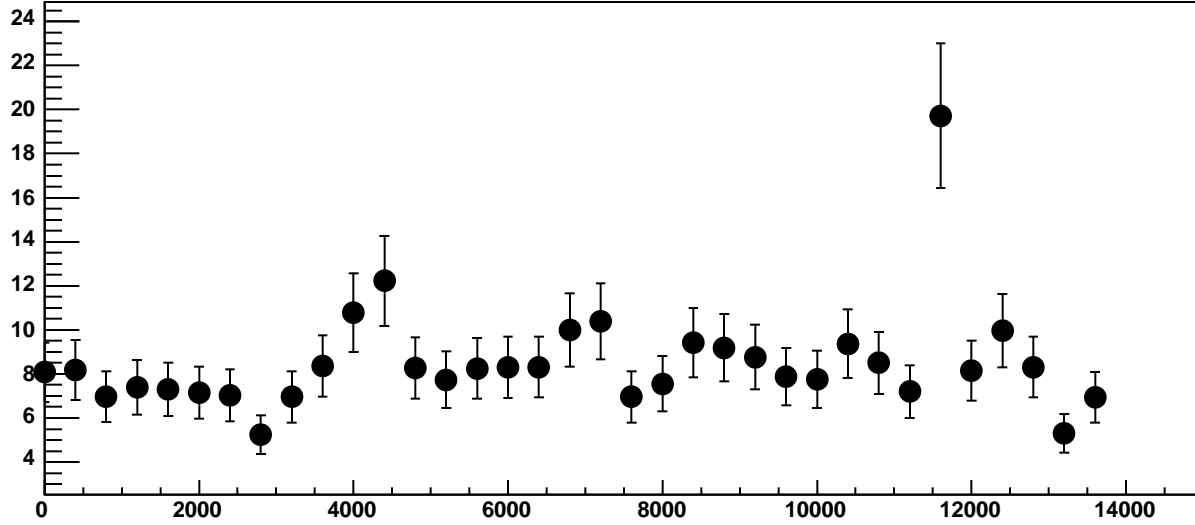


Chip 10, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

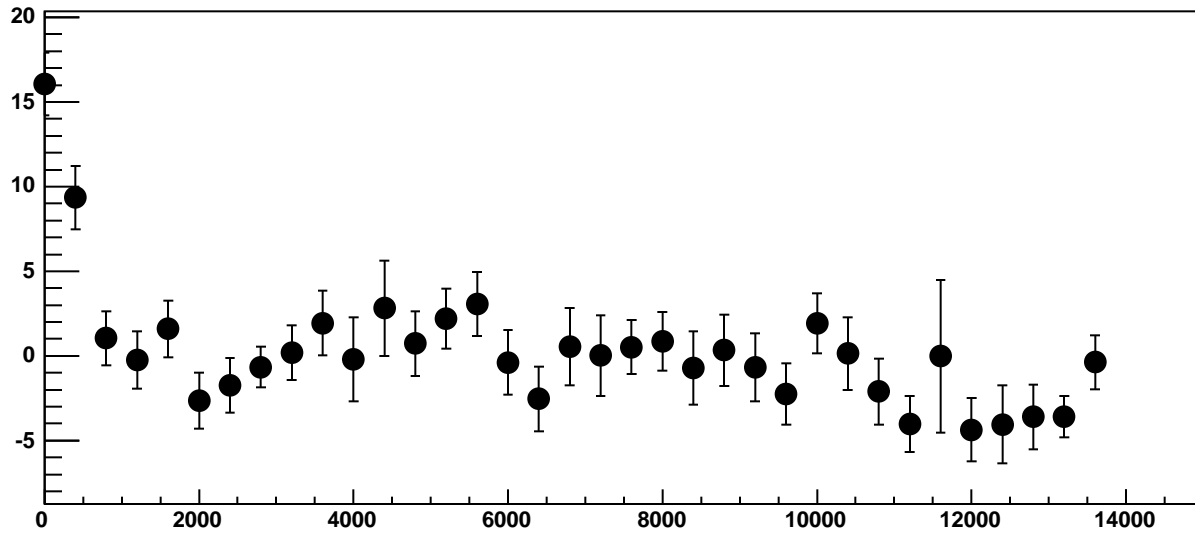


χ^2 / ndf 17.73 / 23
p0 104.2 ± 0.7827
p1 0.02194 ± 0.0001236

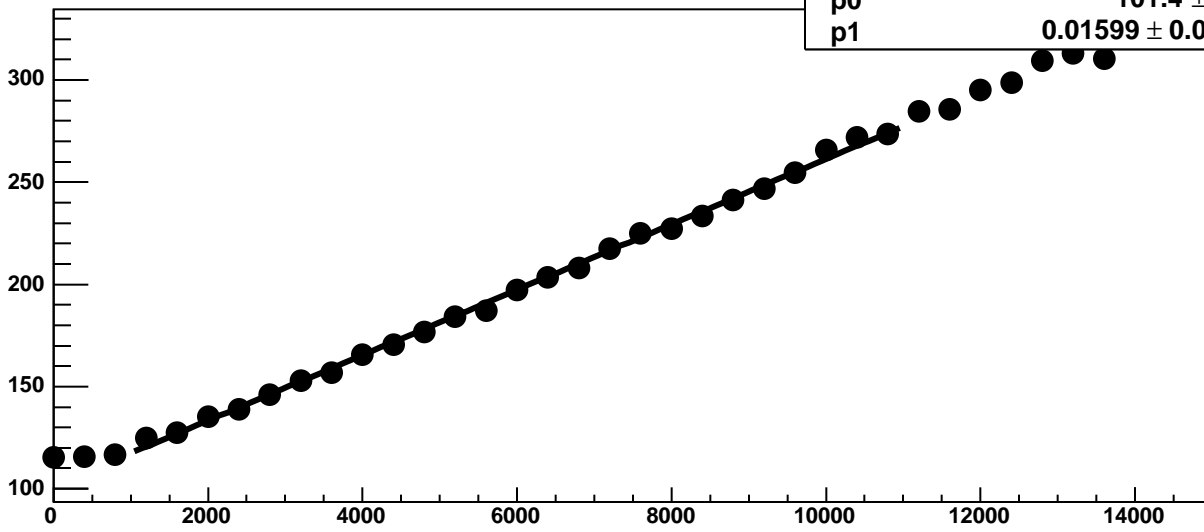
Chip 10, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

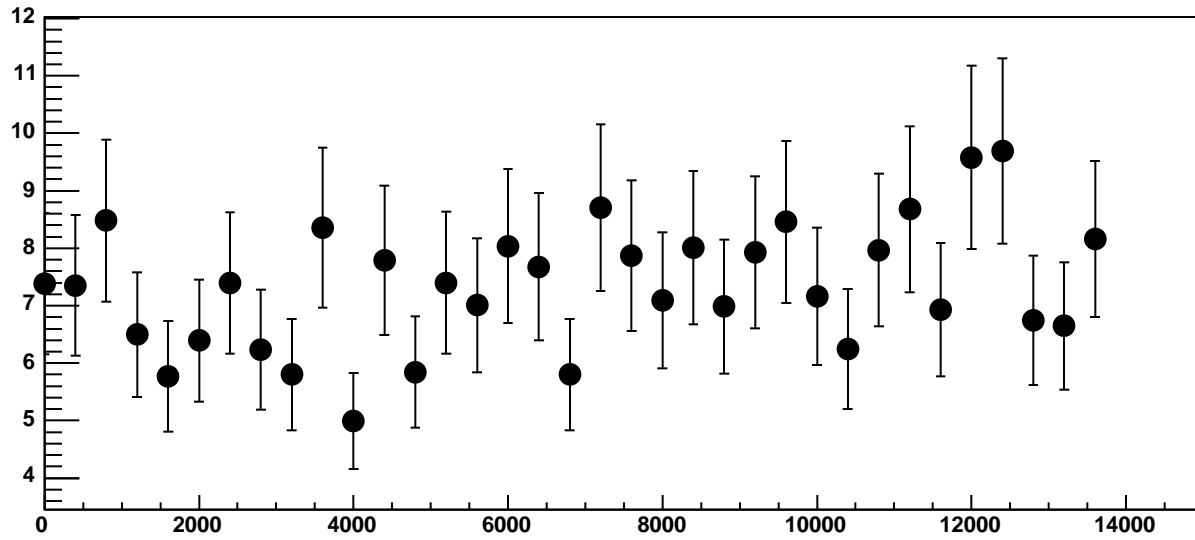


Chip 10, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

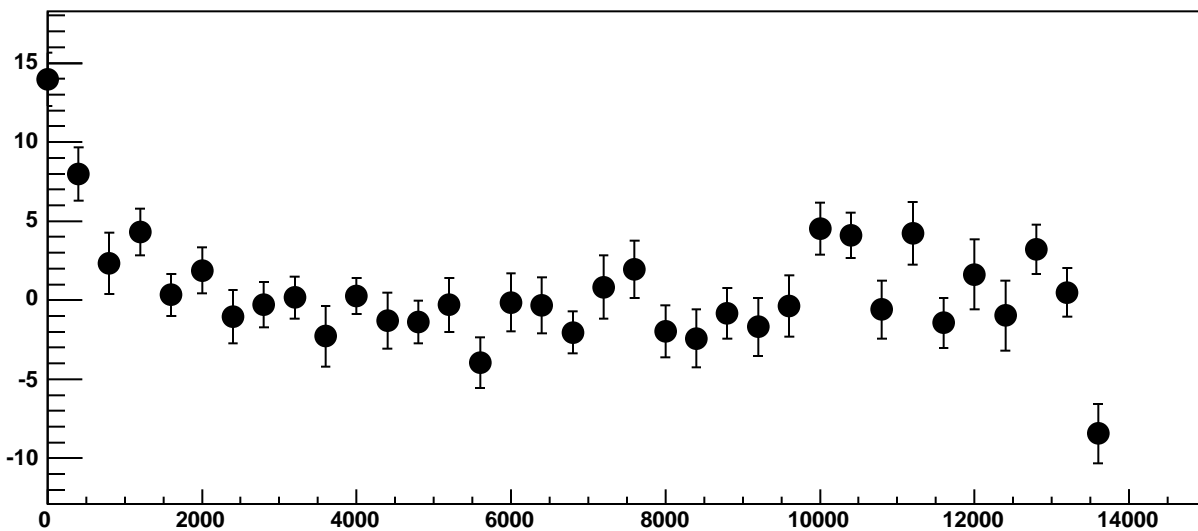


χ^2 / ndf 43.63 / 23
p0 101.4 ± 0.6884
p1 0.01599 ± 0.0001088

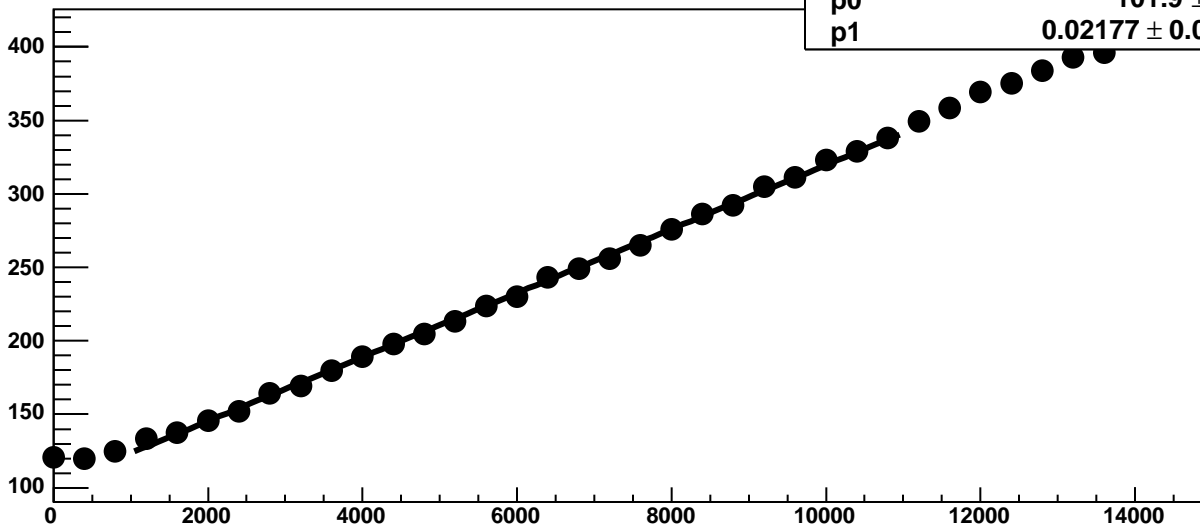
Chip 10, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

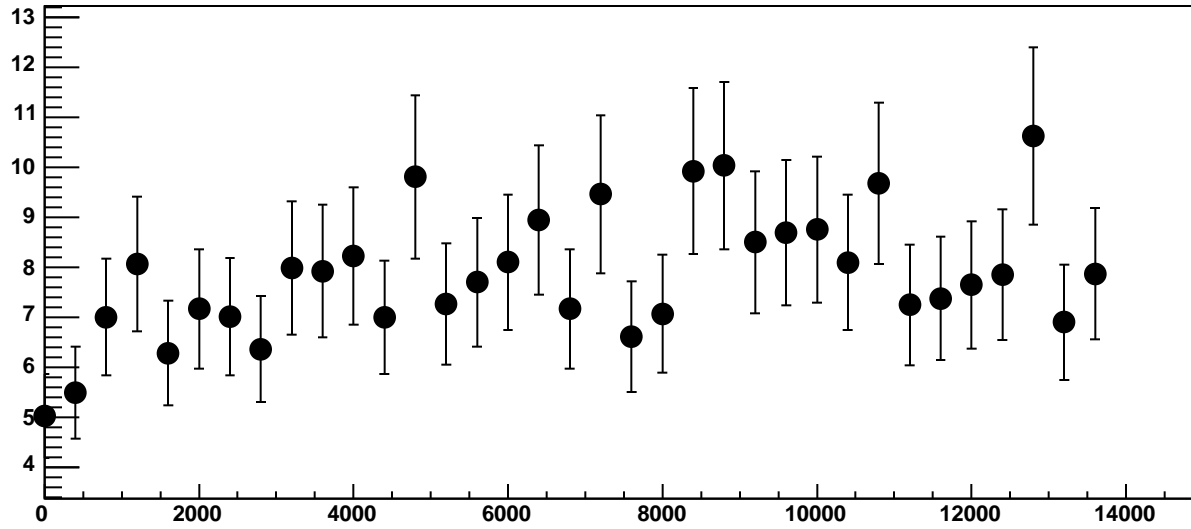


Chip 10, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

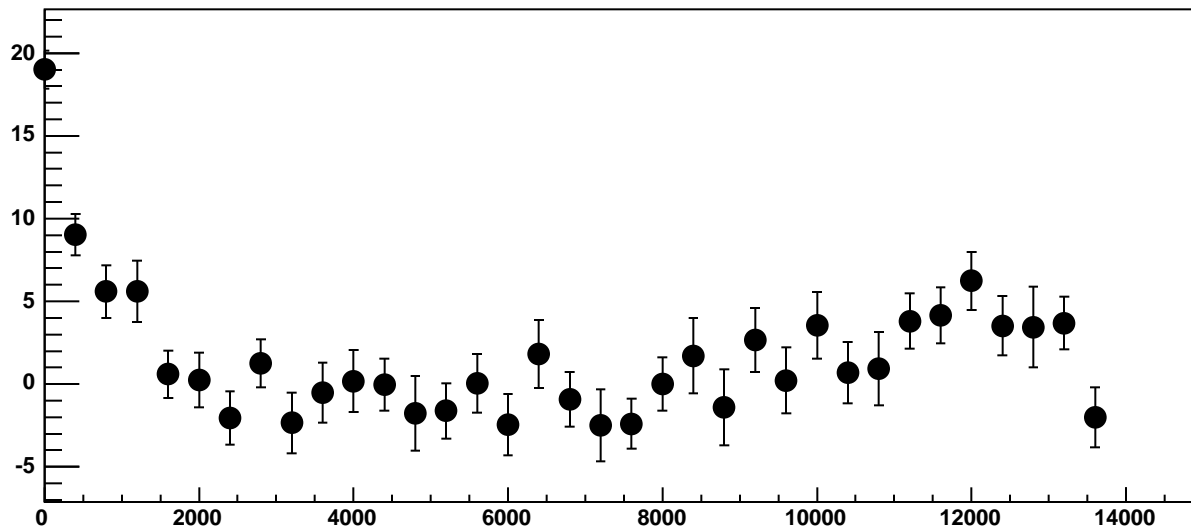


χ^2 / ndf 27.97 / 23
p0 101.9 ± 0.7892
p1 0.02177 ± 0.0001258

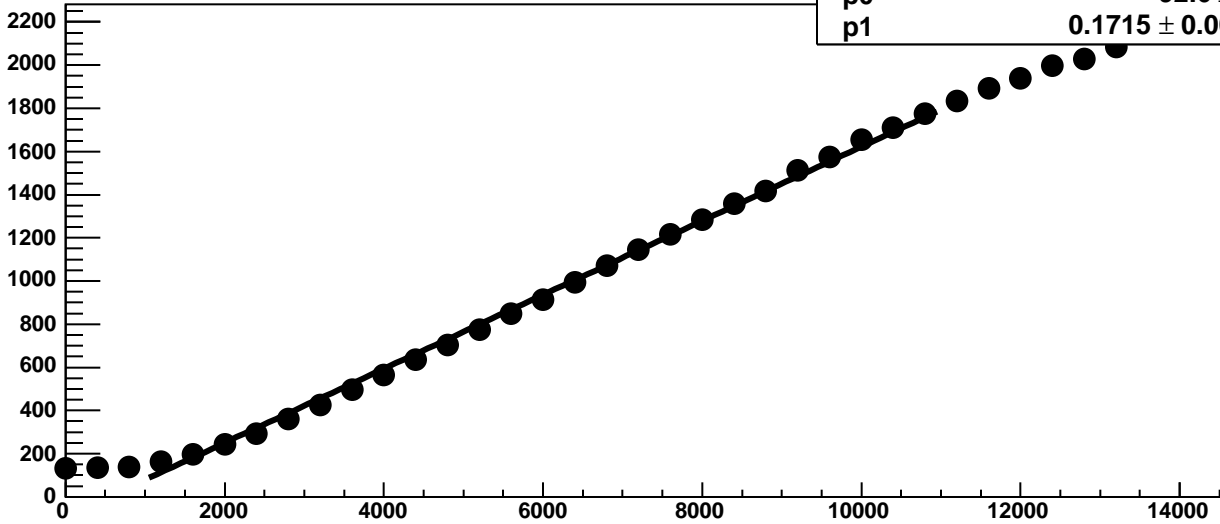
Chip 10, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



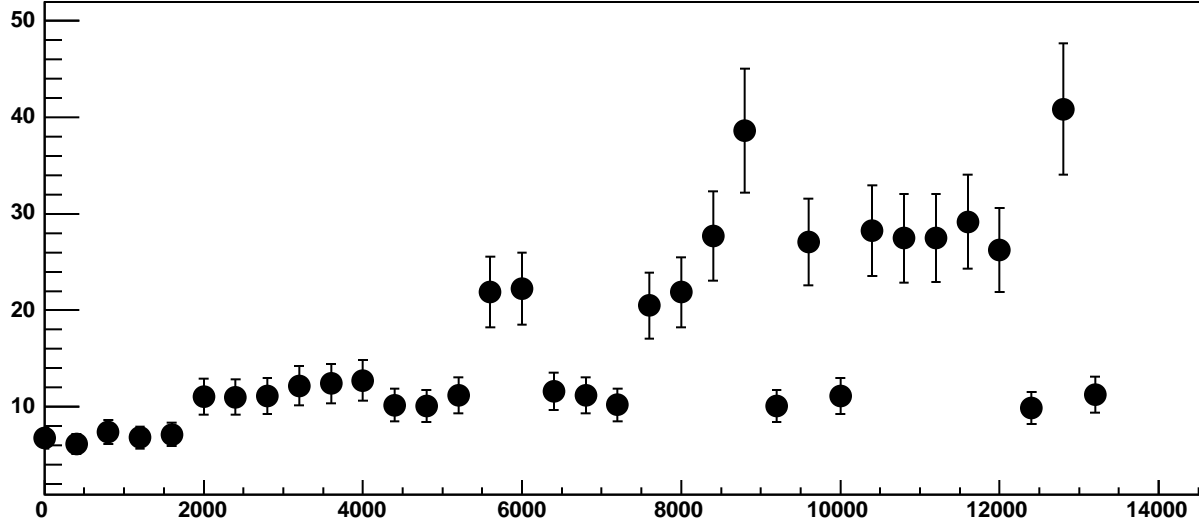
Chip 10, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC



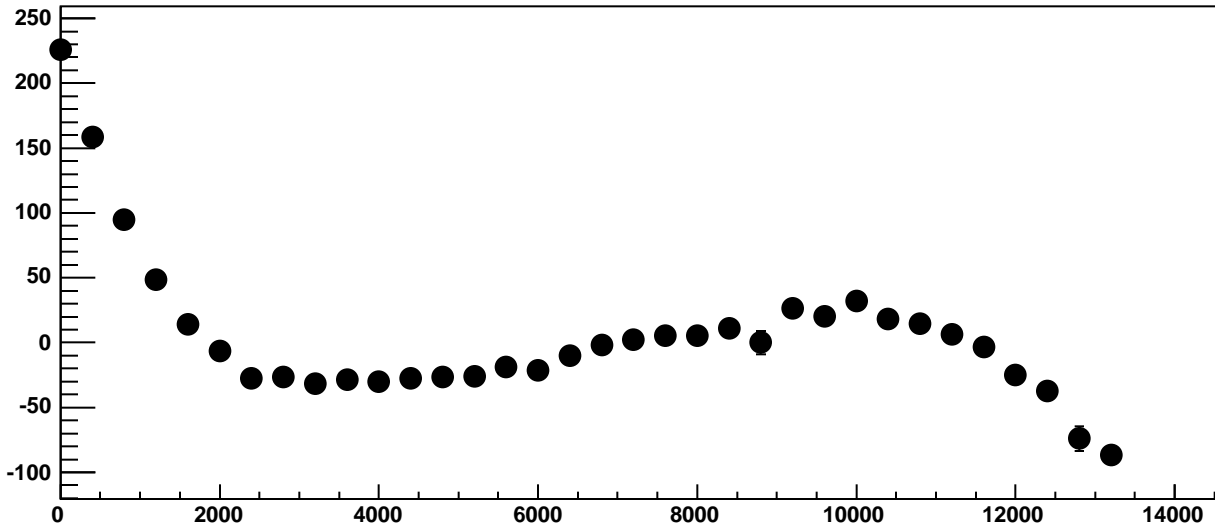
Chip 10, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



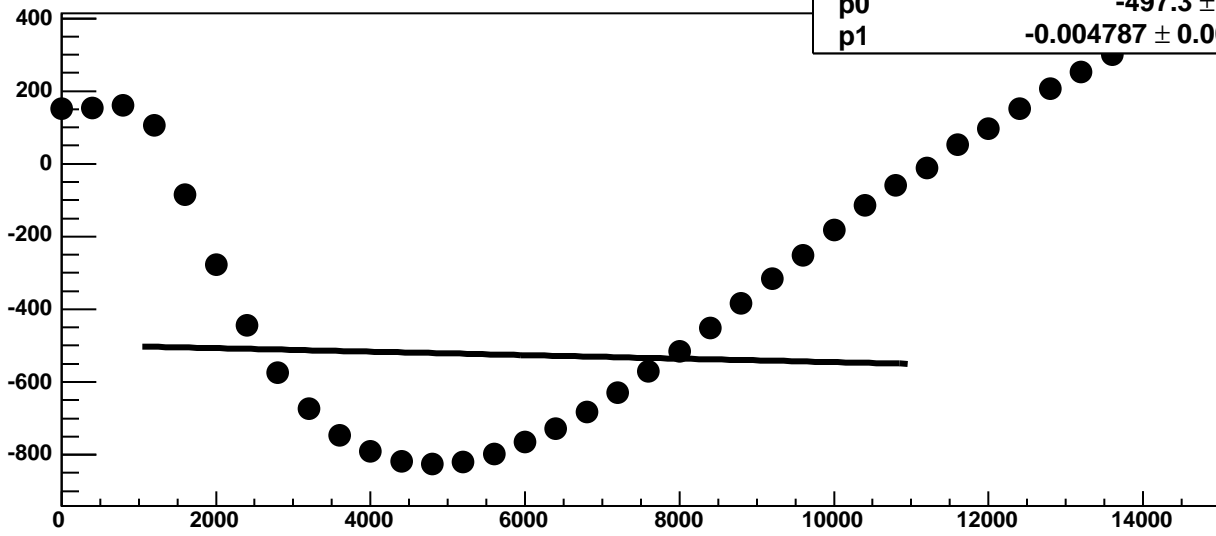
Chip 10, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

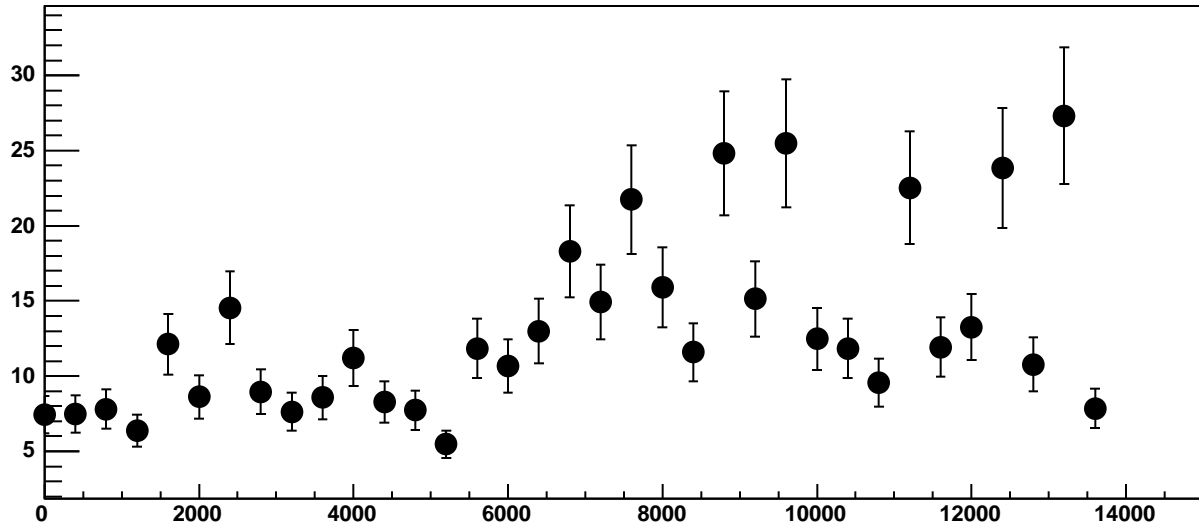


Chip 10, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

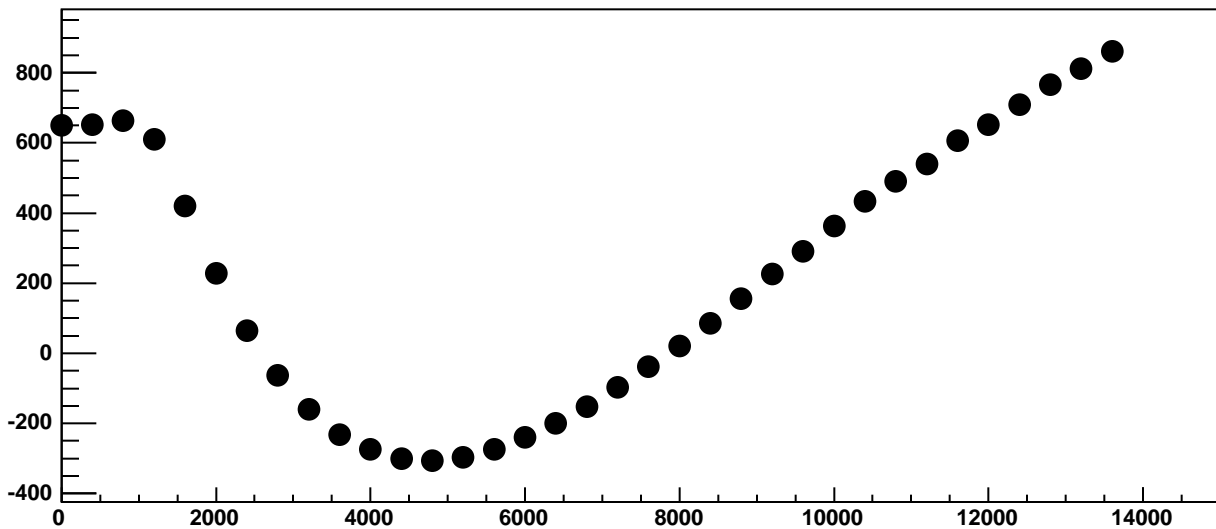


χ^2 / ndf 4.83e+05 / 23
p0 -497.3 ± 0.9639
p1 -0.004787 ± 0.0001708

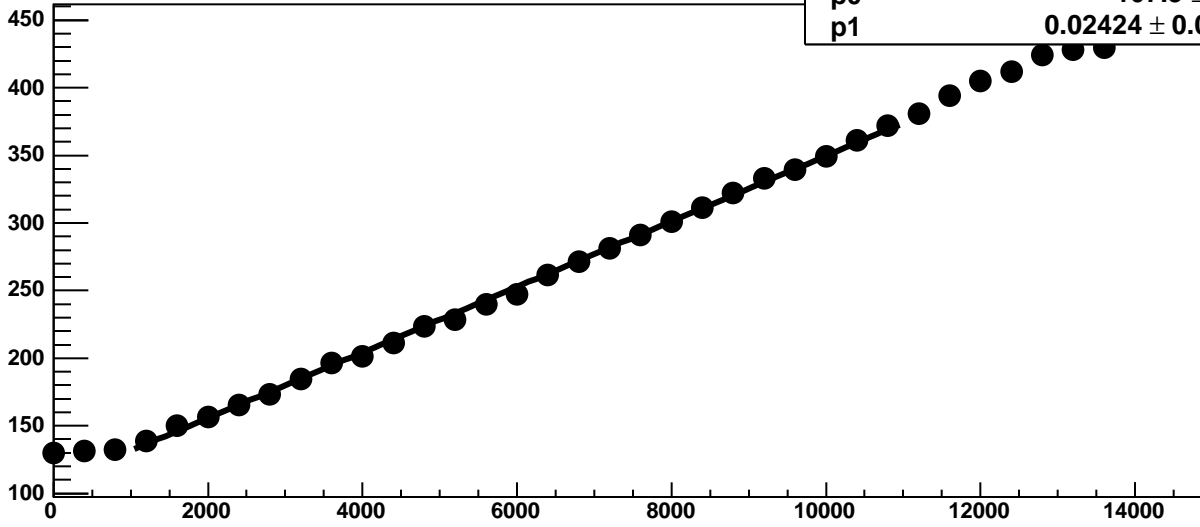
Chip 10, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

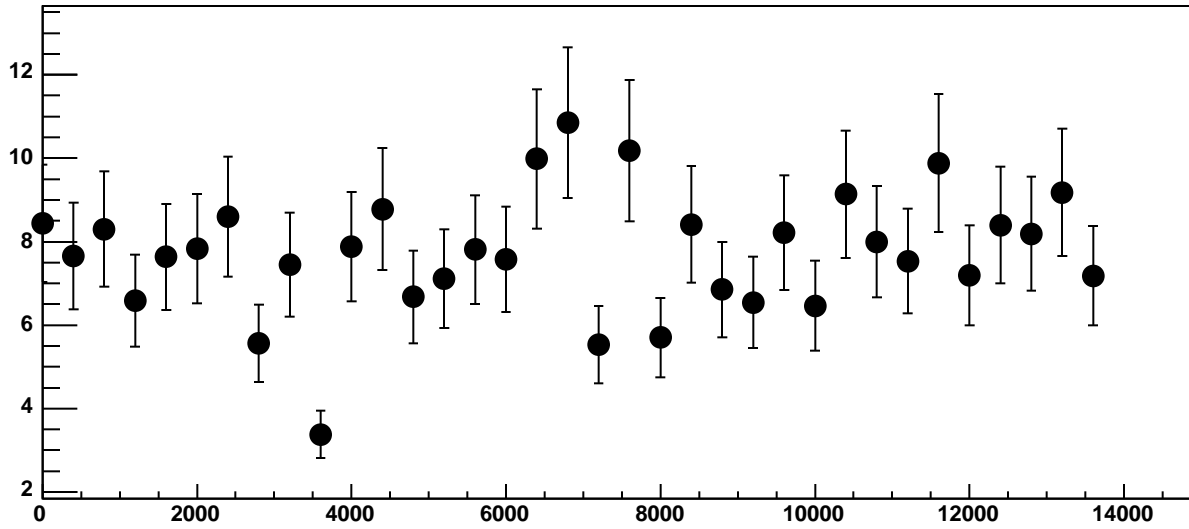


Chip 10, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

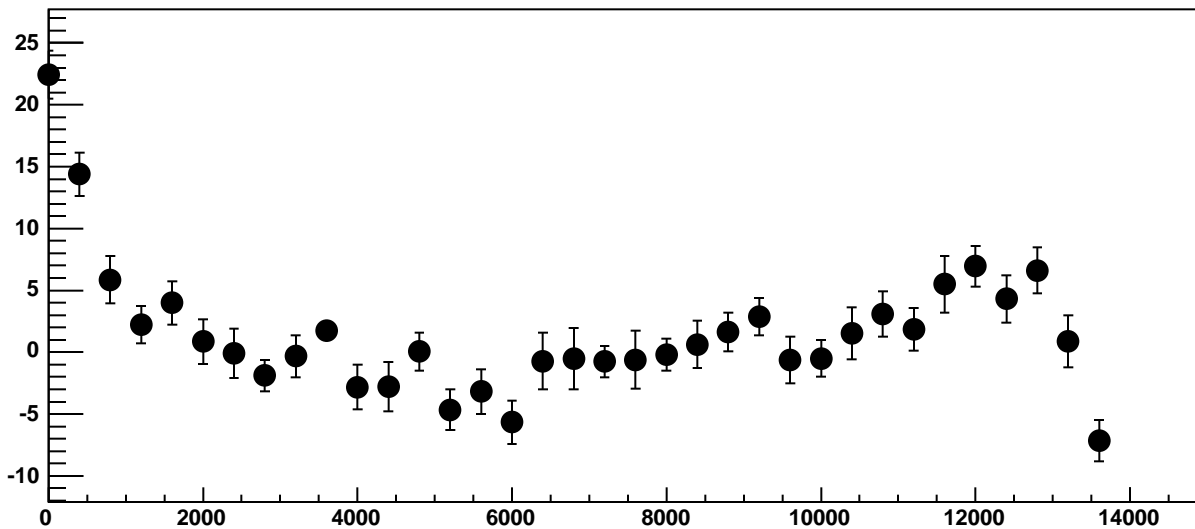


χ^2 / ndf 49.91 / 23
p0 107.3 ± 0.7046
p1 0.02424 ± 0.0001119

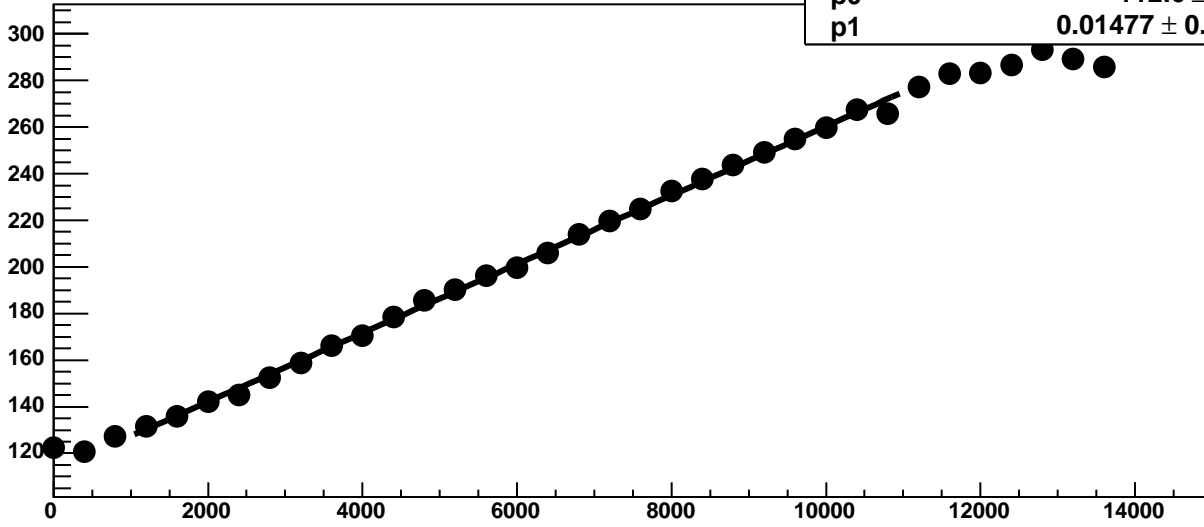
Chip 10, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



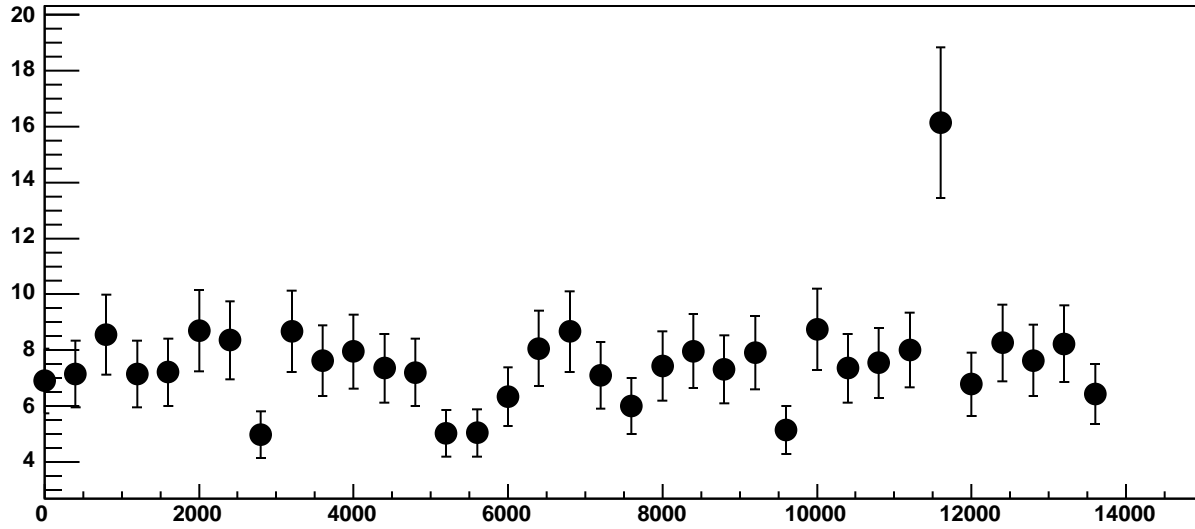
Chip 10, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC



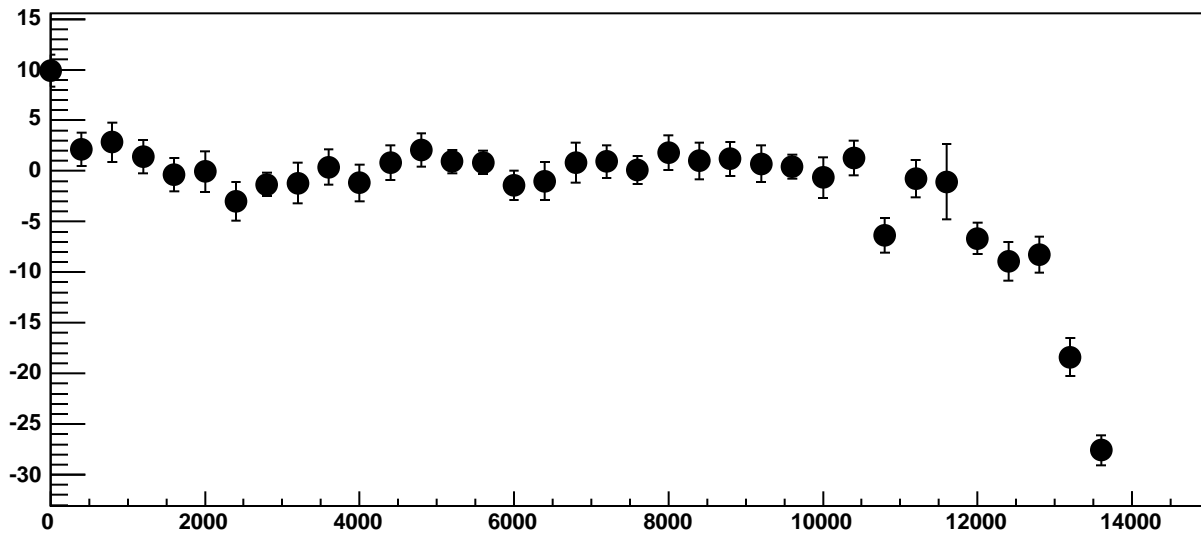
Chip 10, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



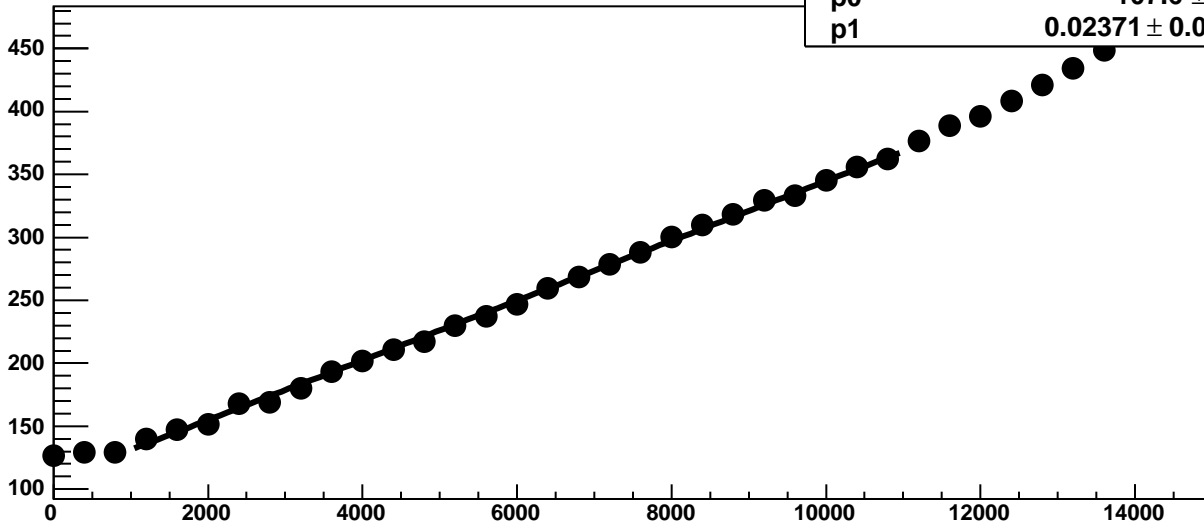
Chip 10, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

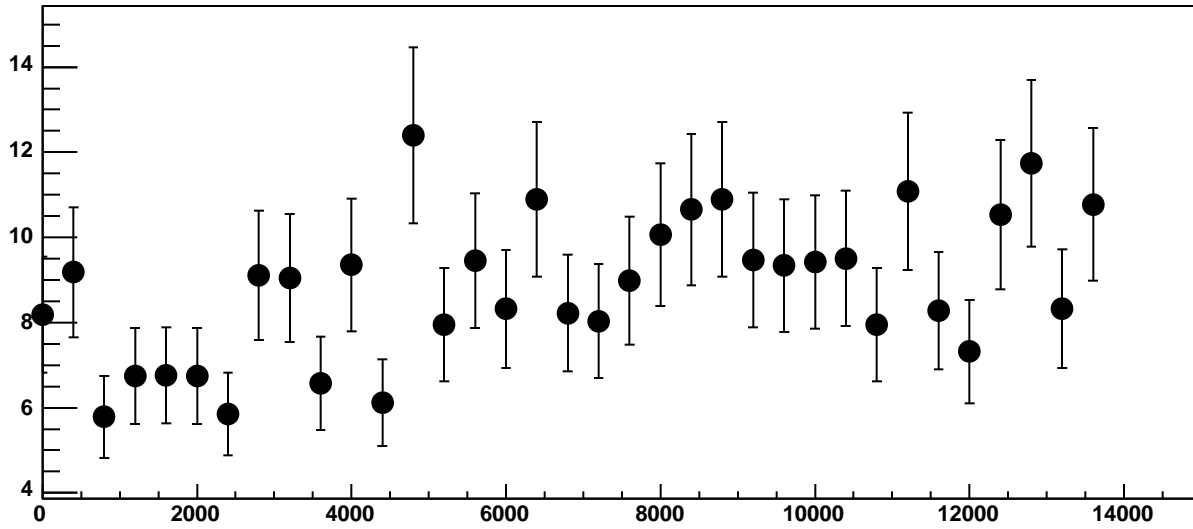


Chip 10, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

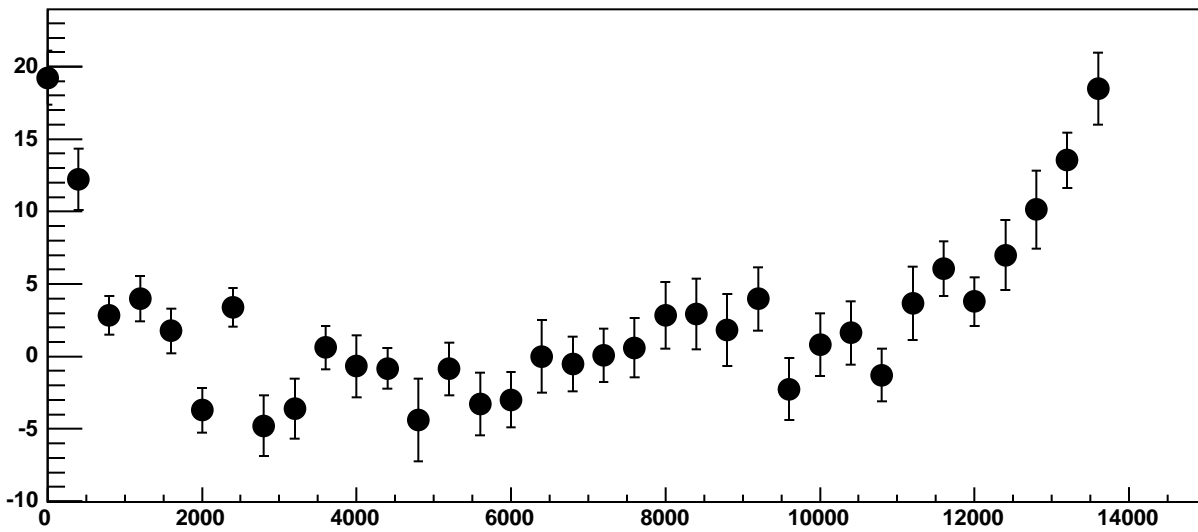


χ^2 / ndf 45.56 / 23
p0 107.6 ± 0.7785
p1 0.02371 ± 0.0001278

Chip 10, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

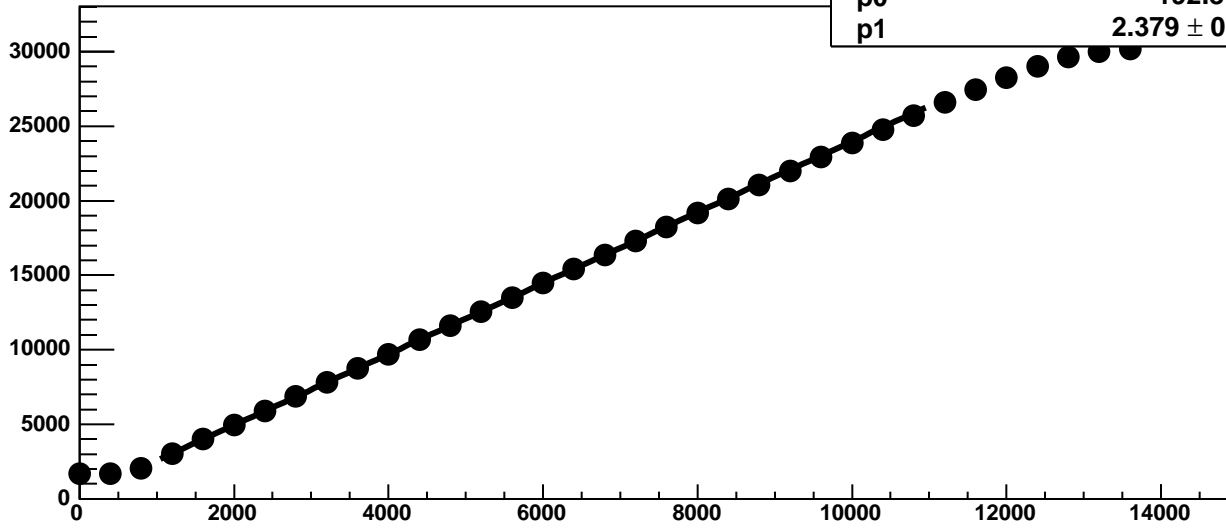


Chip 10, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

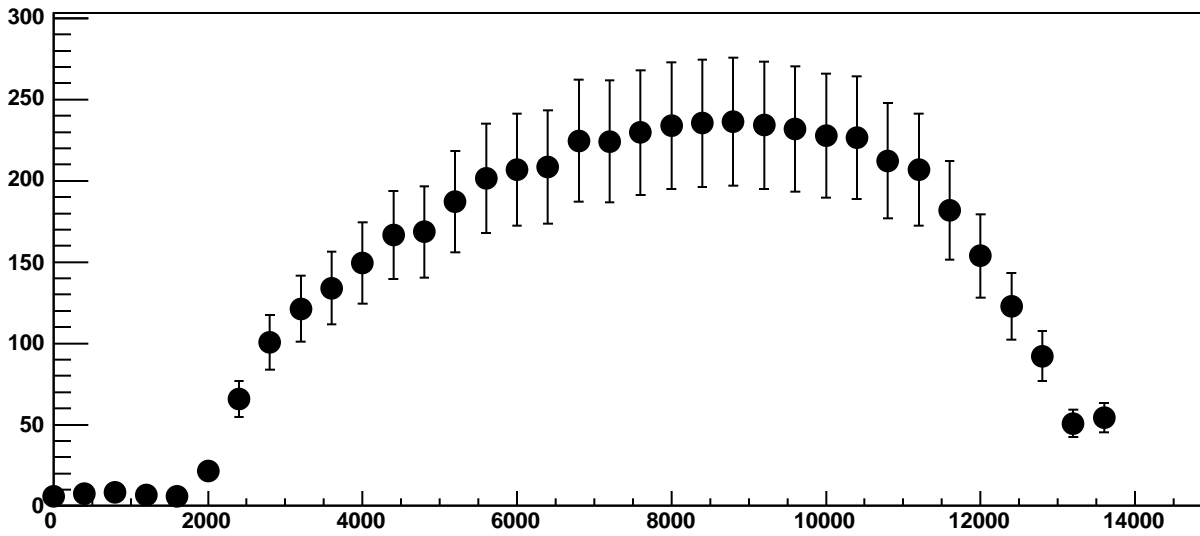


Chip 10, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC

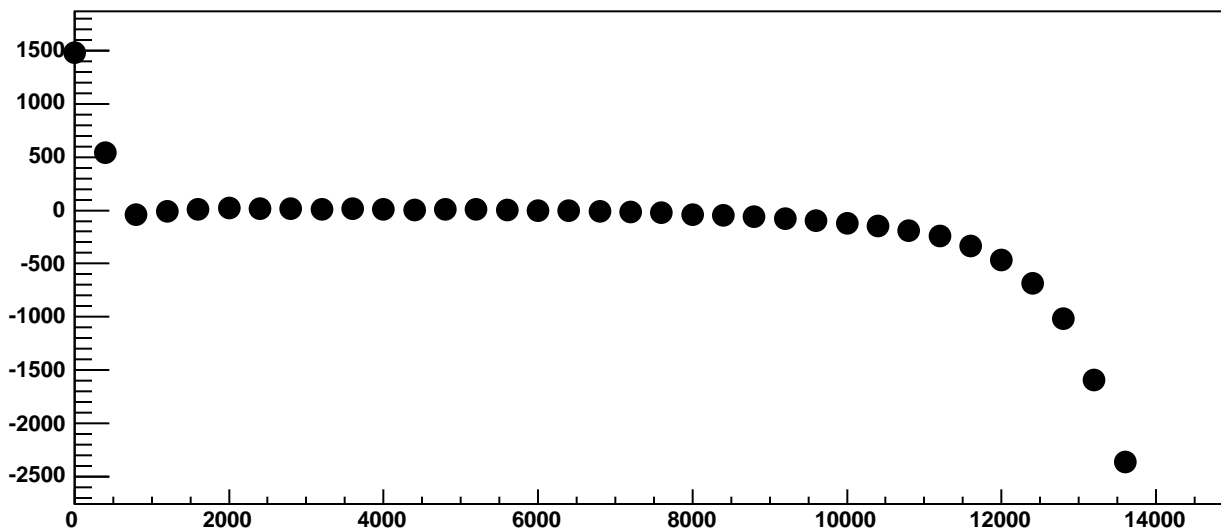
χ^2 / ndf 149.7 / 23
p0 192.8 ± 2.774
p1 2.379 ± 0.001712



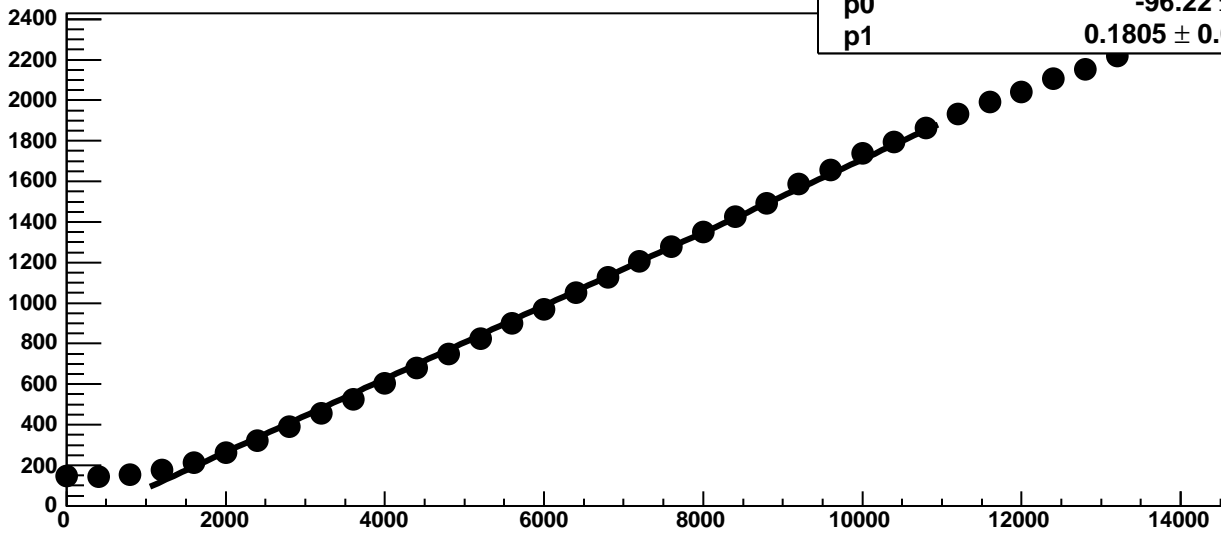
Chip 10, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



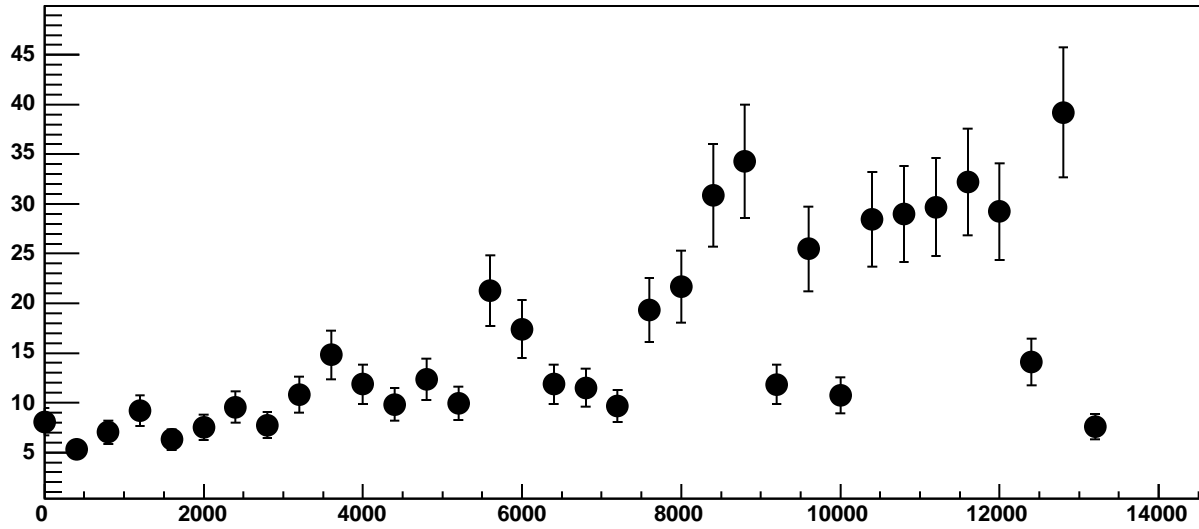
Chip 10, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC



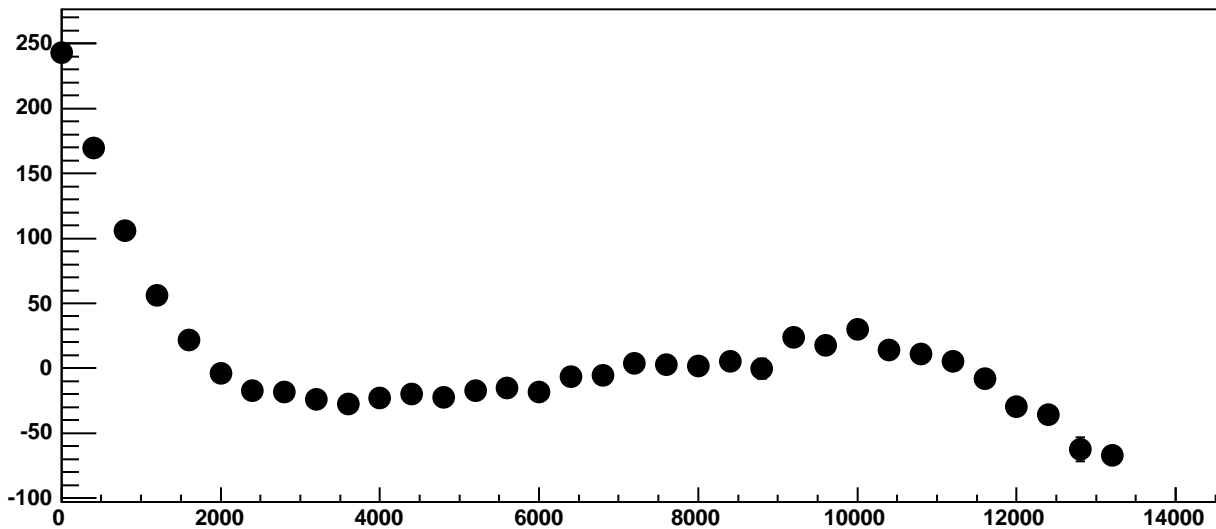
Chip 10, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



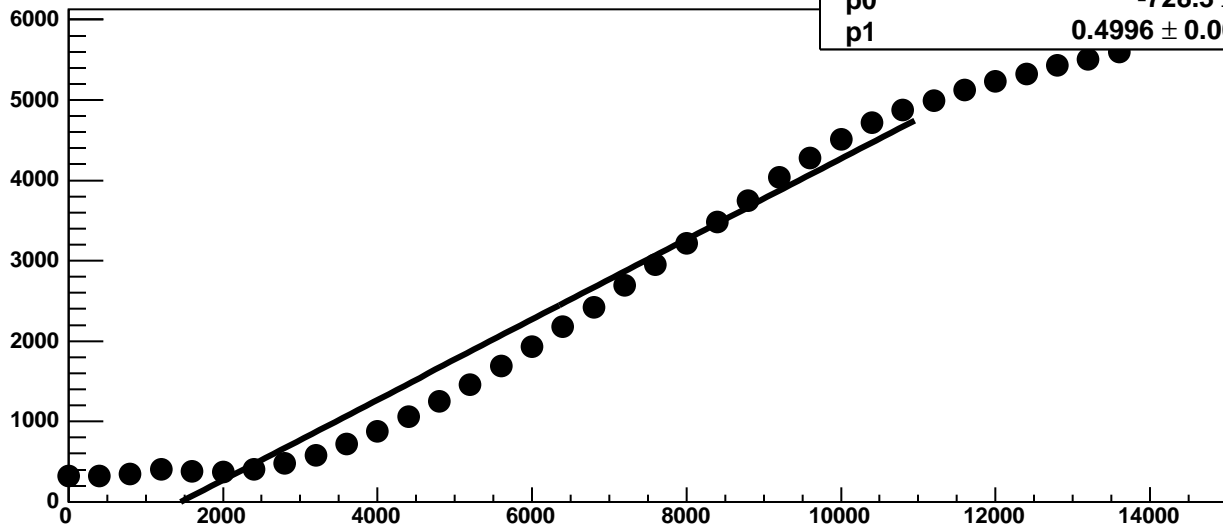
Chip 10, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



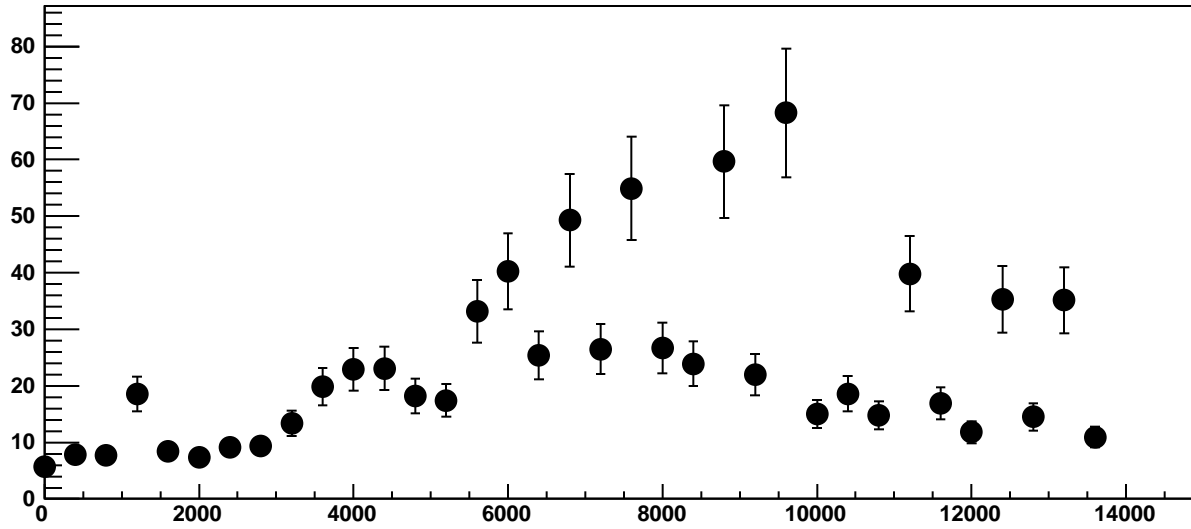
Chip 10, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



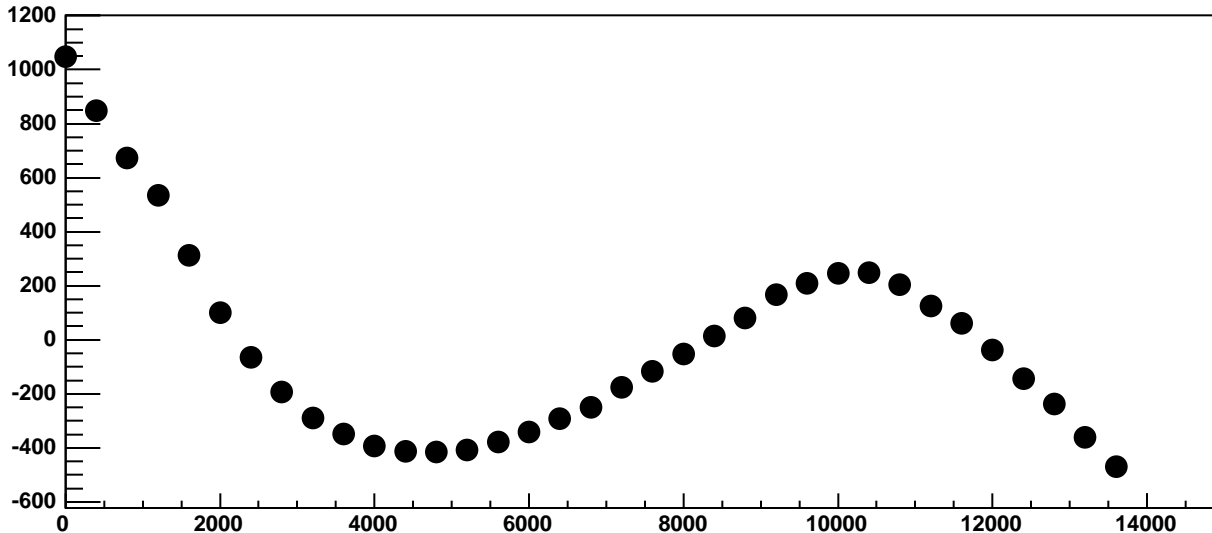
Chip 10, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



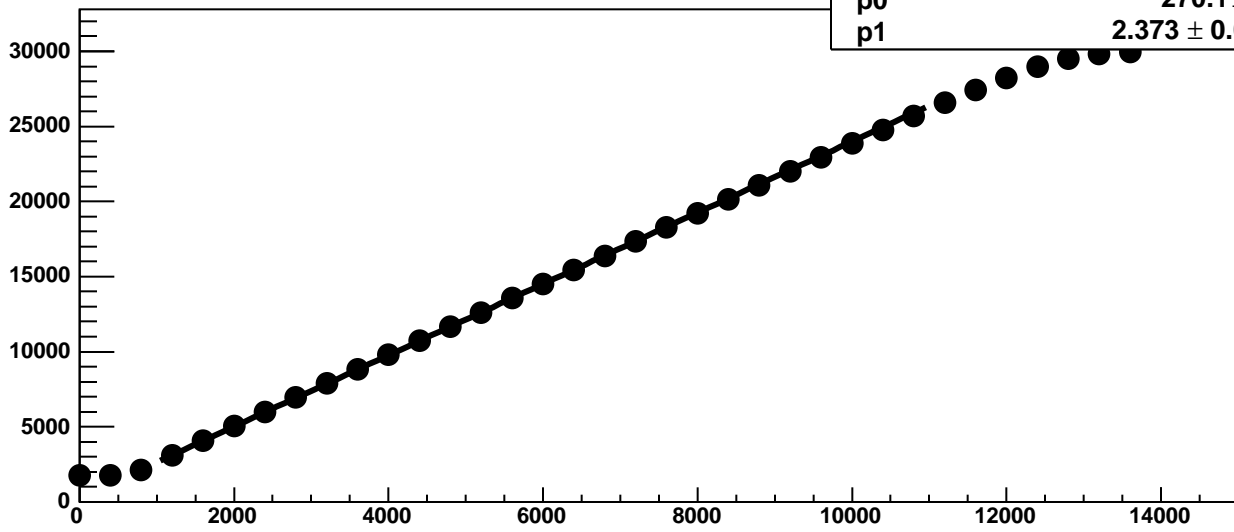
Chip 10, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



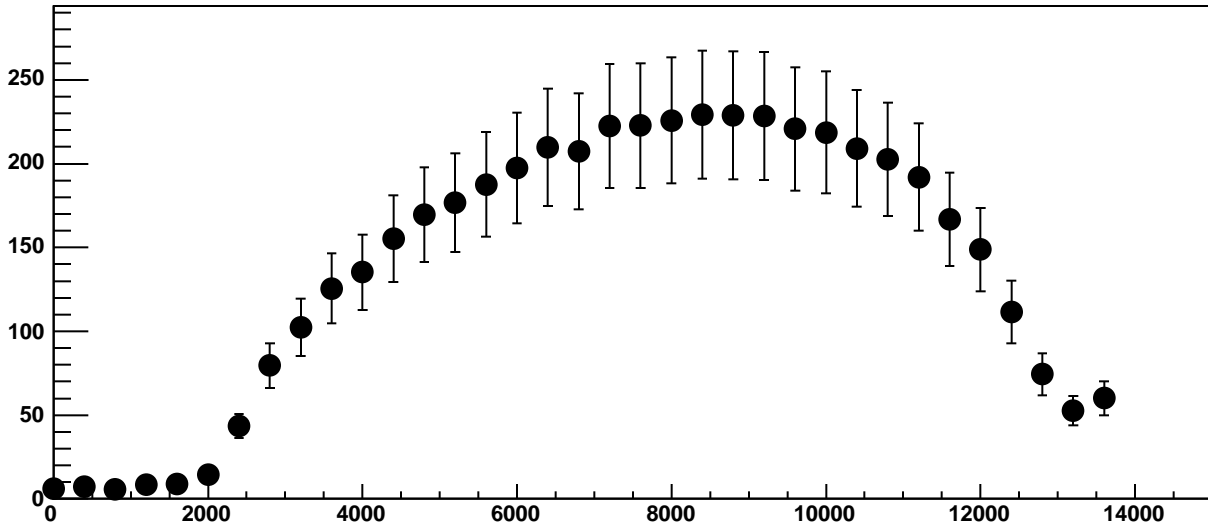
Chip 10, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



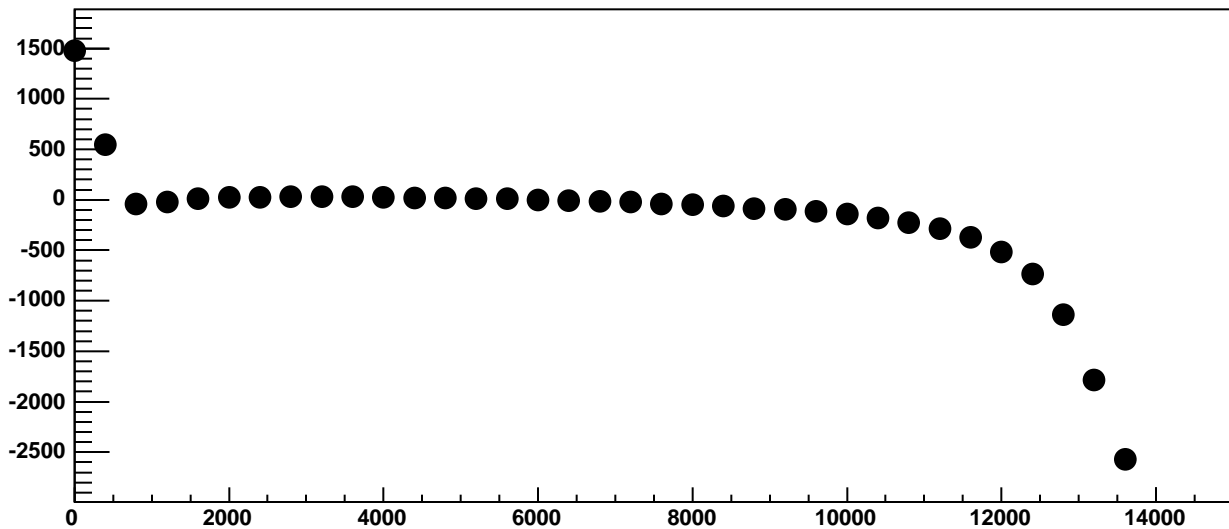
Chip 10, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC



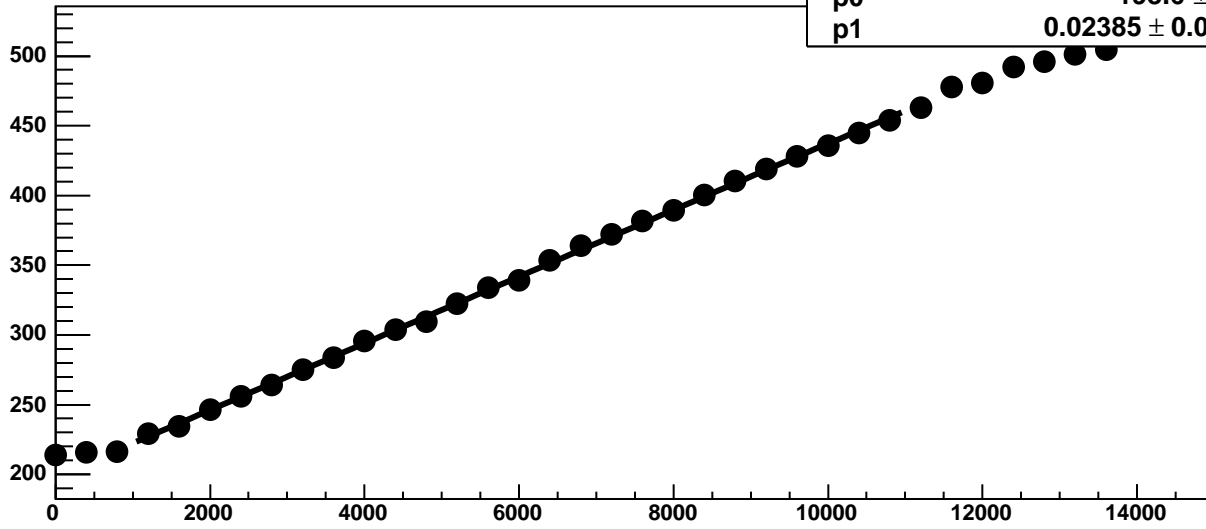
Chip 10, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC

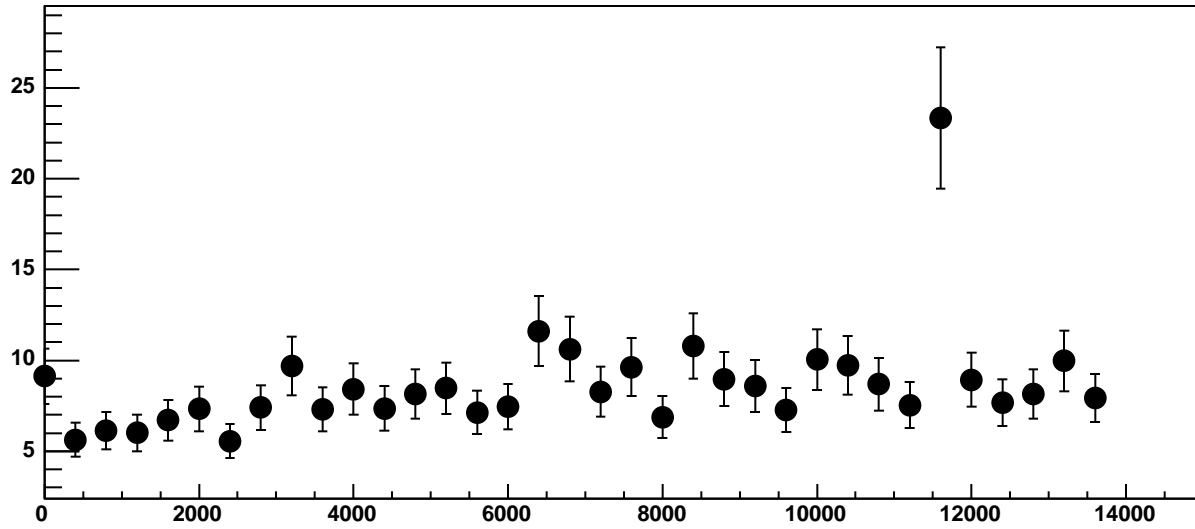


Chip 10, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

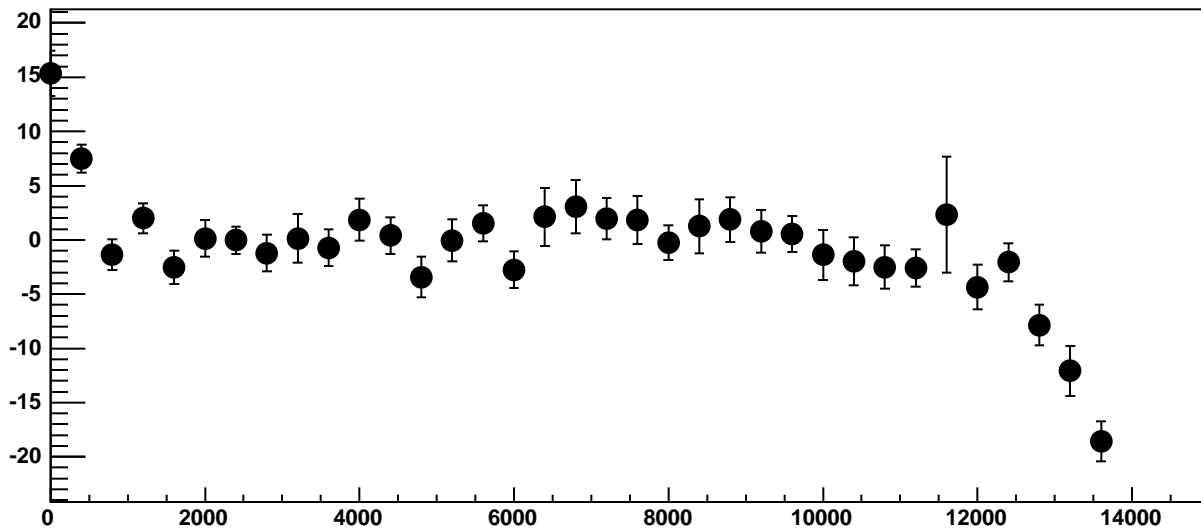


χ^2 / ndf 21.39 / 23
p0 198.6 ± 0.7547
p1 0.02385 ± 0.0001229

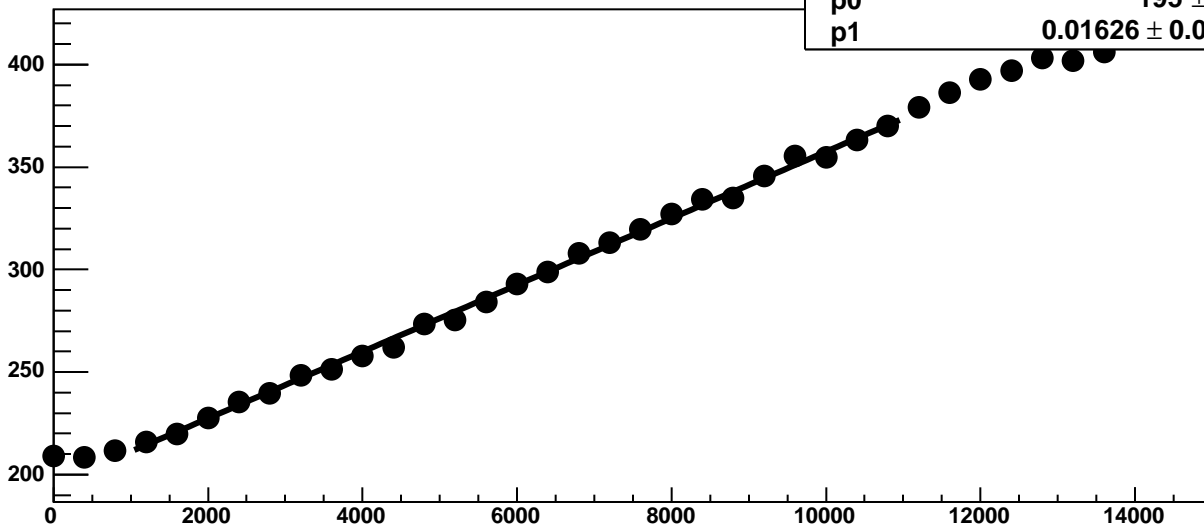
Chip 10, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



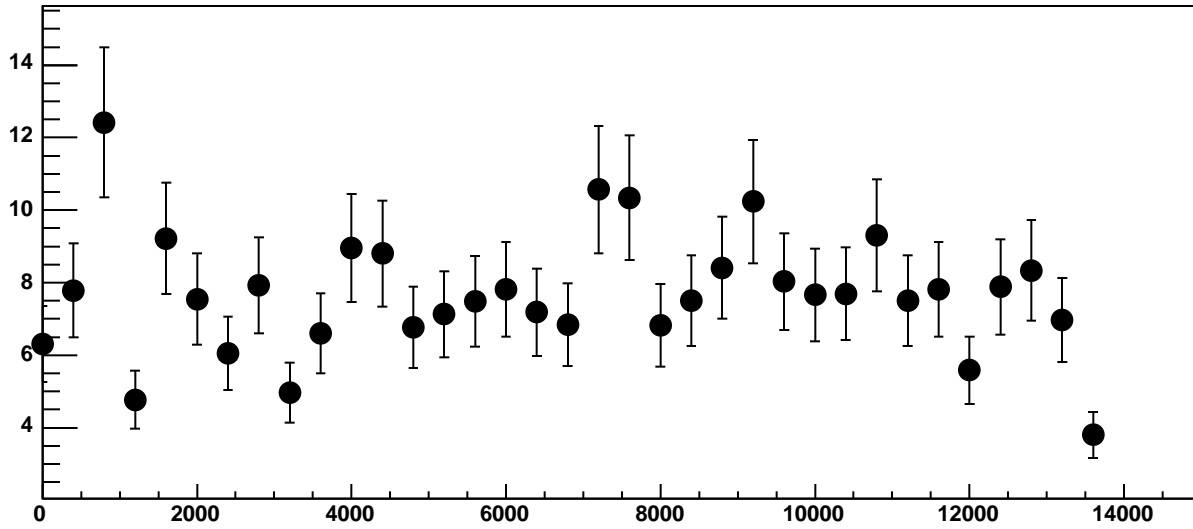
Chip 10, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



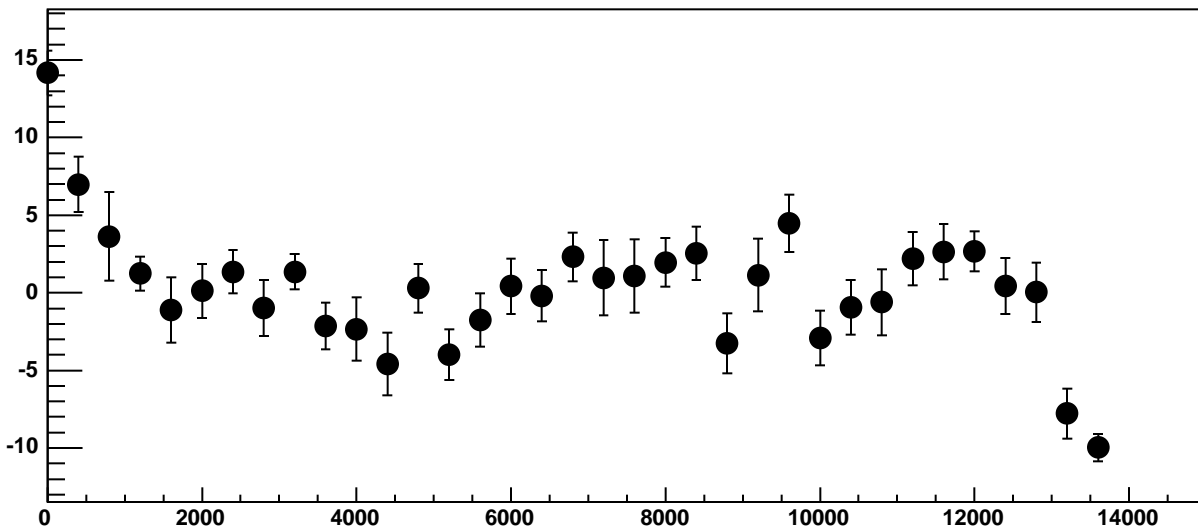
Chip 10, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



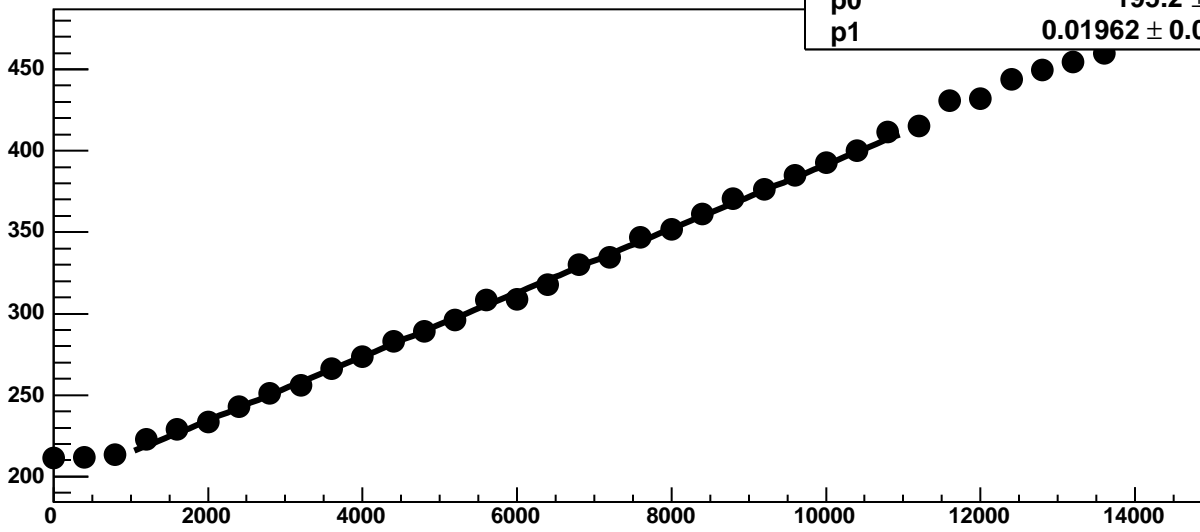
Chip 10, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

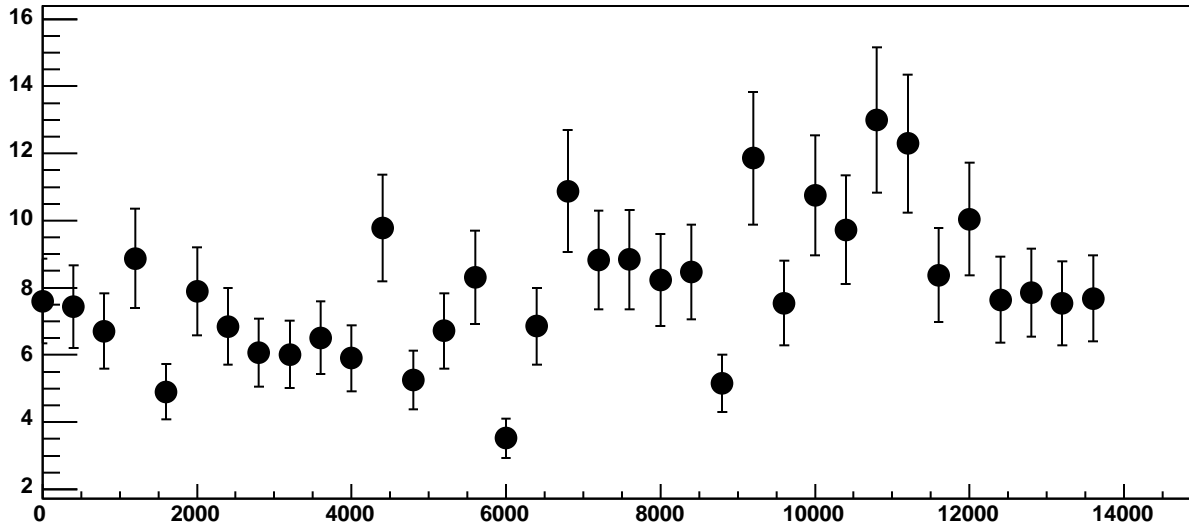


Chip 10, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

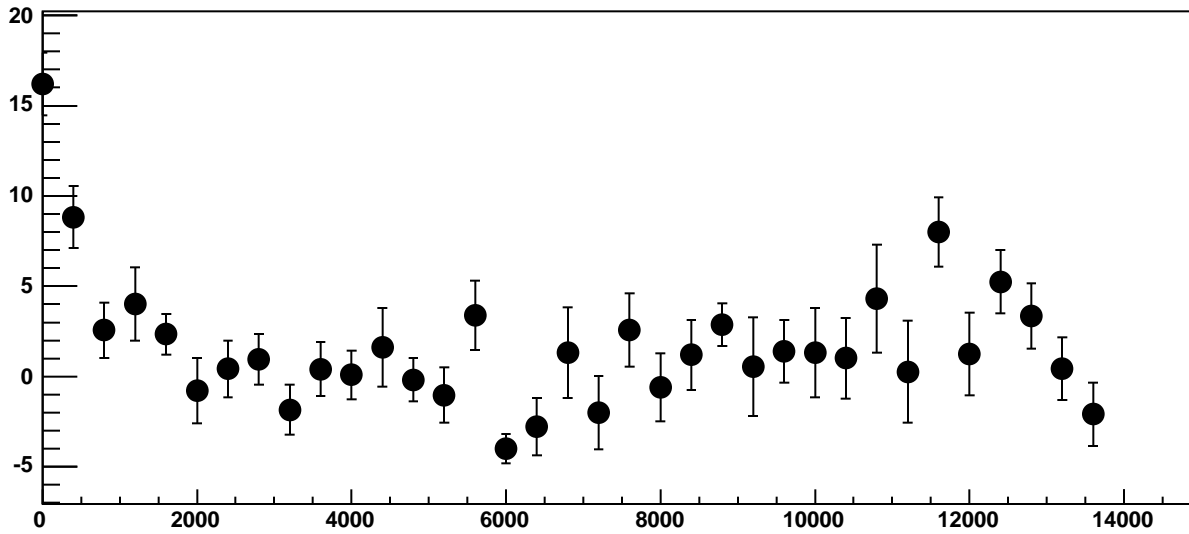


χ^2 / ndf 55 / 23
p0 195.2 ± 0.7268
p1 0.01962 ± 0.0001219

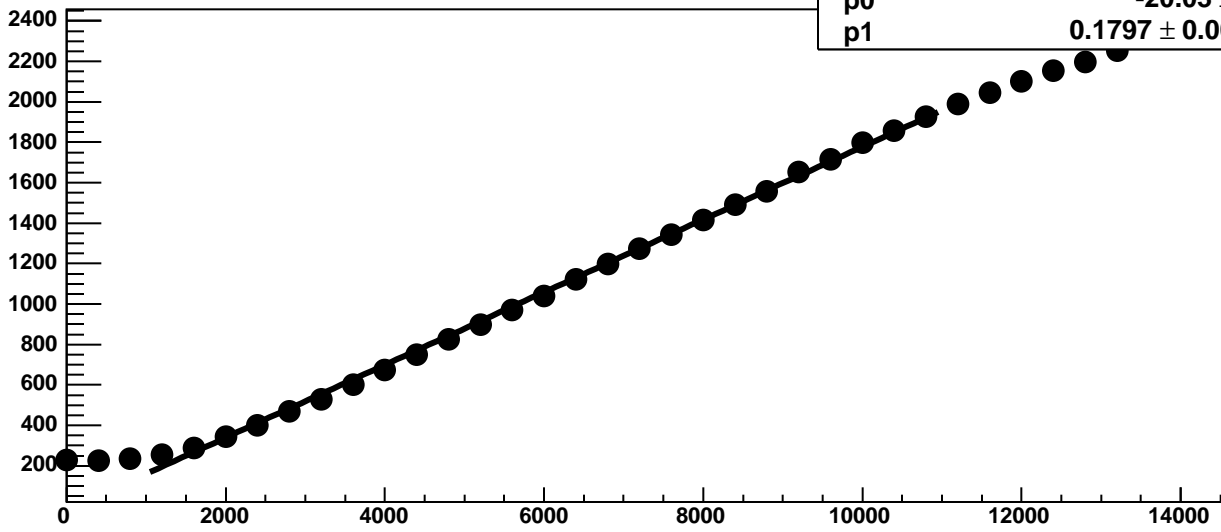
Chip 10, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



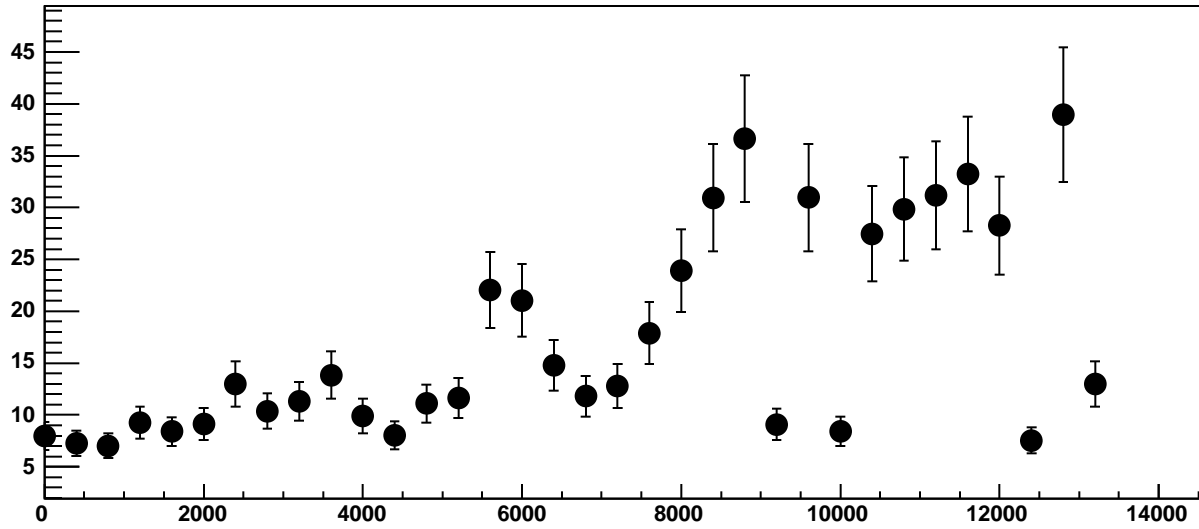
Chip 10, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



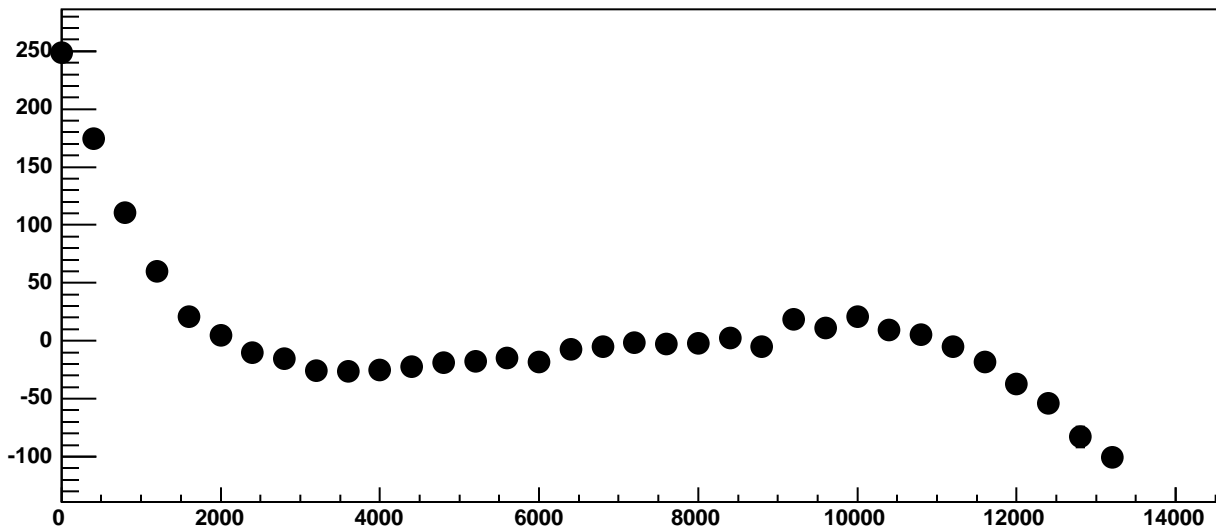
Chip 10, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



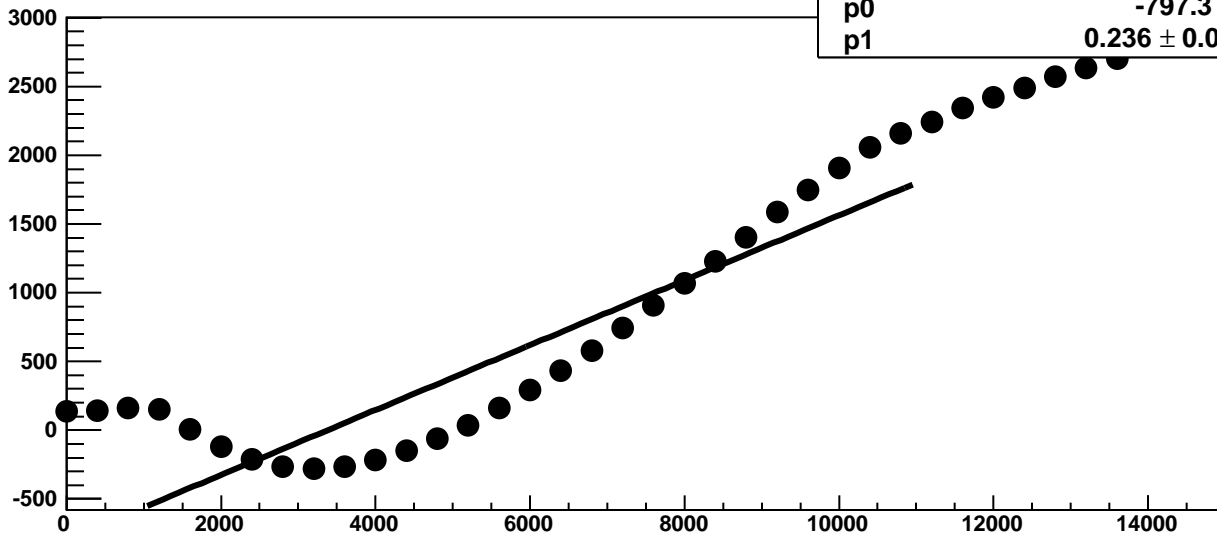
Chip 10, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

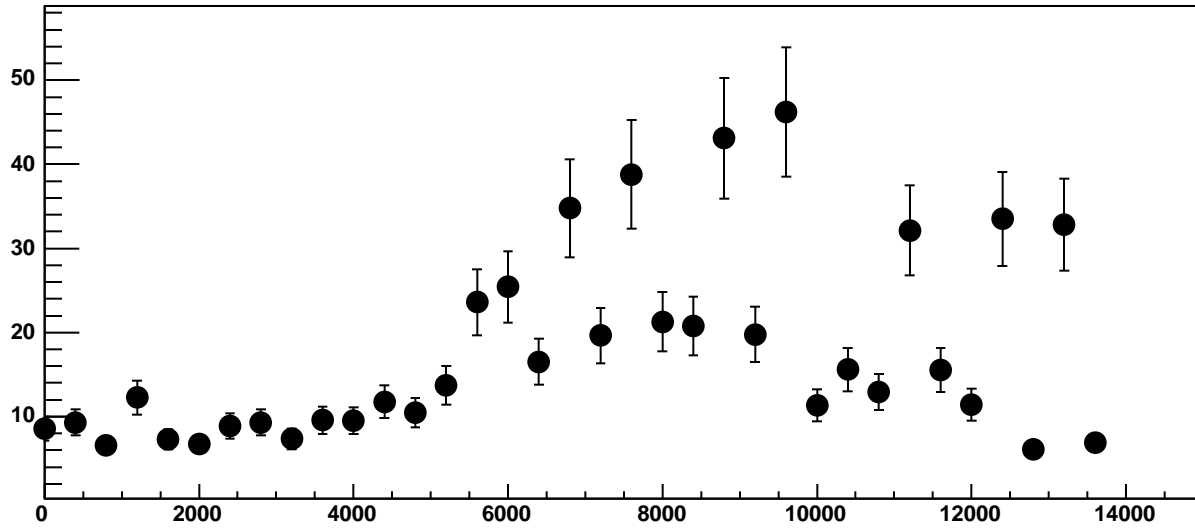


Chip 10, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

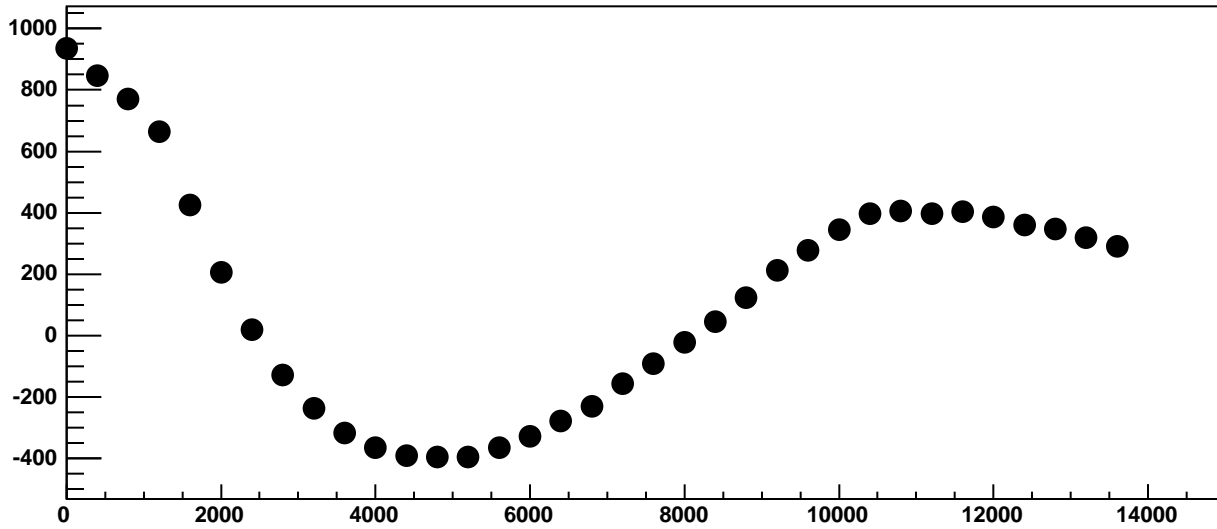


χ^2 / ndf 3.411e+05 / 23
p0 -797.3 ± 1.014
p1 0.236 ± 0.0002018

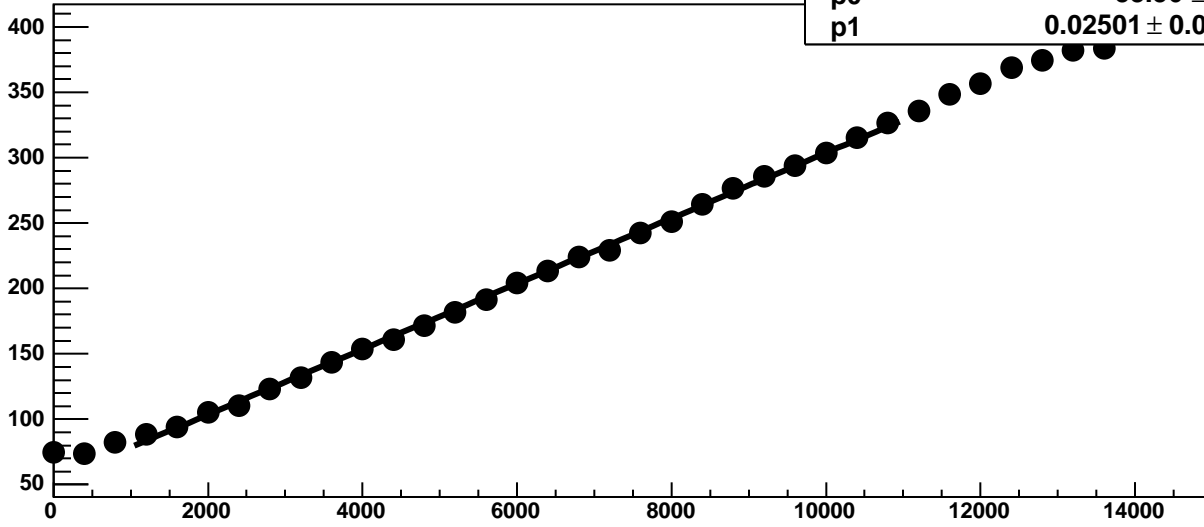
Chip 10, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

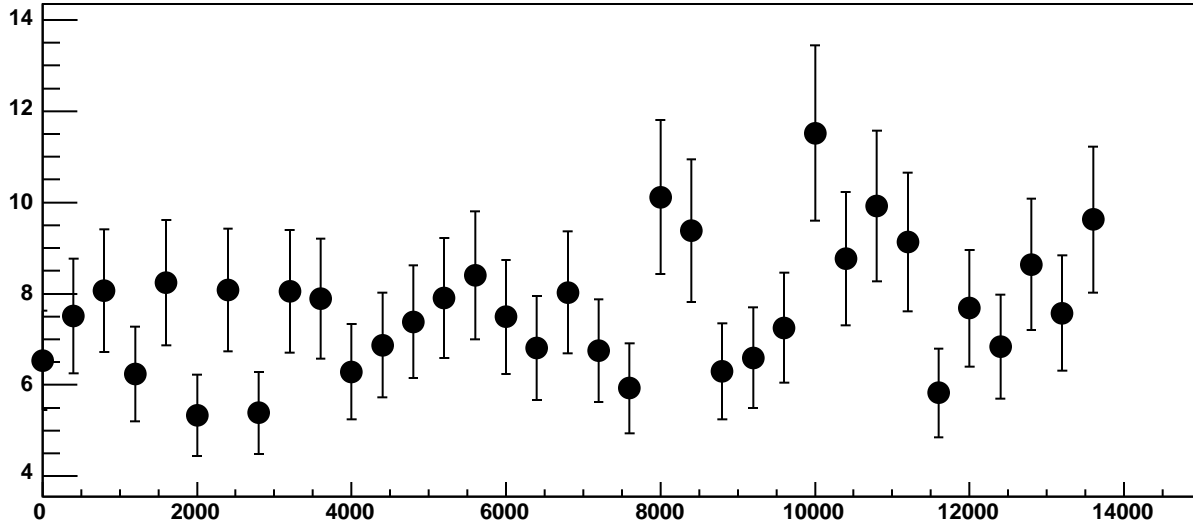


Chip 10, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

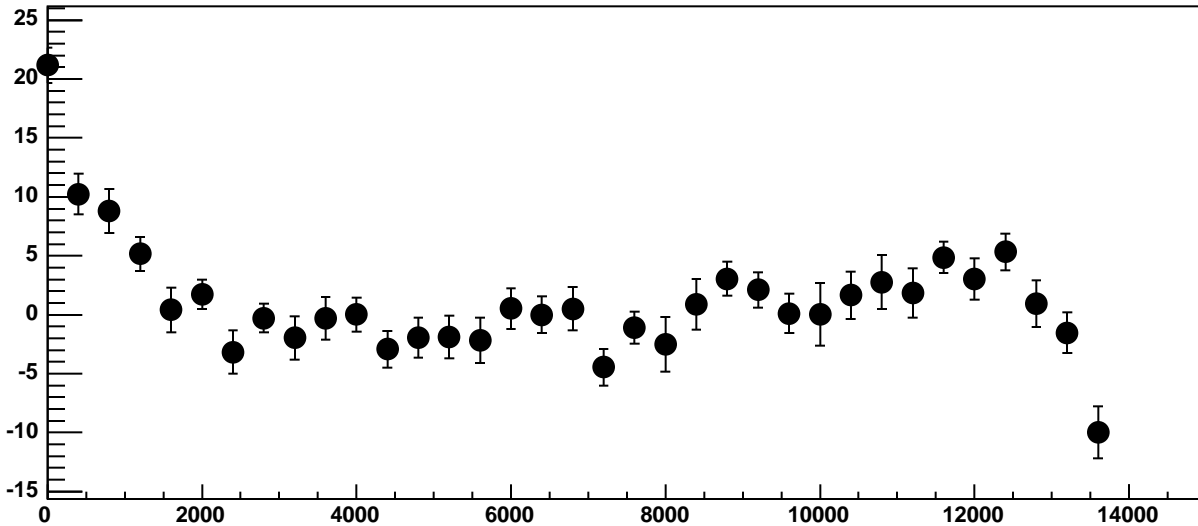


χ^2 / ndf 45.26 / 23
p0 53.56 ± 0.7326
p1 0.02501 ± 0.0001175

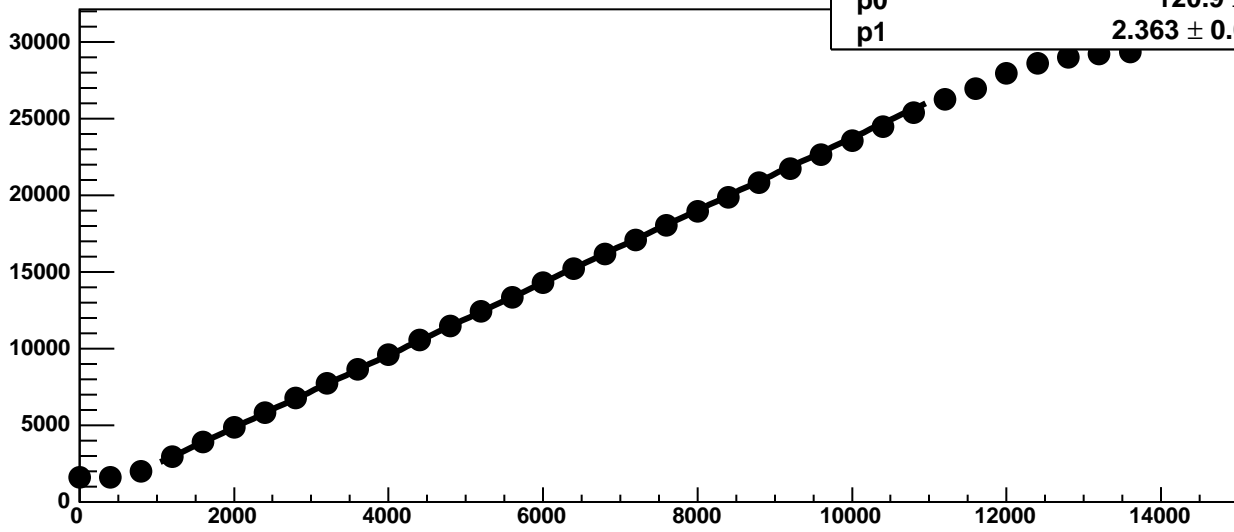
Chip 10, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



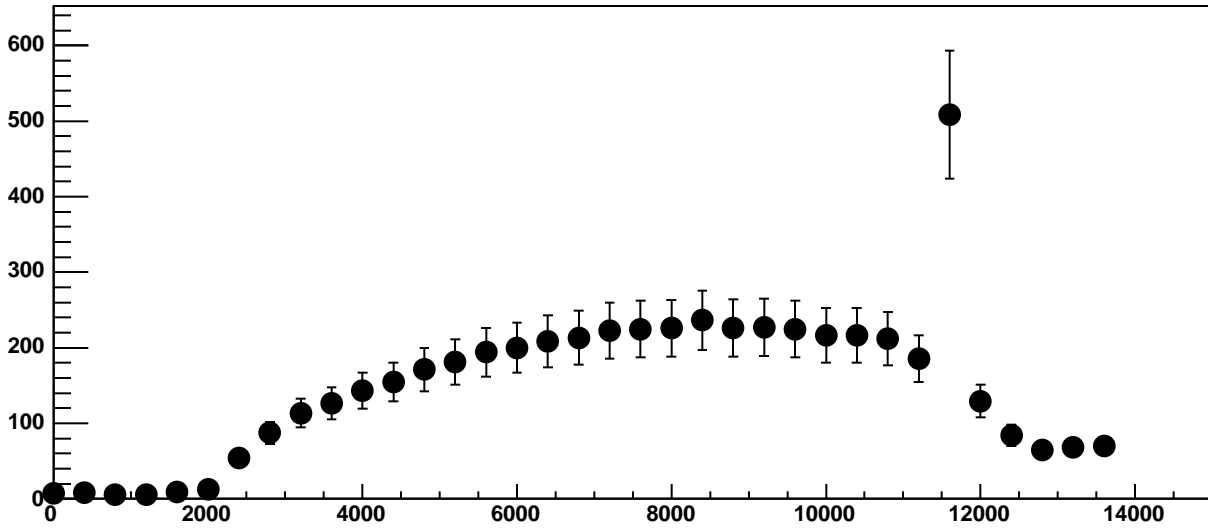
Chip 10, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



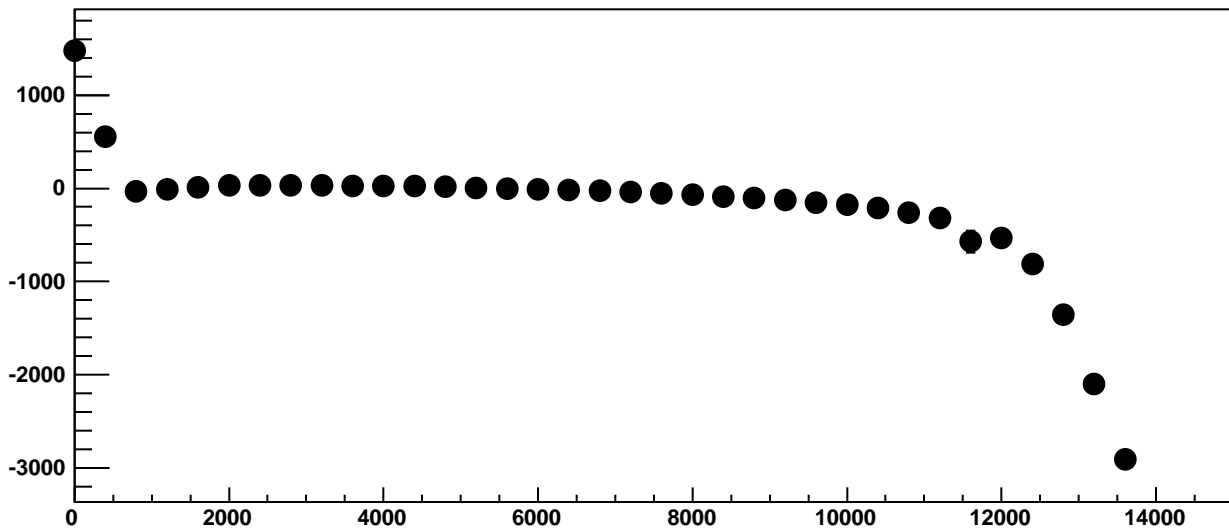
Chip 10, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



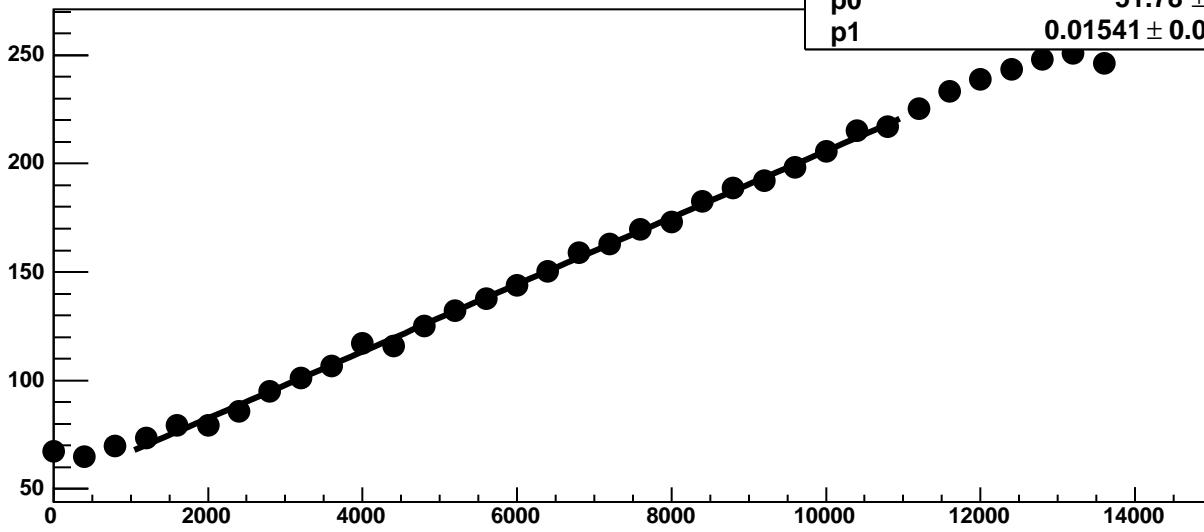
Chip 10, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

29.98 / 23

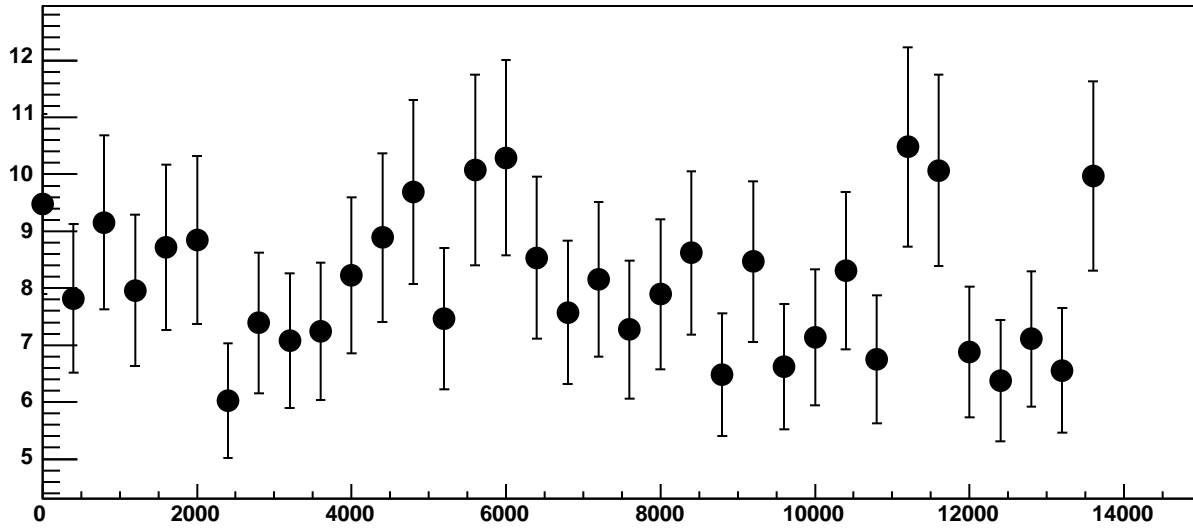
p0

51.78 ± 0.8122

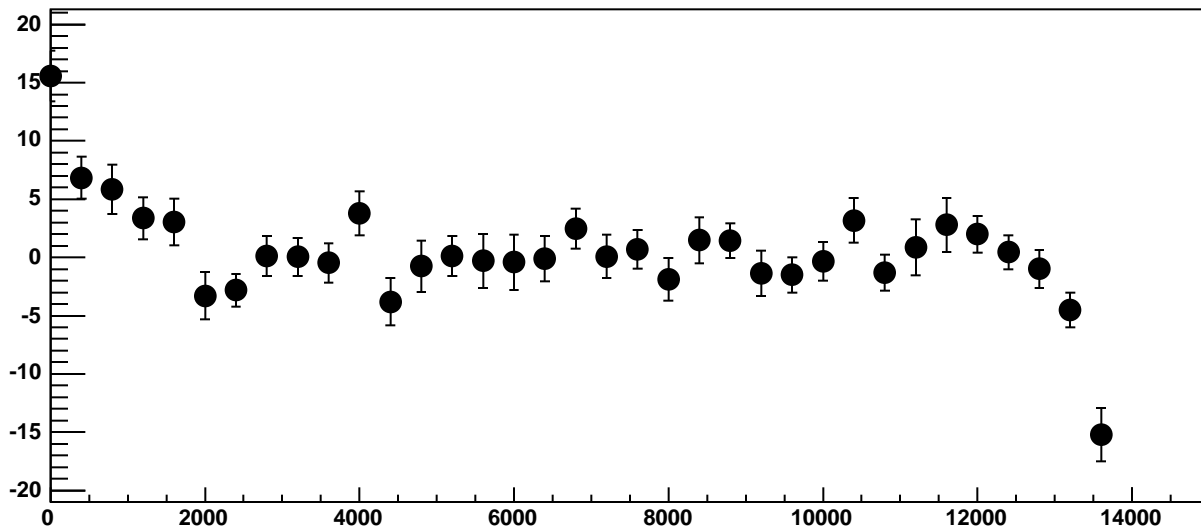
p1

0.01541 ± 0.0001193

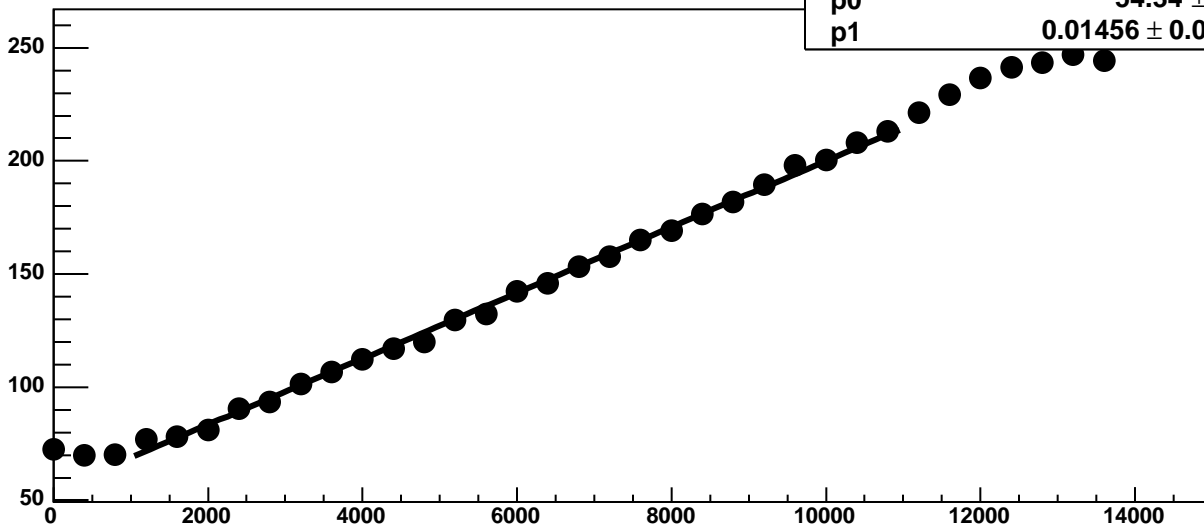
Chip 10, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

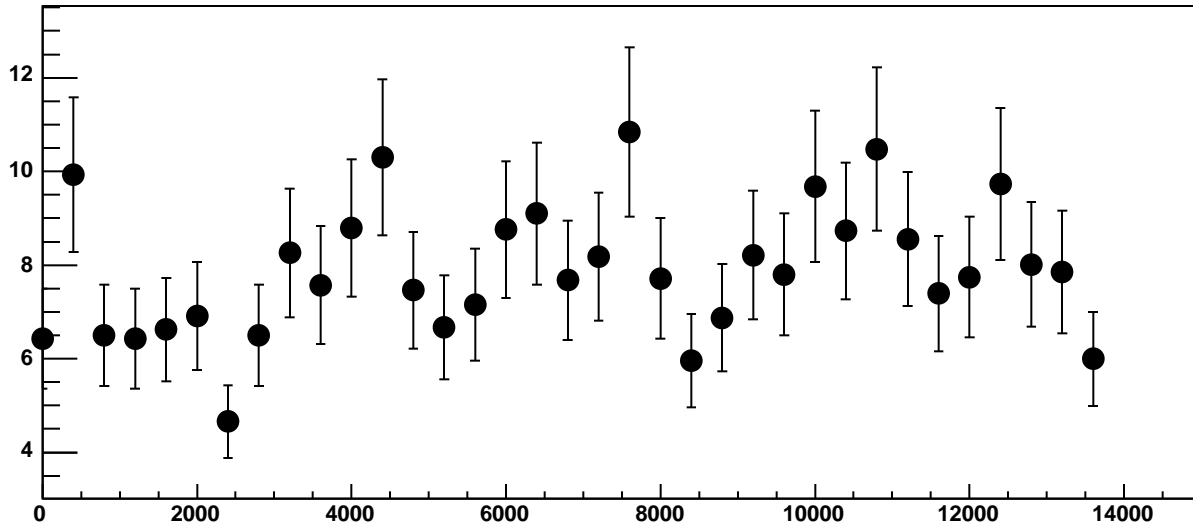


Chip 10, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

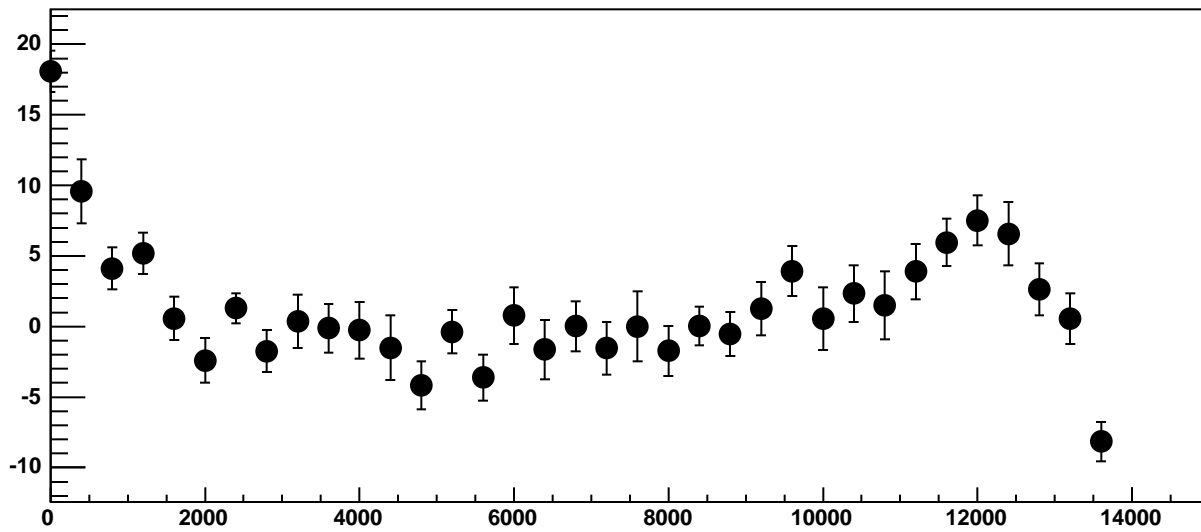


χ^2 / ndf 38.61 / 23
p0 54.34 ± 0.7244
p1 0.01456 ± 0.0001174

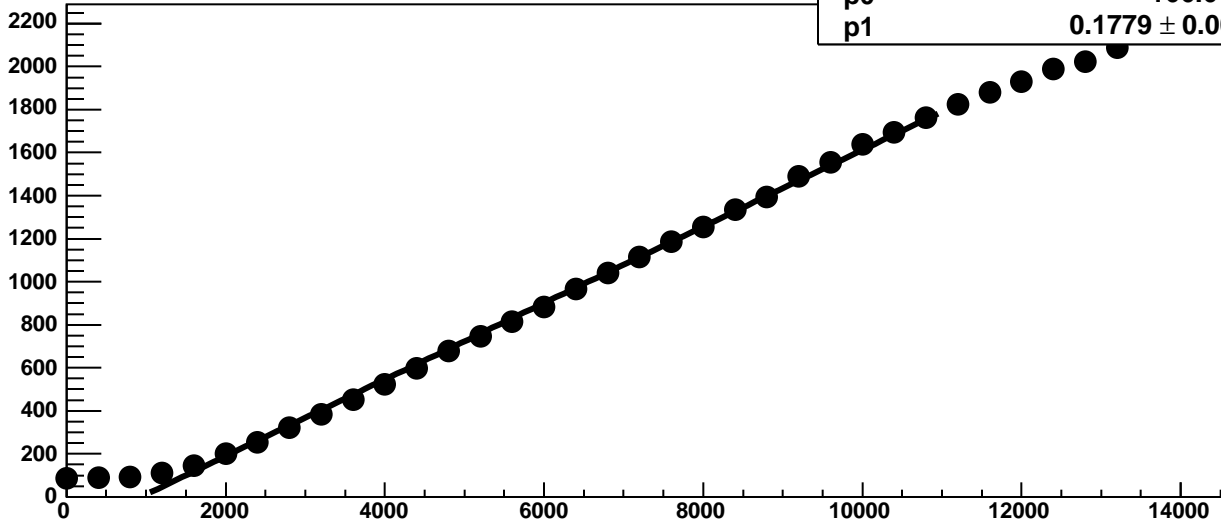
Chip 10, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



Chip 10, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1327 / 23

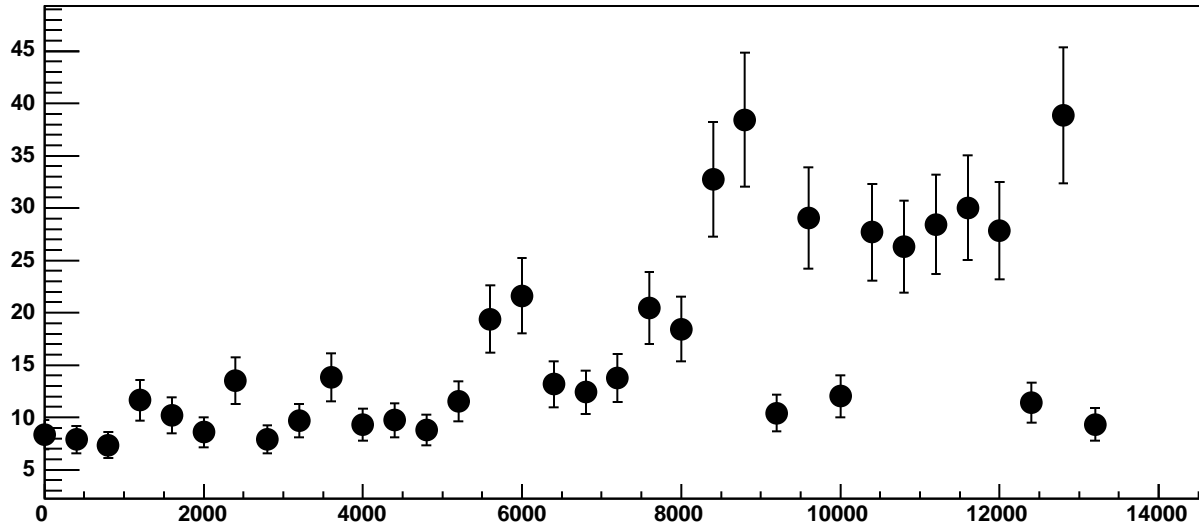
p0

-166.6 ± 1.191

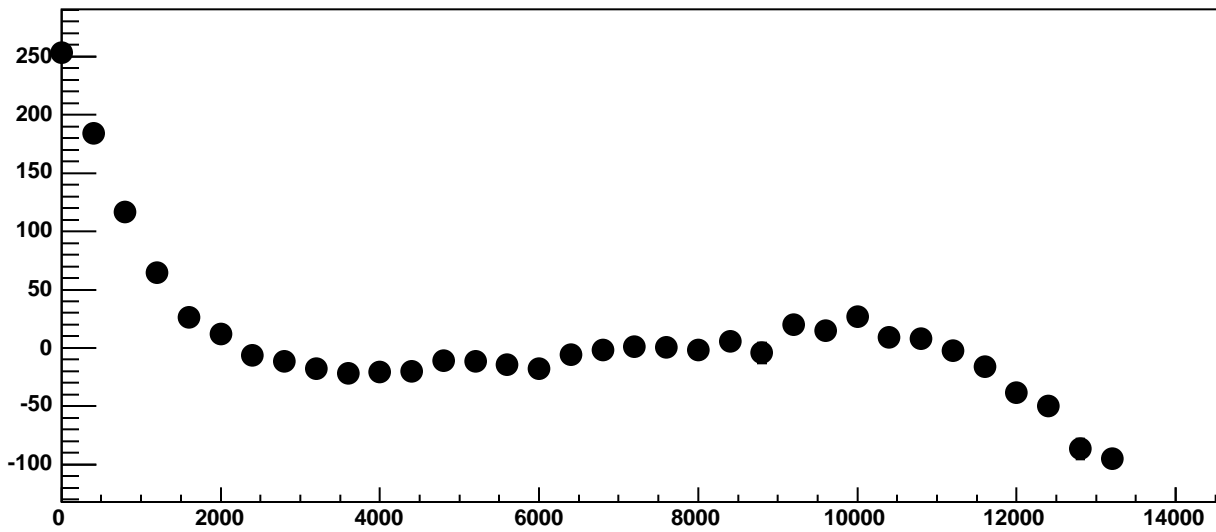
p1

0.1779 ± 0.0002215

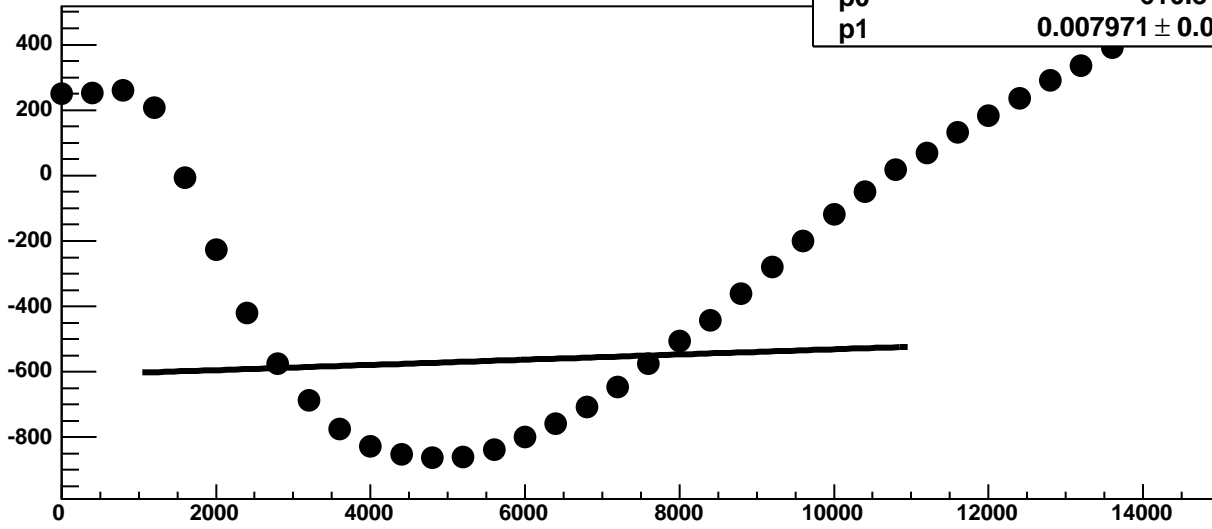
Chip 10, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC

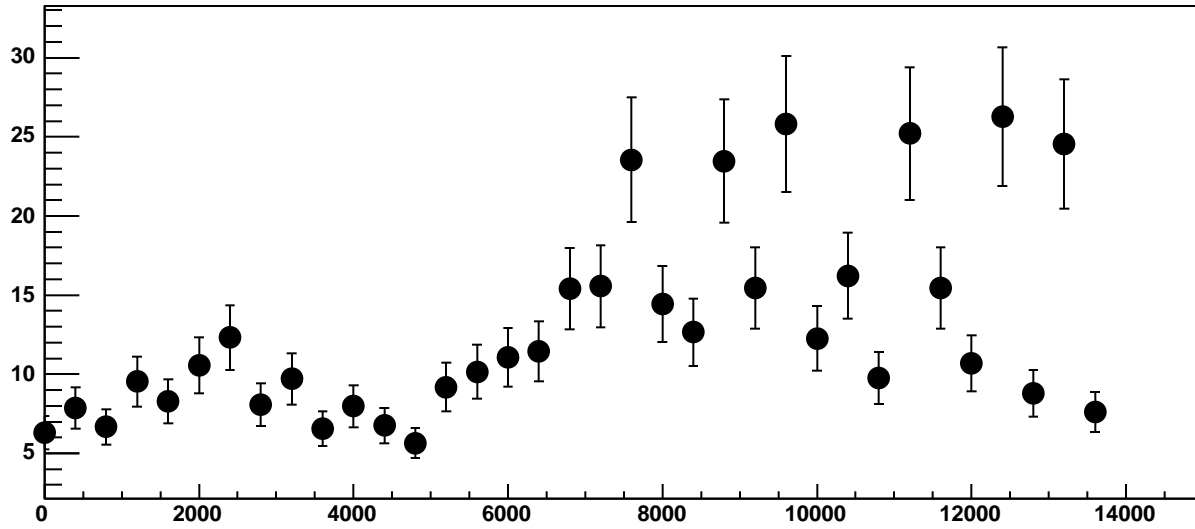


Chip 10, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

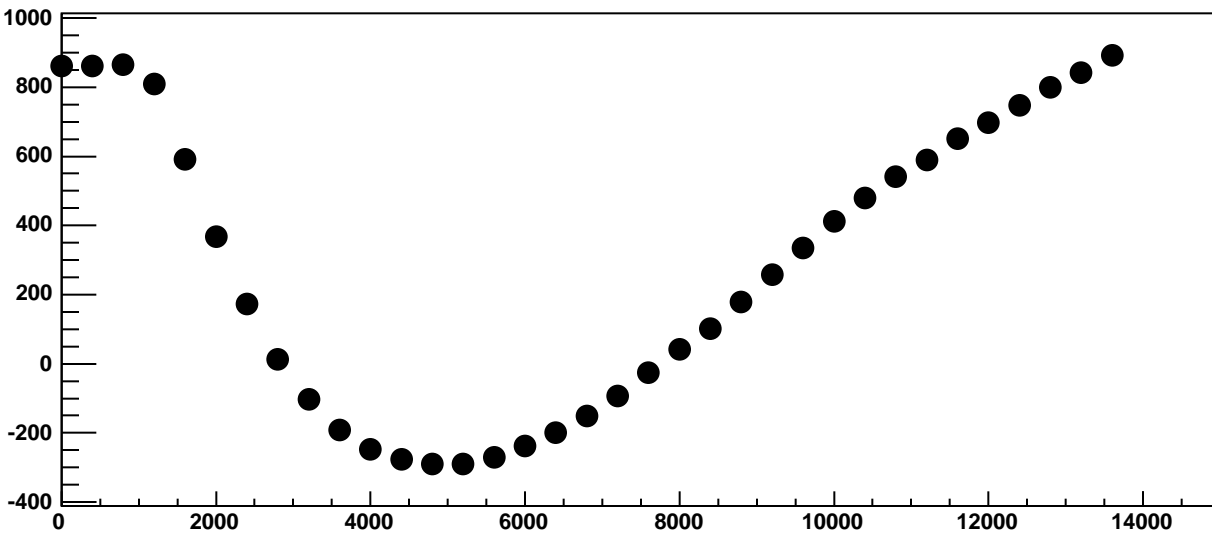


χ^2 / ndf 5.364e+05 / 23
p0 -610.8 ± 1.006
p1 0.007971 ± 0.0001825

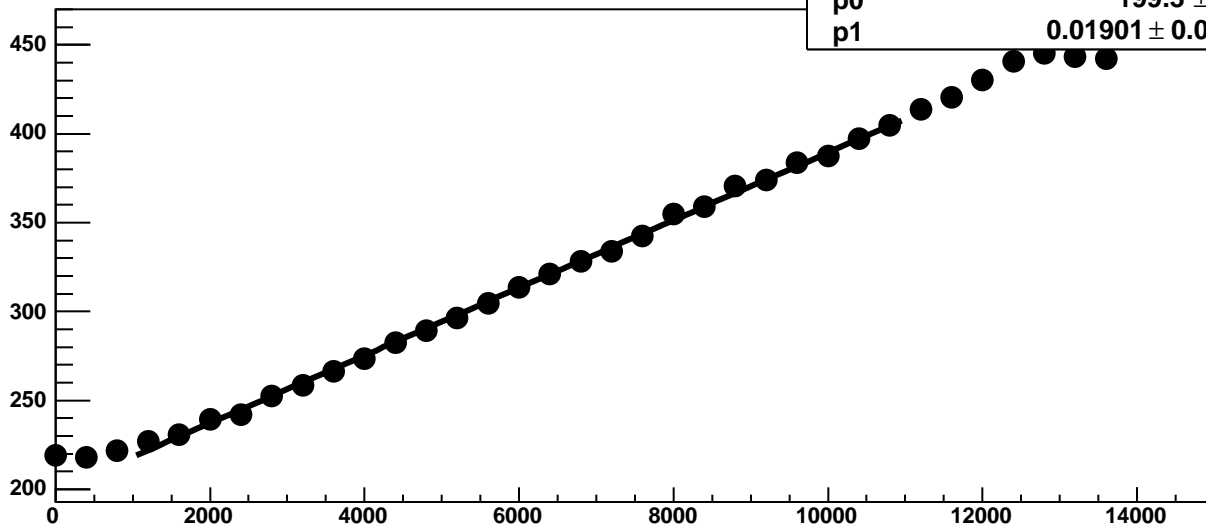
Chip 10, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

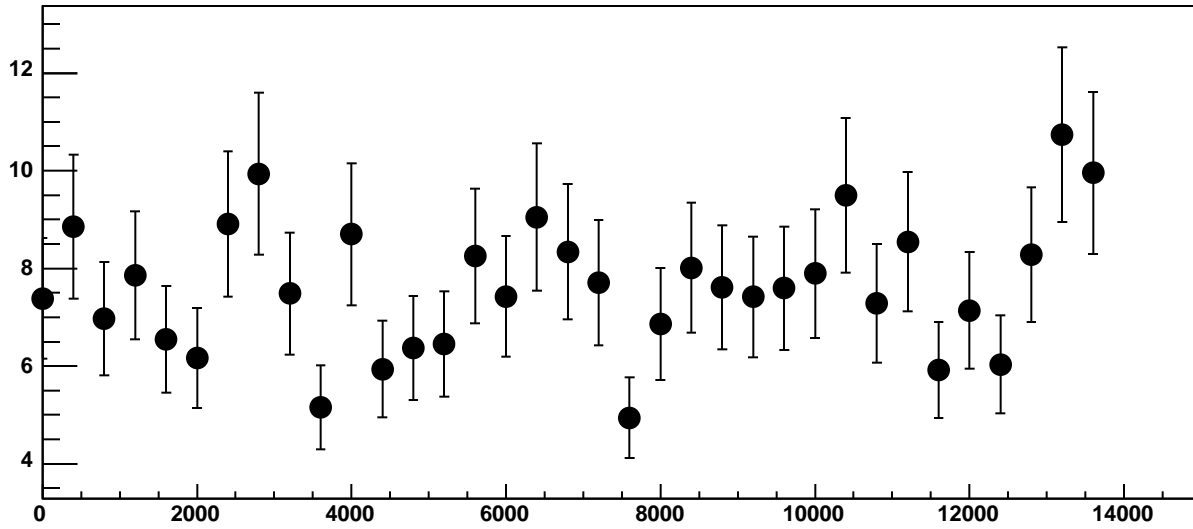


Chip 10, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

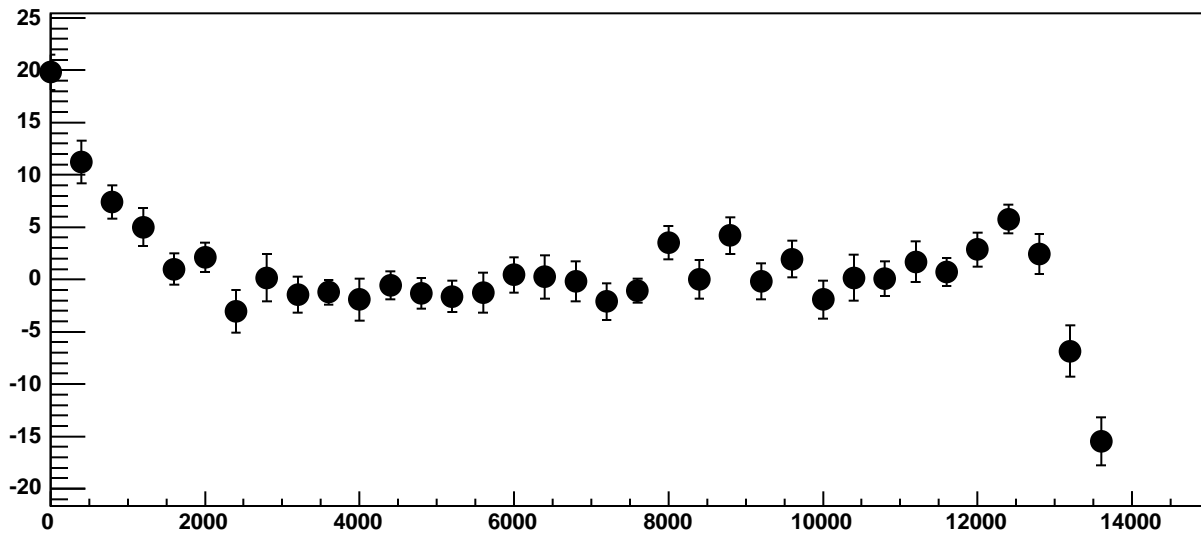


χ^2 / ndf 33.47 / 23
p0 199.3 ± 0.7622
p1 0.01901 ± 0.0001176

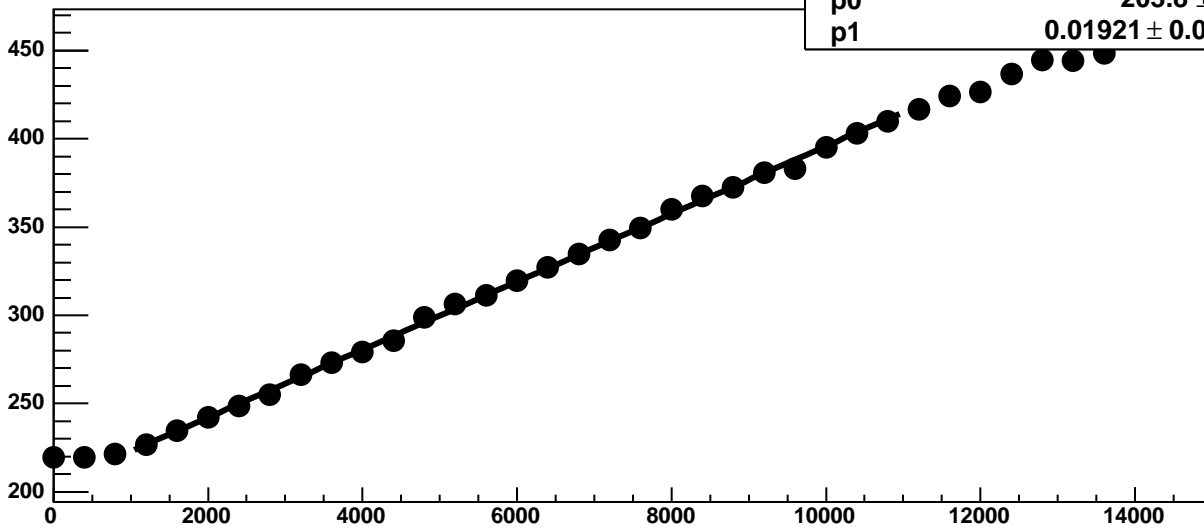
Chip 10, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

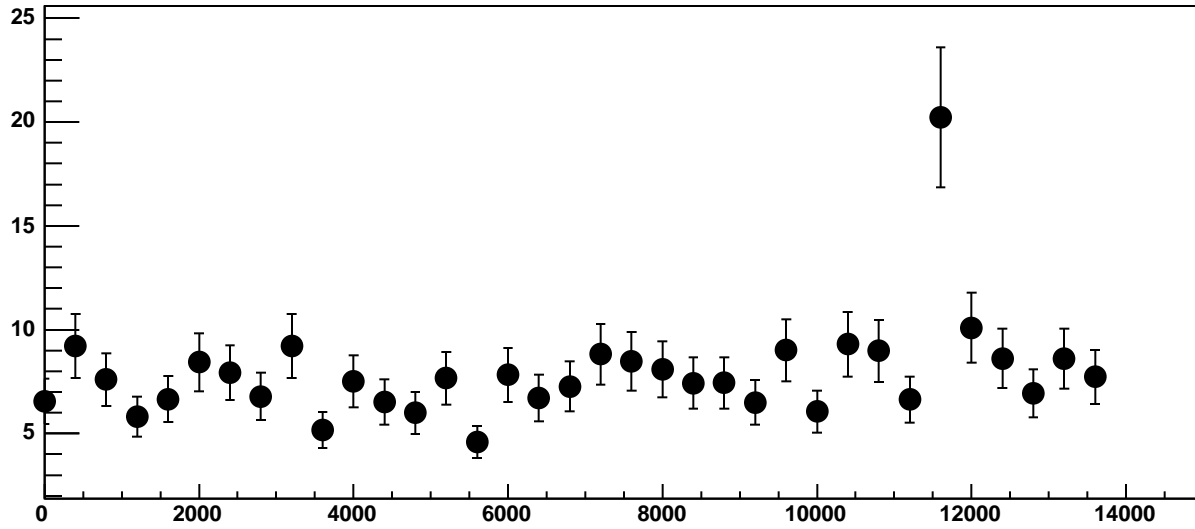


Chip 10, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC

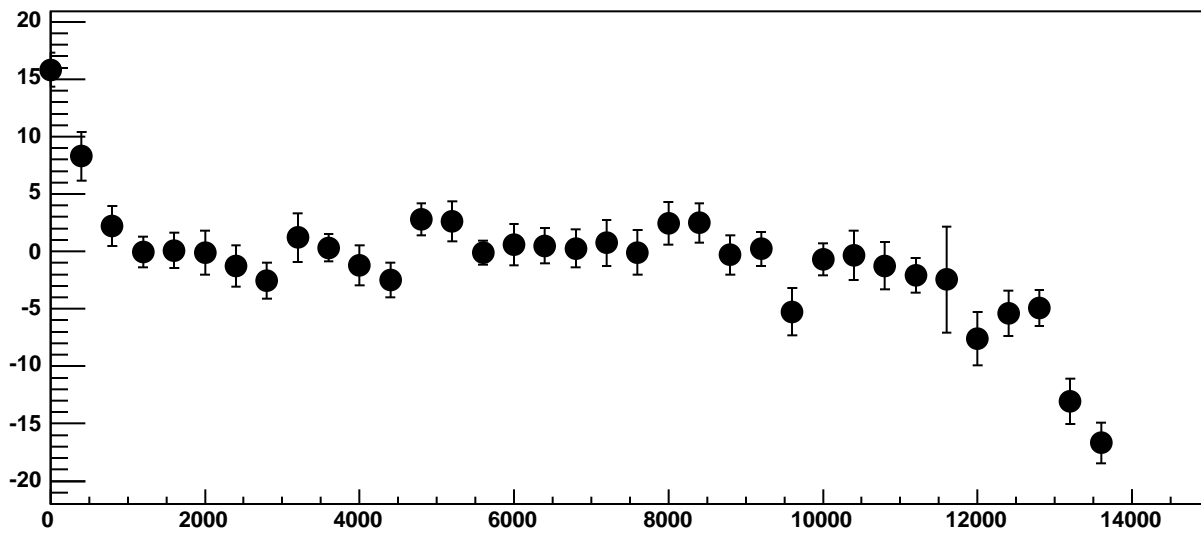


χ^2 / ndf 24.48 / 23
p0 203.8 ± 0.7311
p1 0.01921 ± 0.0001155

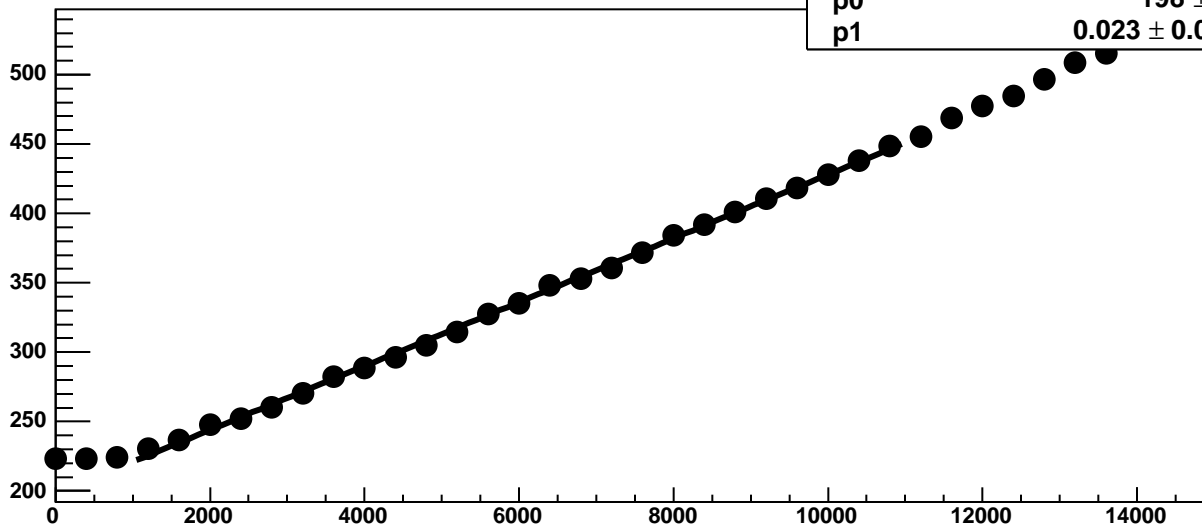
Chip 10, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

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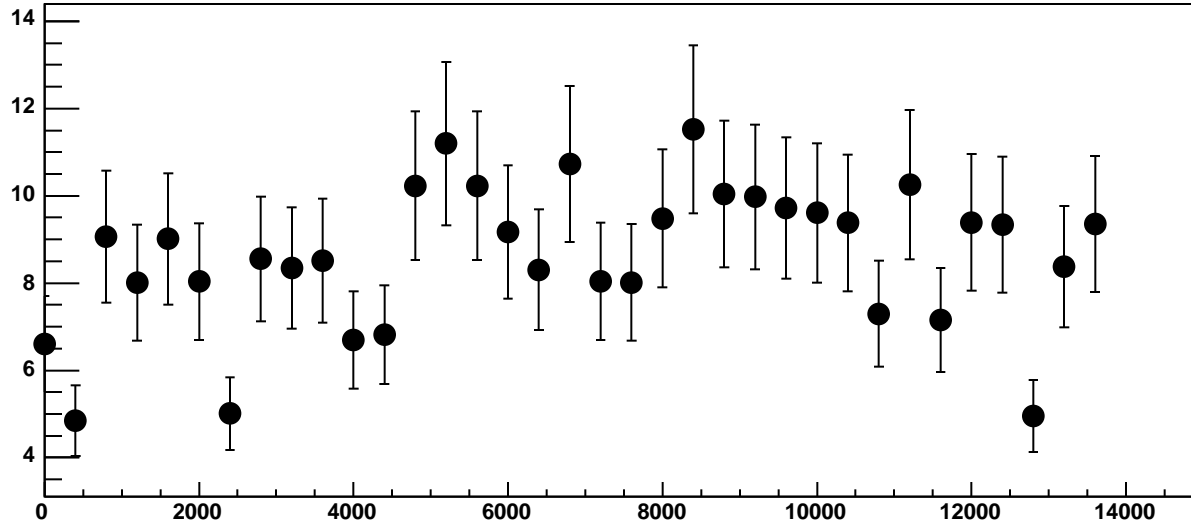
p0

198 ± 0.8175

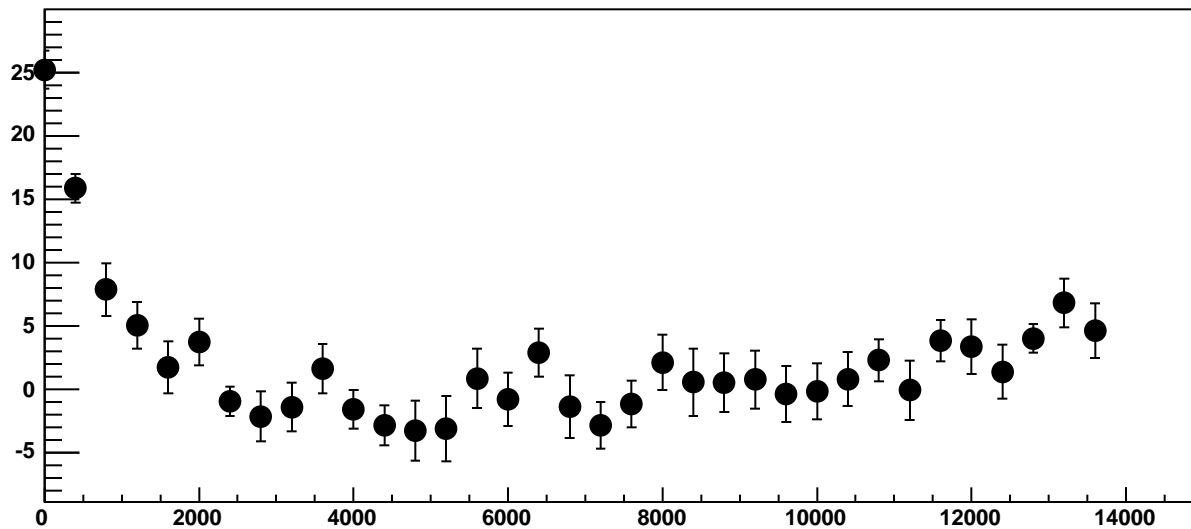
p1

0.023 ± 0.0001308

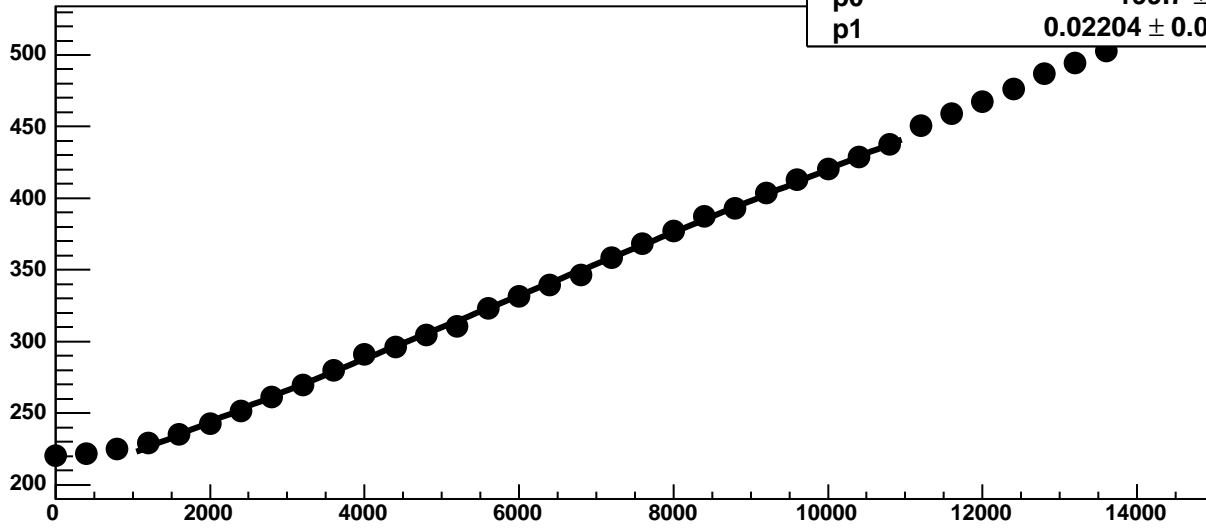
Chip 10, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

15.68 / 23

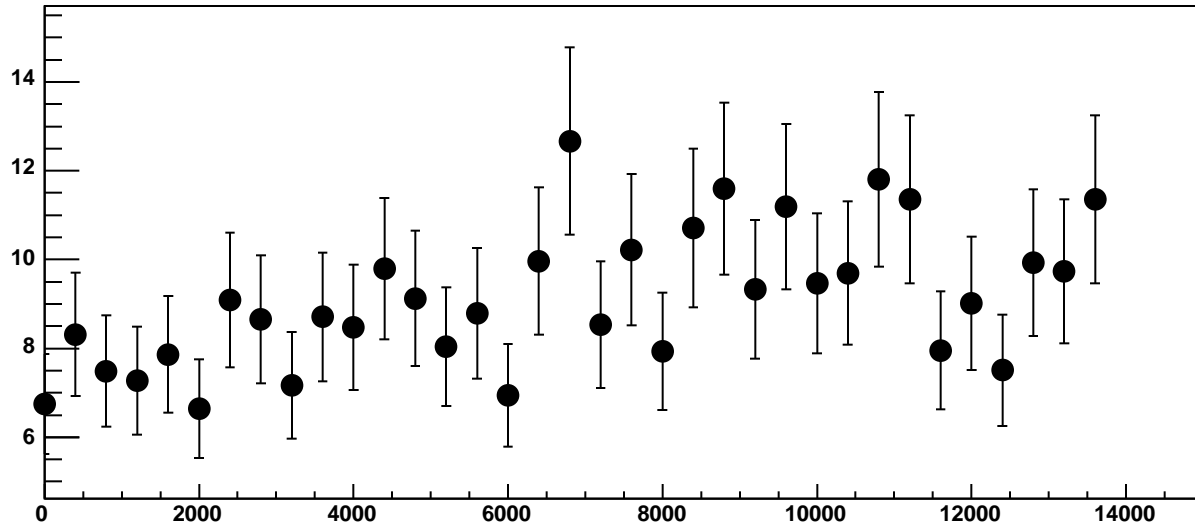
p0

199.7 ± 0.8644

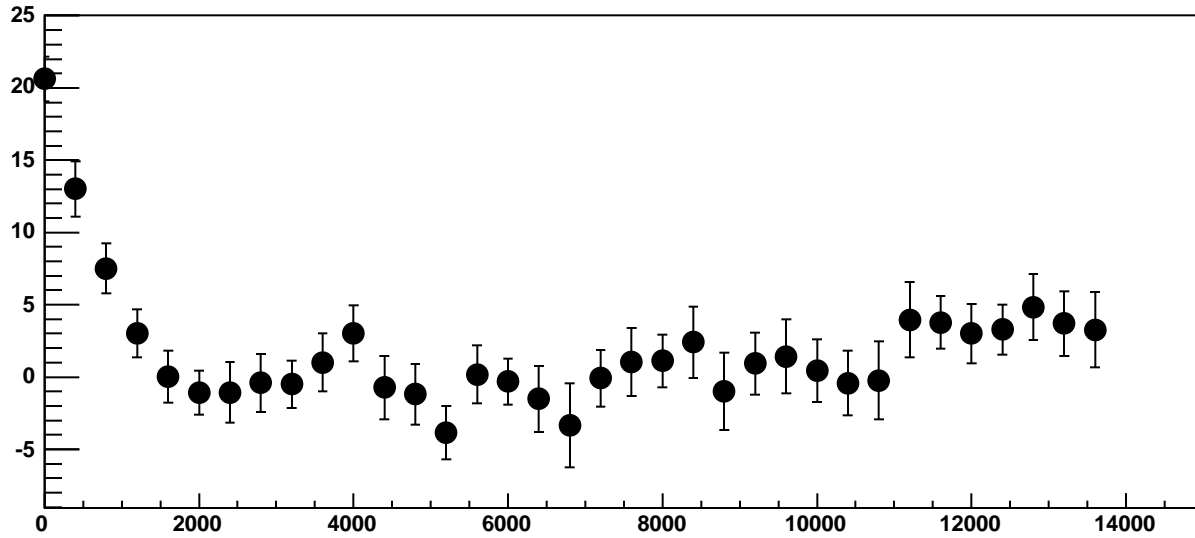
p1

0.02204 ± 0.0001418

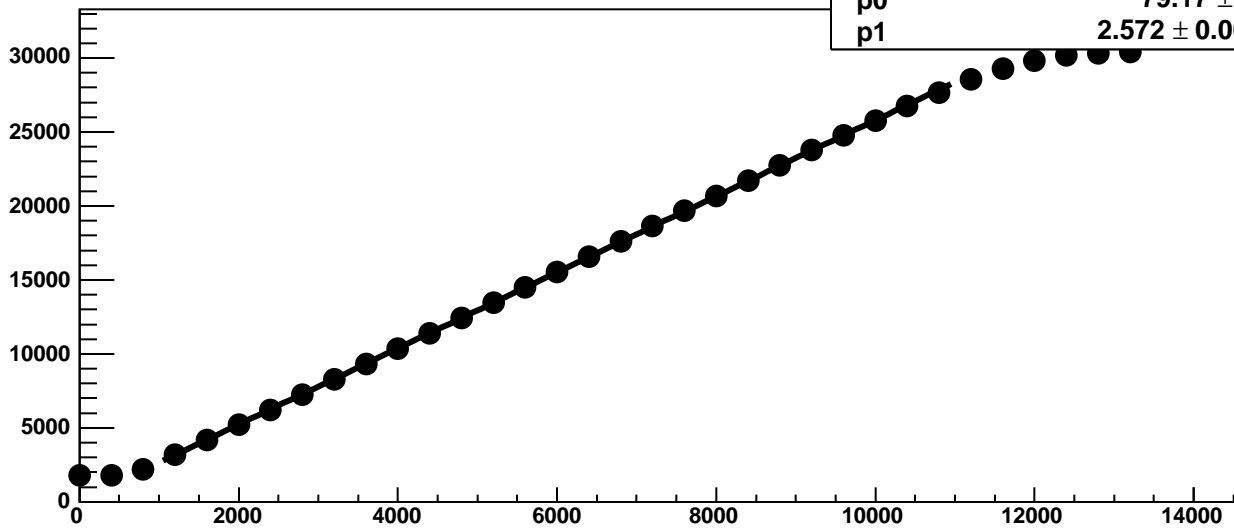
Chip 10, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



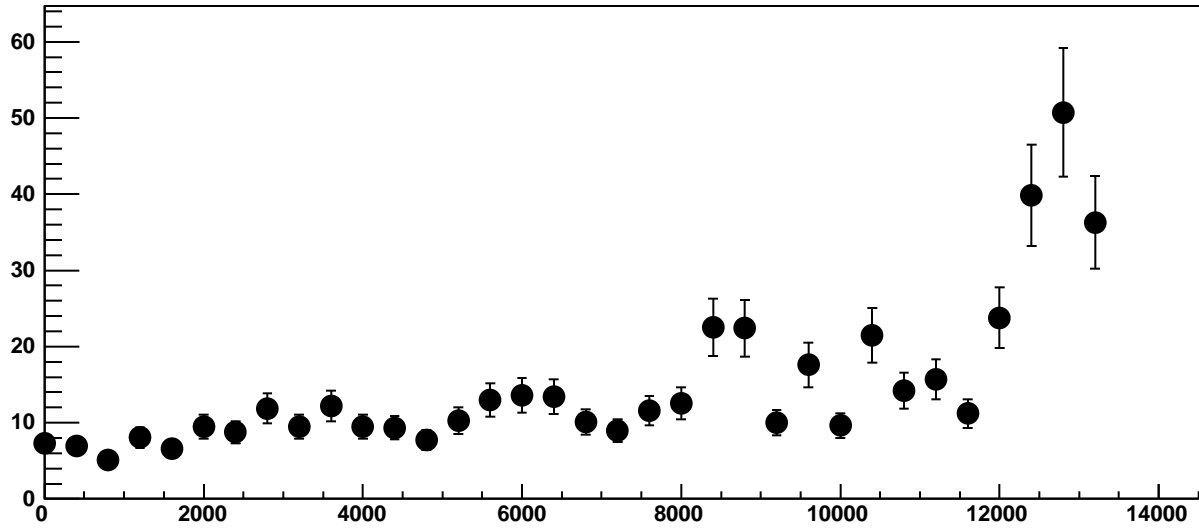
Chip 10, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



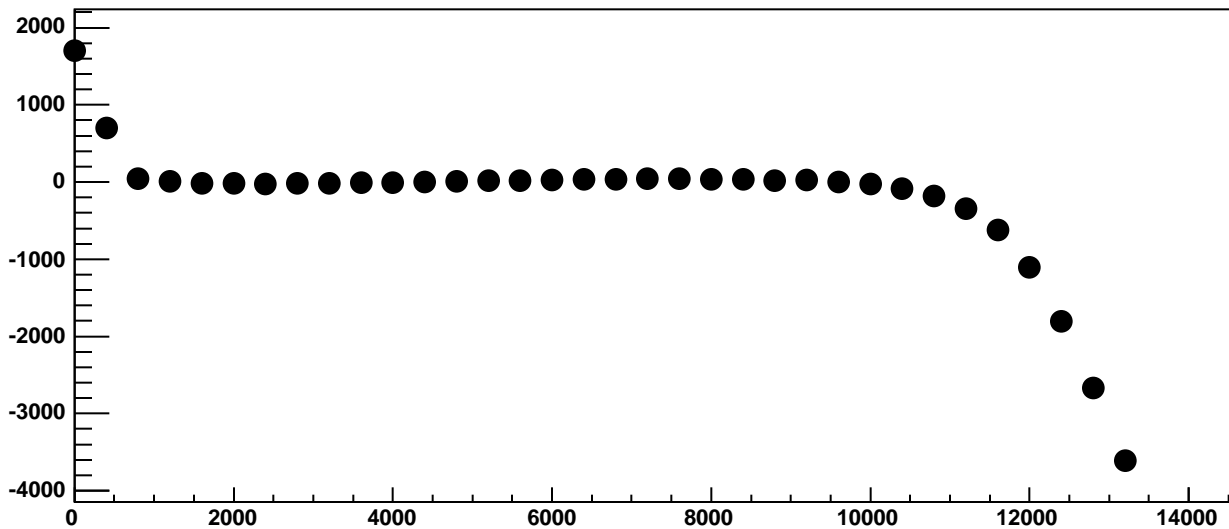
Chip 10, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC



Chip 10, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC

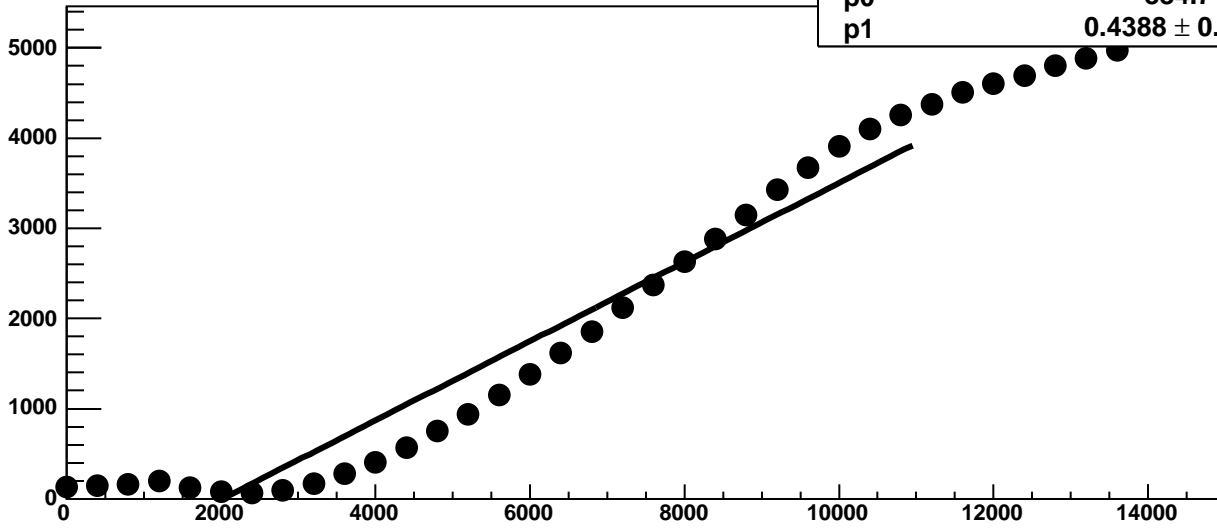


Chip 10, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC

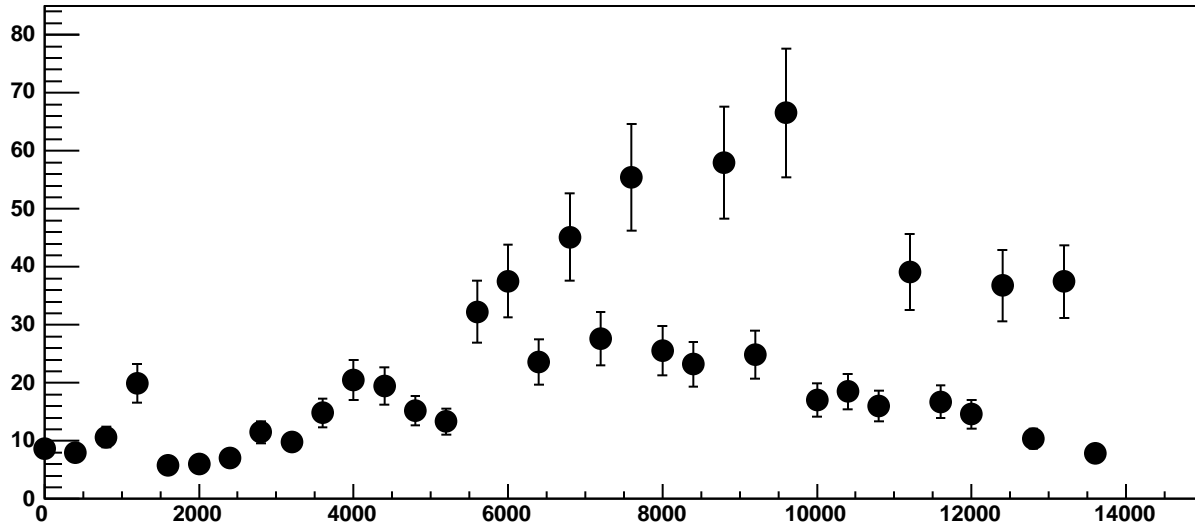


Chip 10, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

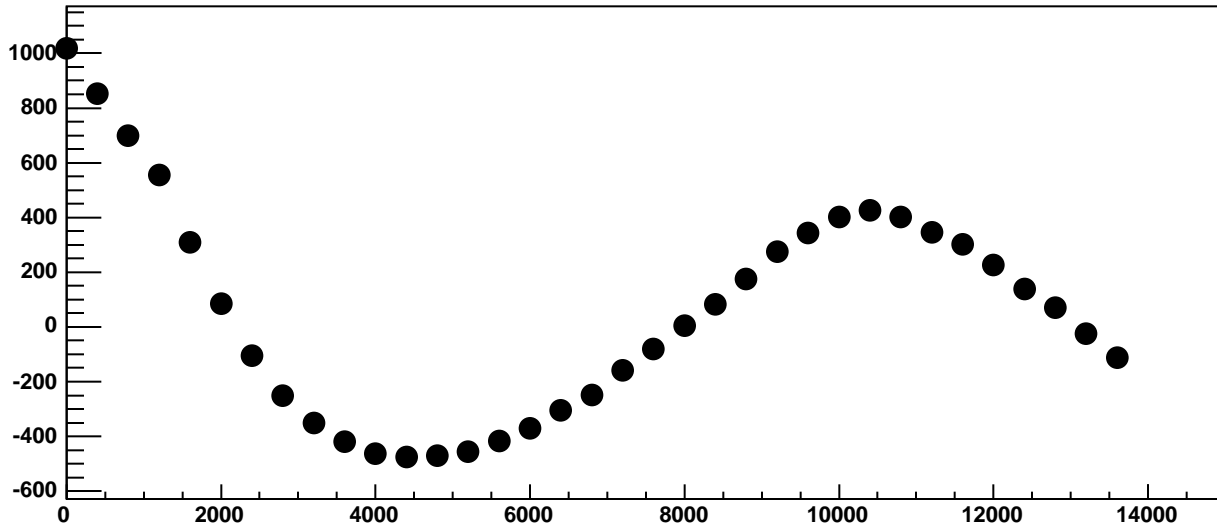
χ^2 / ndf 2.324e+05 / 23
p0 -884.7 ± 1.035
p1 0.4388 ± 0.000238



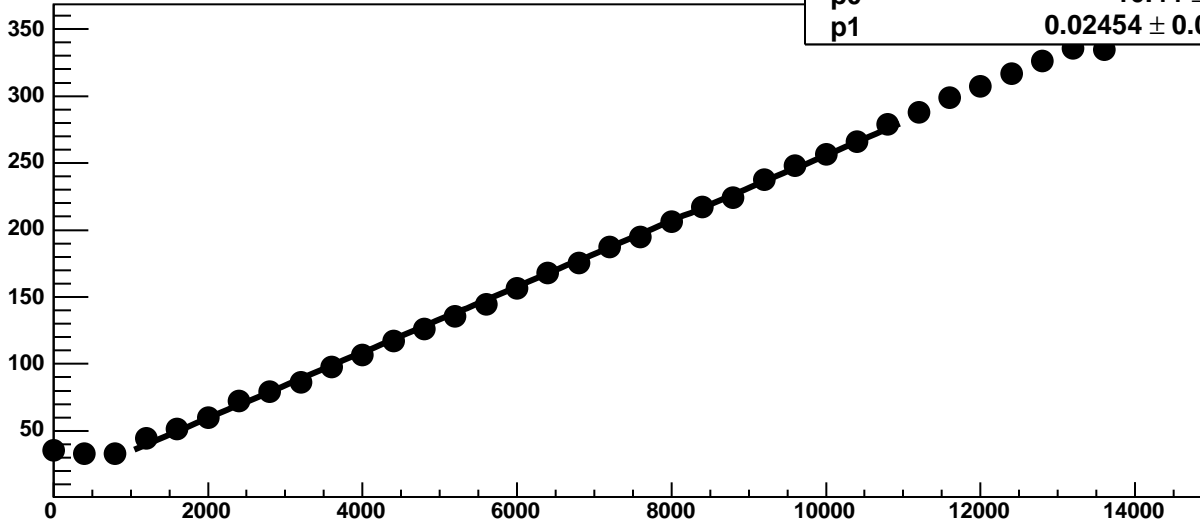
Chip 10, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

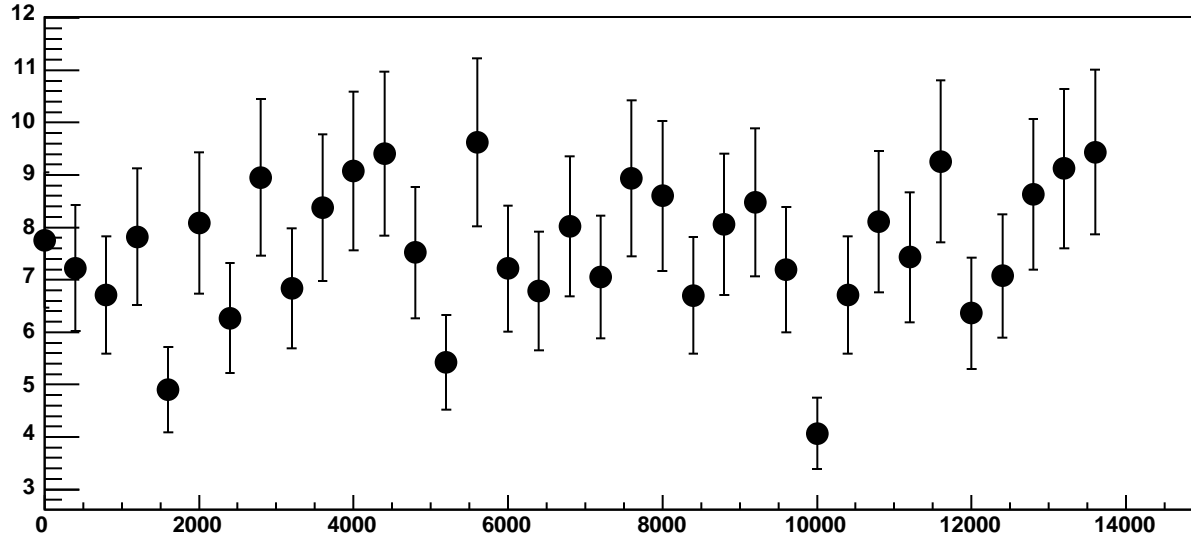


Chip 10, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

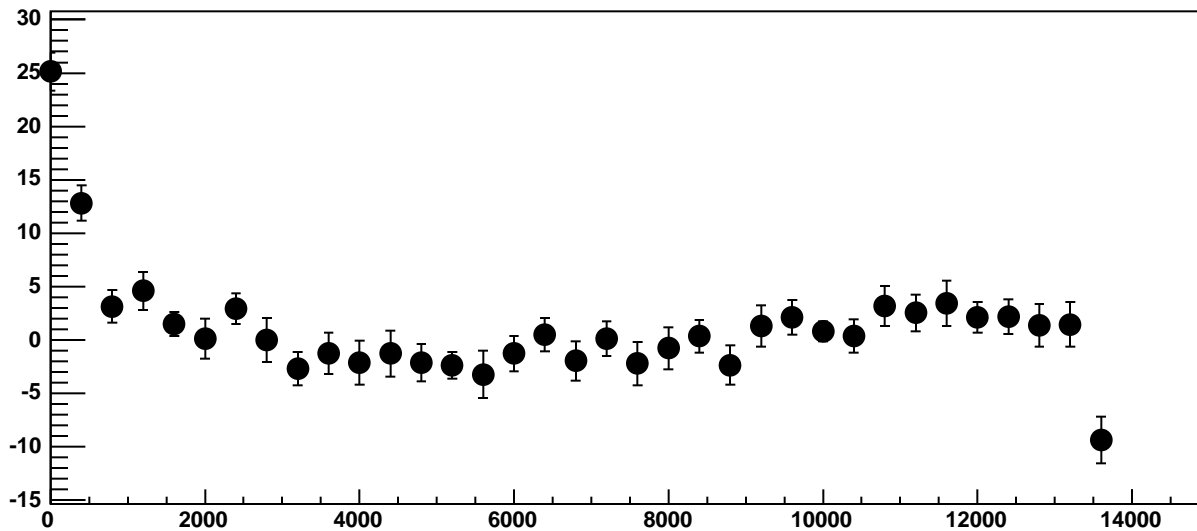


χ^2 / ndf 35.29 / 23
p0 10.44 ± 0.7219
p1 0.02454 ± 0.0001051

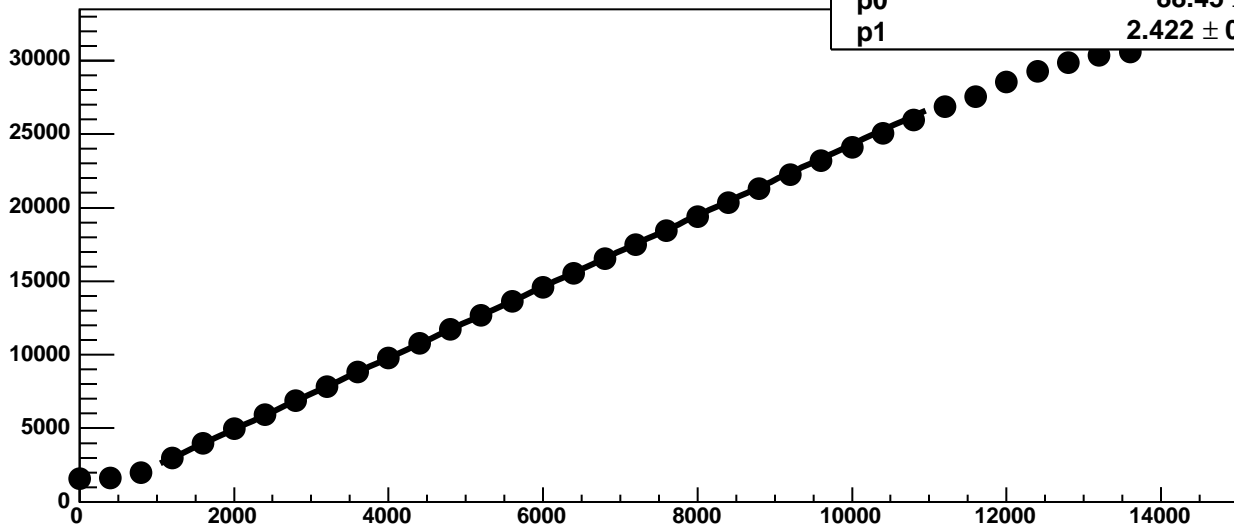
Chip 10, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

245 / 23

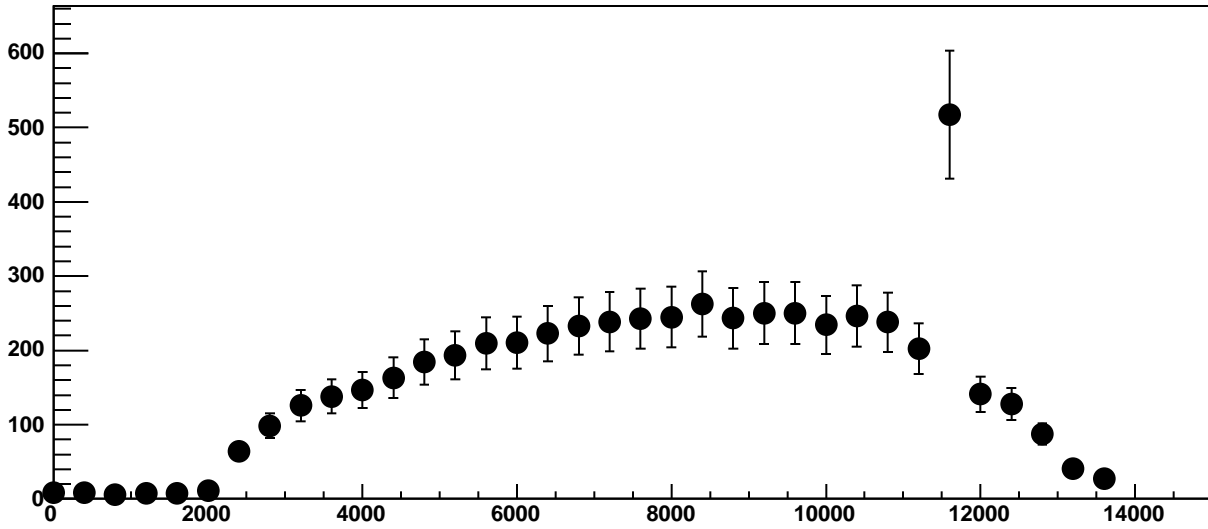
p0

88.45 ± 2.948

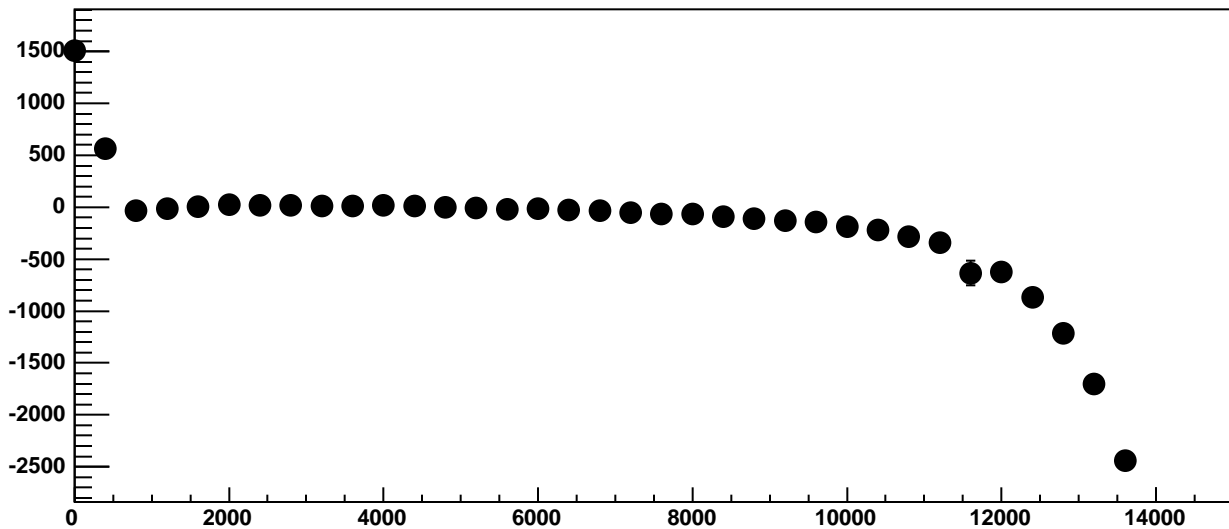
p1

2.422 ± 0.00175

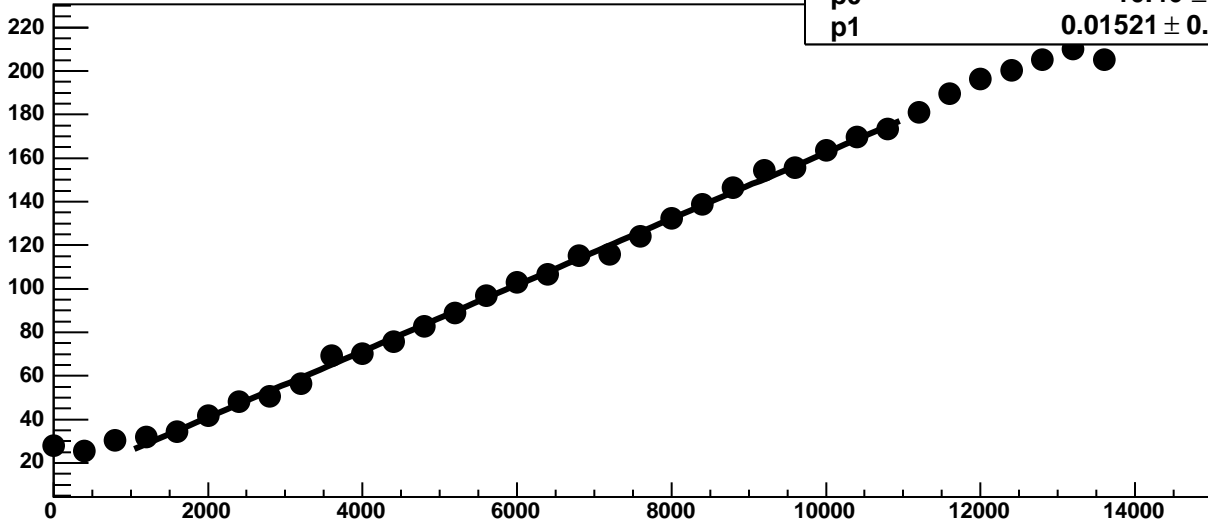
Chip 10, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

37.92 / 23

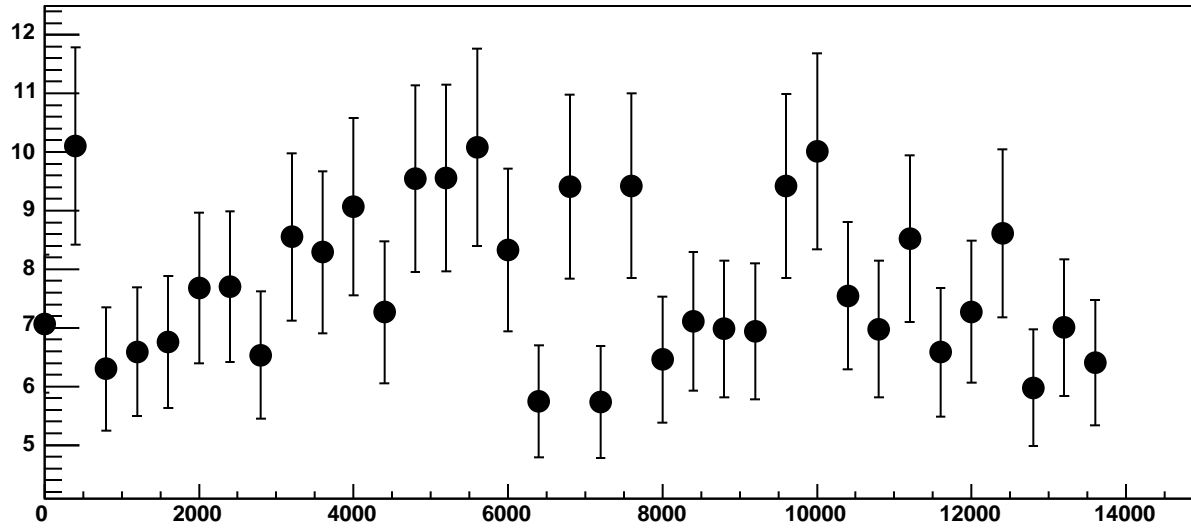
p0

10.46 ± 0.7855

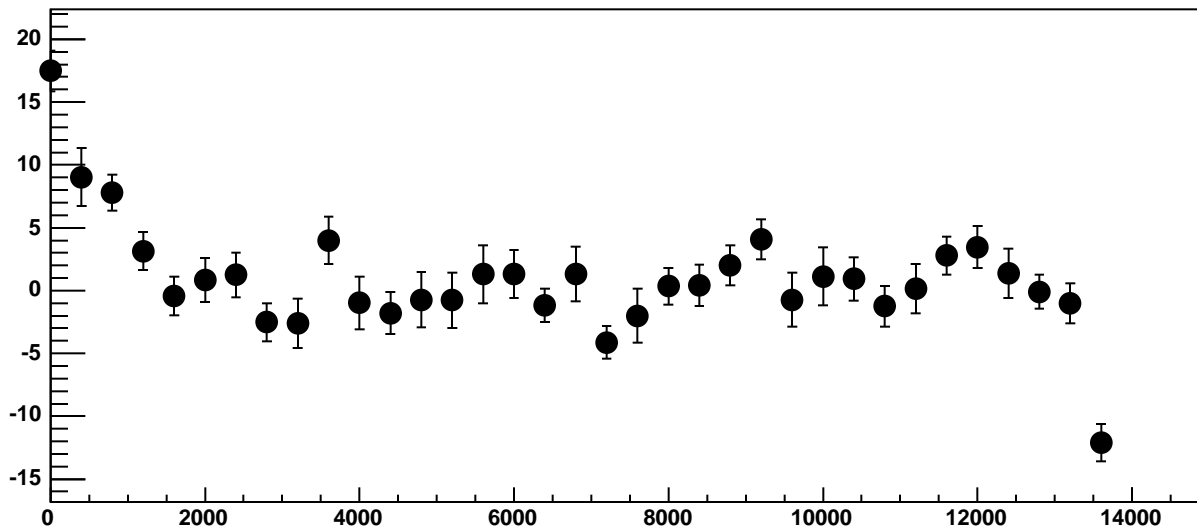
p1

0.01521 ± 0.000118

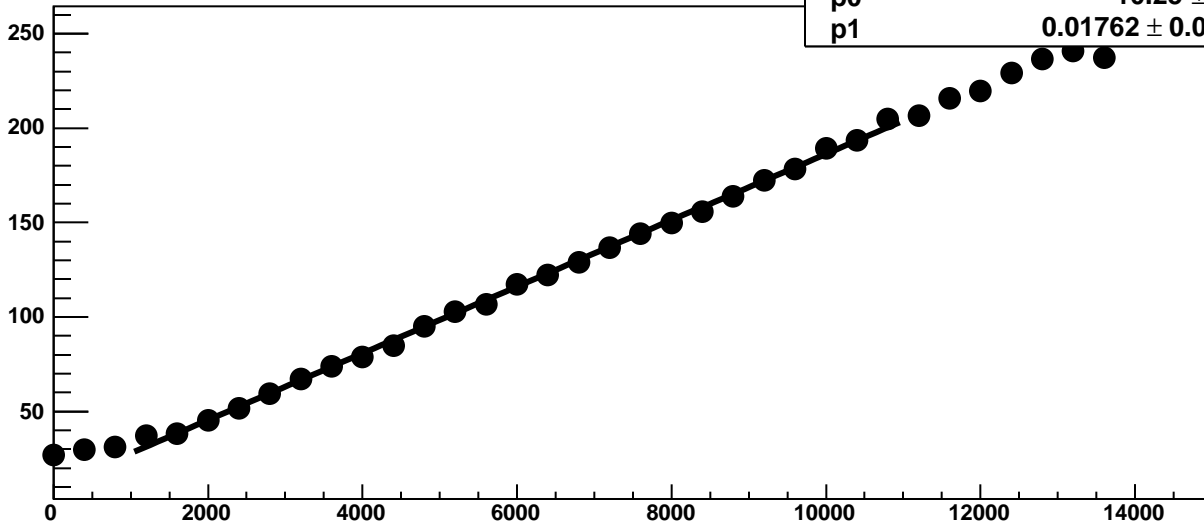
Chip 10, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

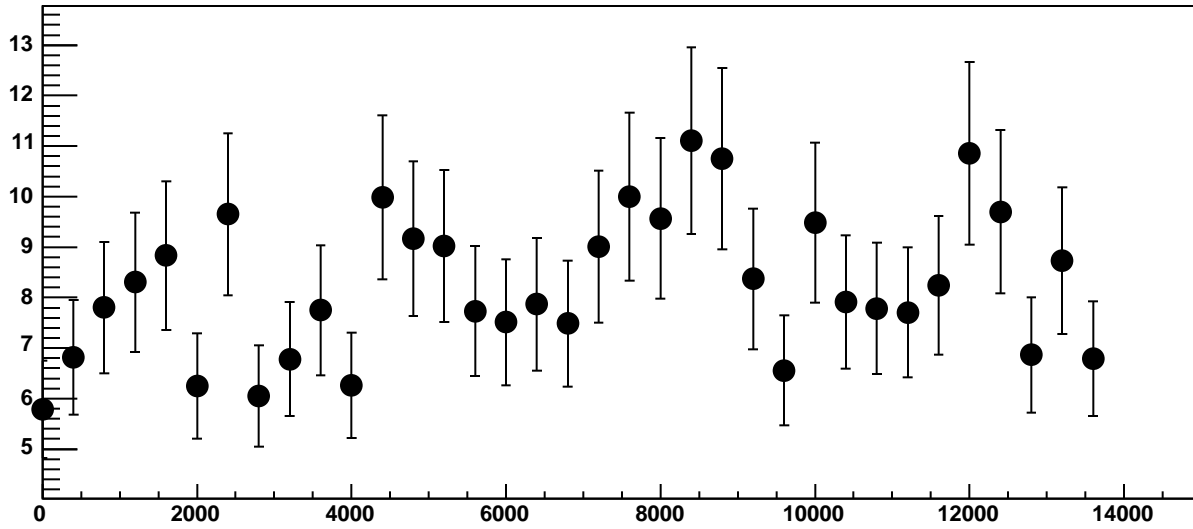


Chip 10, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

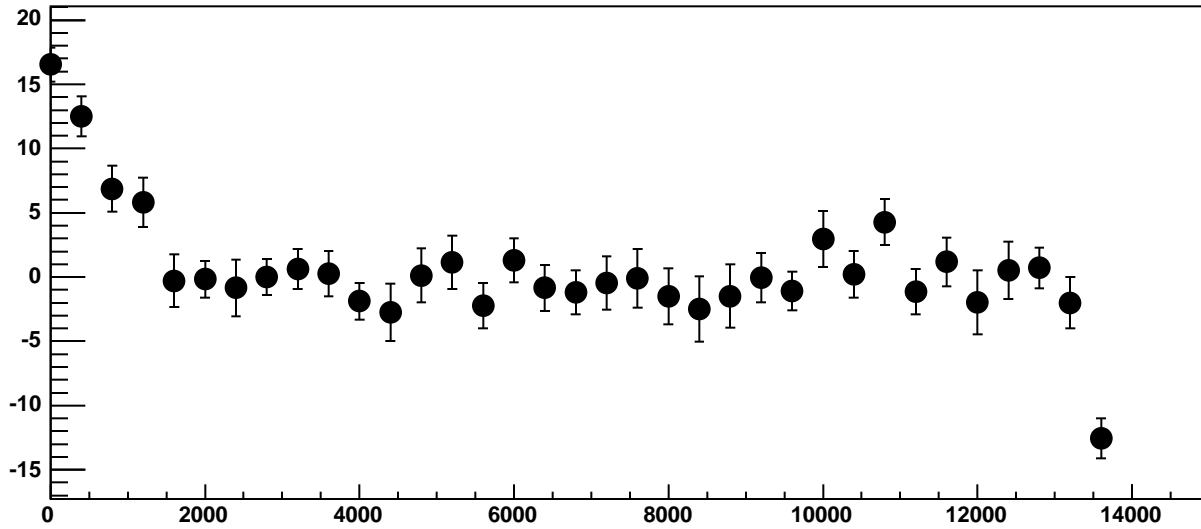


χ^2 / ndf 26.03 / 23
p0 10.25 ± 0.8063
p1 0.01762 ± 0.0001257

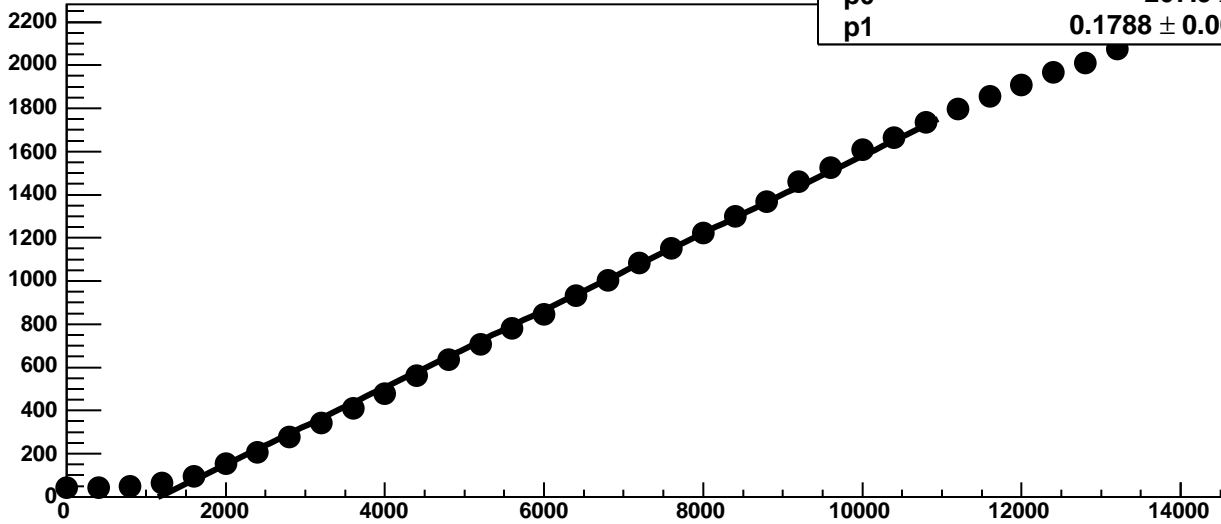
Chip 10, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



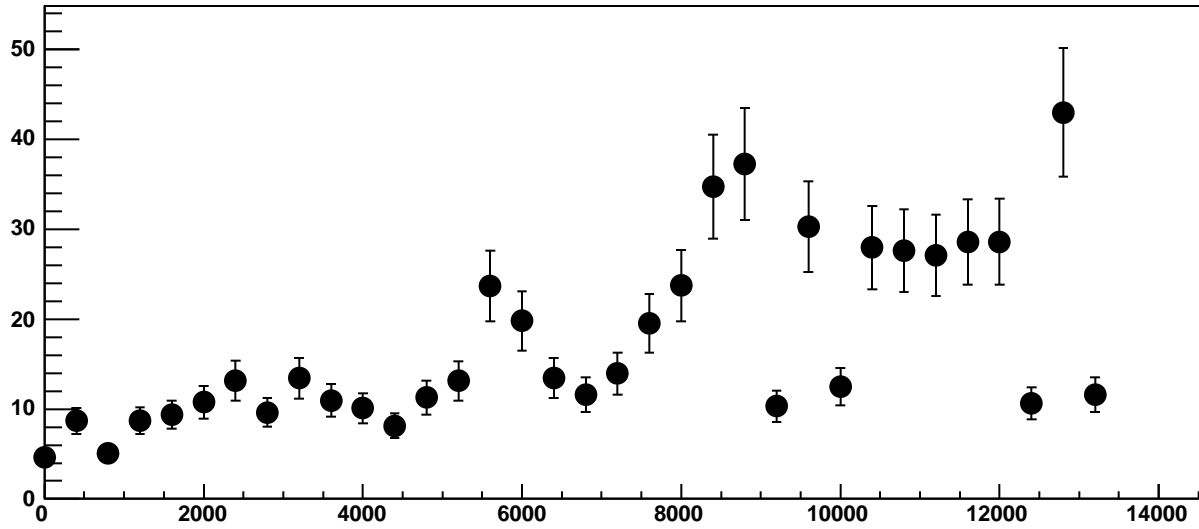
Chip 10, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



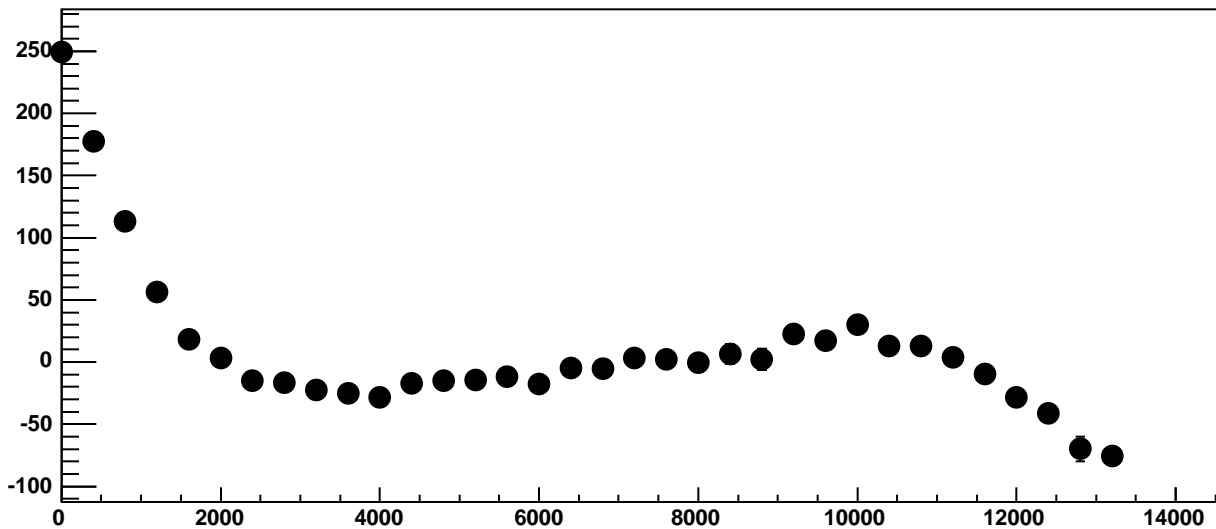
Chip 10, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC



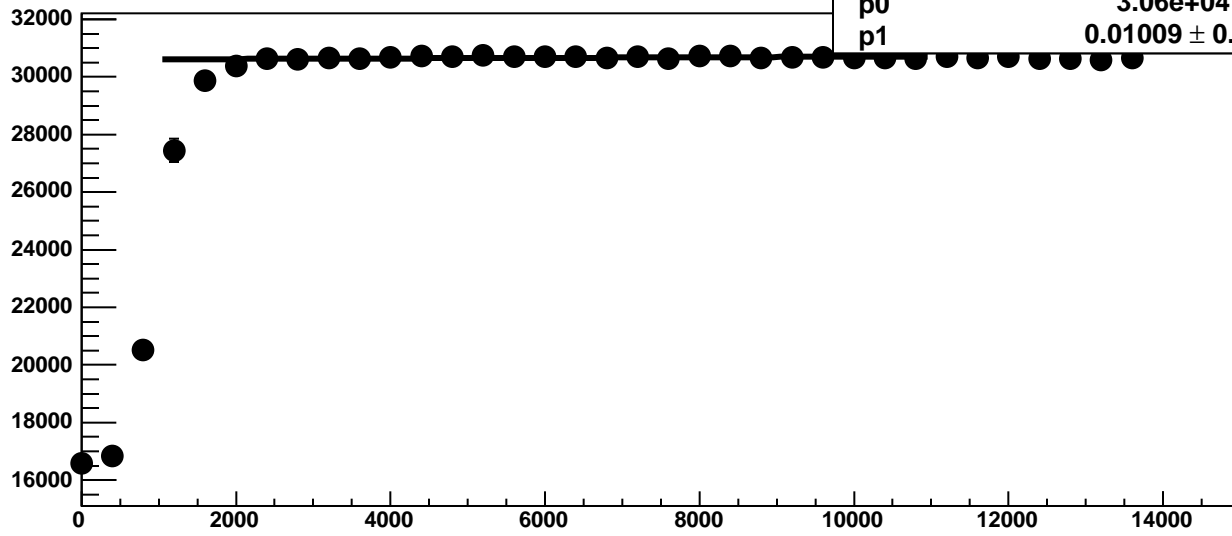
Chip 10, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

255.5 / 23

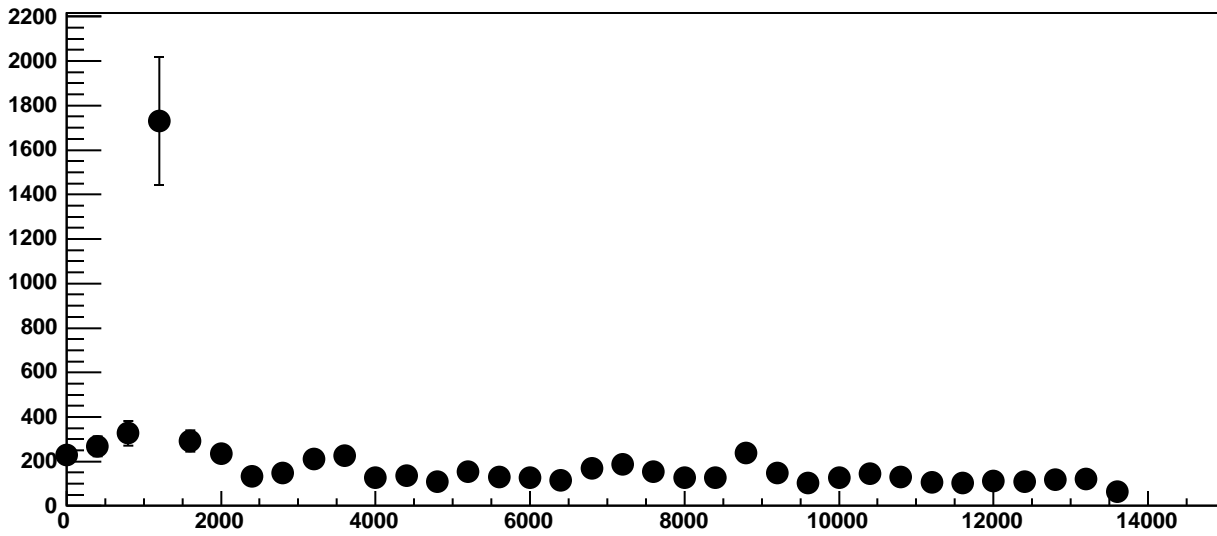
p0

$3.06 \times 10^4 \pm 18.41$

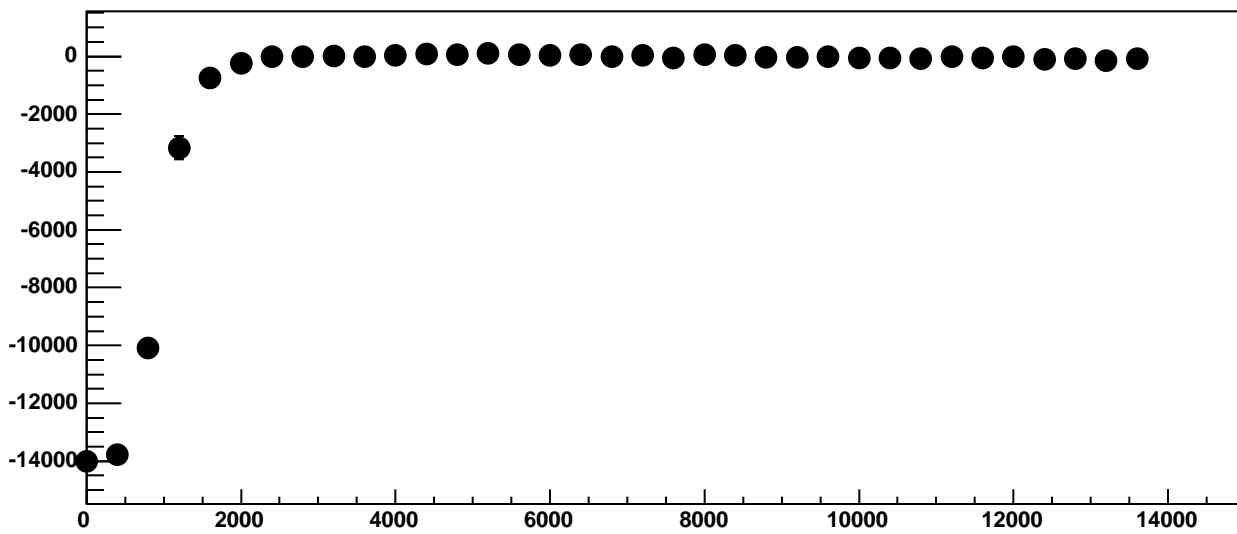
p1

0.01009 ± 0.002578

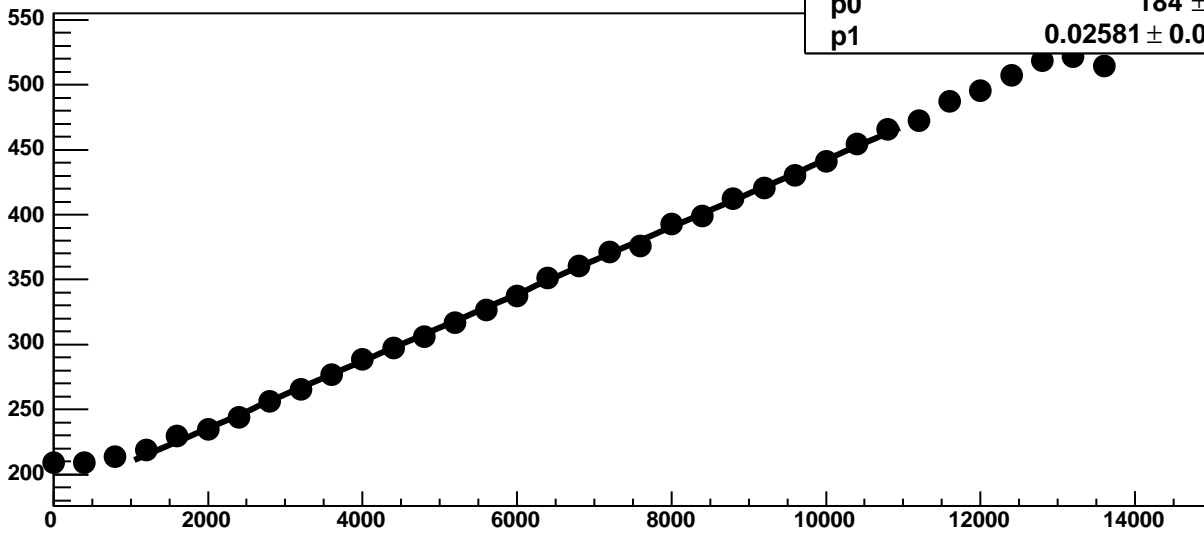
Chip 10, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC

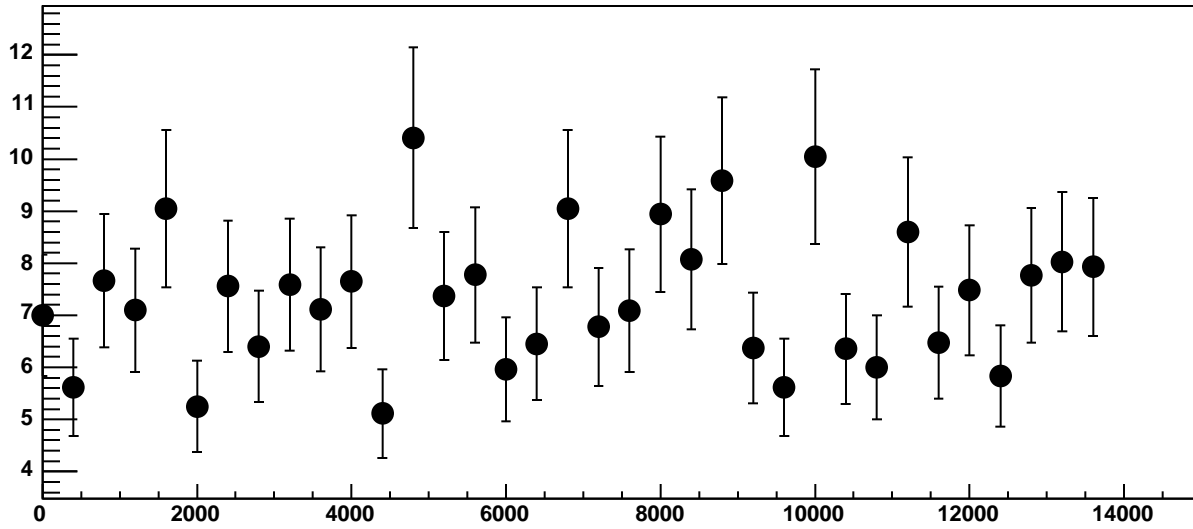


Chip 10, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

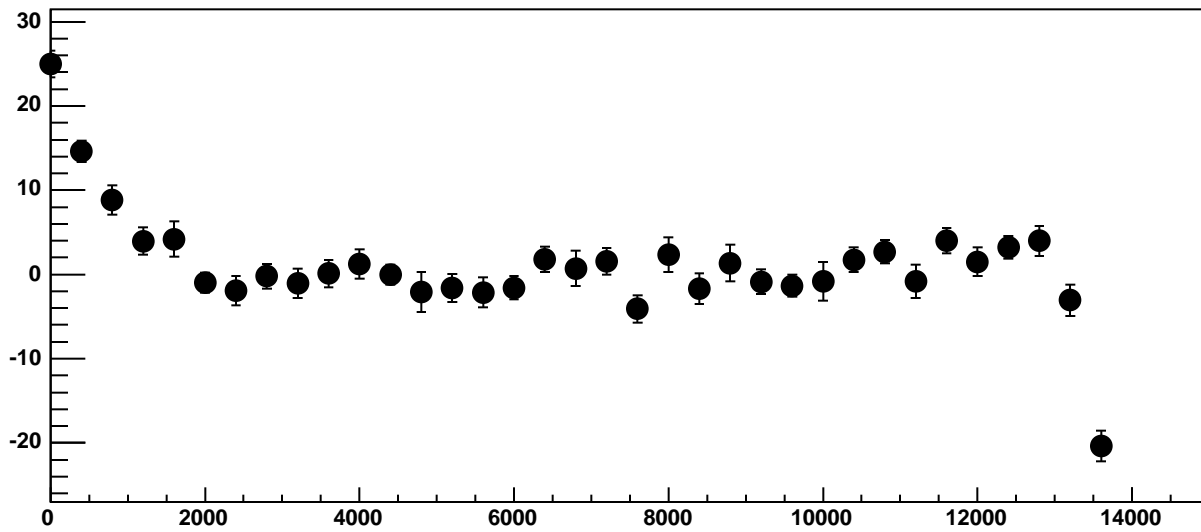


χ^2 / ndf 35.35 / 23
p0 184 ± 0.7243
p1 0.02581 ± 0.0001087

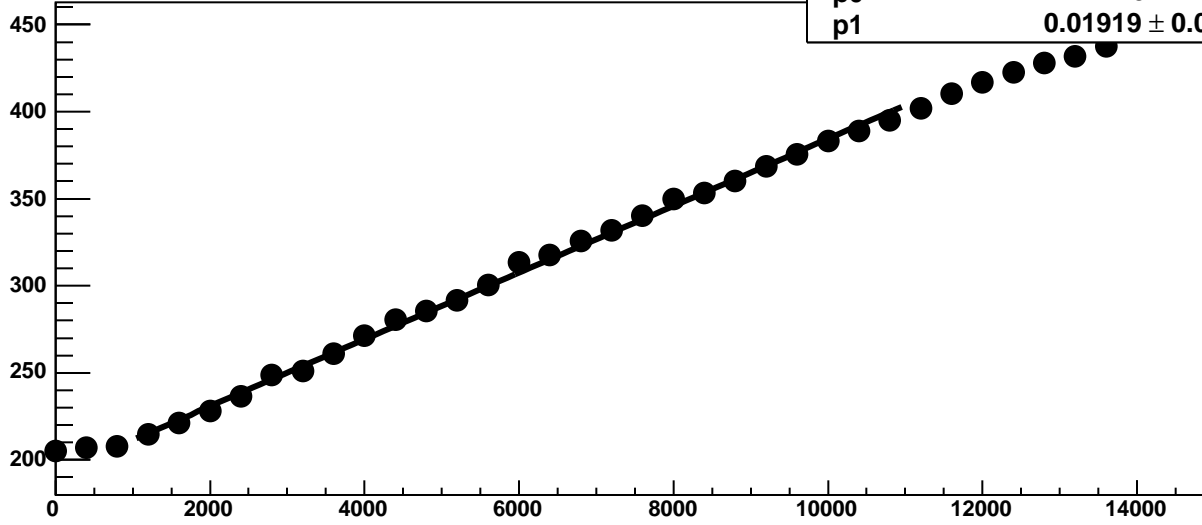
Chip 10, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



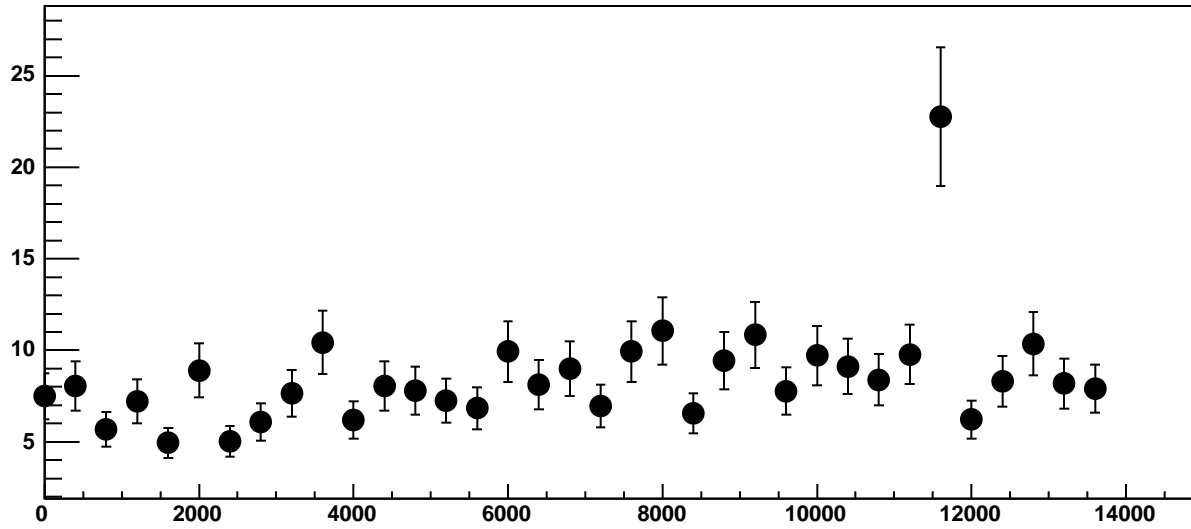
Chip 10, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



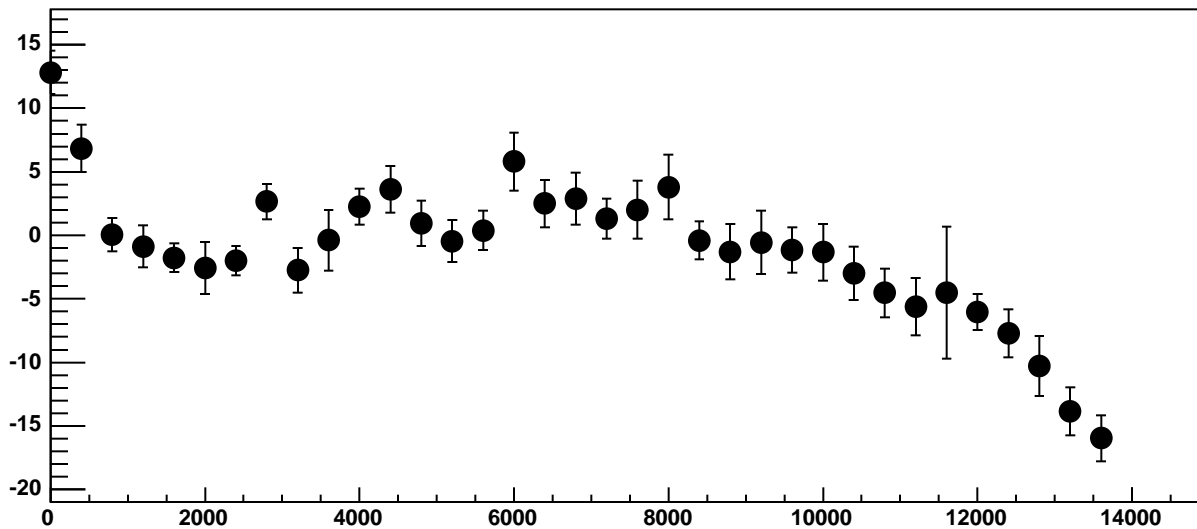
Chip 10, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



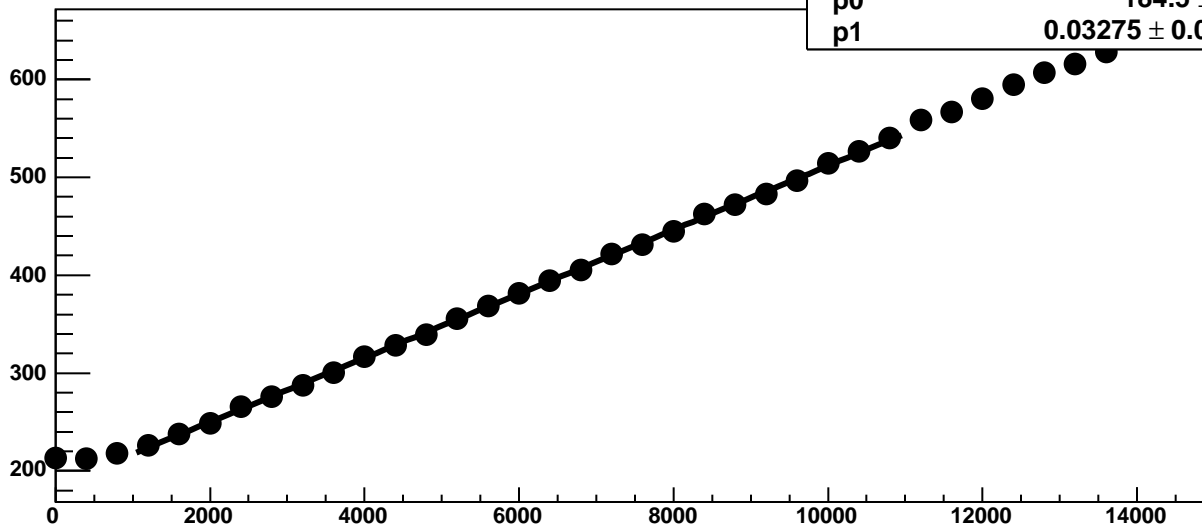
Chip 10, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

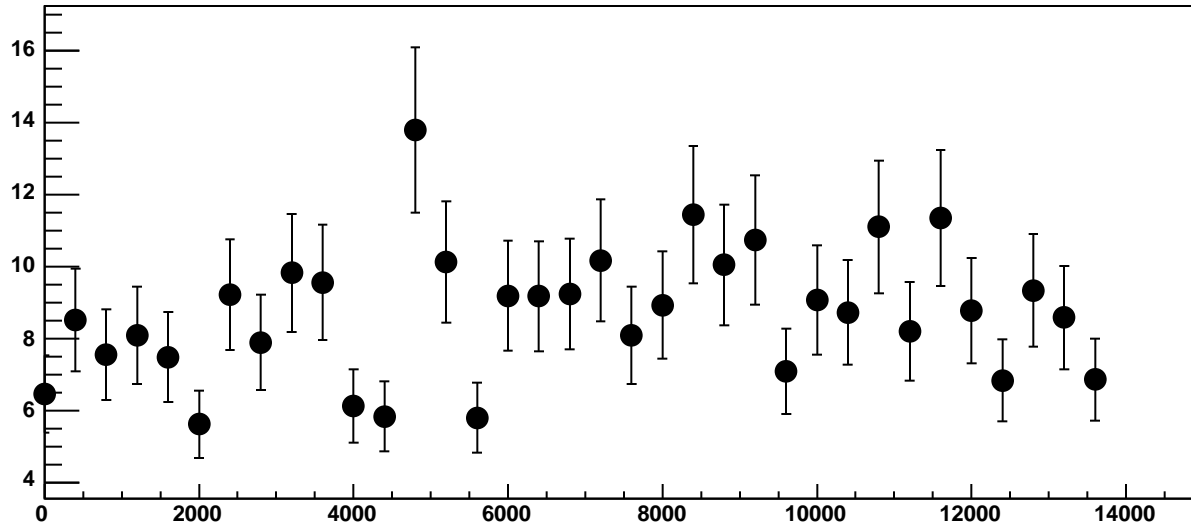


Chip 10, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

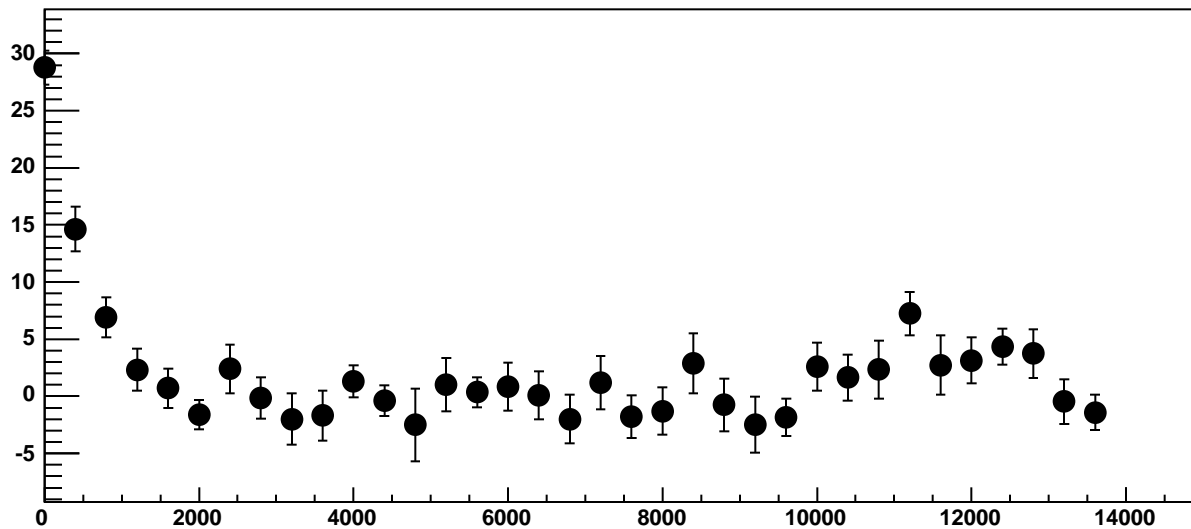


χ^2 / ndf 17.08 / 23
p0 184.5 ± 0.8221
p1 0.03275 ± 0.0001329

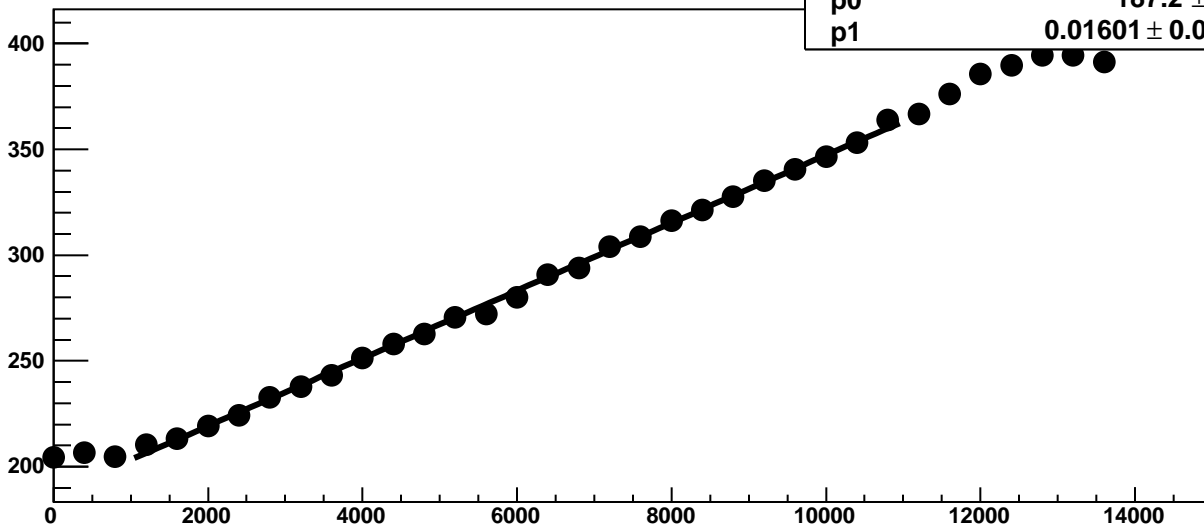
Chip 10, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

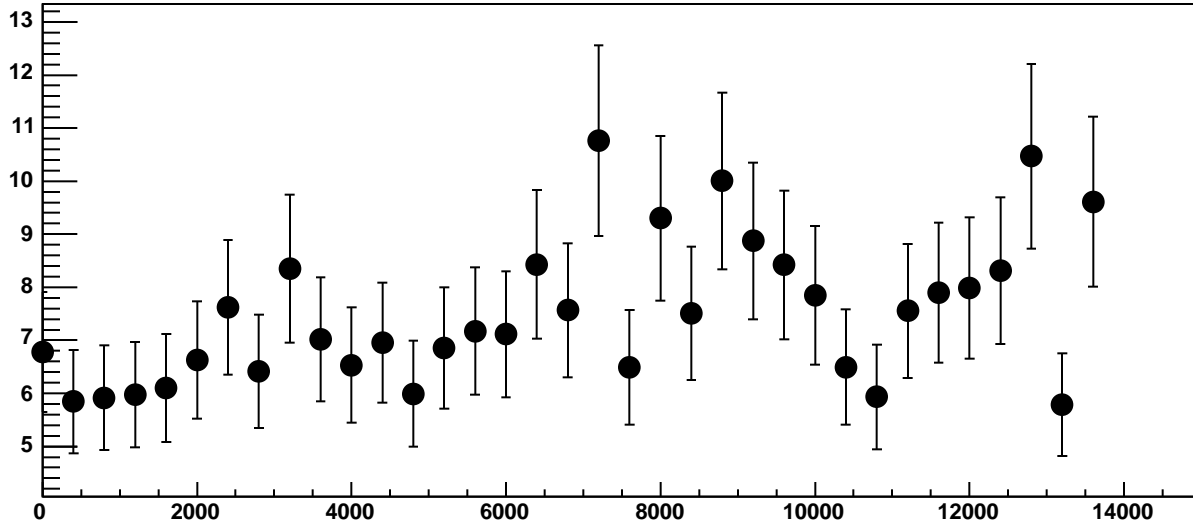


Chip 10, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

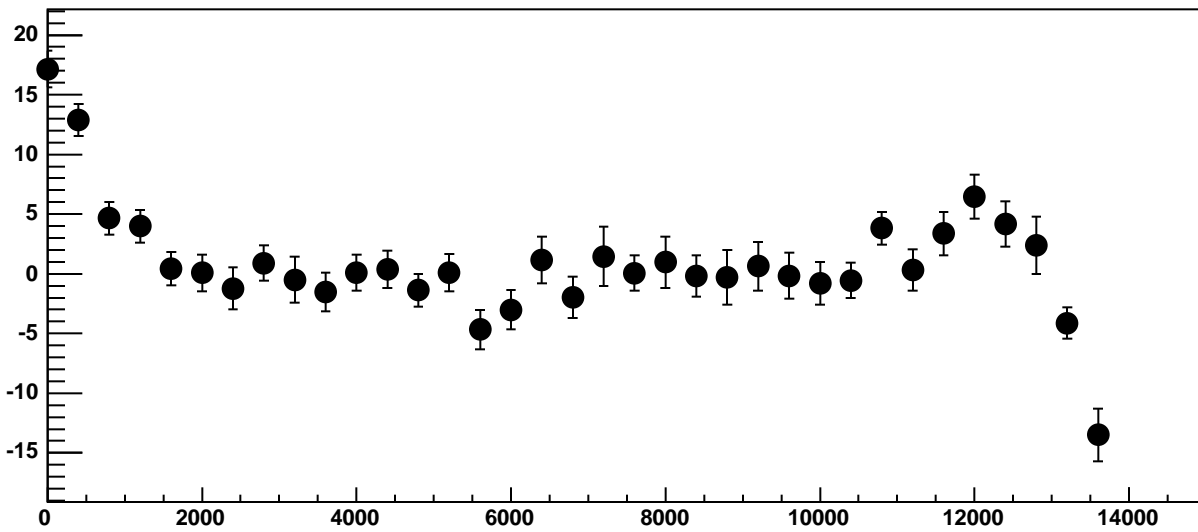


χ^2 / ndf 33.51 / 23
p0 187.2 ± 0.7042
p1 0.01601 ± 0.0001095

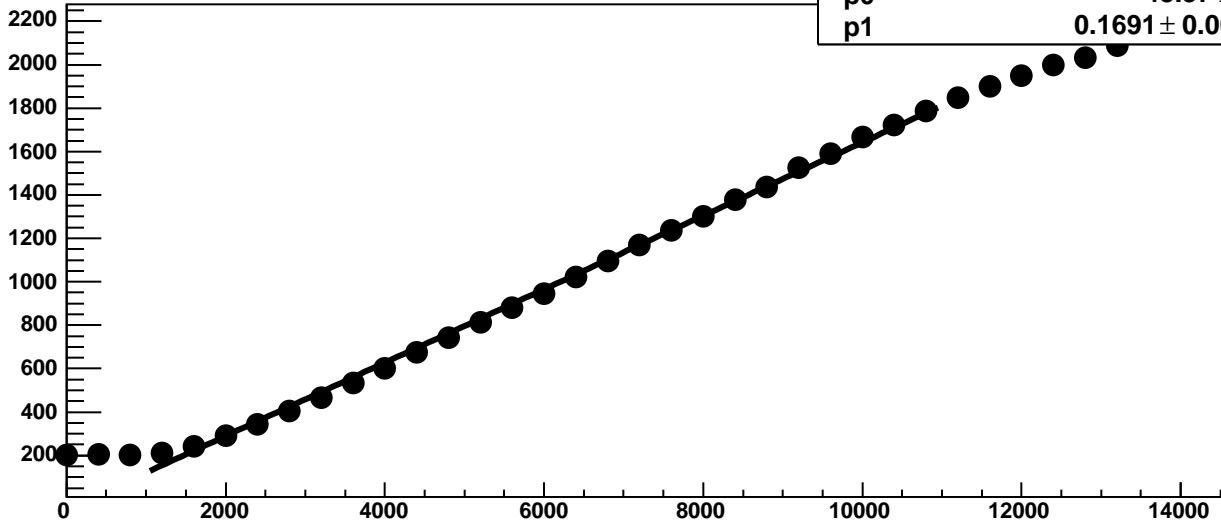
Chip 10, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



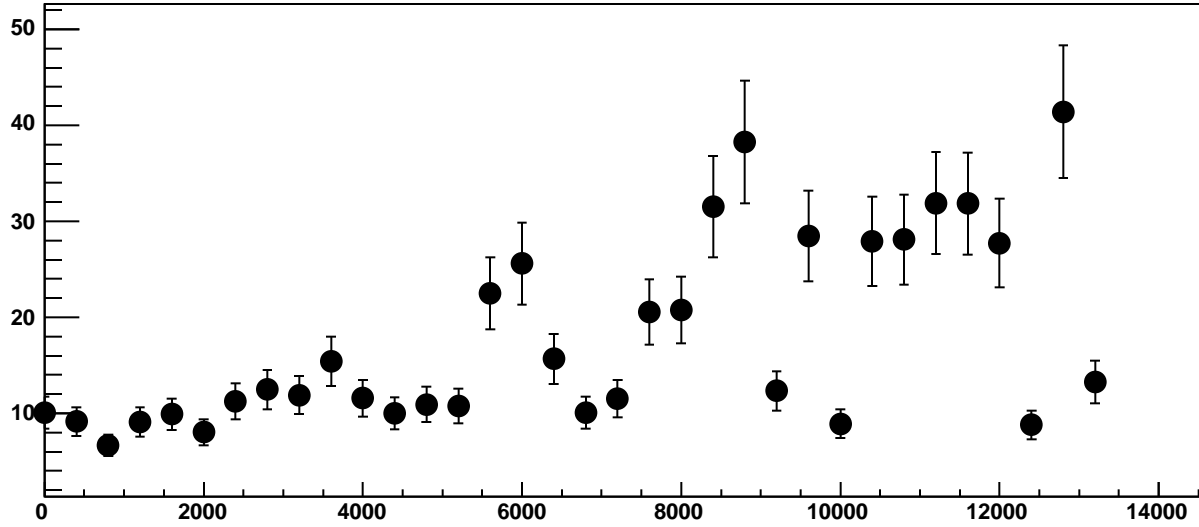
Chip 10, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



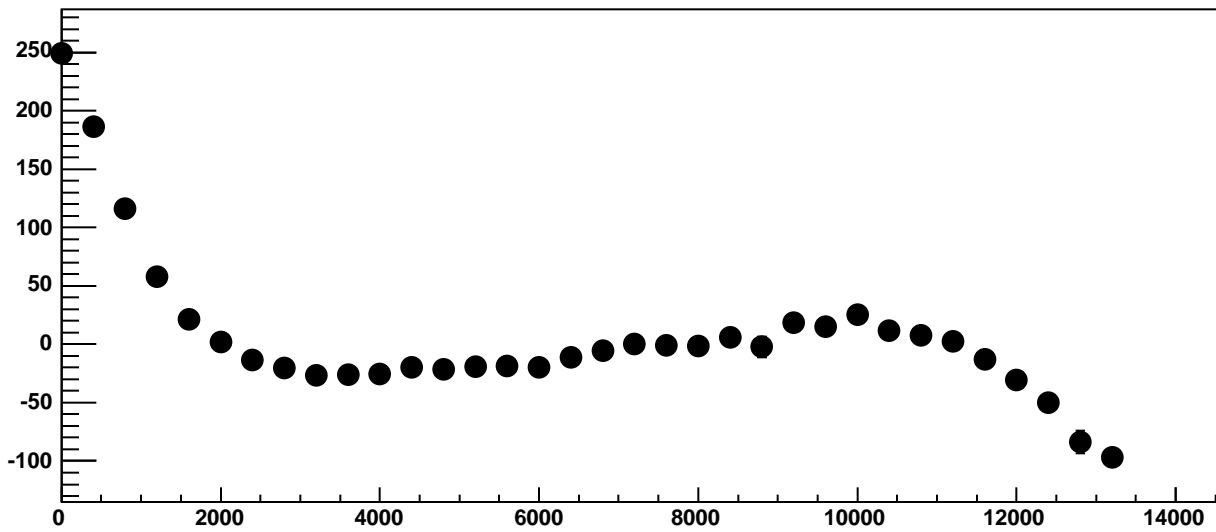
Chip 10, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



Chip 10, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

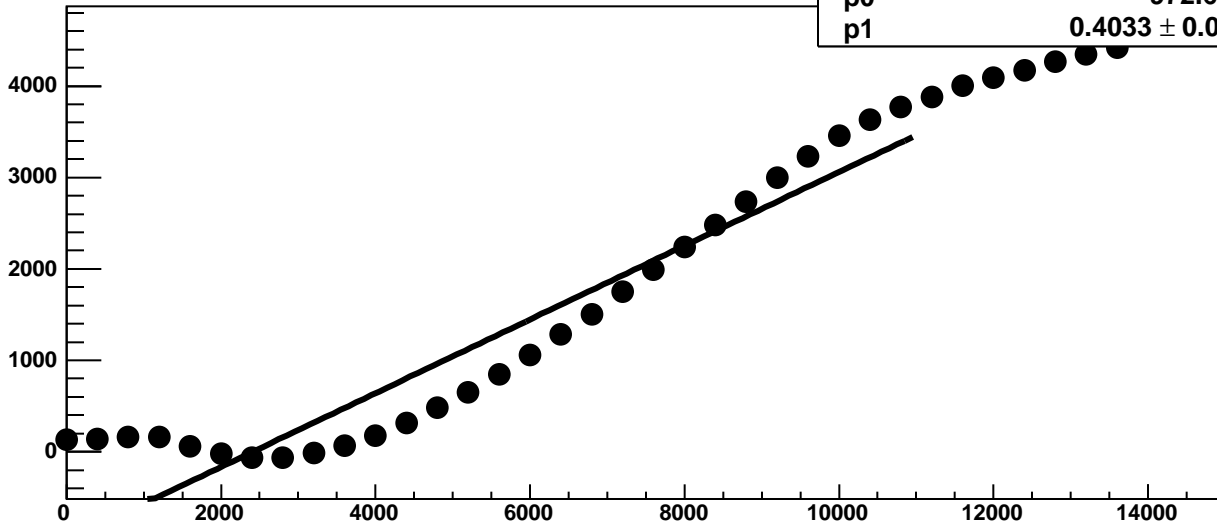


Chip 10, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

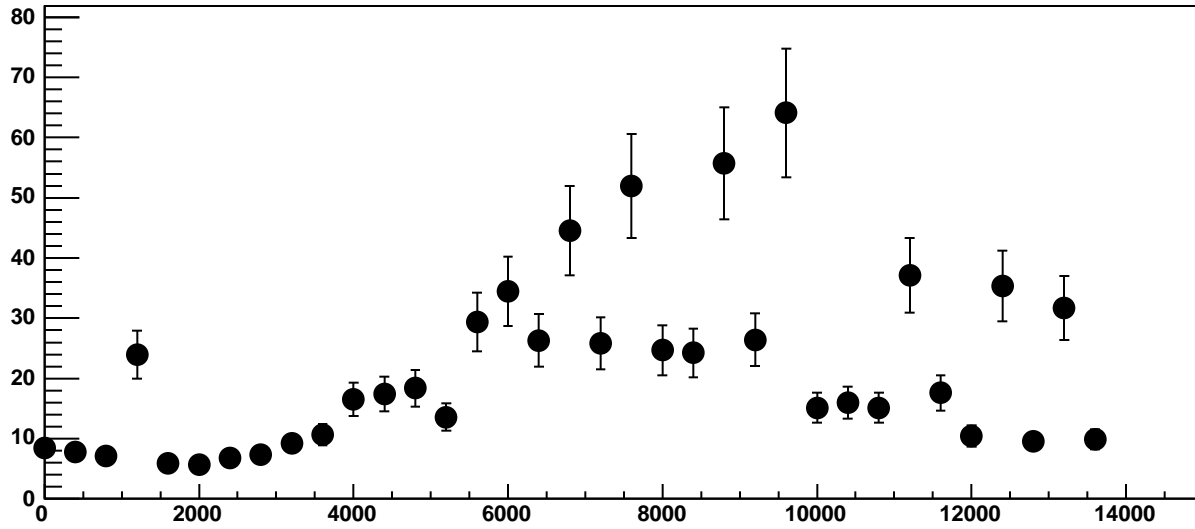


Chip 10, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC

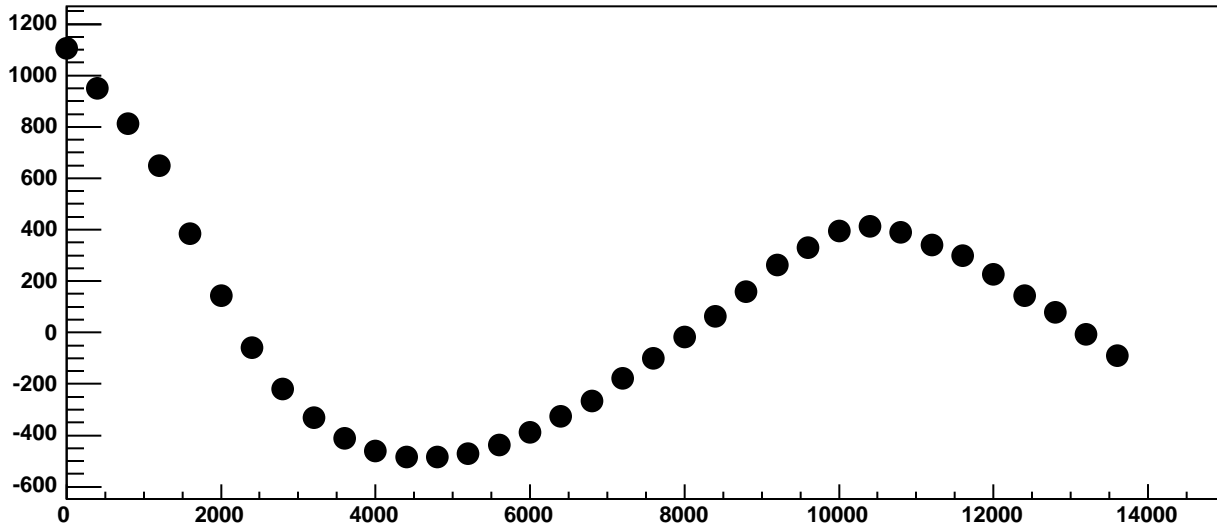
χ^2 / ndf 2.942e+05 / 23
p0 -972.6 ± 0.981
p1 0.4033 ± 0.0002254



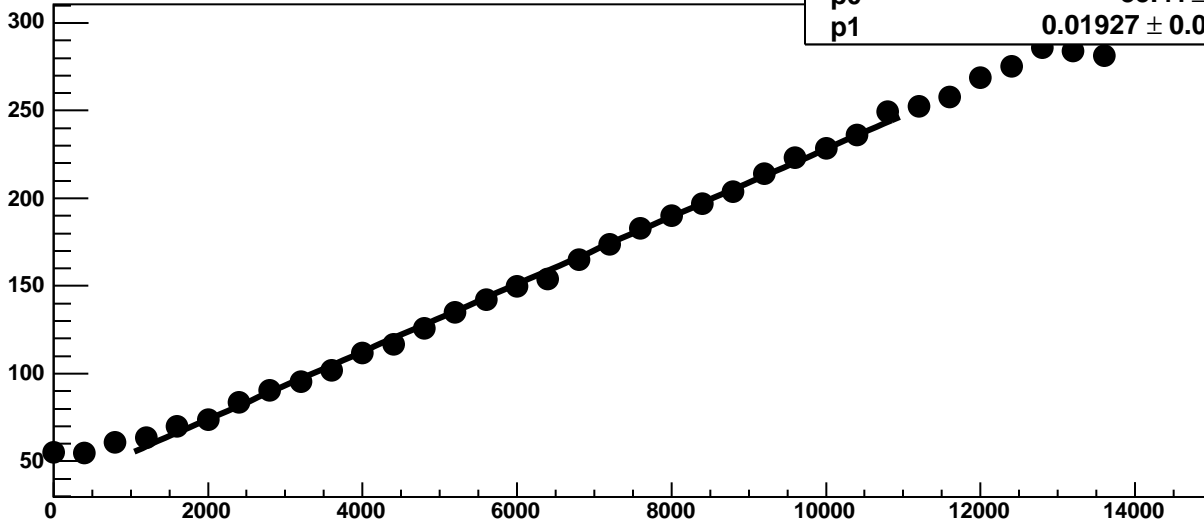
Chip 10, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

51.53 / 23

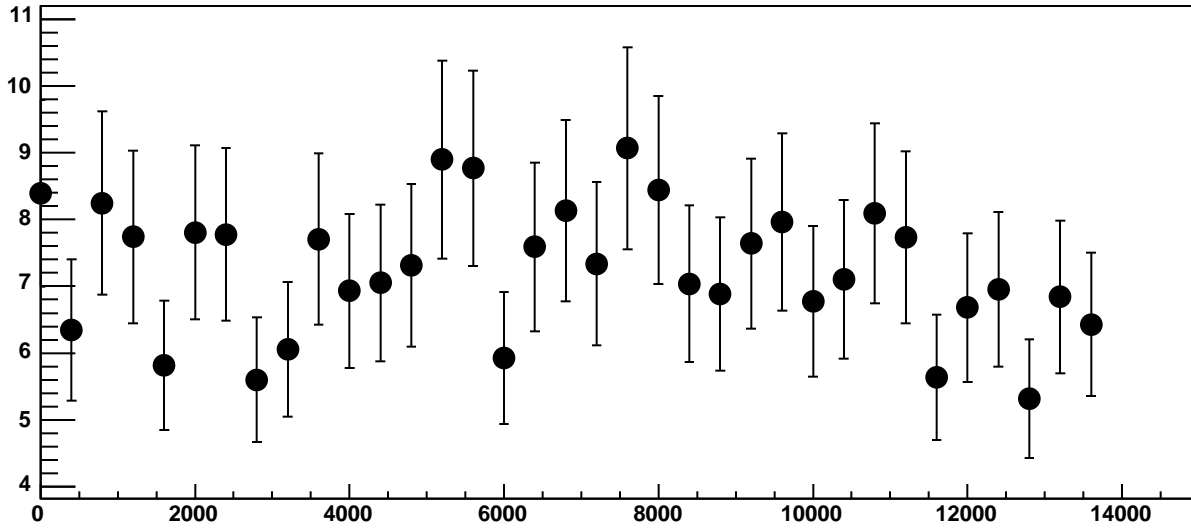
p0

35.41 ± 0.7339

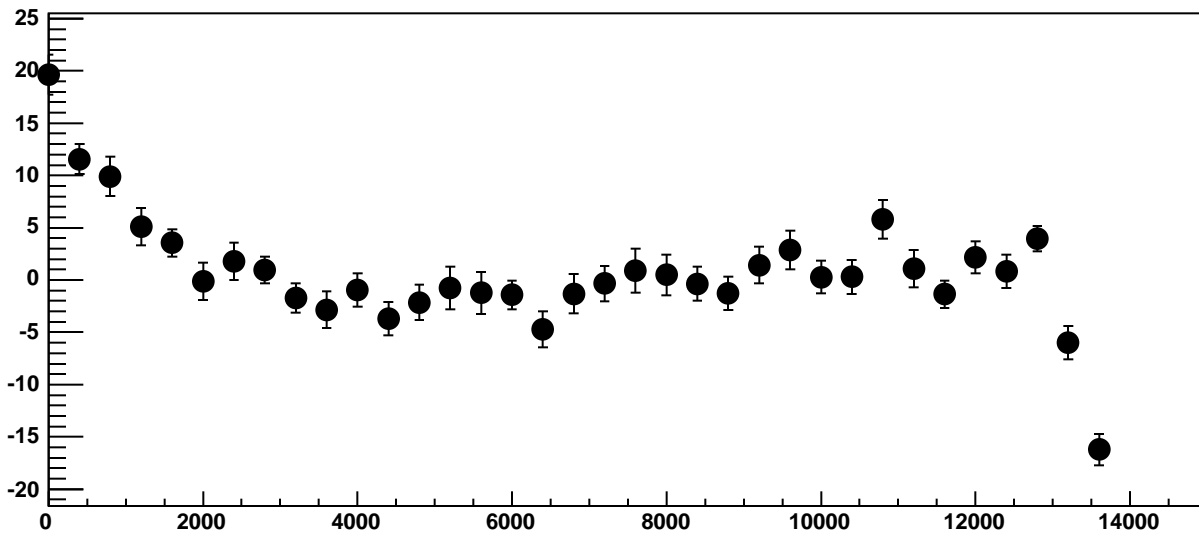
p1

0.01927 ± 0.0001134

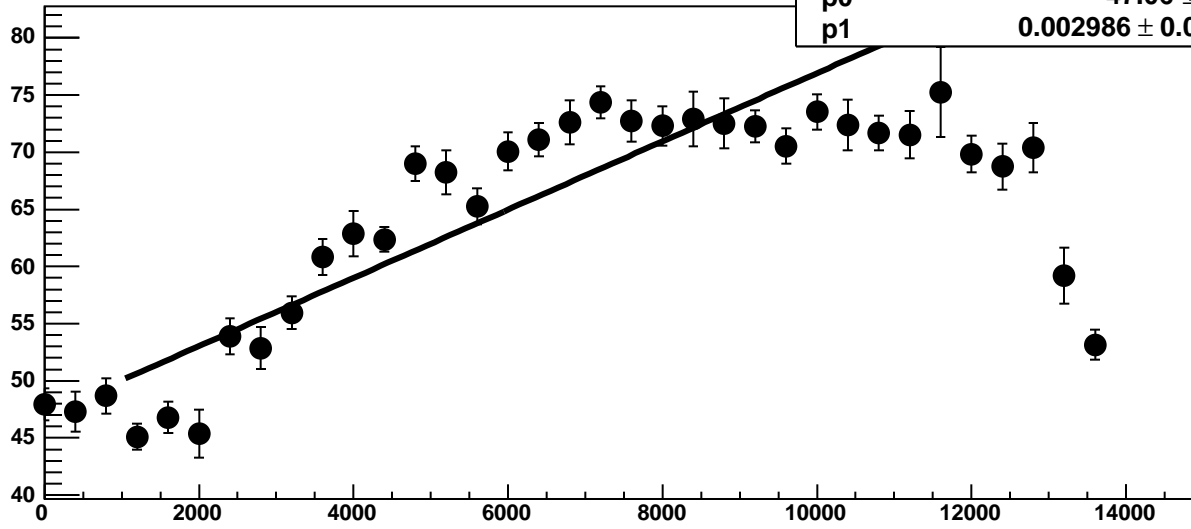
Chip 10, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



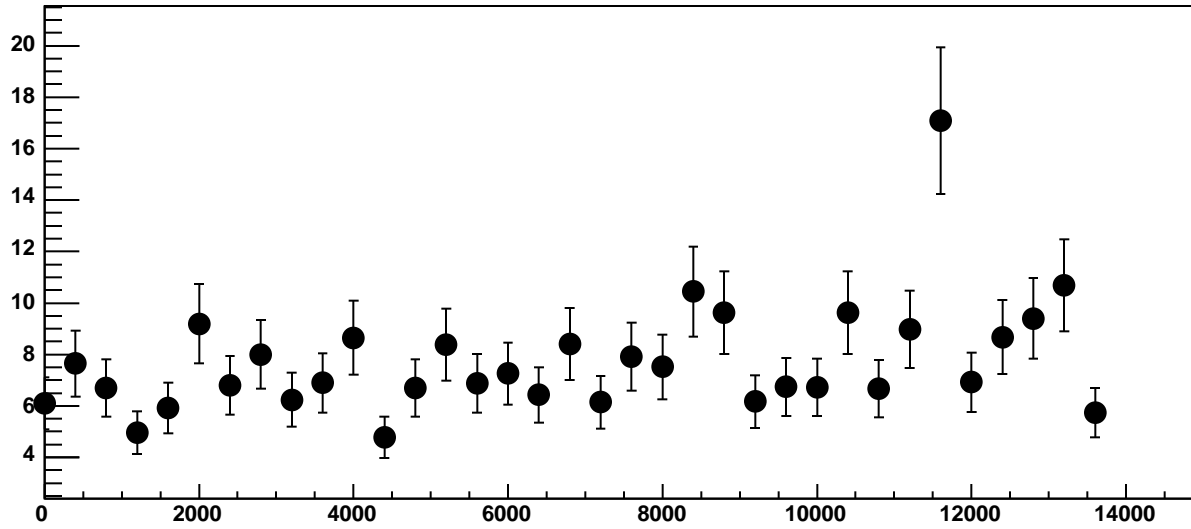
Chip 10, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



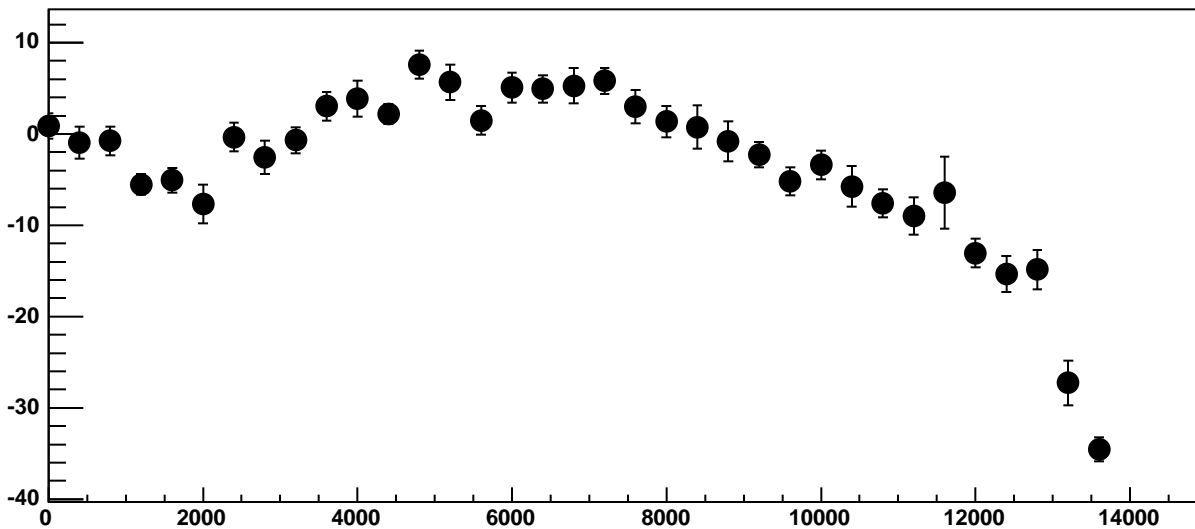
Chip 10, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



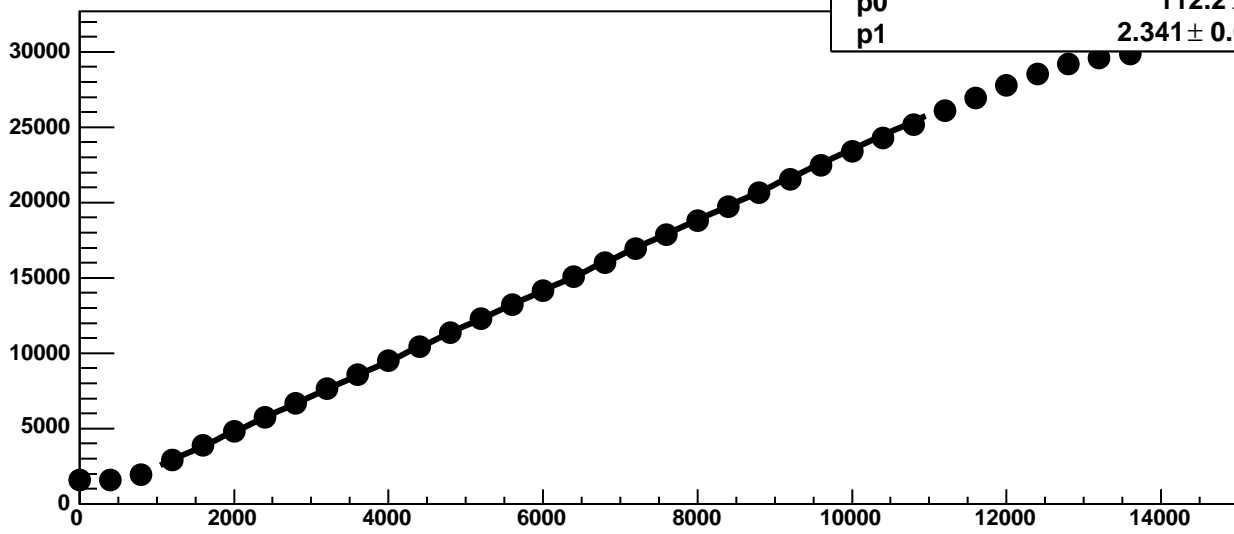
Chip 10, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



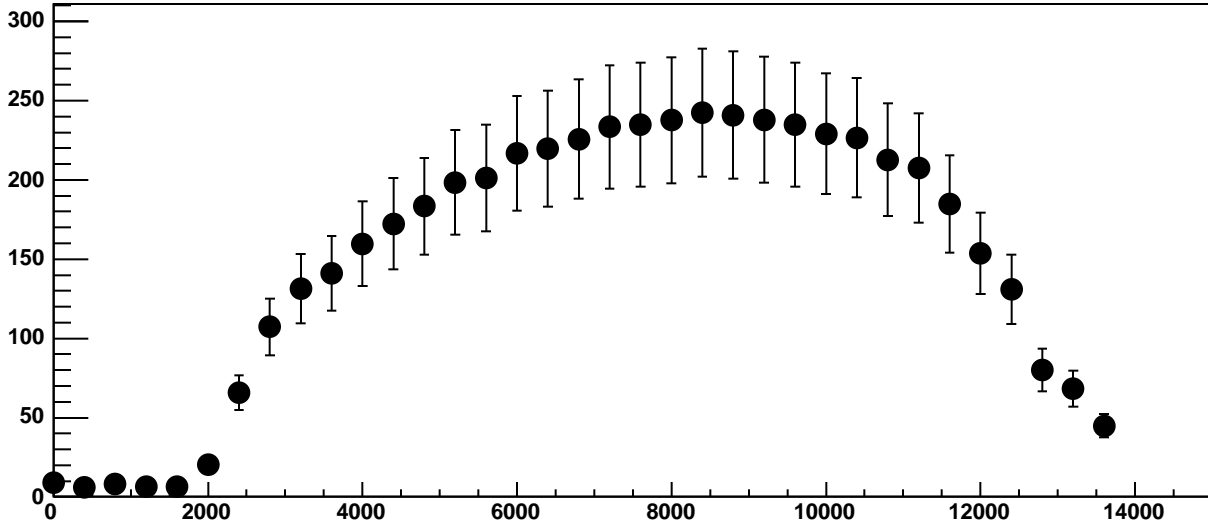
Chip 10, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



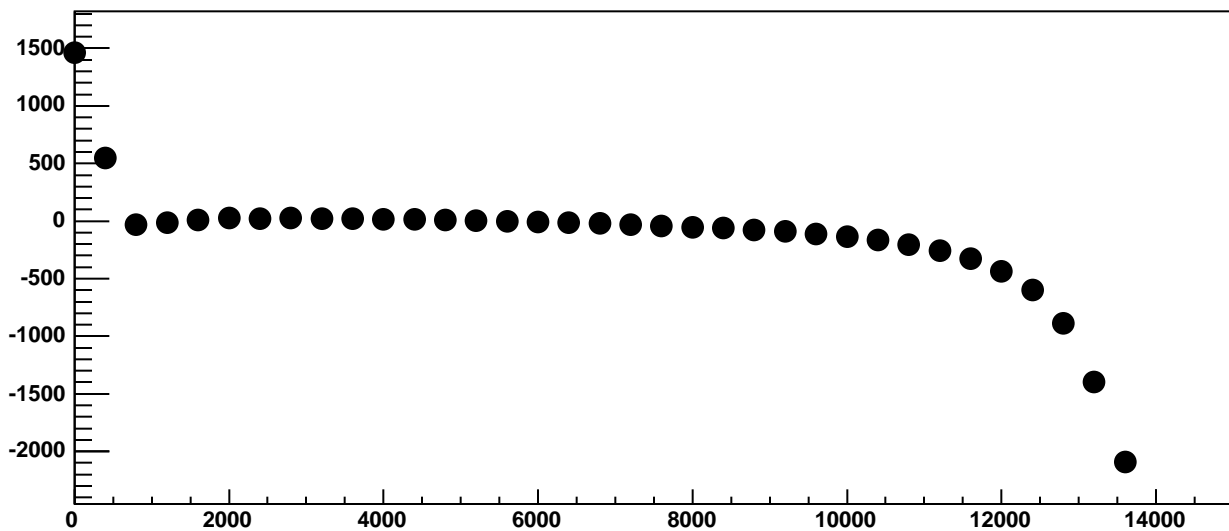
Chip 10, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC



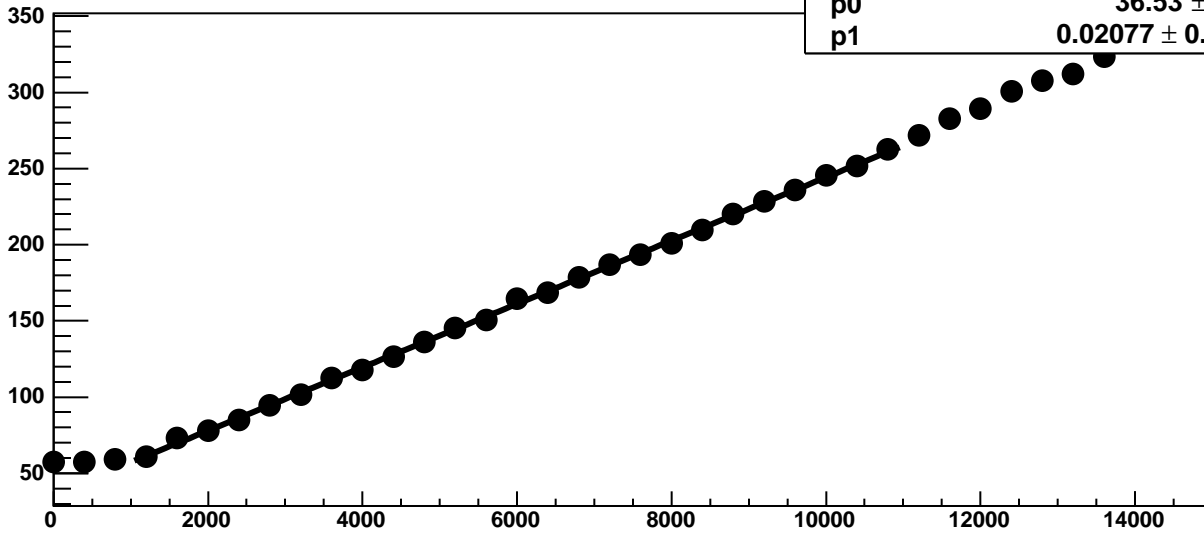
Chip 10, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

15.33 / 23

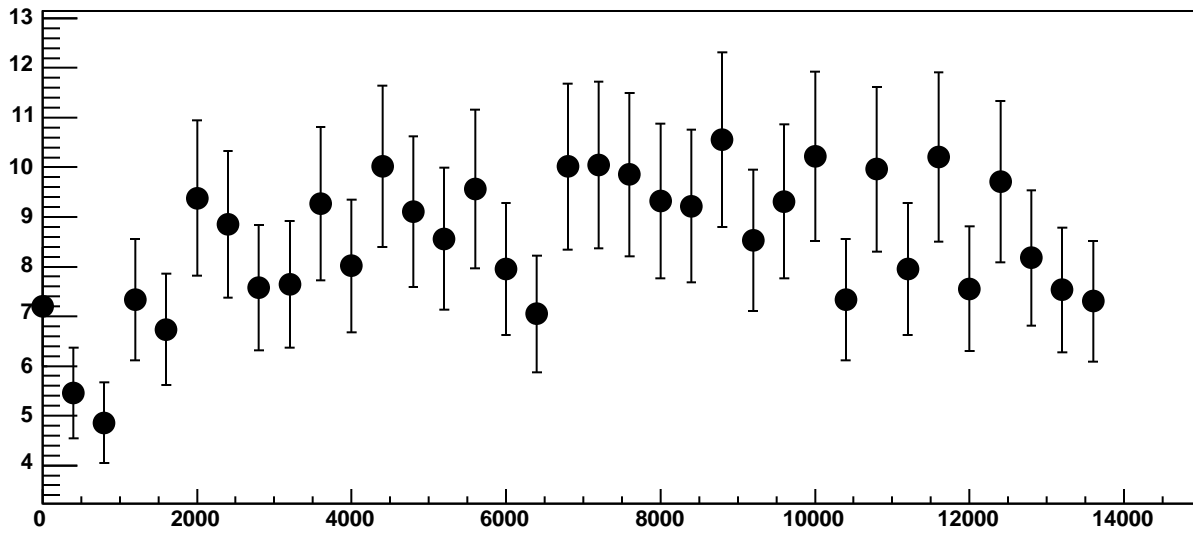
p0

36.53 ± 0.8563

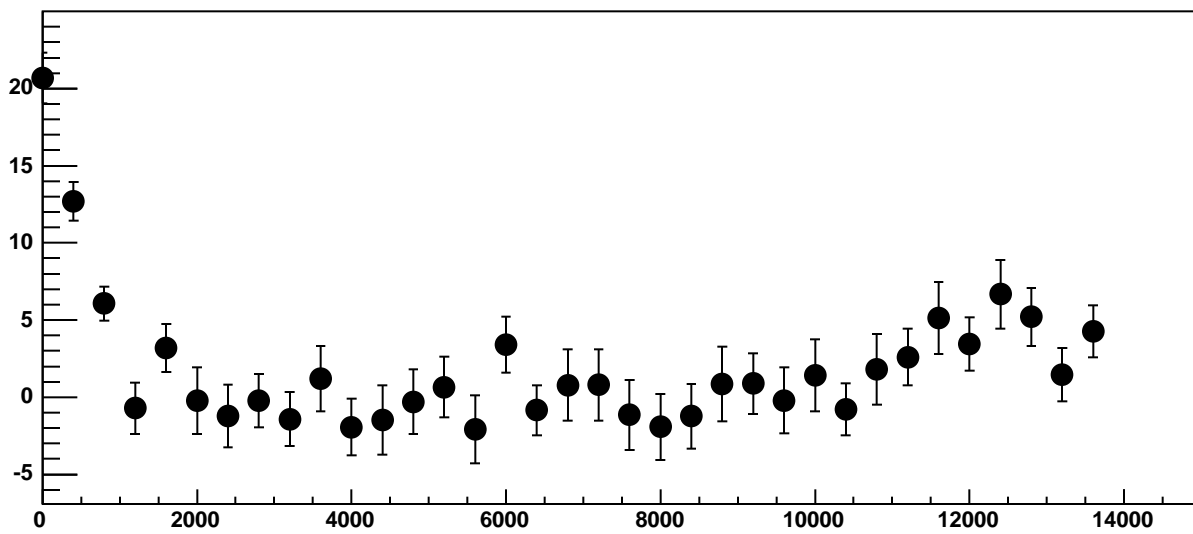
p1

0.02077 ± 0.000134

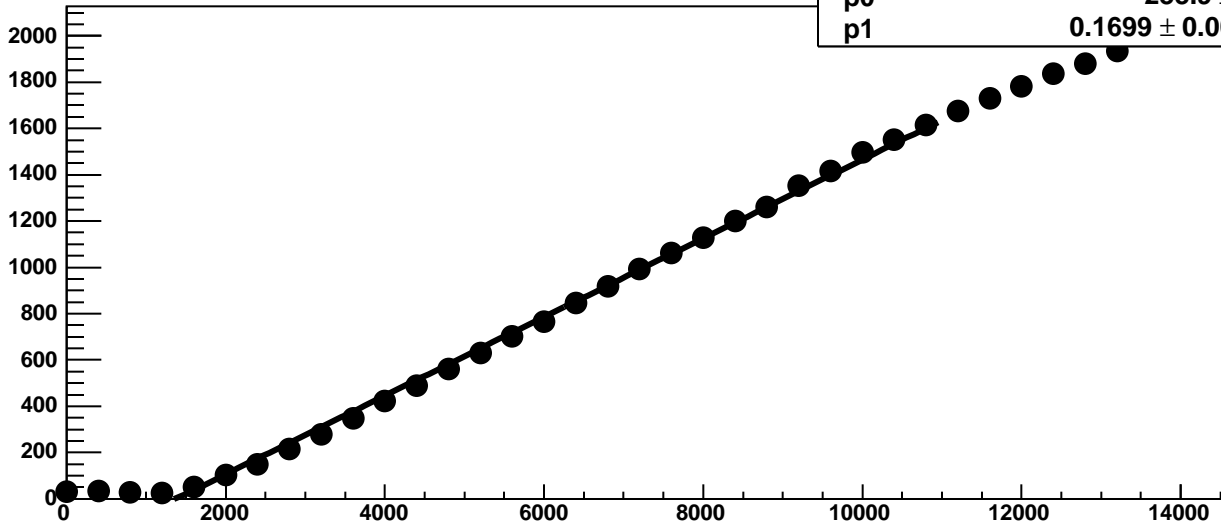
Chip 10, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



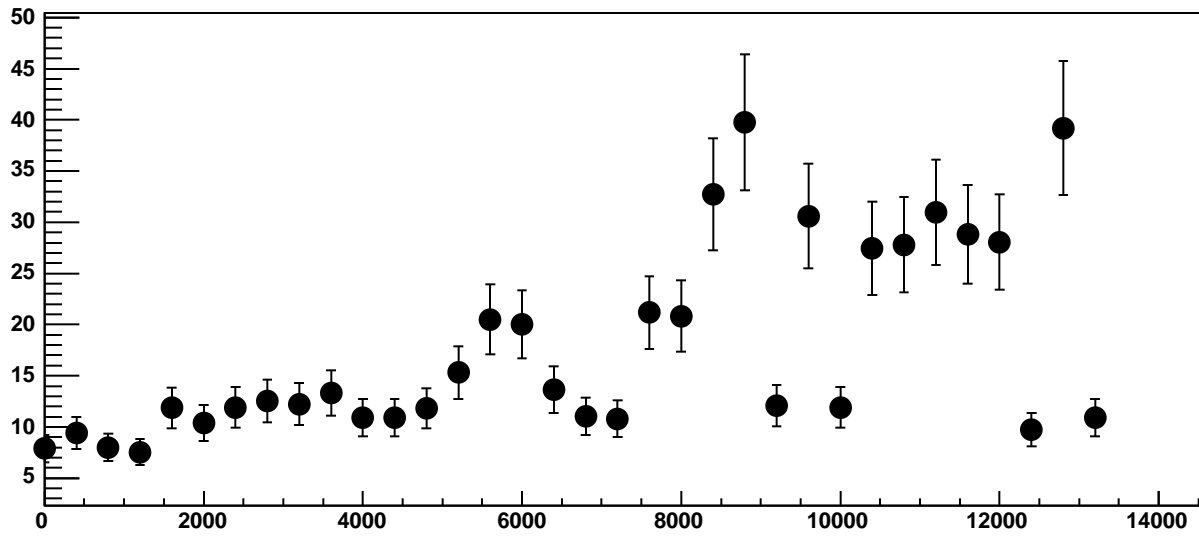
Chip 10, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



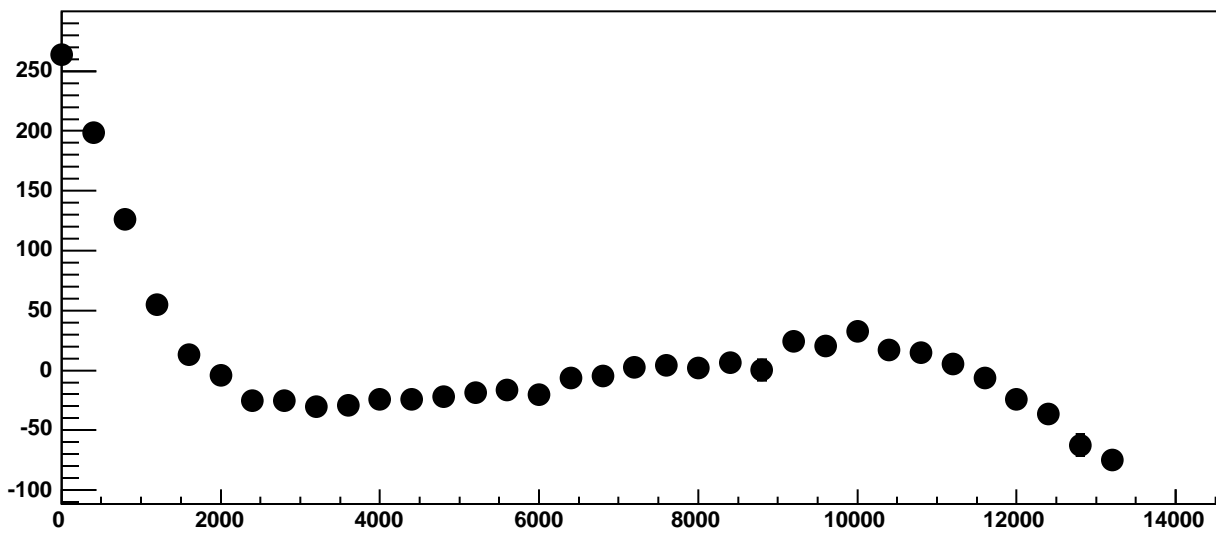
Chip 10, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



Chip 10, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

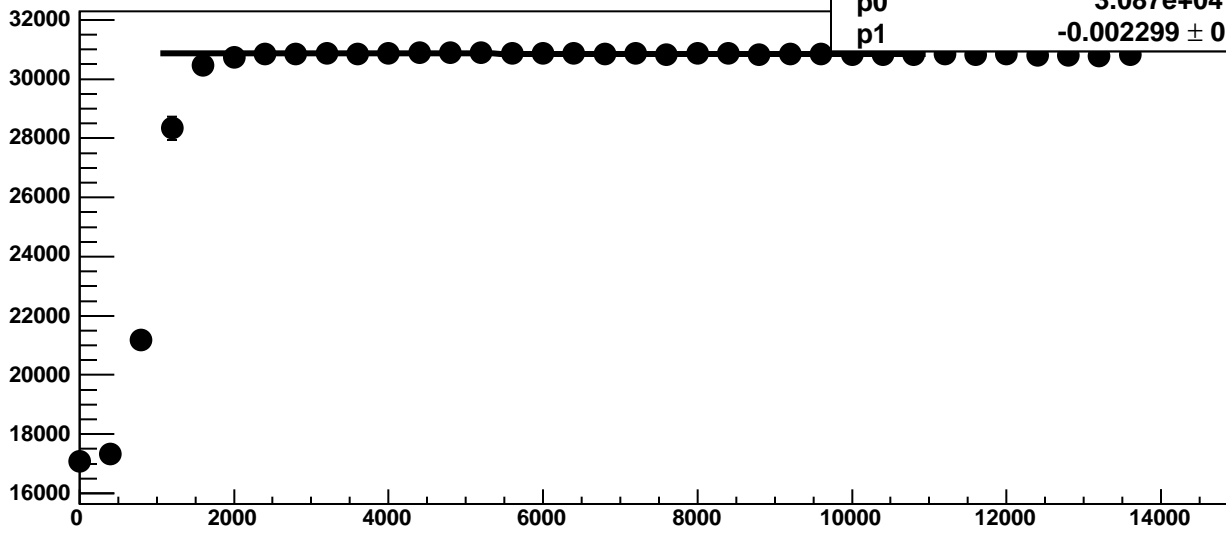


Chip 10, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

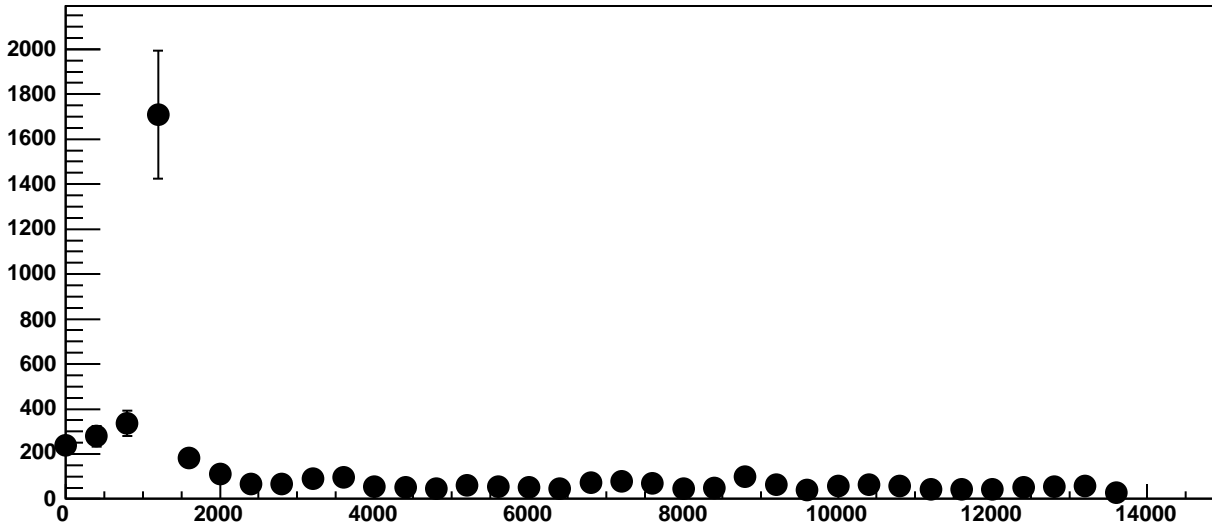


Chip 10, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC

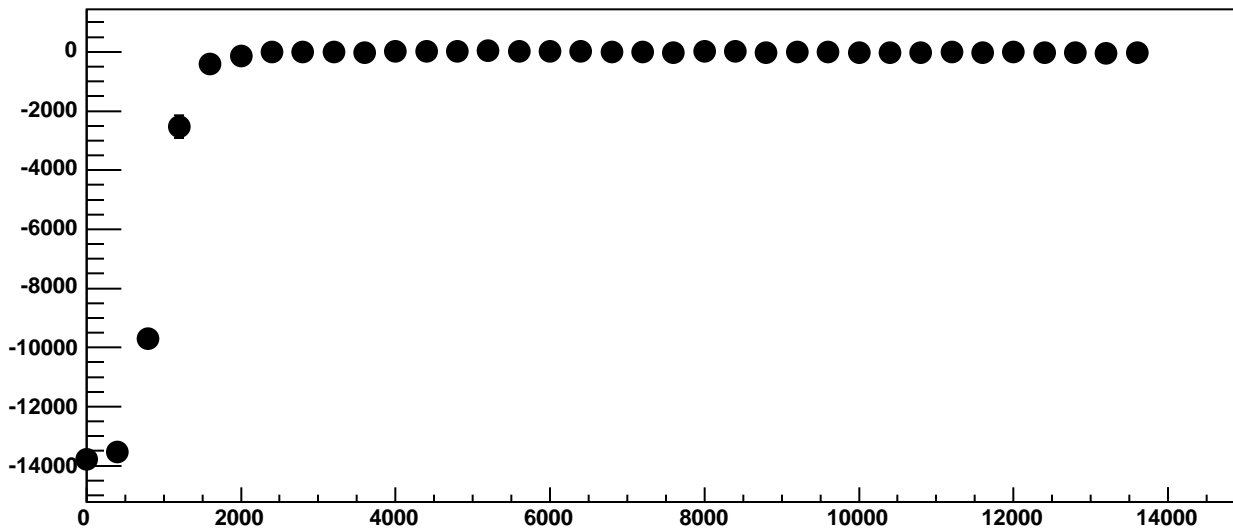
χ^2 / ndf 199.9 / 23
p0 $3.087\text{e}+04 \pm 8.169$
p1 -0.002299 ± 0.001135



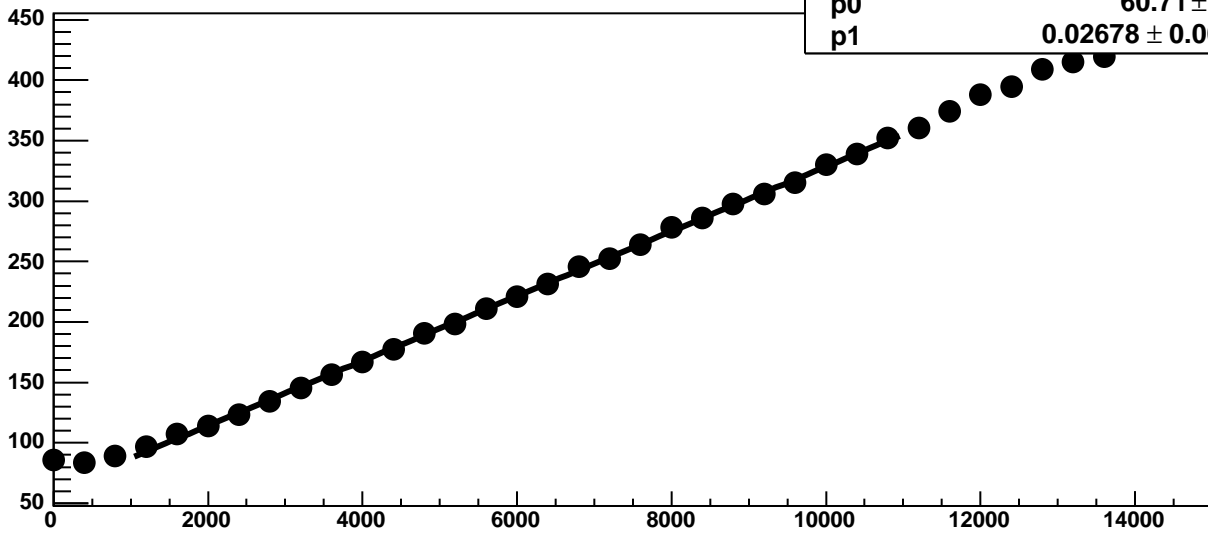
Chip 10, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC

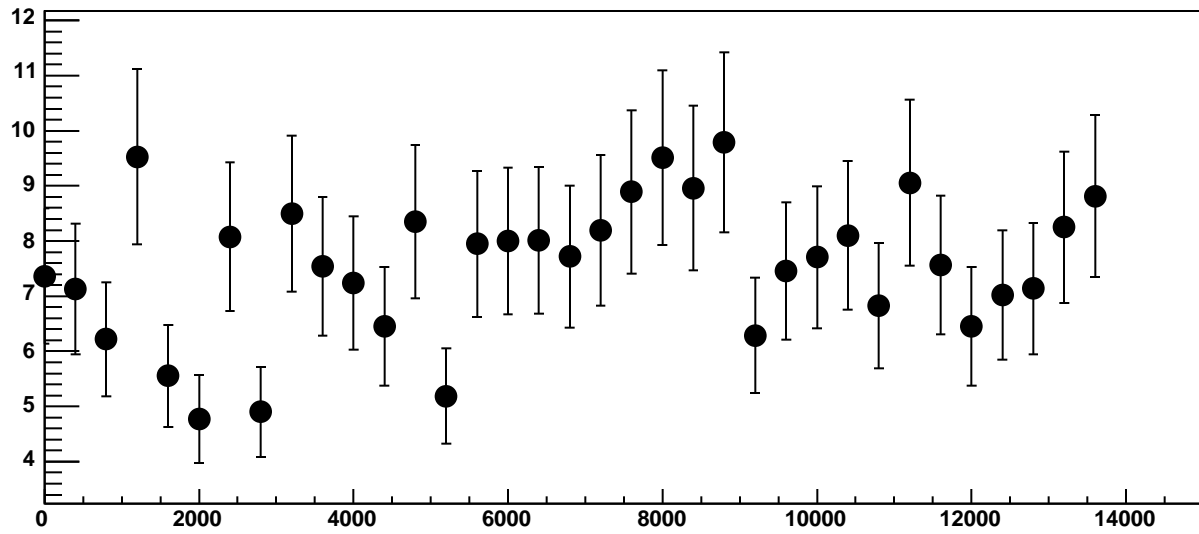


Chip 10, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

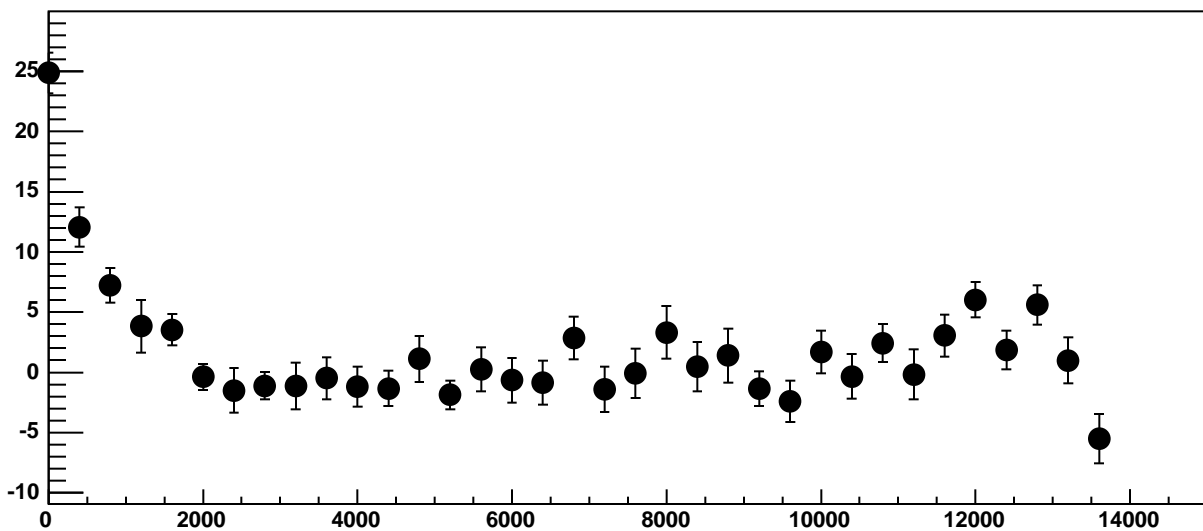


χ^2 / ndf 29.62 / 23
p0 60.71 ± 0.6908
p1 0.02678 ± 0.0001107

Chip 10, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

χ^2 / ndf

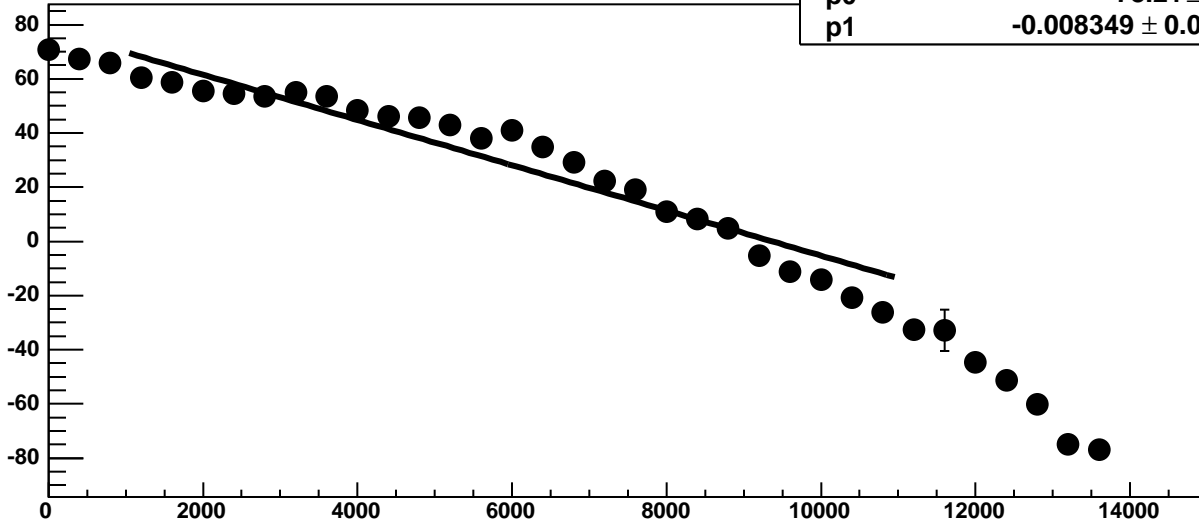
311 / 23

p0

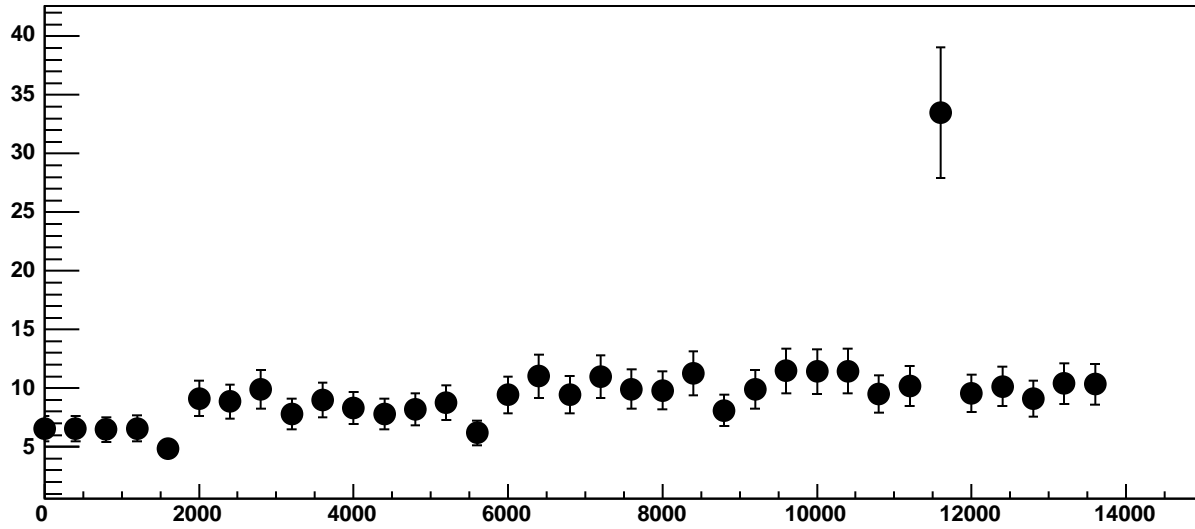
78.21 ± 0.7985

p1

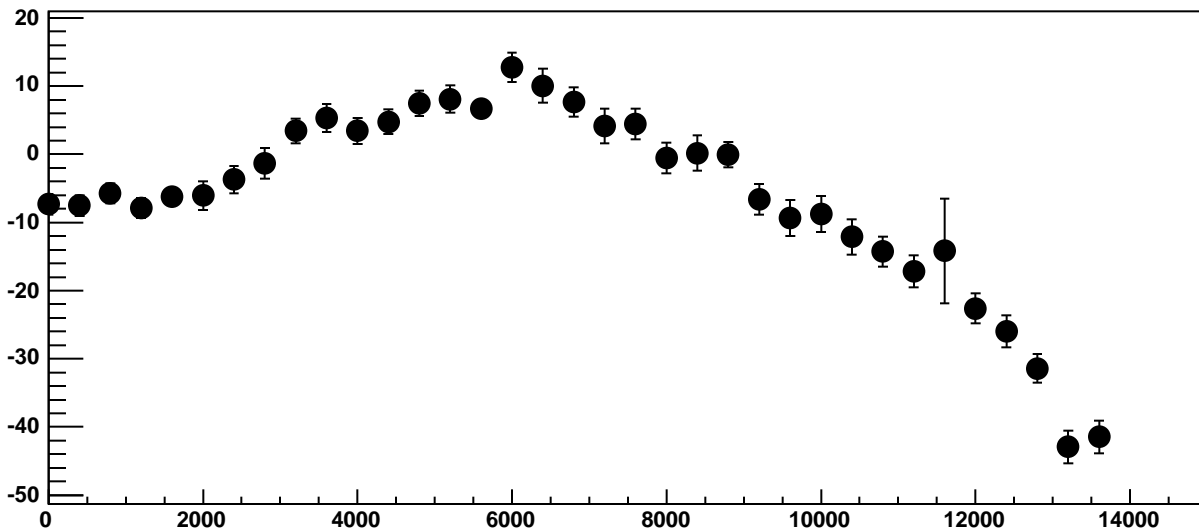
-0.008349 ± 0.0001354



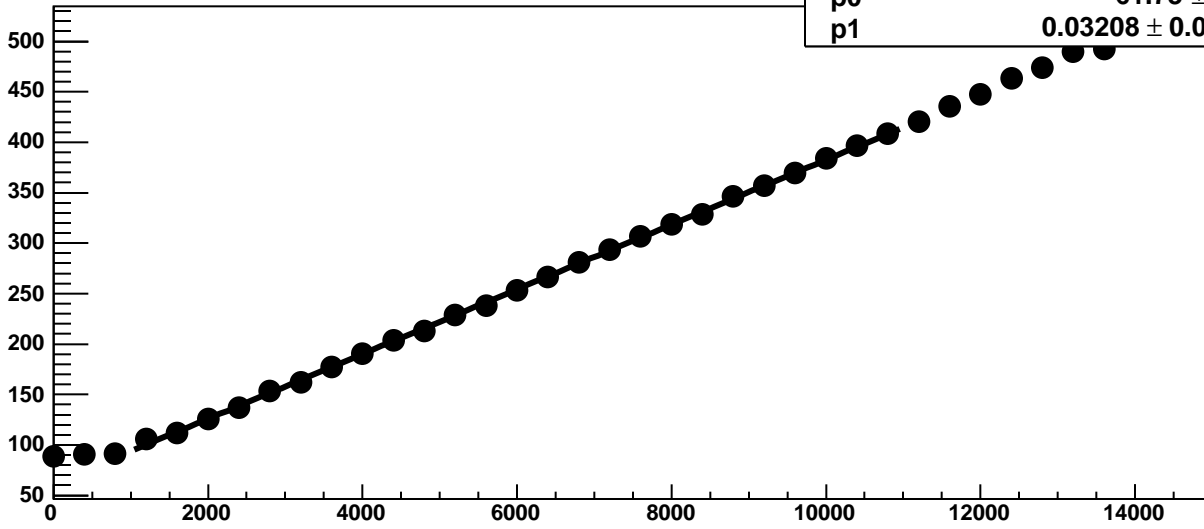
Chip 10, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

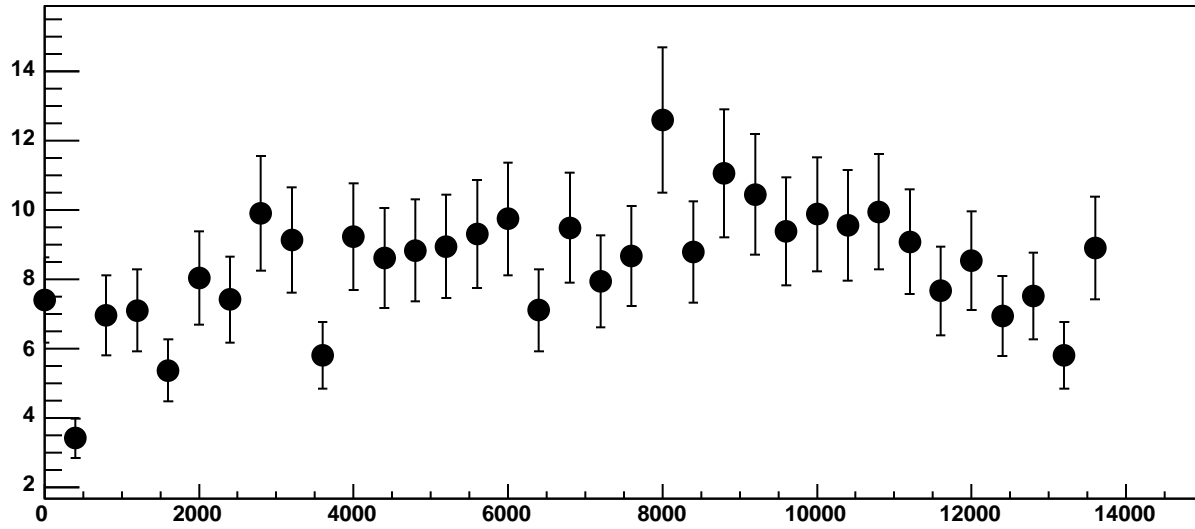


Chip 10, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

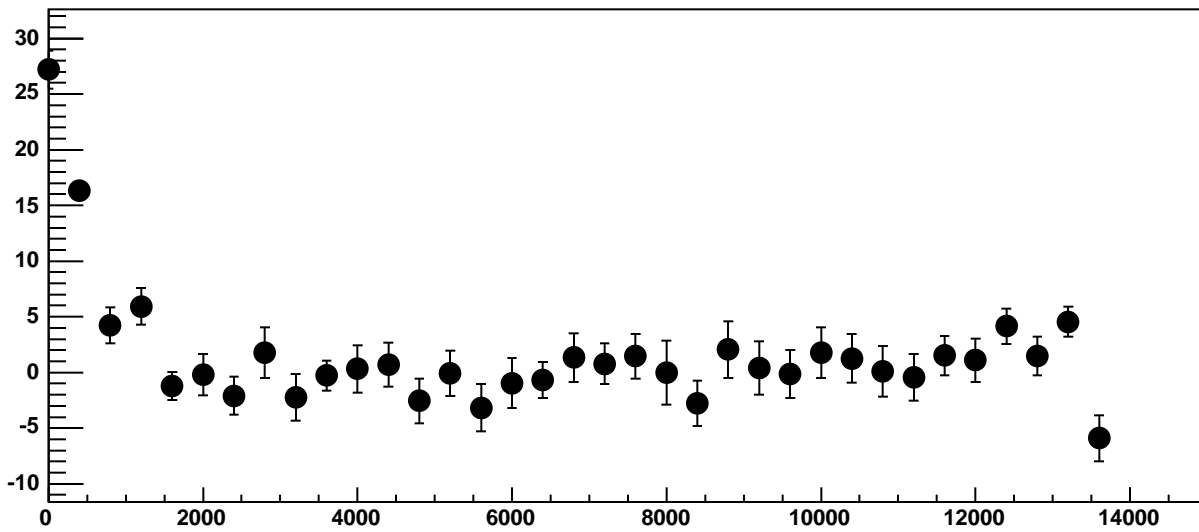


χ^2 / ndf 26.49 / 23
p0 61.73 ± 0.7995
p1 0.03208 ± 0.0001327

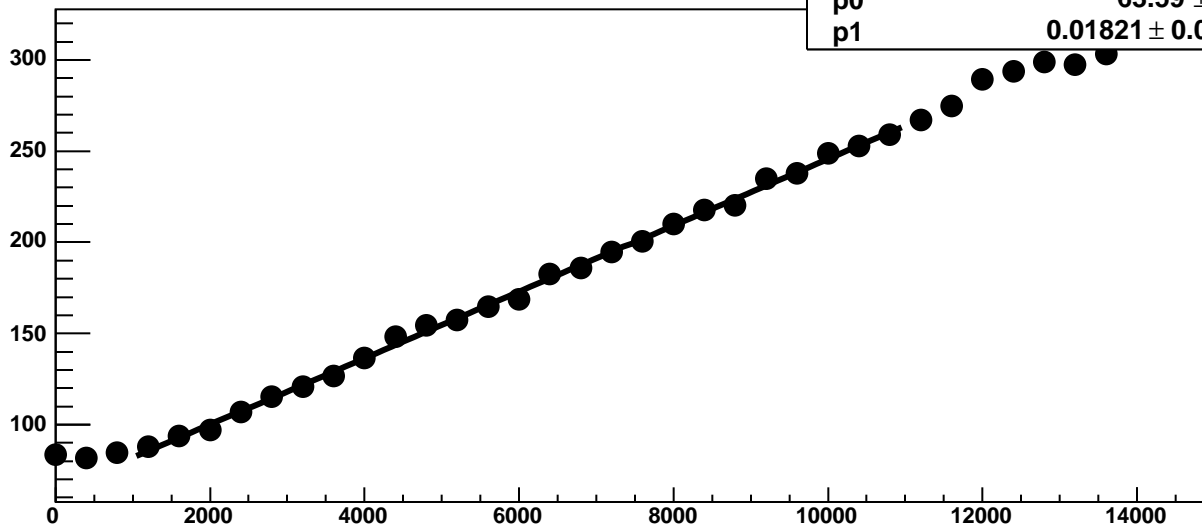
Chip 10, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



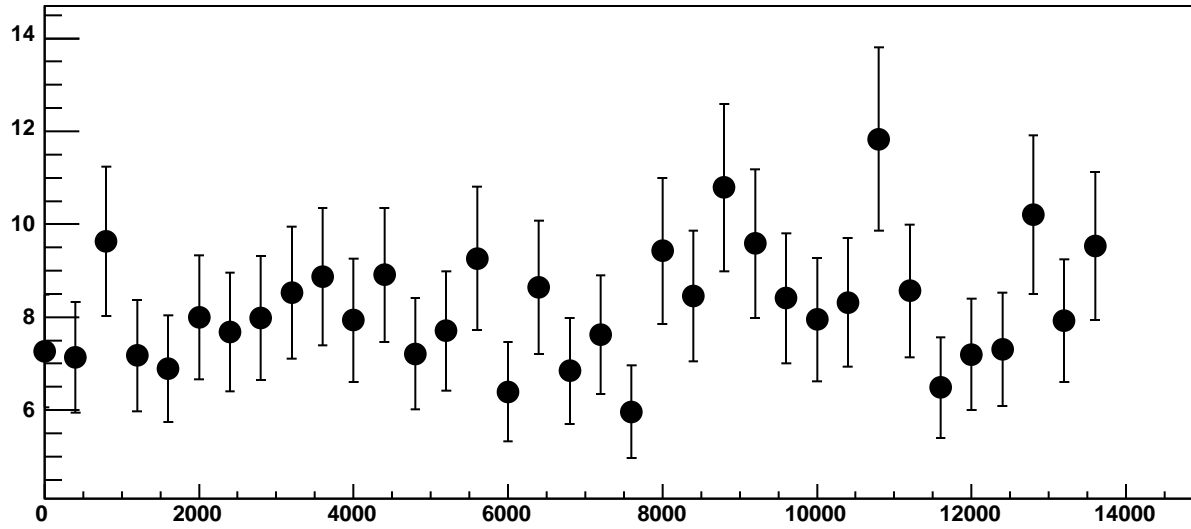
Chip 10, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC



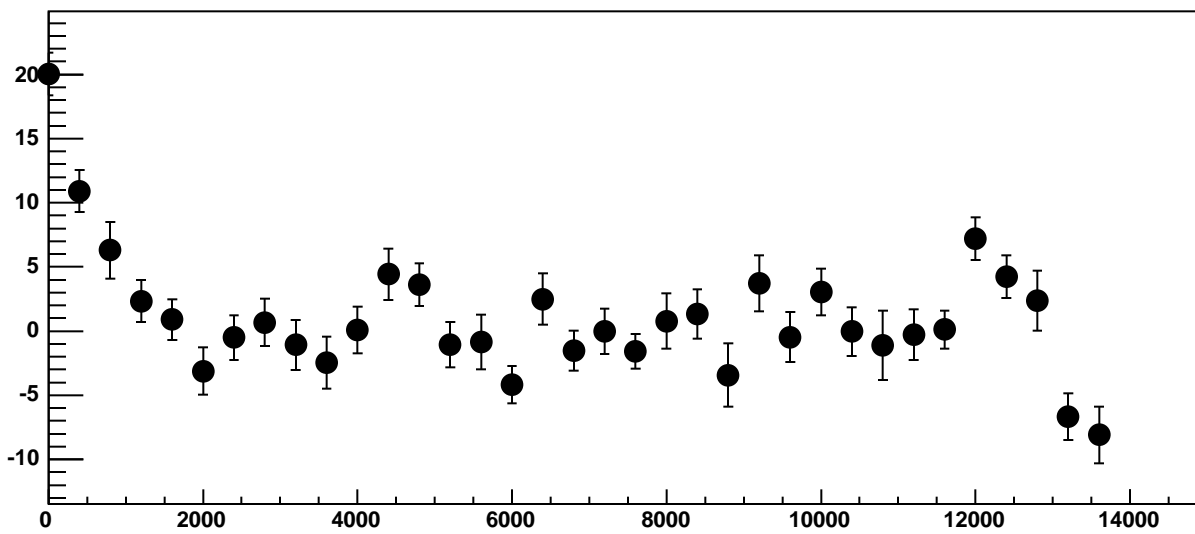
Chip 10, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



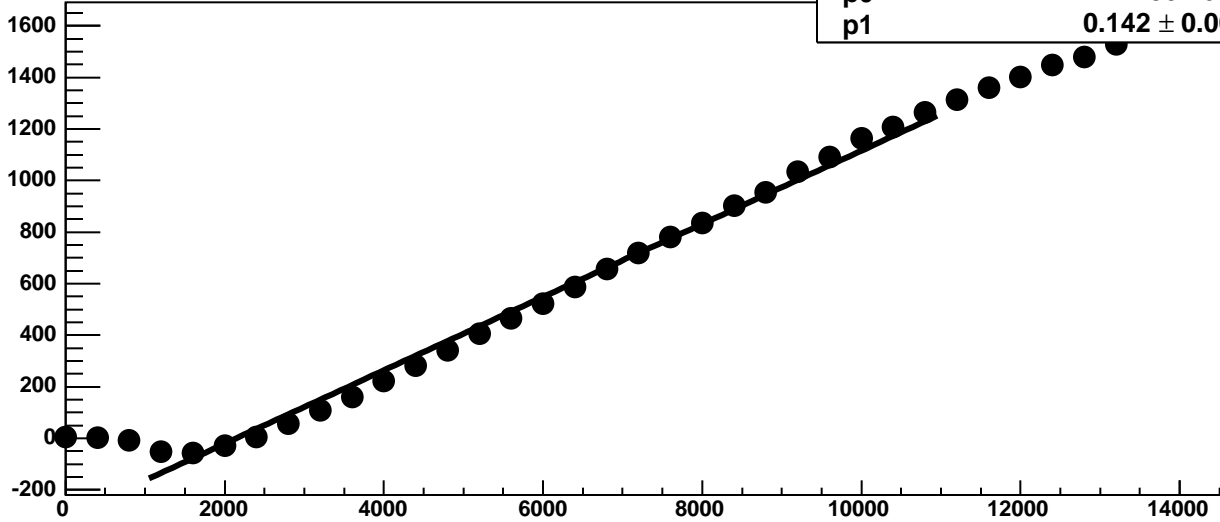
Chip 10, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



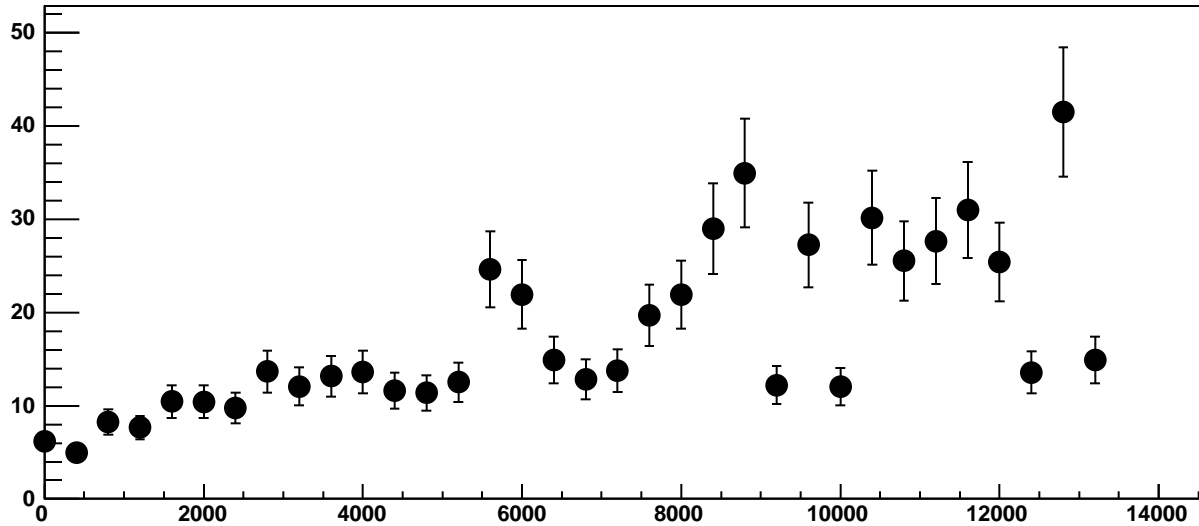
Chip 10, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



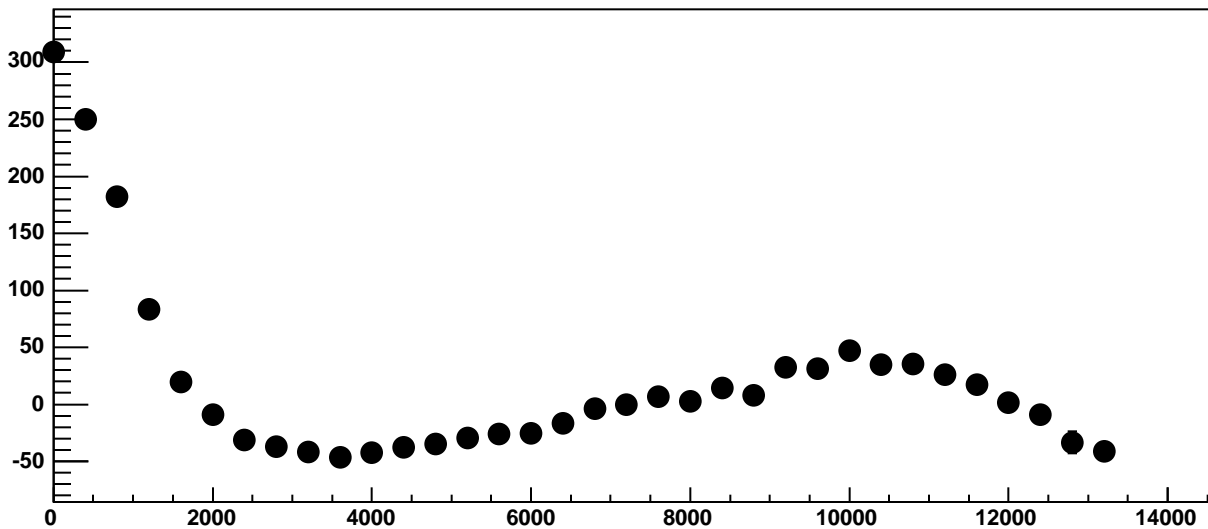
Chip 10, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



Chip 10, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

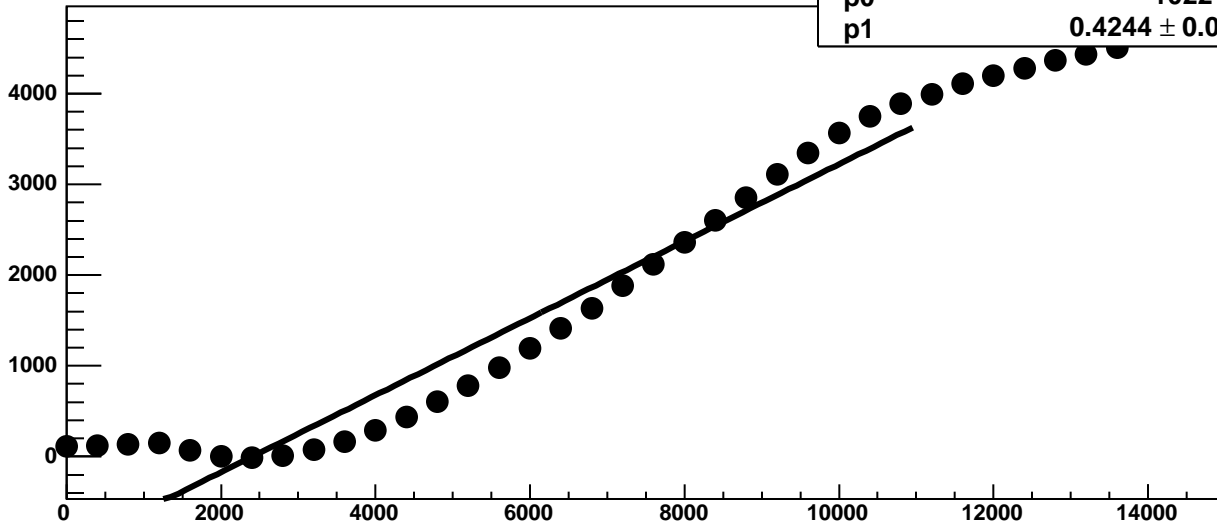


Chip 10, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

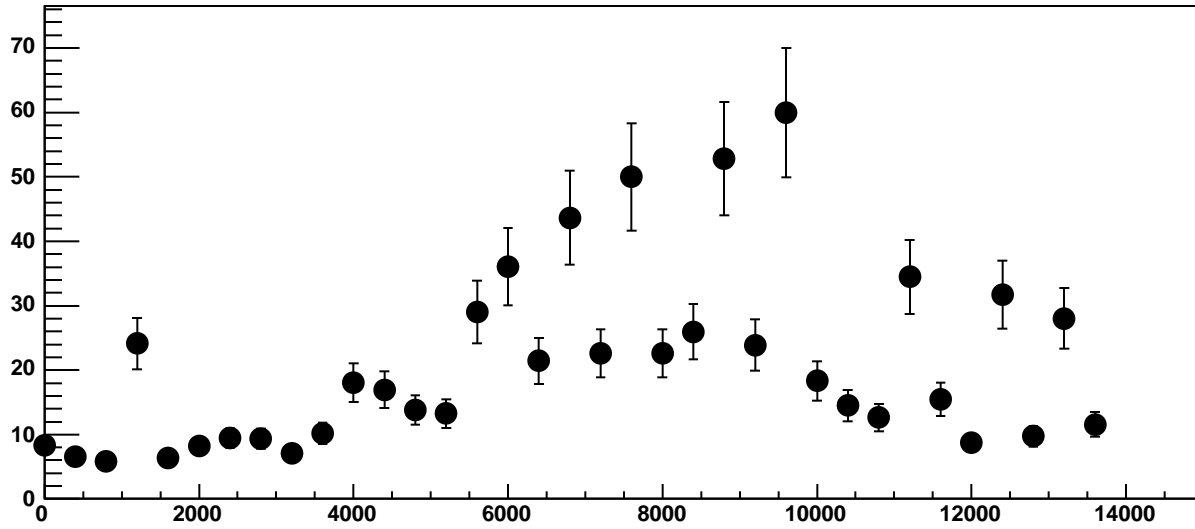


Chip 10, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

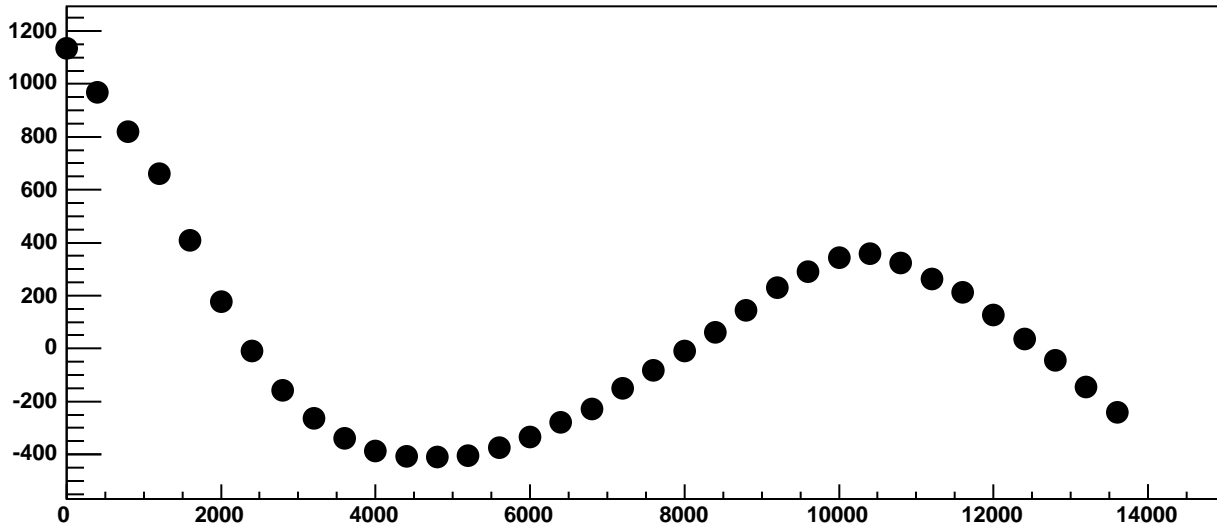
χ^2 / ndf 2.512e+05 / 23
p0 -1022 ± 1.092
p1 0.4244 ± 0.0002239



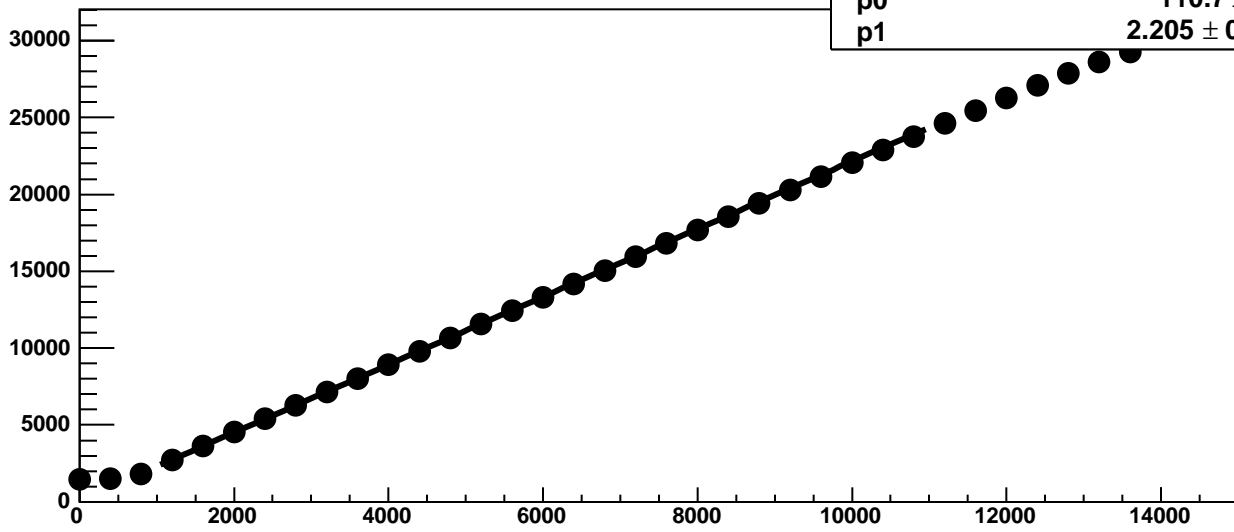
Chip 10, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



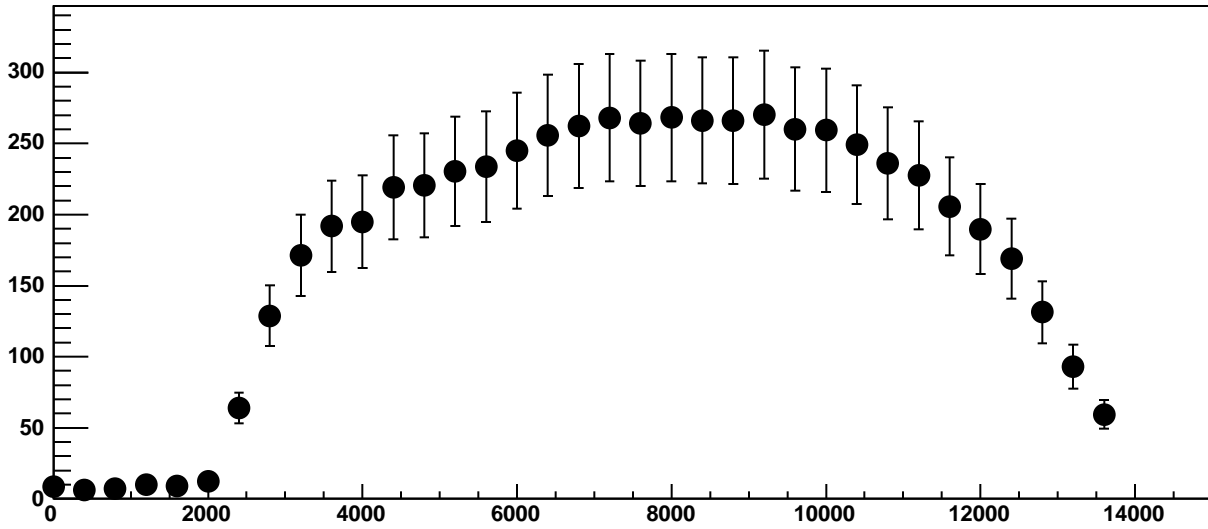
Chip 10, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



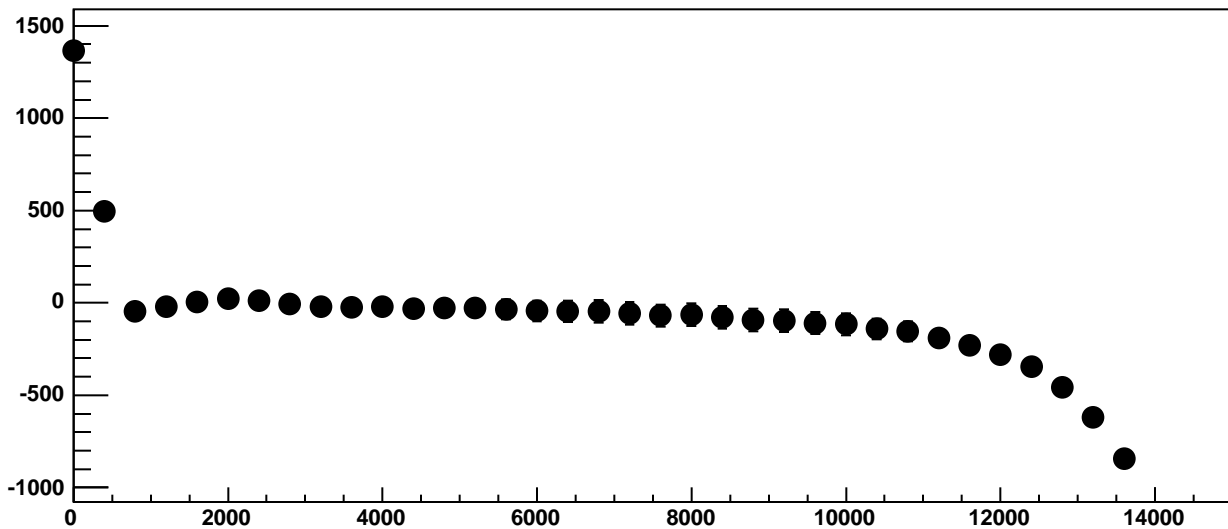
Chip 10, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC



Chip 10, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

χ^2 / ndf

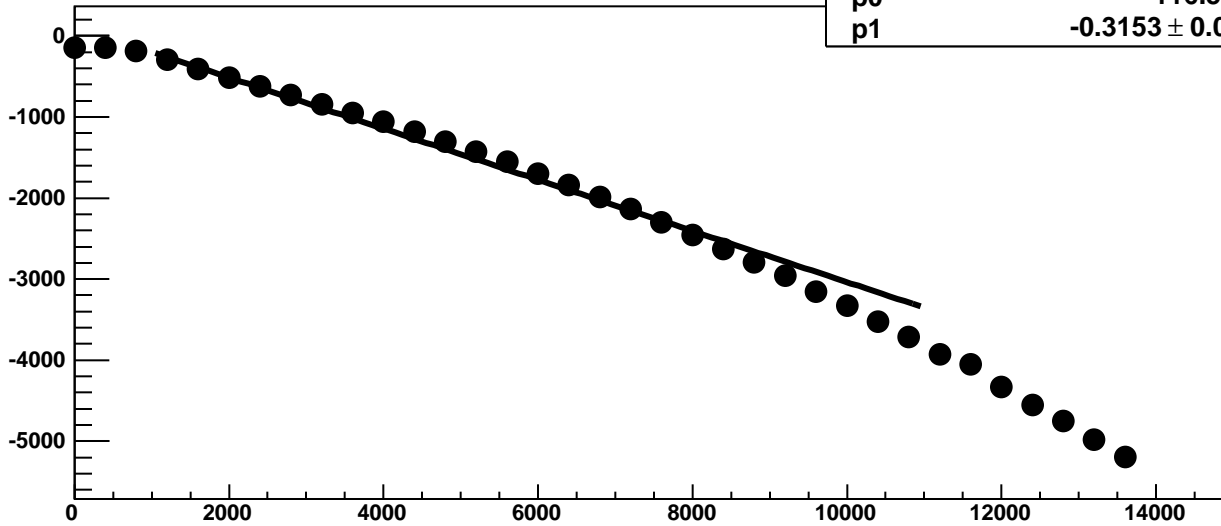
3698 / 23

p0

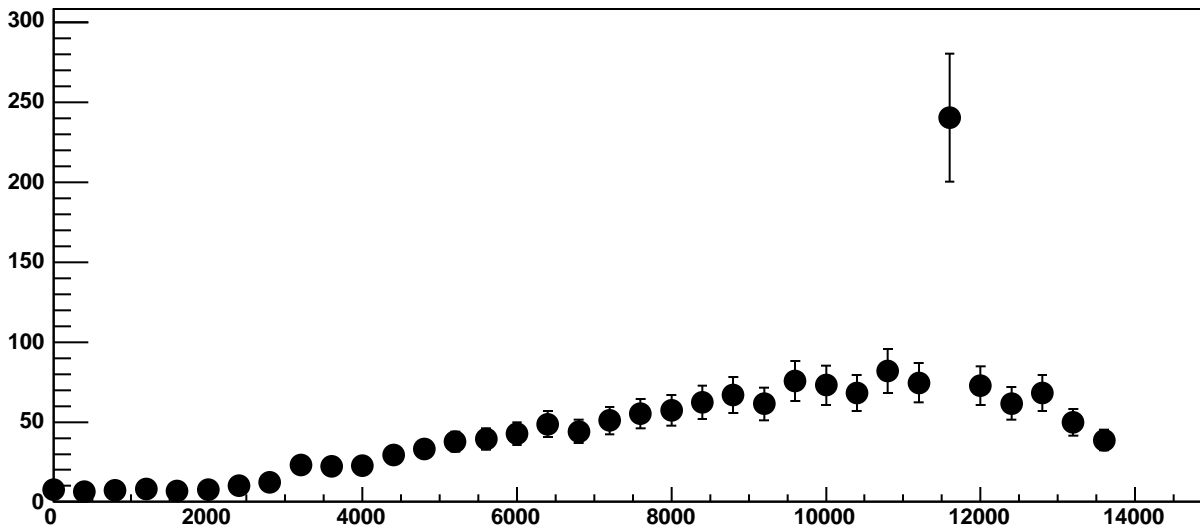
116.5 ± 1.476

p1

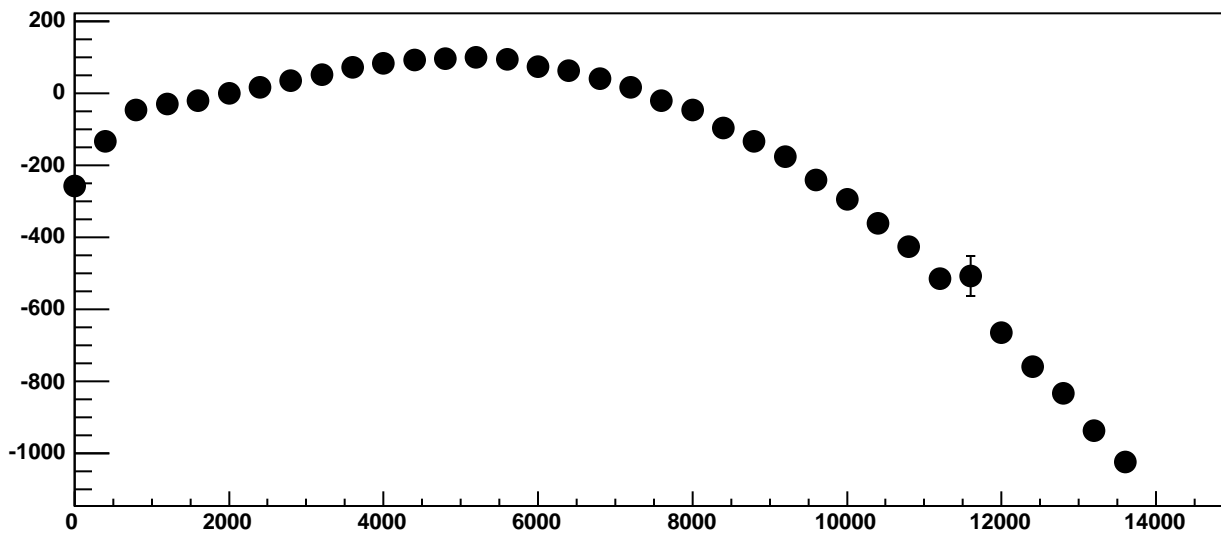
-0.3153 ± 0.0005189



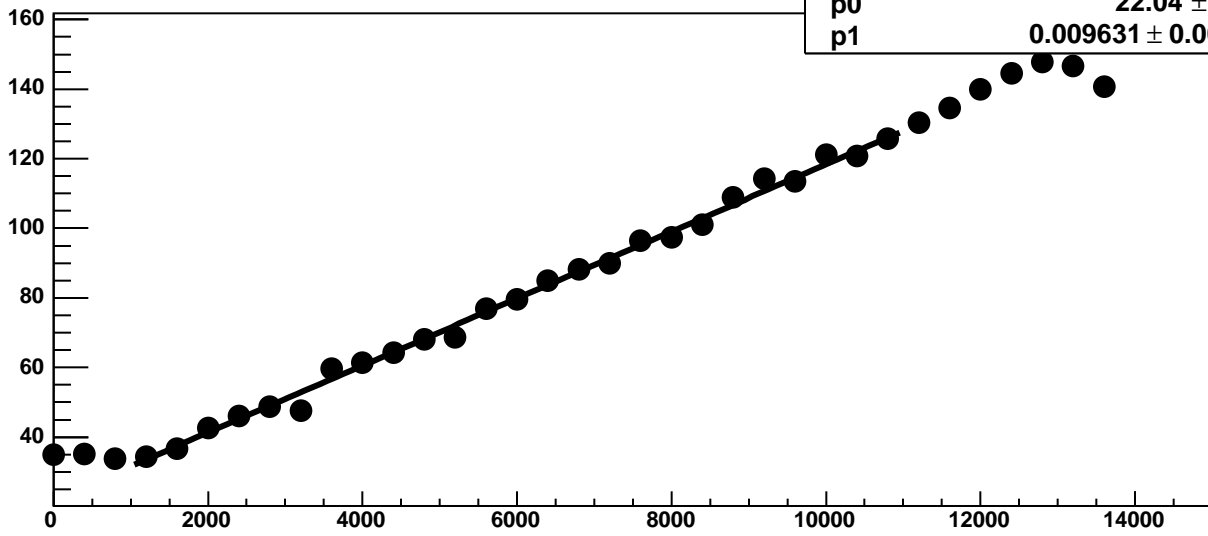
Chip 10, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

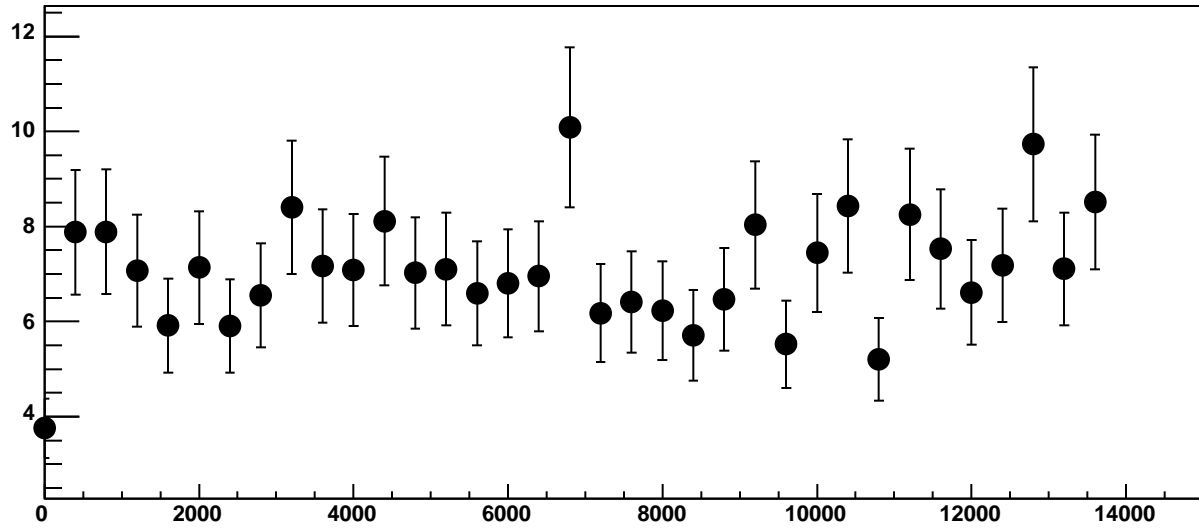


Chip 10, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

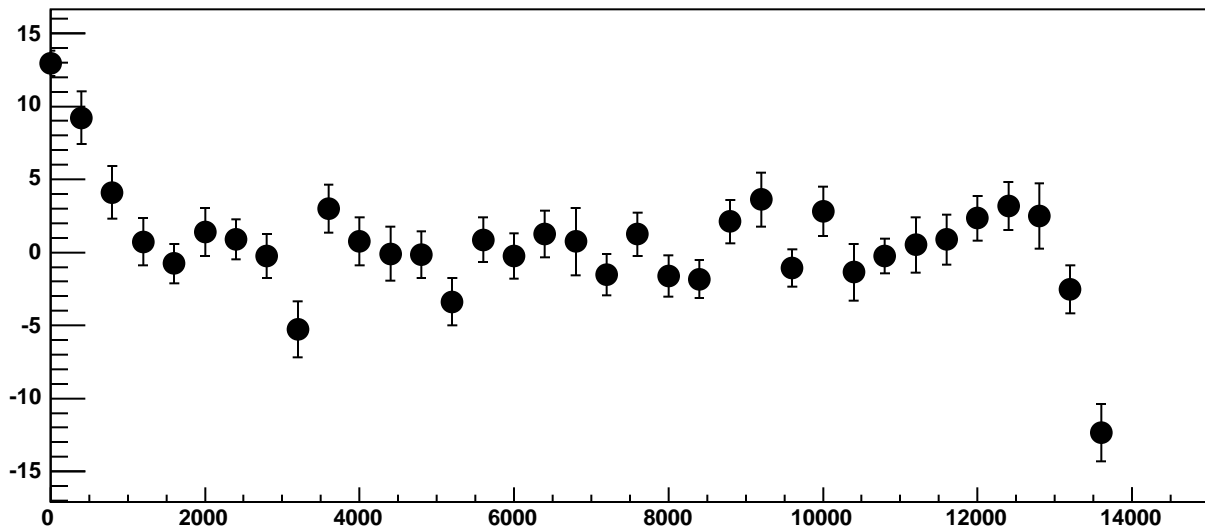


χ^2 / ndf 33.06 / 23
p0 22.04 ± 0.7084
p1 0.009631 ± 0.0001038

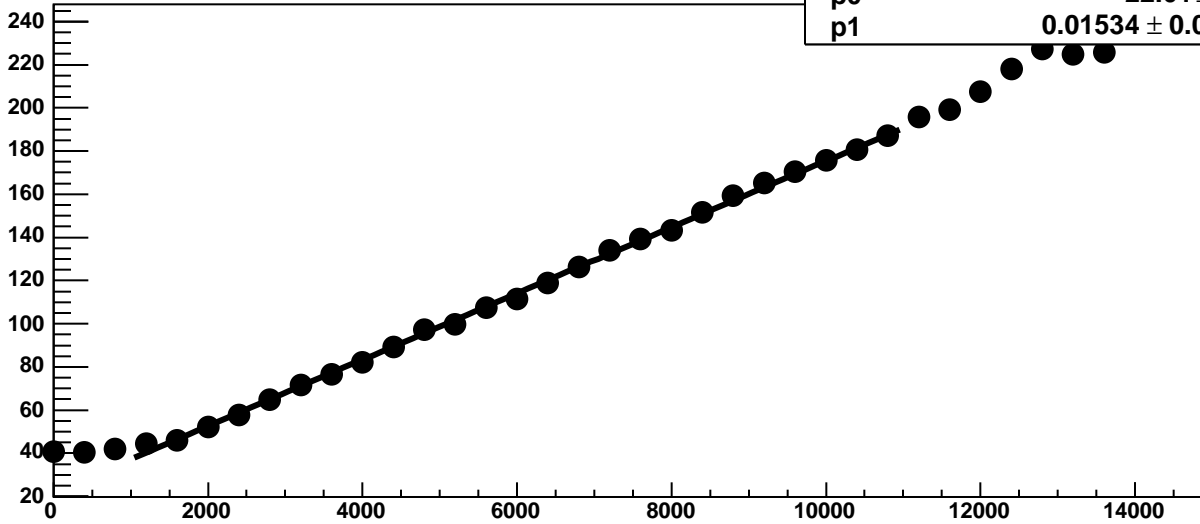
Chip 10, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

15.46 / 23

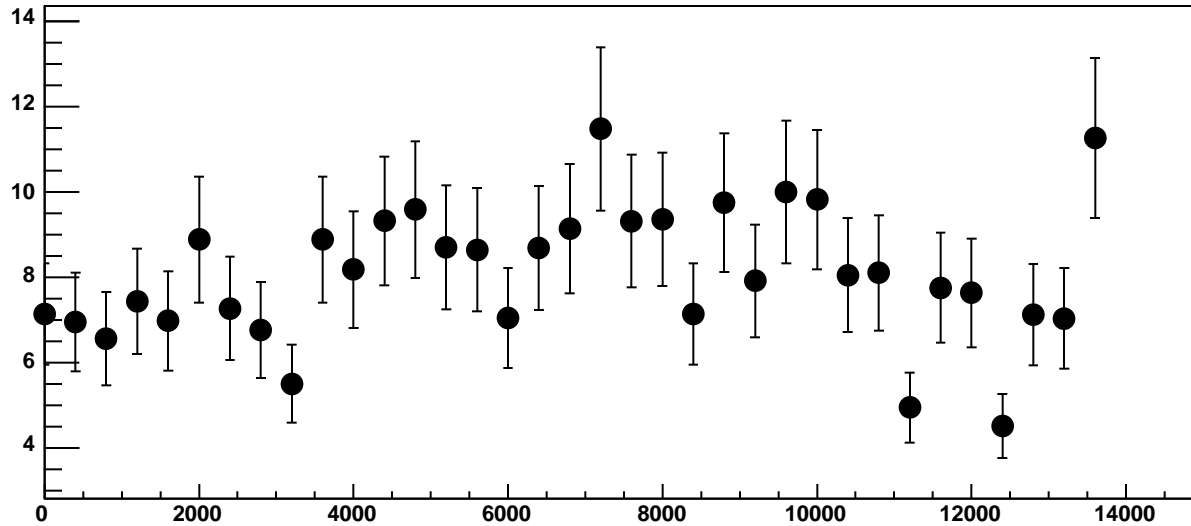
p0

22.01 ± 0.8011

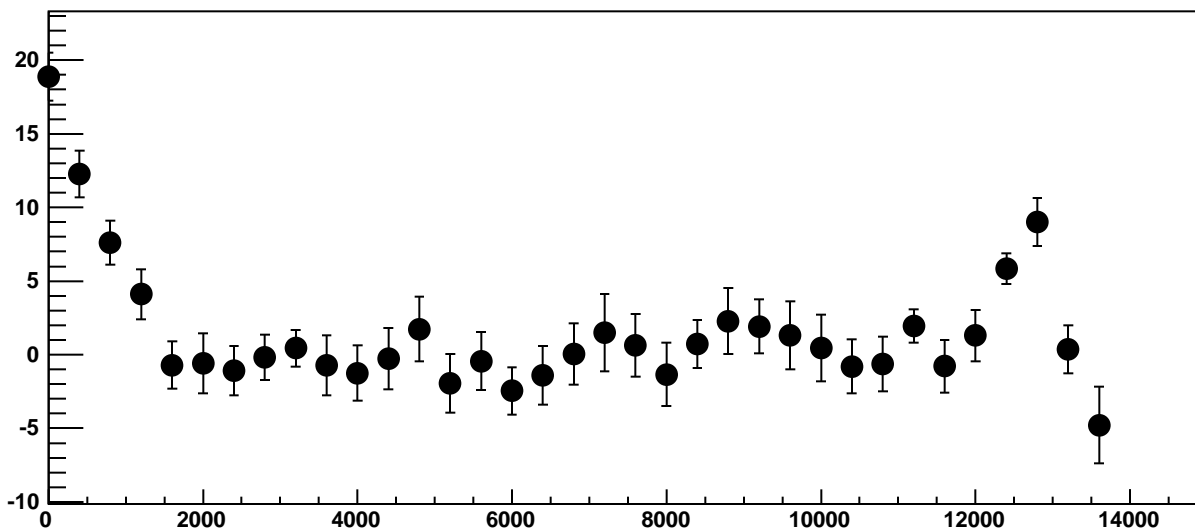
p1

0.01534 ± 0.0001268

Chip 10, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



Chip 10, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

χ^2 / ndf

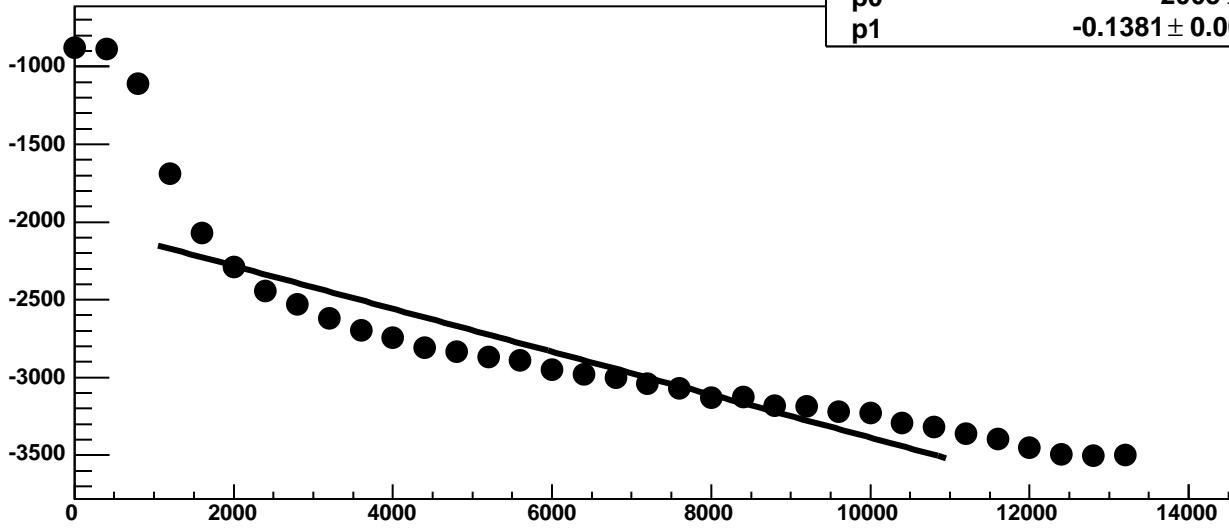
9583 / 23

p0

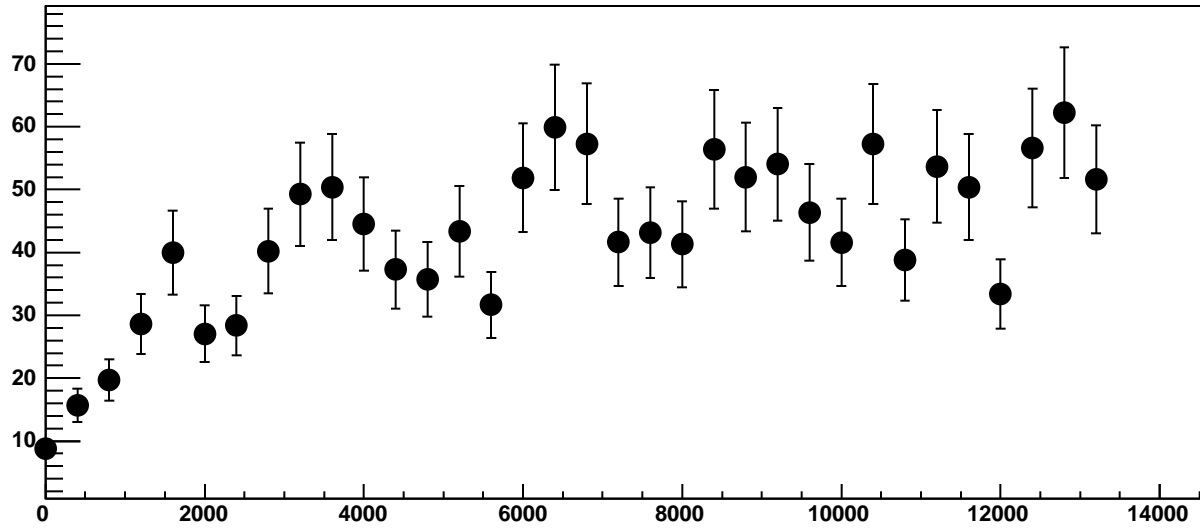
-2005 ± 3.744

p1

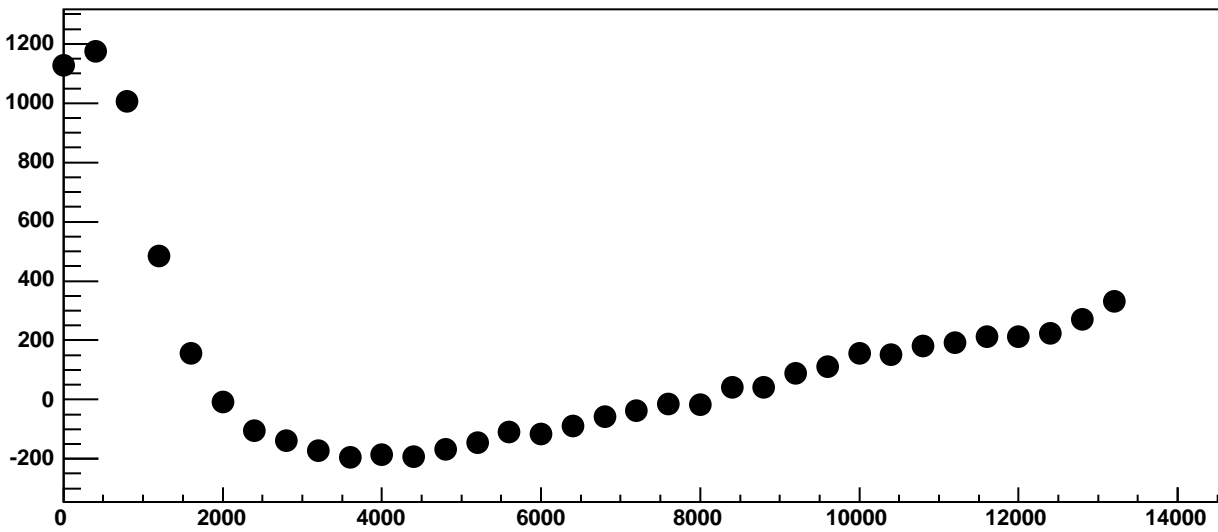
-0.1381 ± 0.0006237



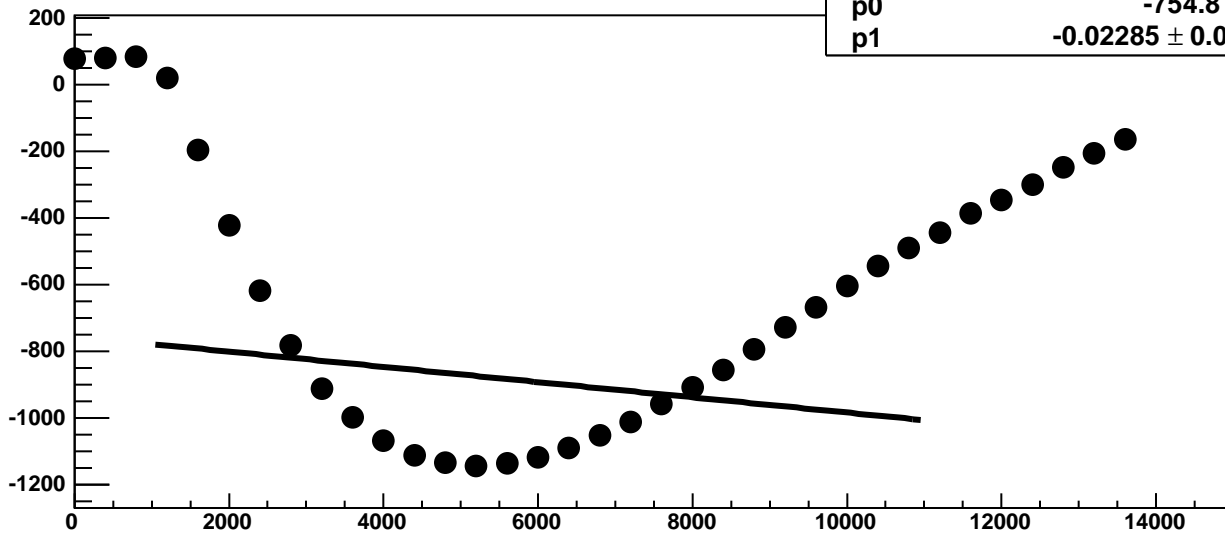
Chip 10, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

5.292e+05 / 23

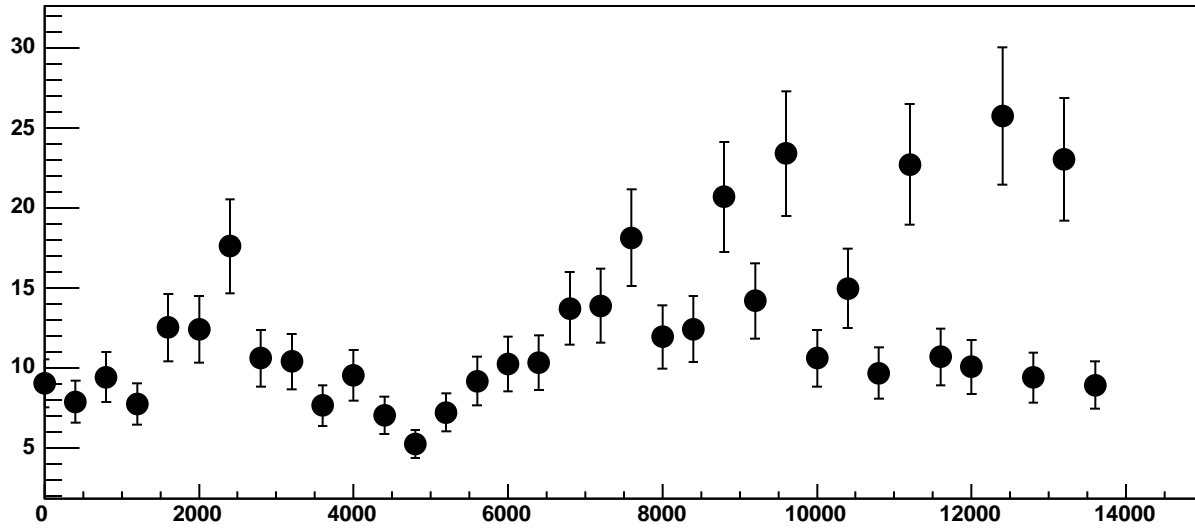
p0

-754.8 ± 1.062

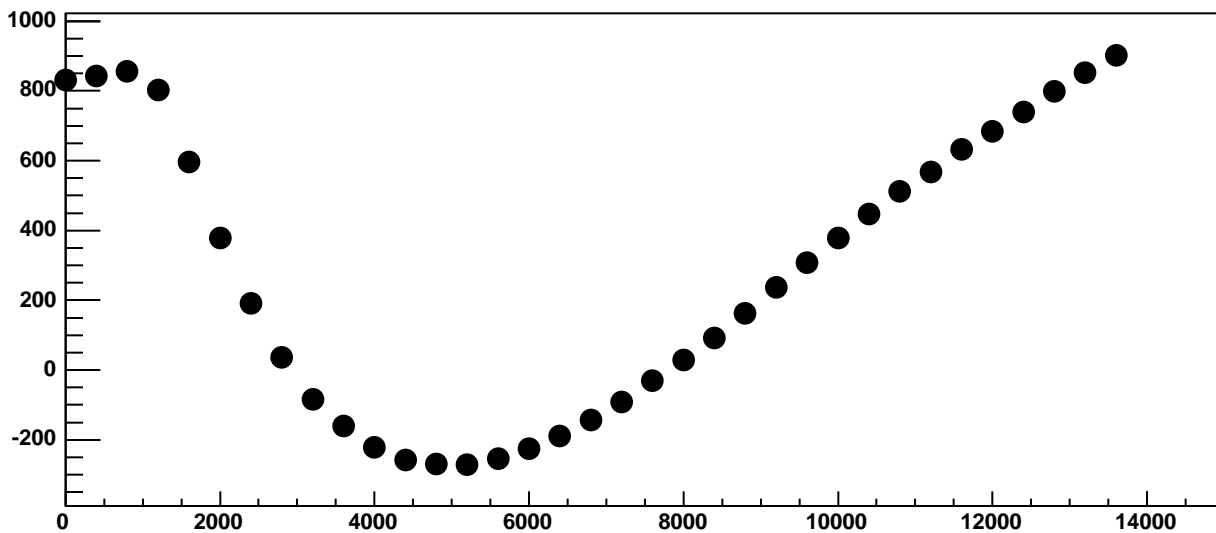
p1

-0.02285 ± 0.0001809

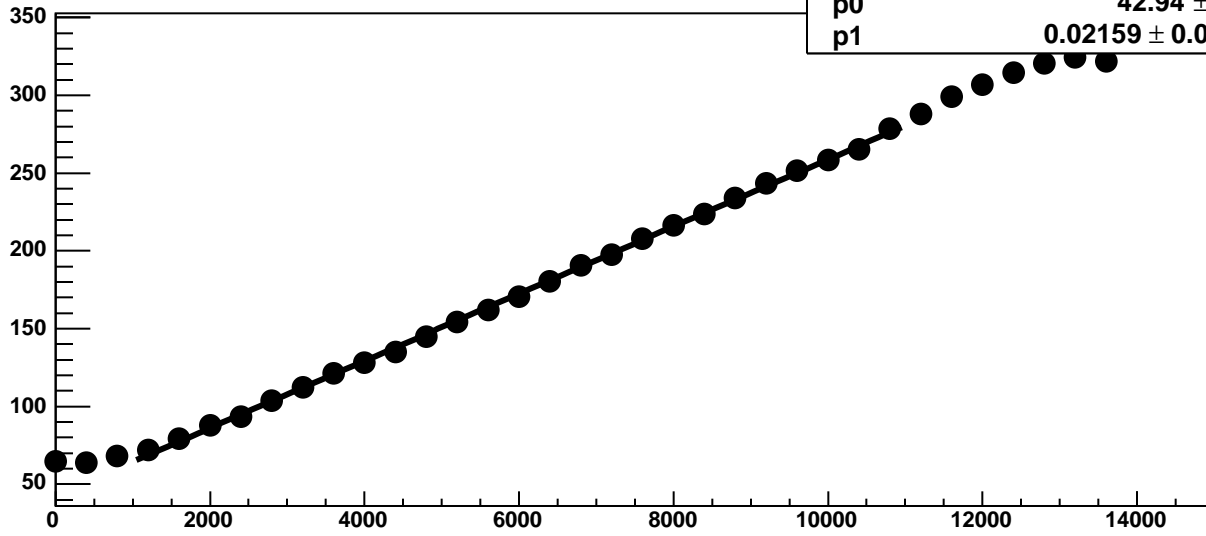
Chip 10, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

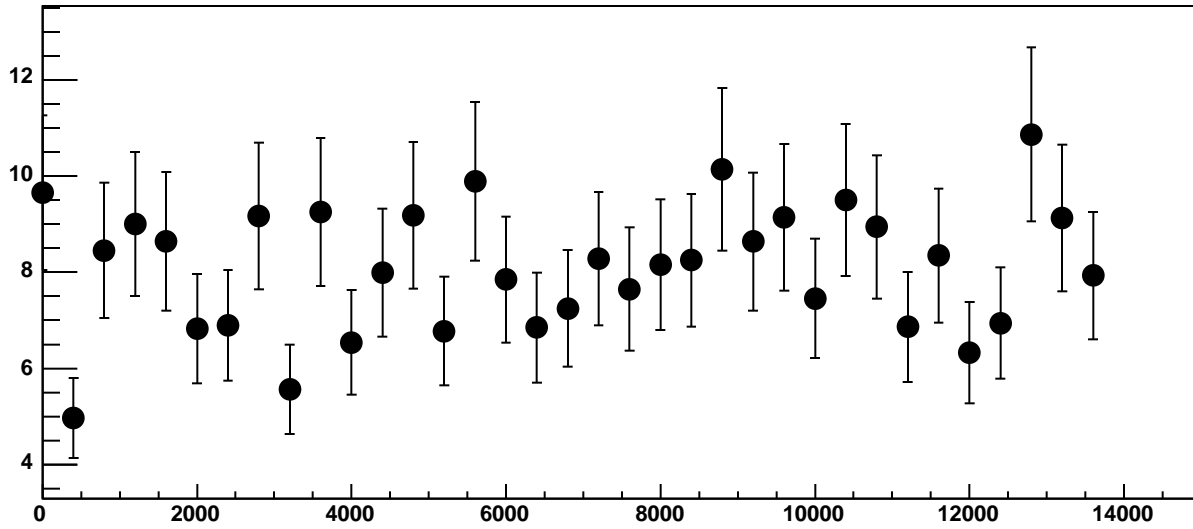


Chip 10, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC

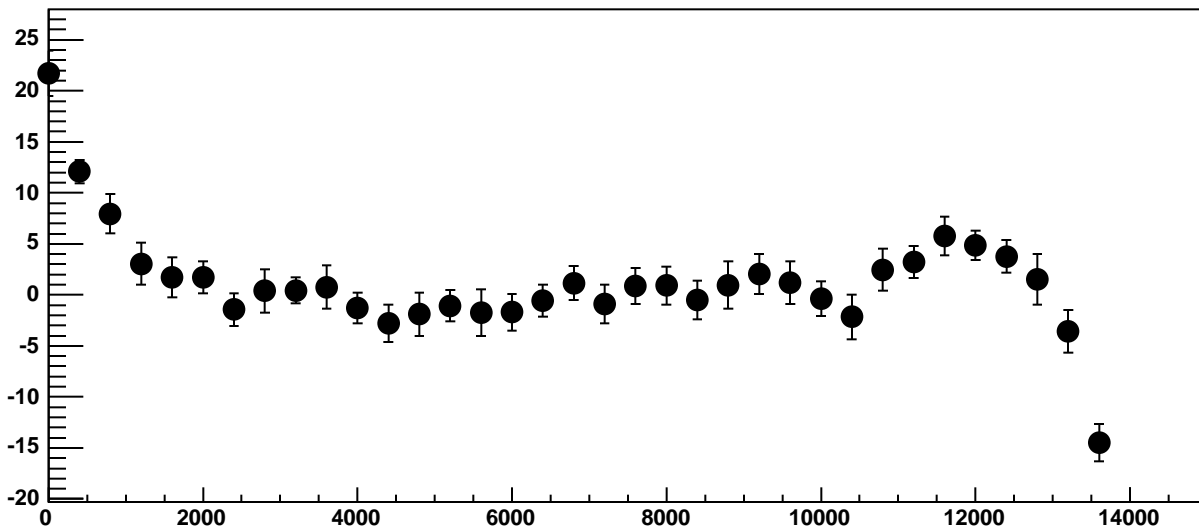


χ^2 / ndf 16.46 / 23
p0 42.94 ± 0.8255
p1 0.02159 ± 0.0001299

Chip 10, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC

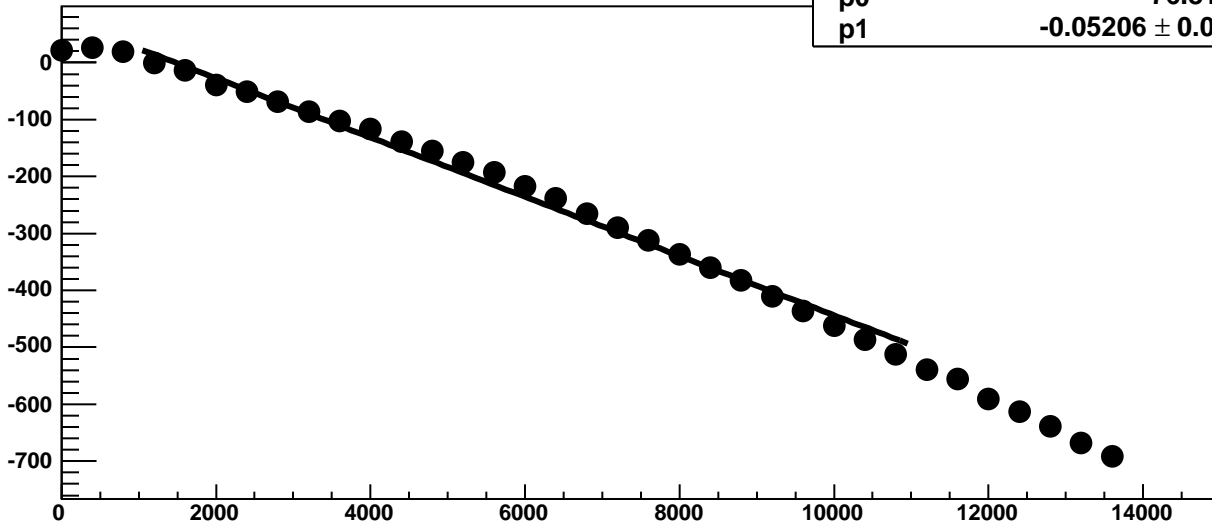


Chip 10, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC

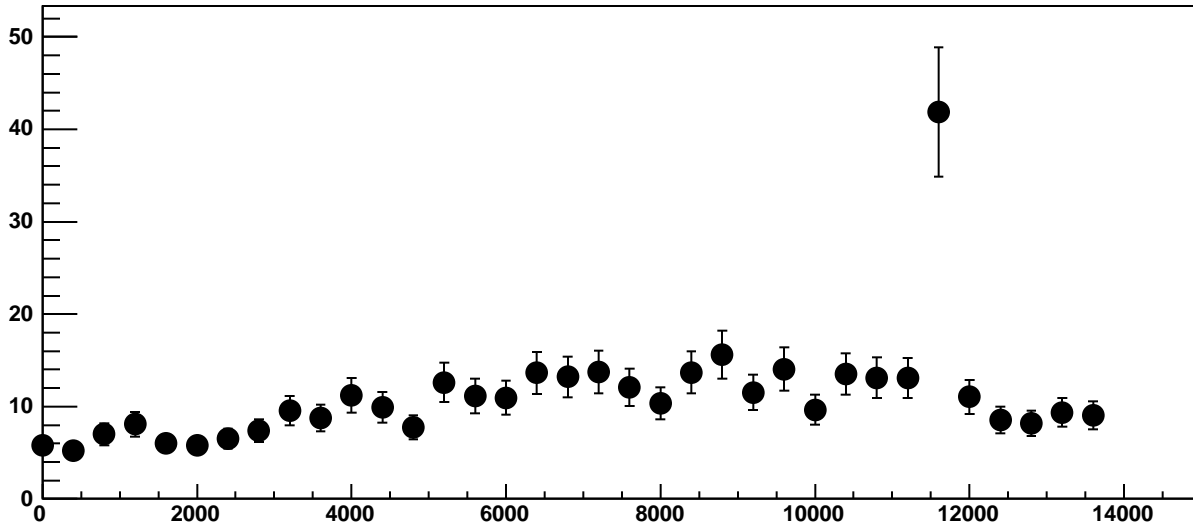


Chip 10, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

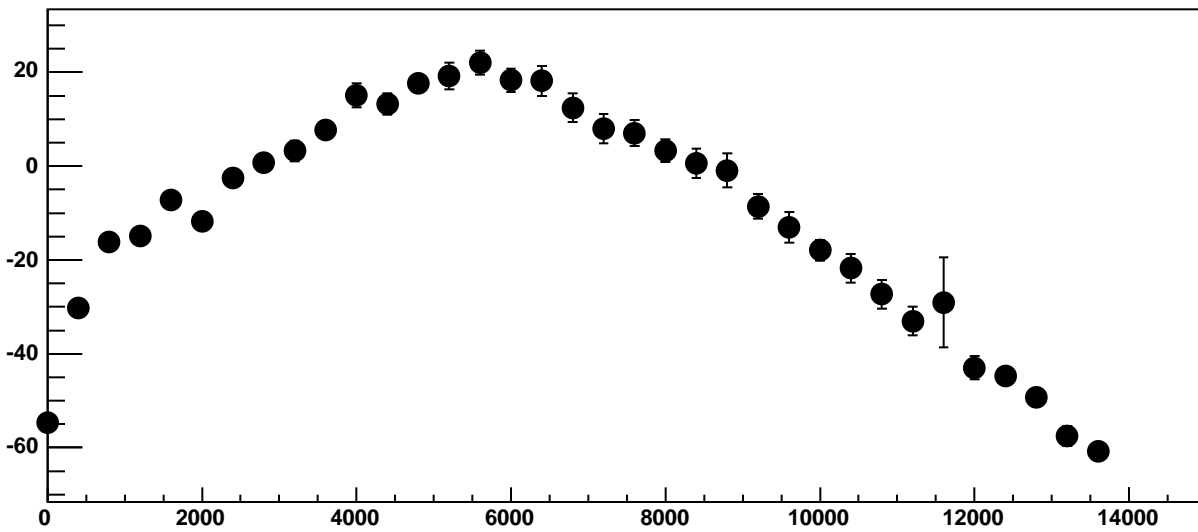
χ^2 / ndf 816.7 / 23
p0 76.81 ± 0.83
p1 -0.05206 ± 0.0001523



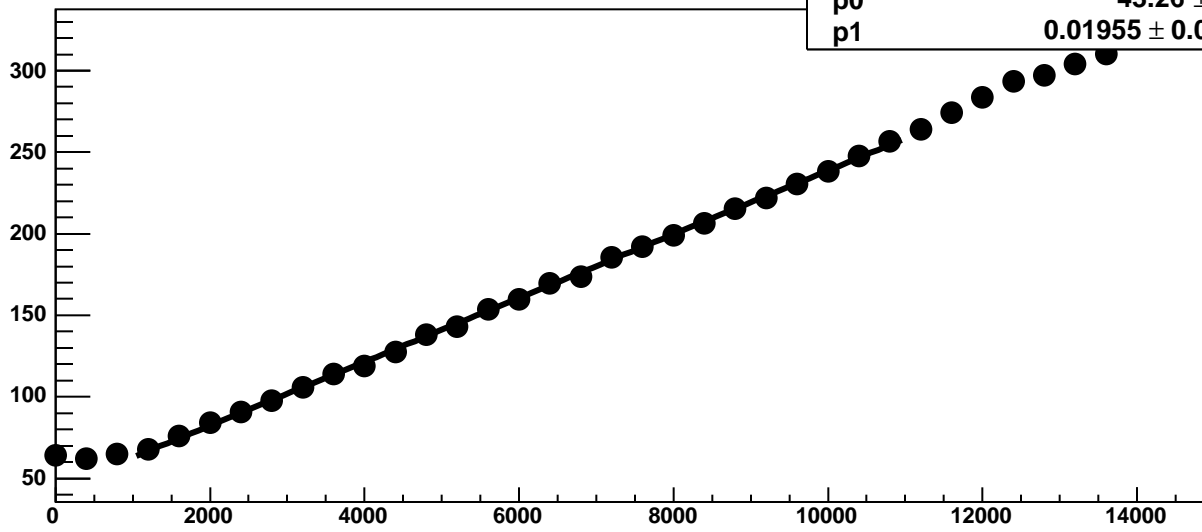
Chip 10, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

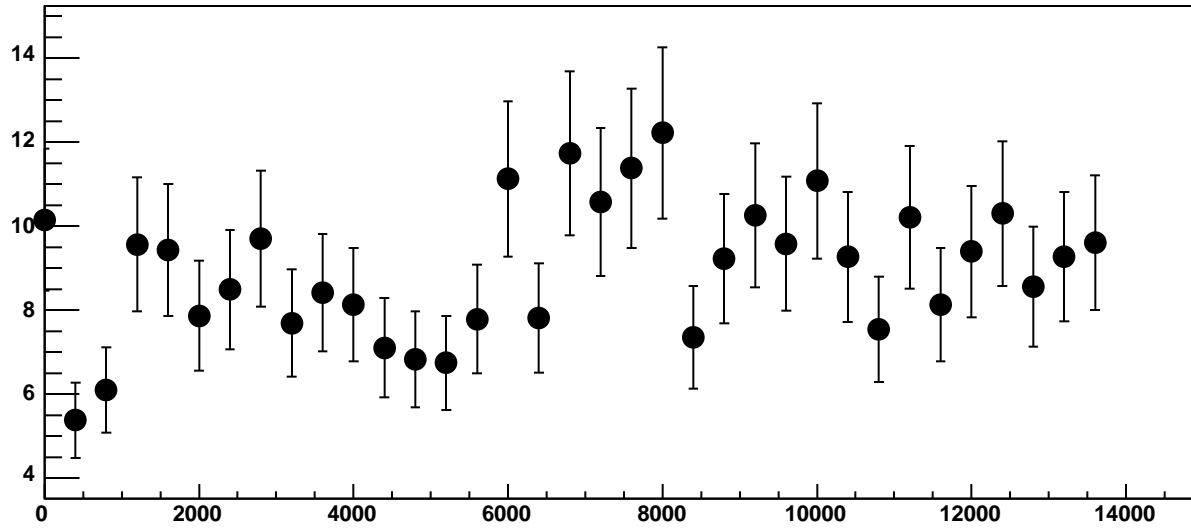


Chip 10, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

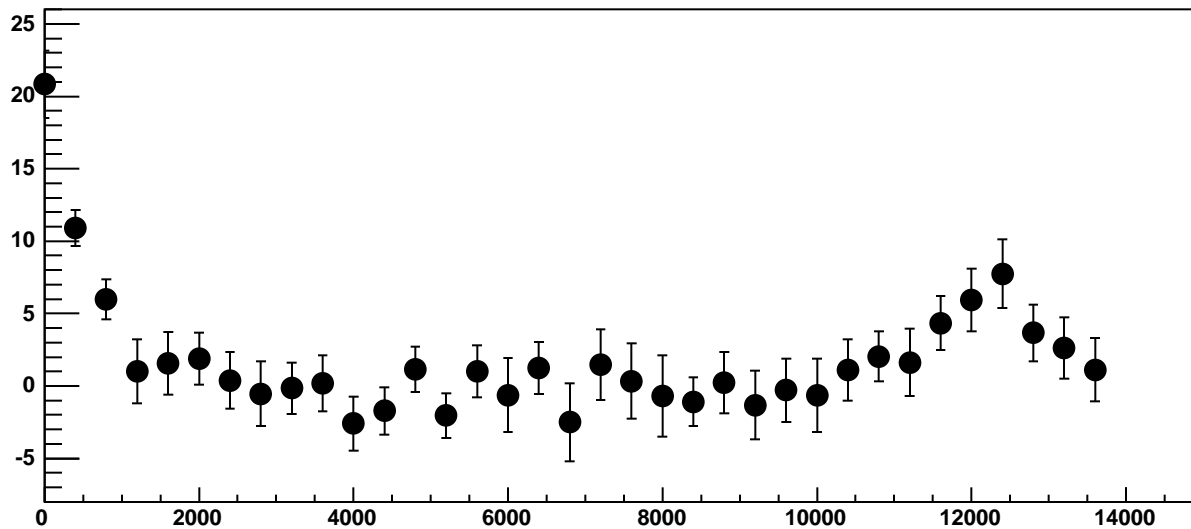


χ^2 / ndf 11.9 / 23
p0 43.26 ± 0.9062
p1 0.01955 ± 0.0001413

Chip 10, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

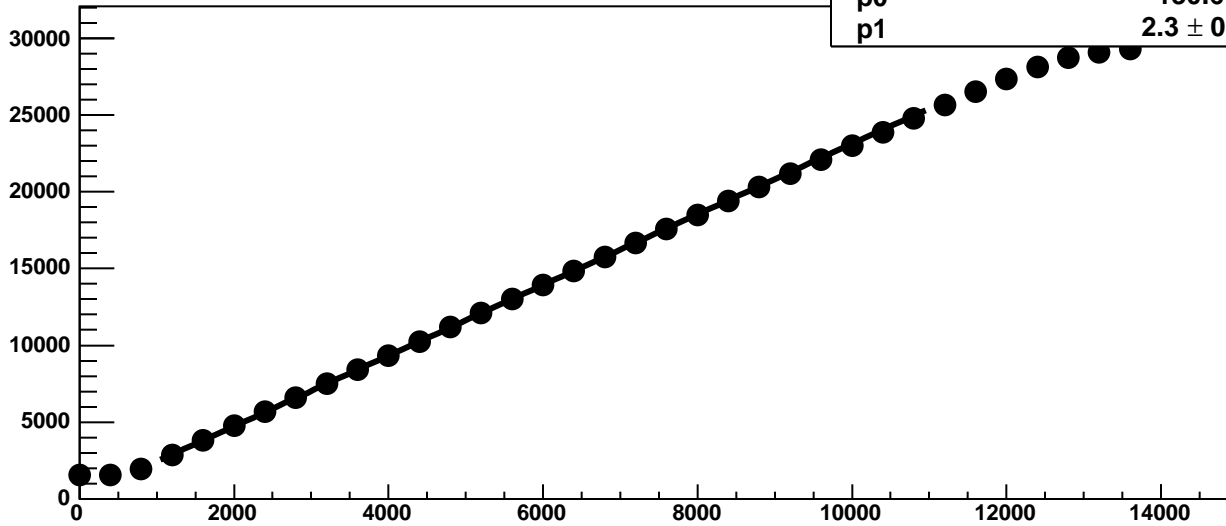


Chip 10, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

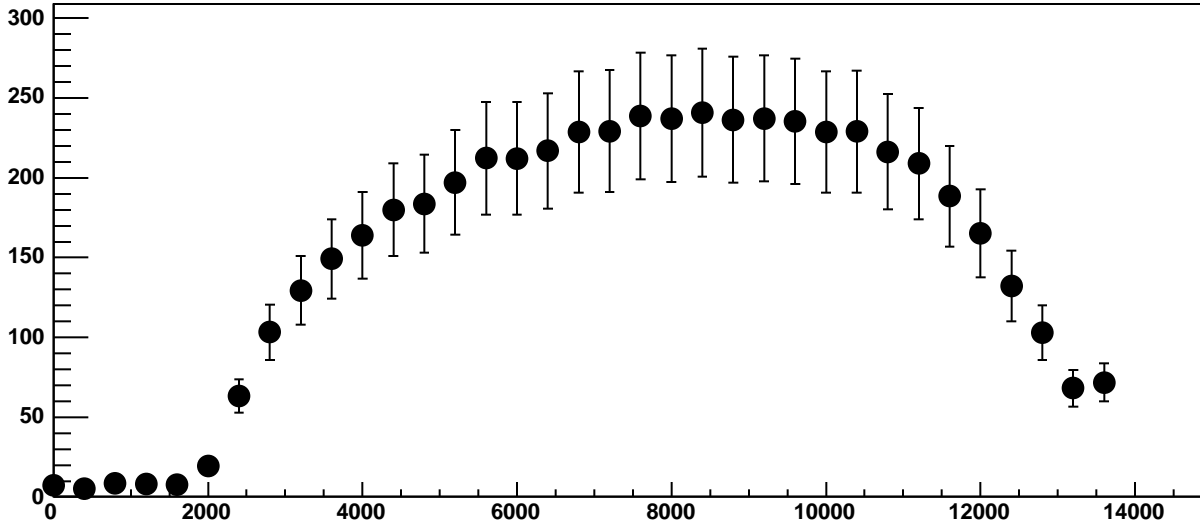


Chip 10, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

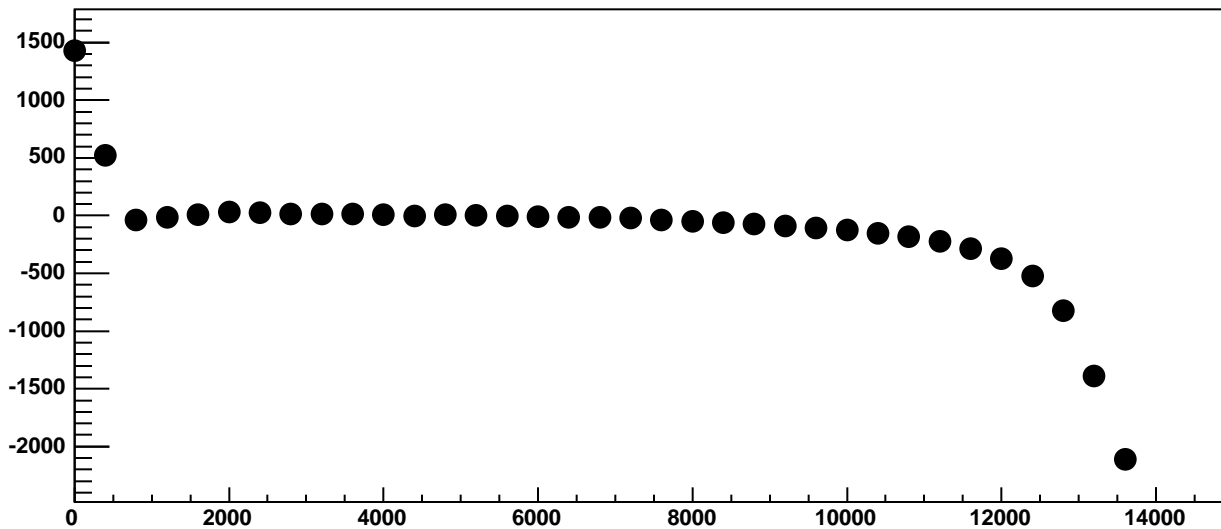
χ^2 / ndf 194.6 / 23
p0 130.6 ± 2.976
p1 2.3 ± 0.001773



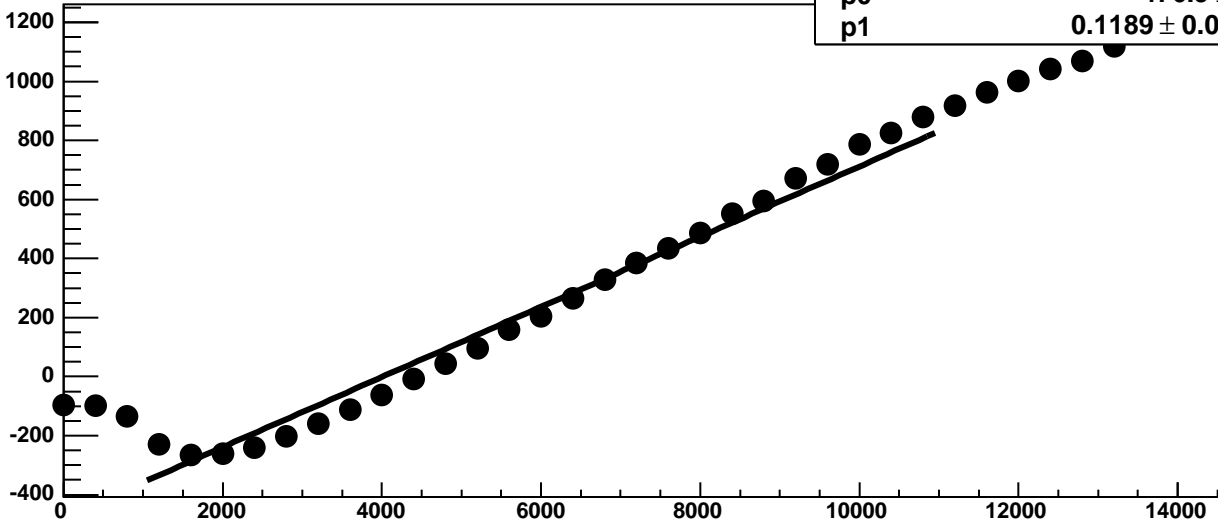
Chip 10, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

7075 / 23

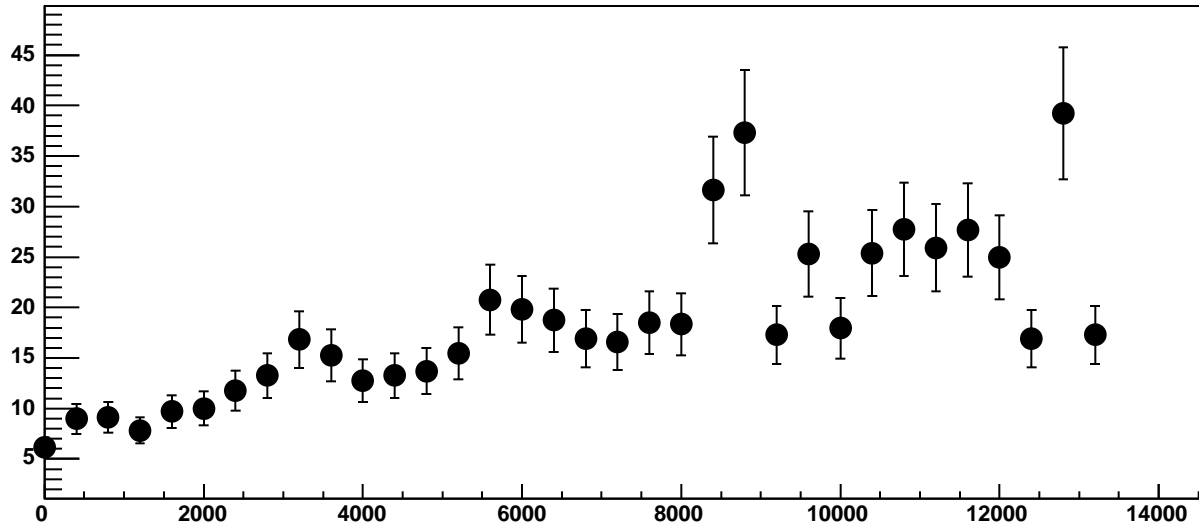
p0

-476.9 ± 1.257

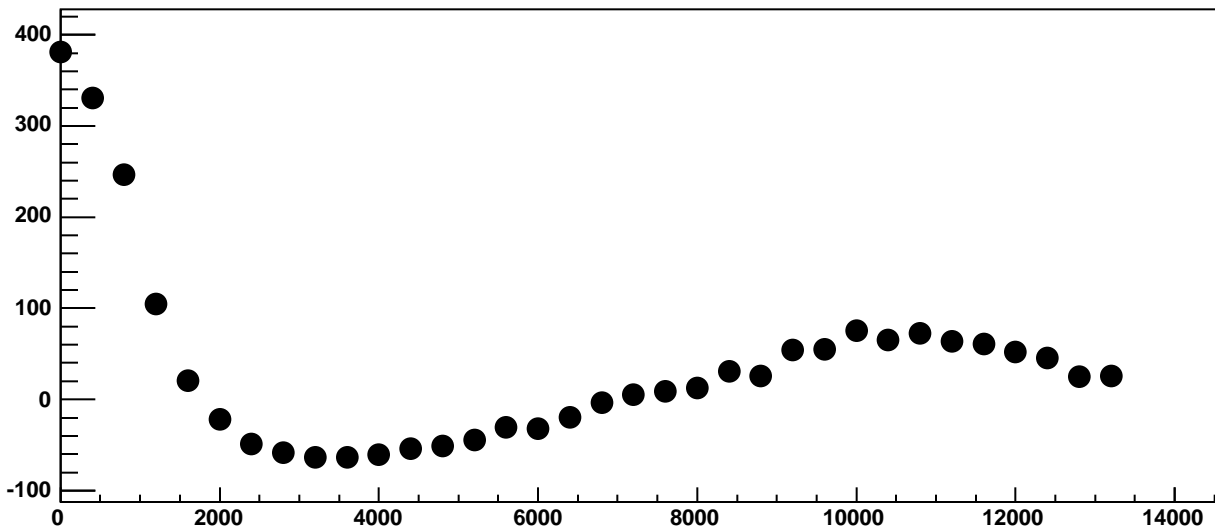
p1

0.1189 ± 0.0002481

Chip 10, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

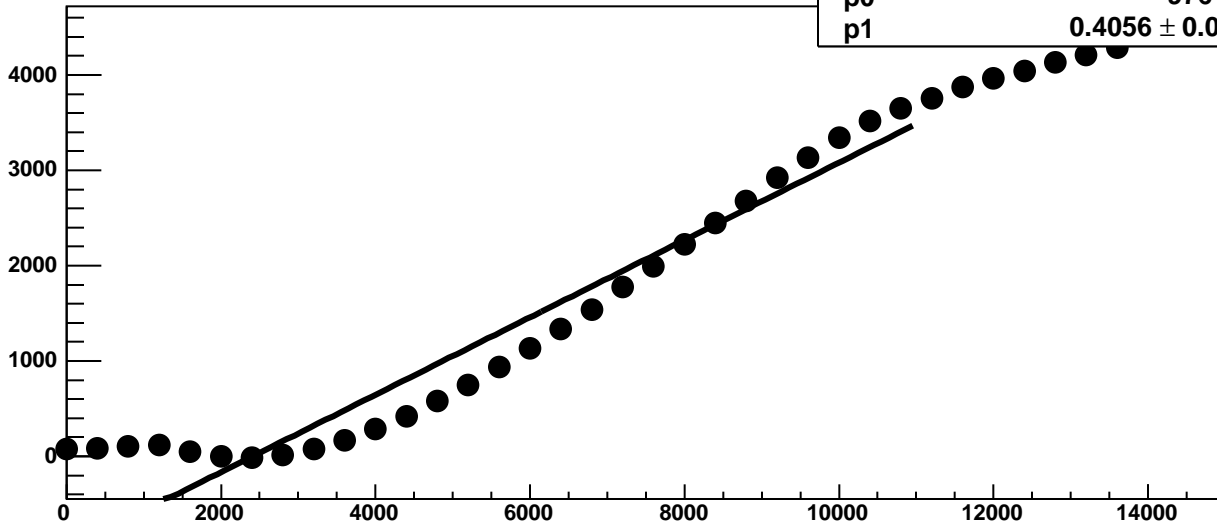


Chip 10, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

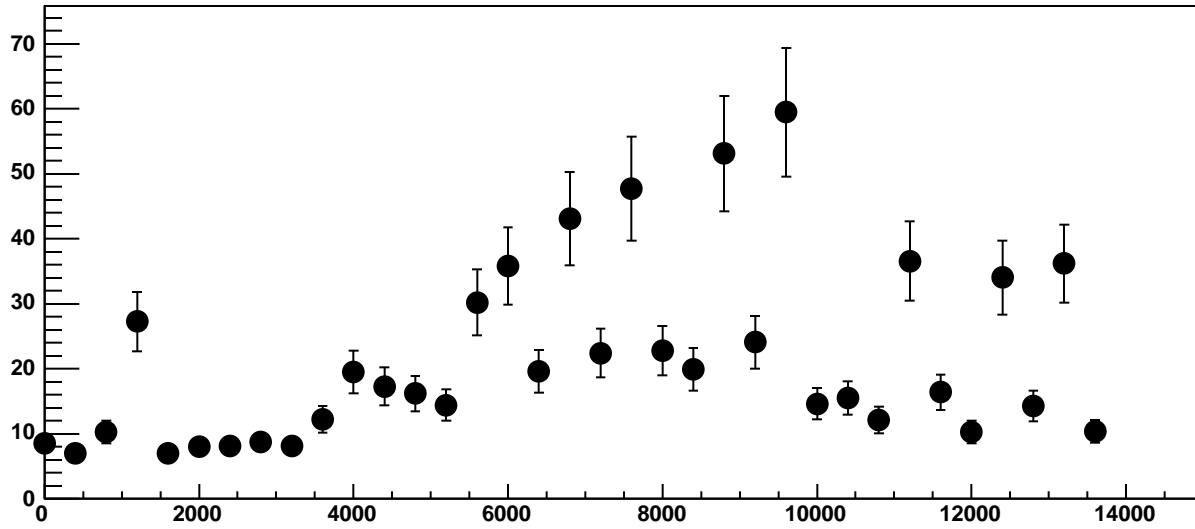


Chip 10, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

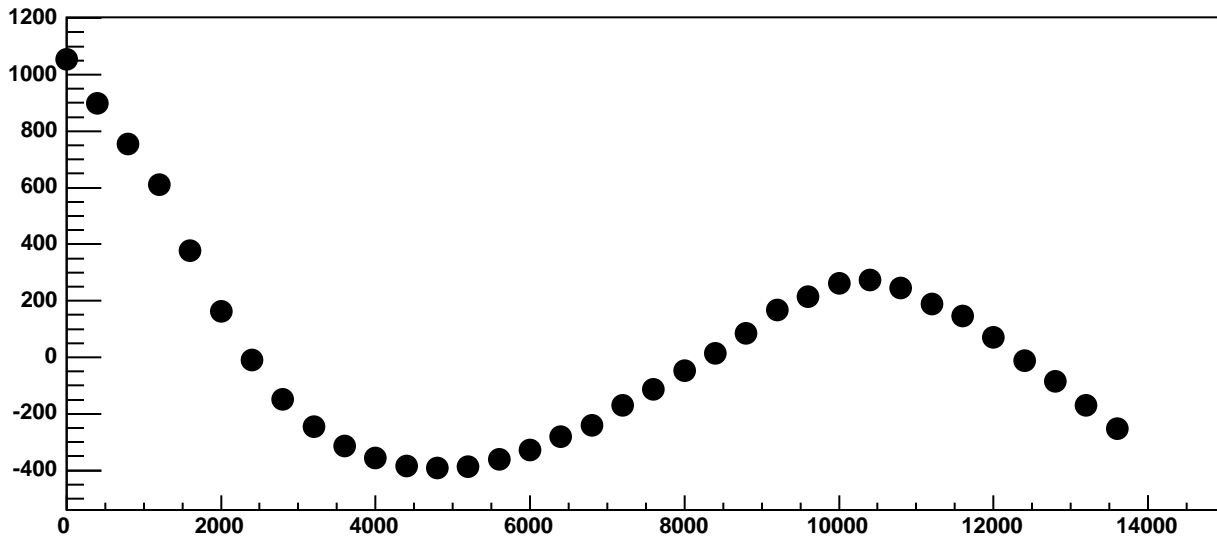
χ^2 / ndf 1.781e+05 / 23
p0 -976 ± 1.108
p1 0.4056 ± 0.0002179



Chip 10, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

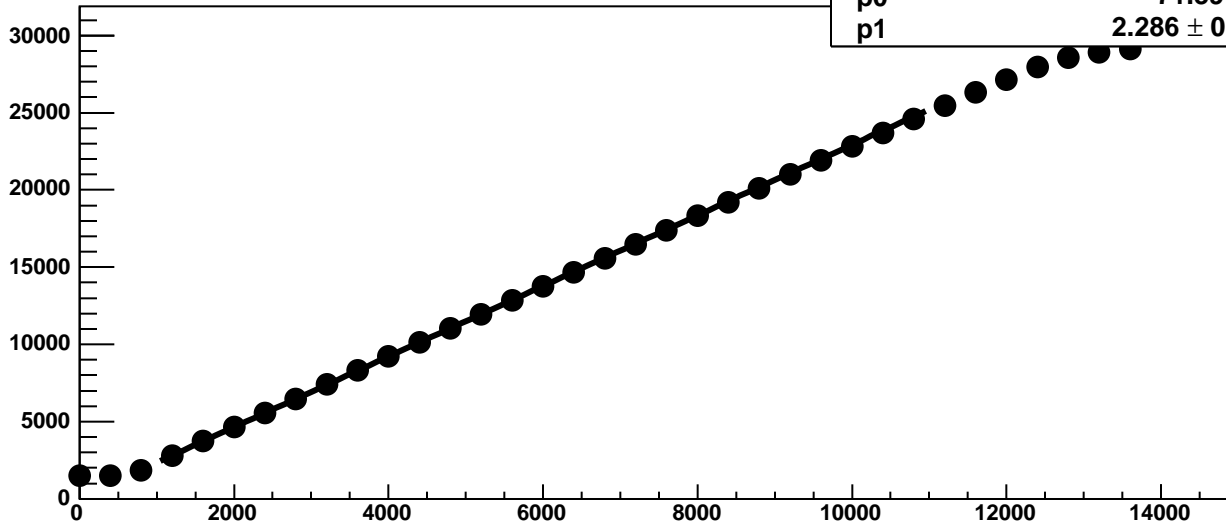


Chip 10, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

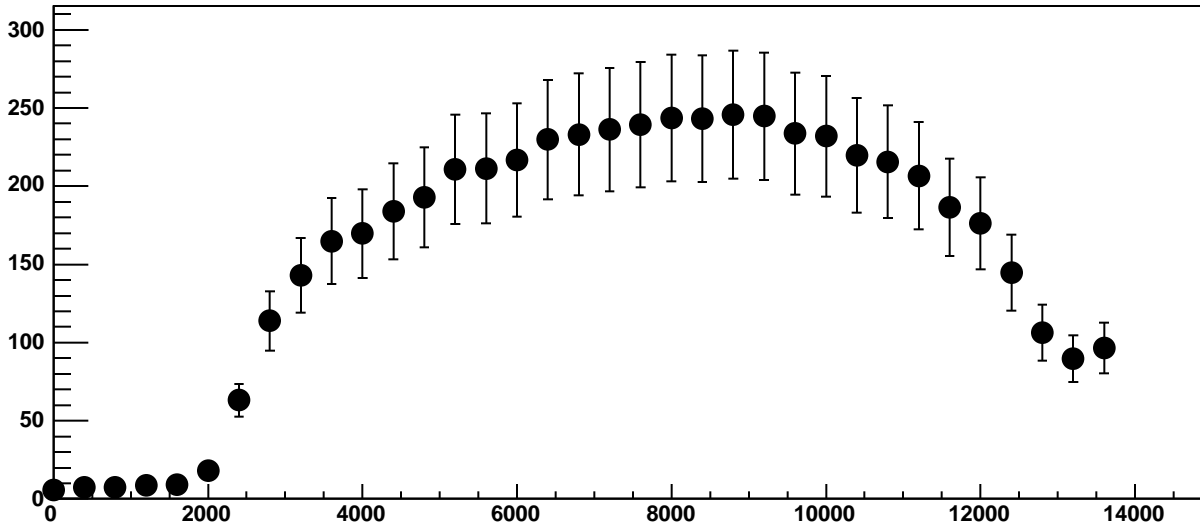


Chip 10, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC

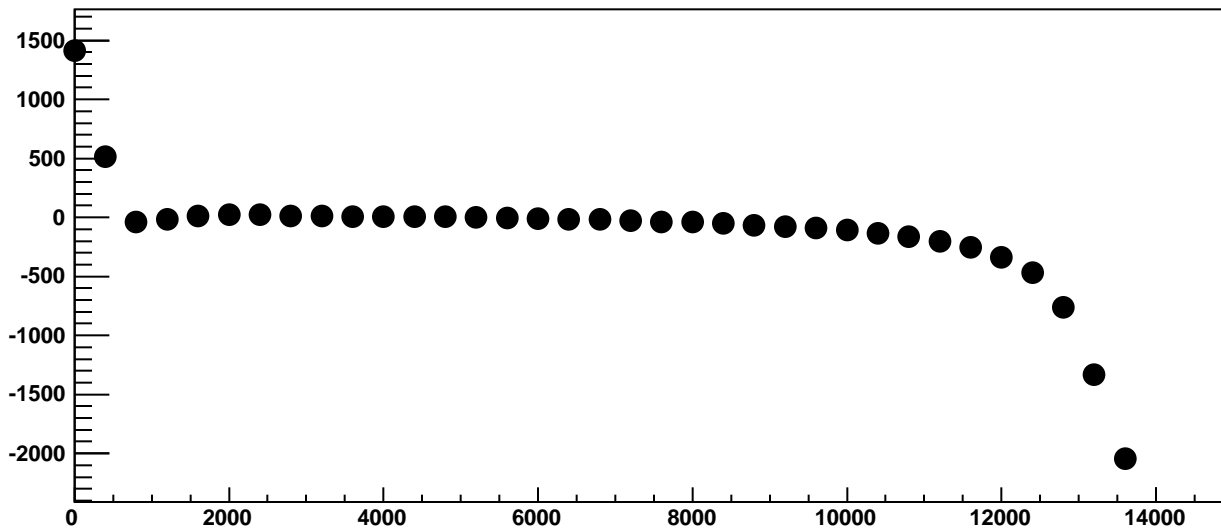
χ^2 / ndf 165.1 / 23
p0 71.39 ± 3.093
p1 2.286 ± 0.001805



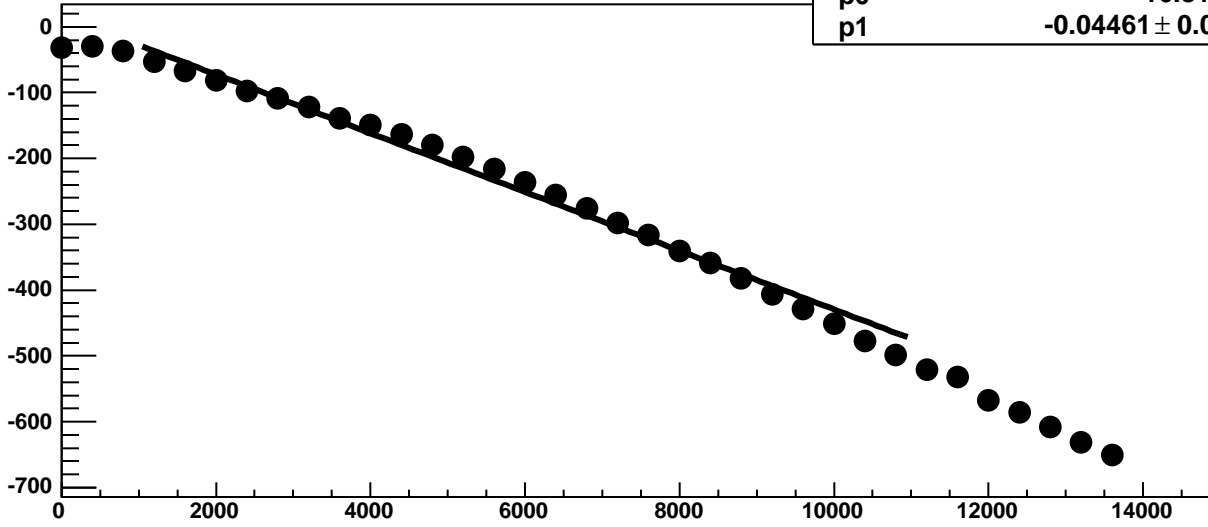
Chip 10, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

603.1 / 23

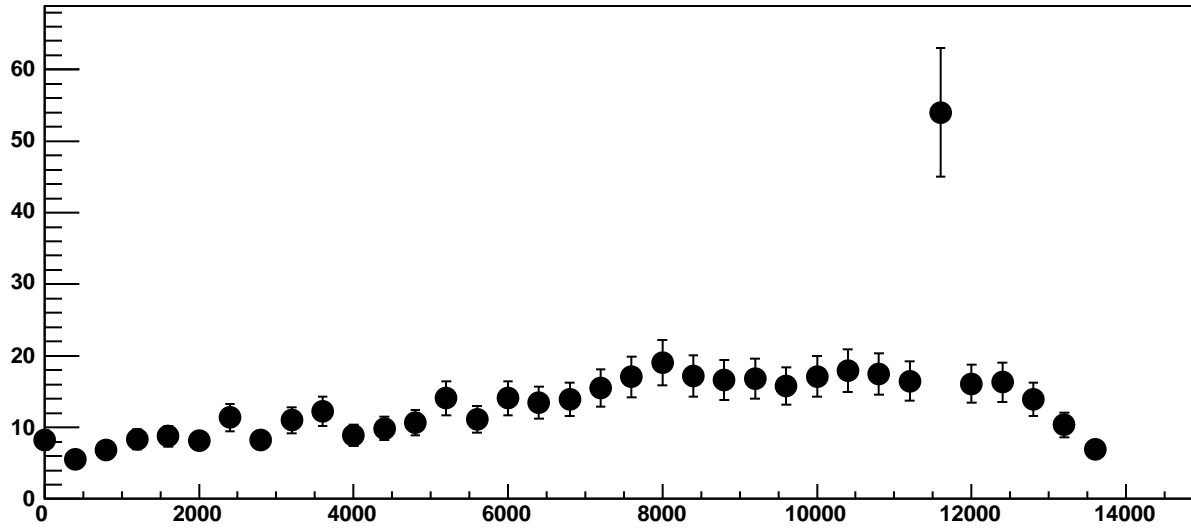
p0

16.81 ± 1.079

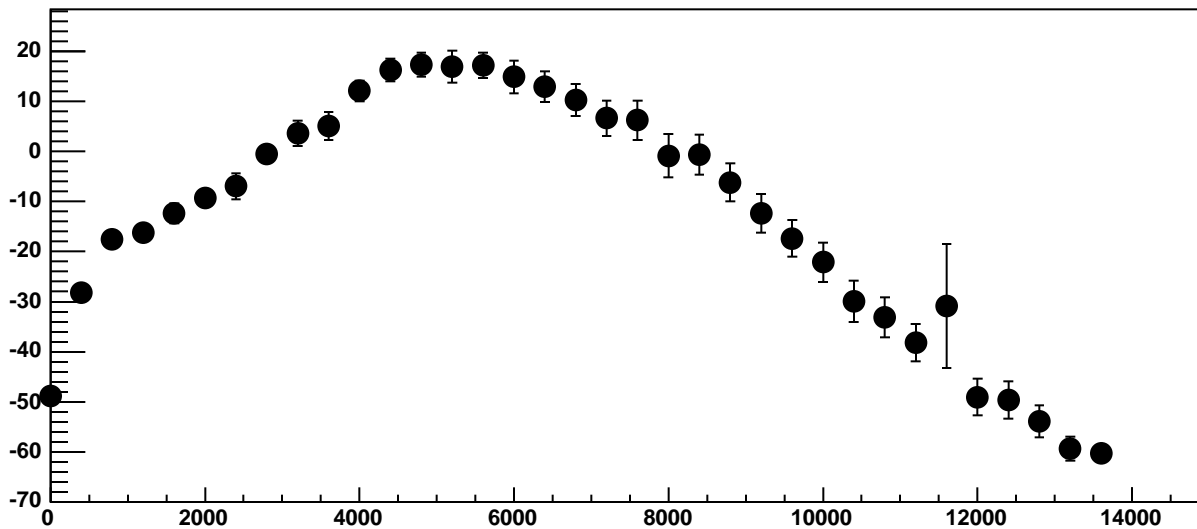
p1

-0.04461 ± 0.0002034

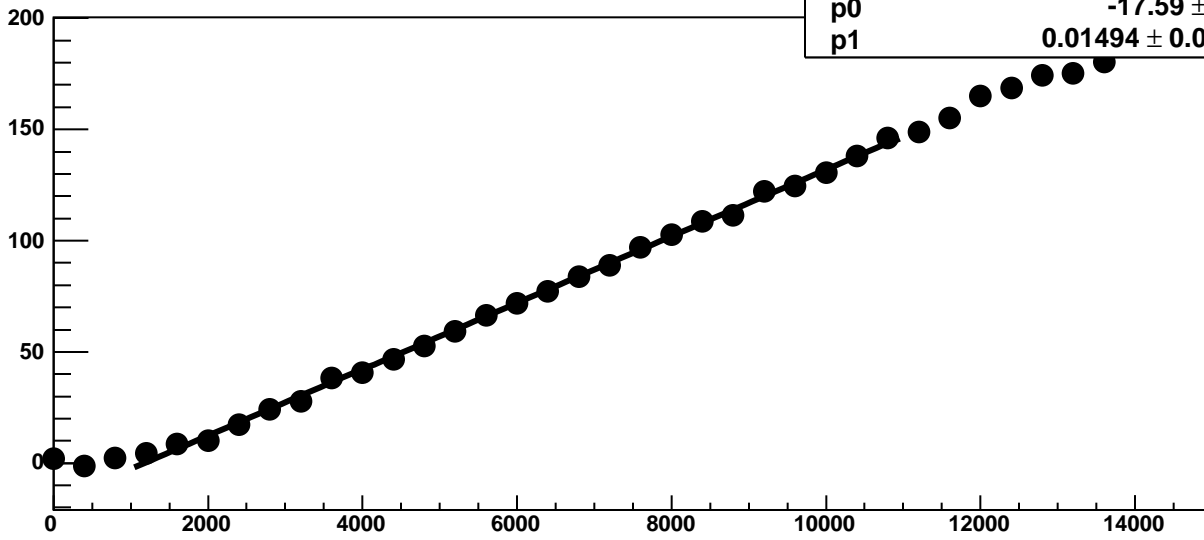
Chip 10, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

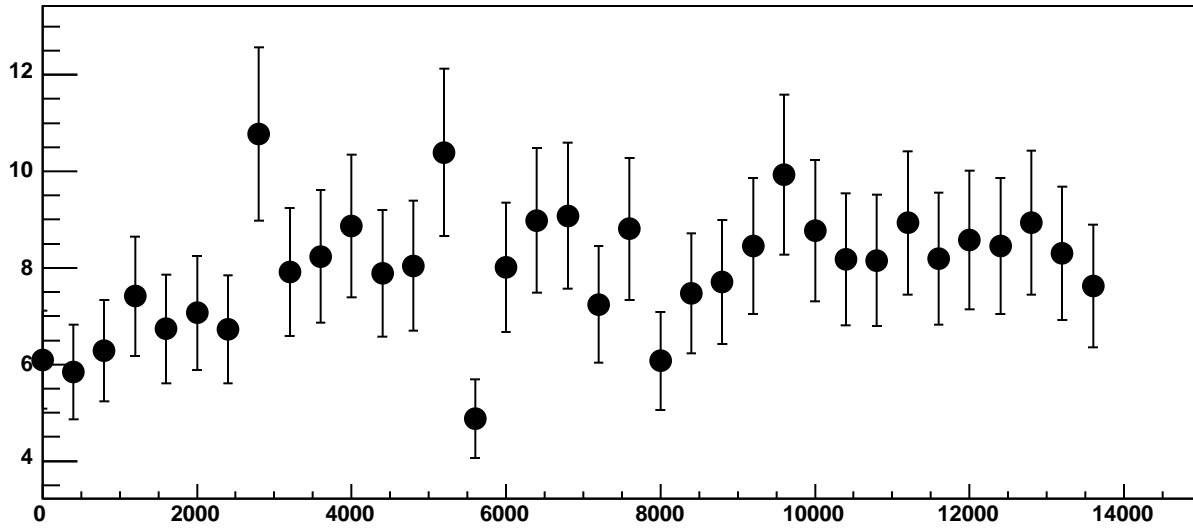


Chip 10, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

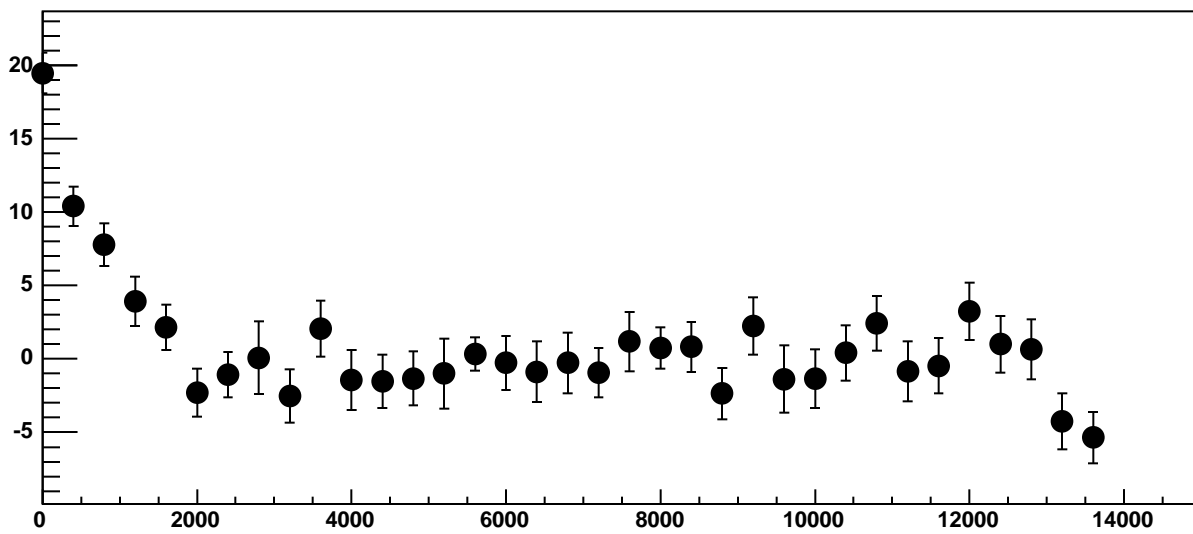


χ^2 / ndf 21.96 / 23
p0 -17.59 ± 0.8076
p1 0.01494 ± 0.0001245

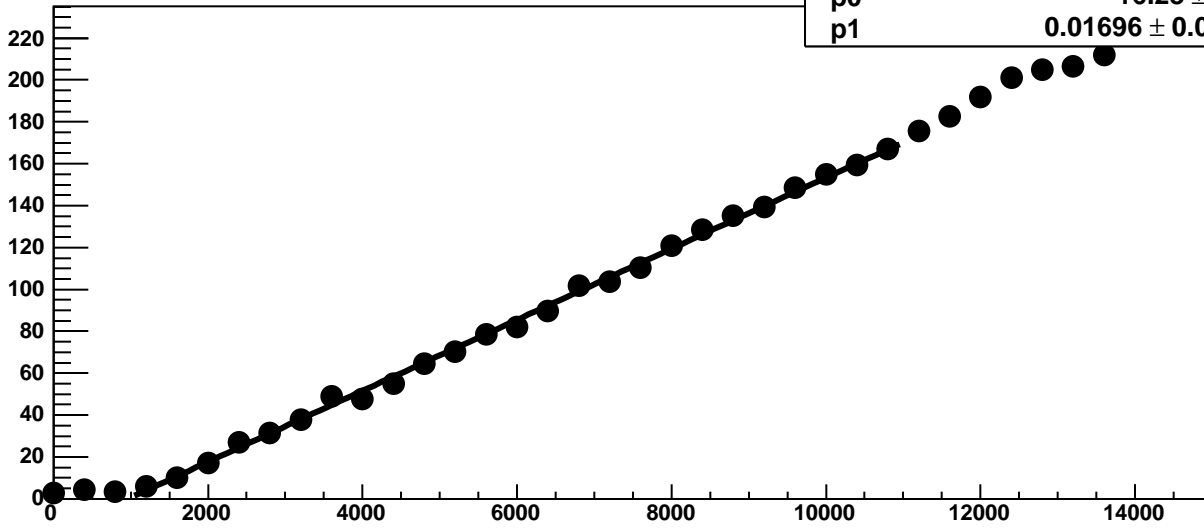
Chip 10, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



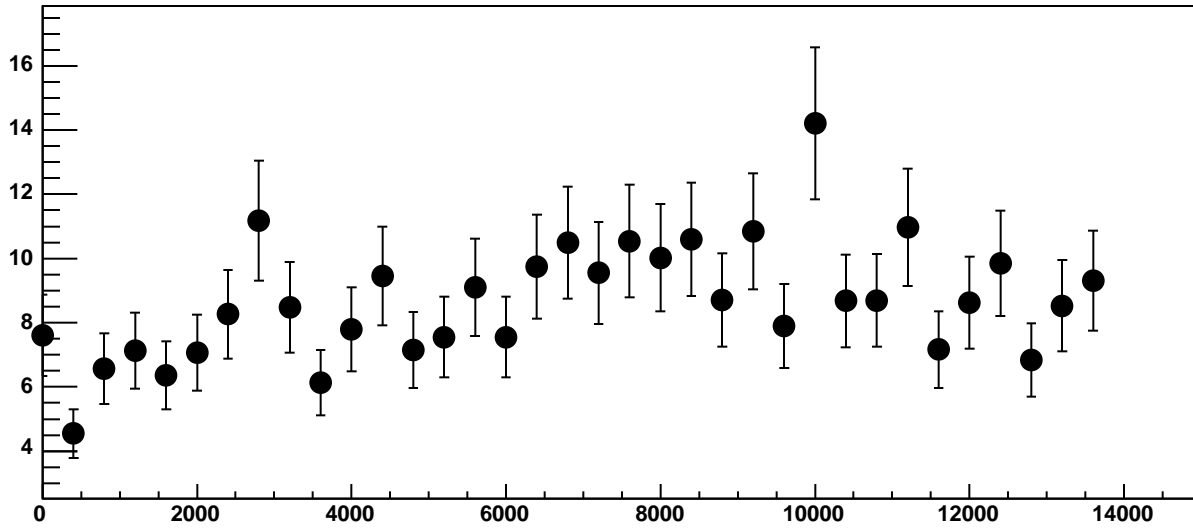
Chip 10, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



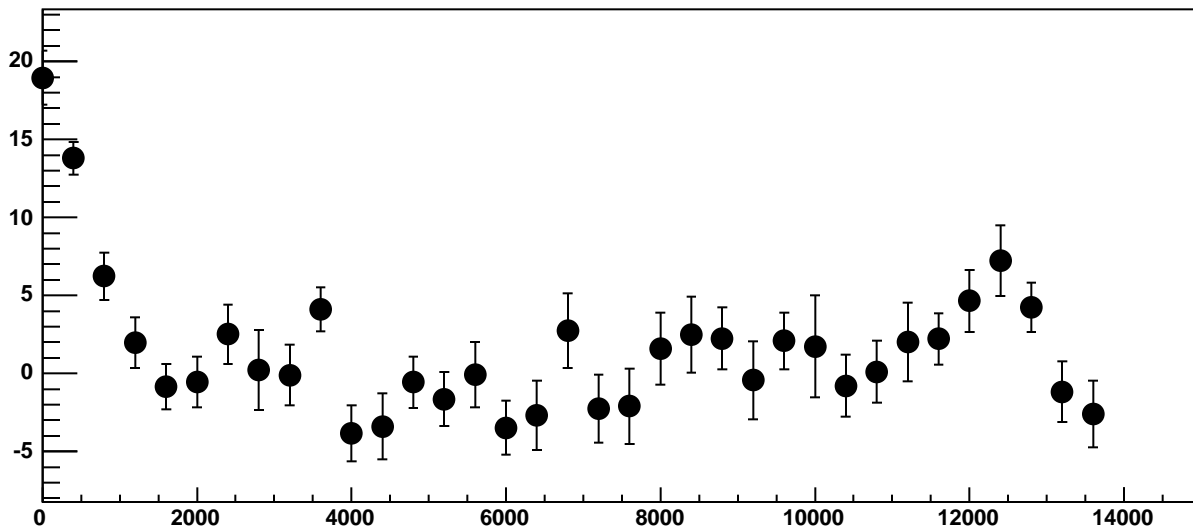
Chip 10, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC



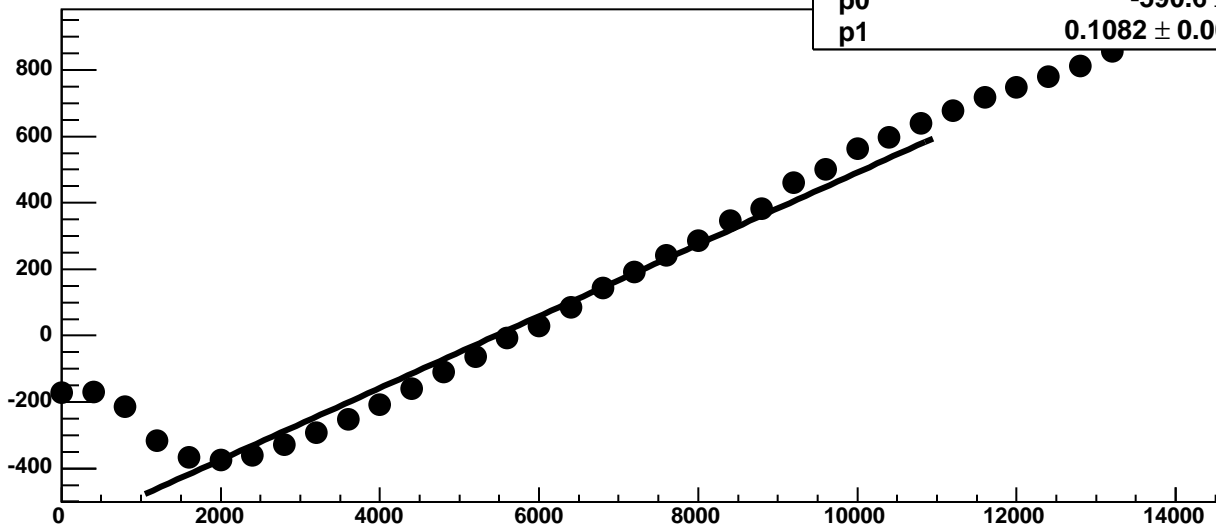
Chip 10, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



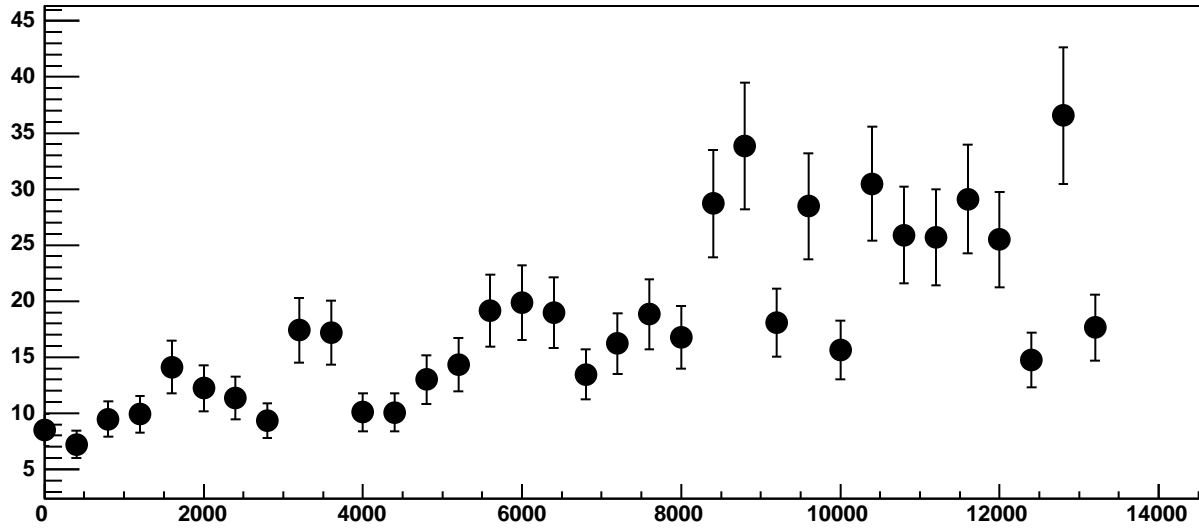
Chip 10, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC



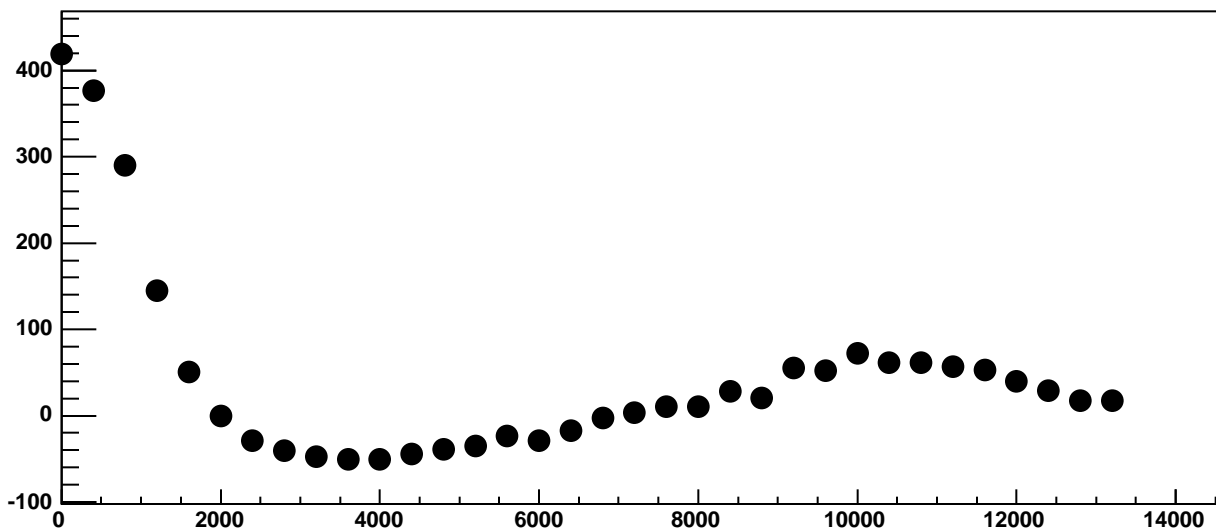
Chip 10, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



Chip 10, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

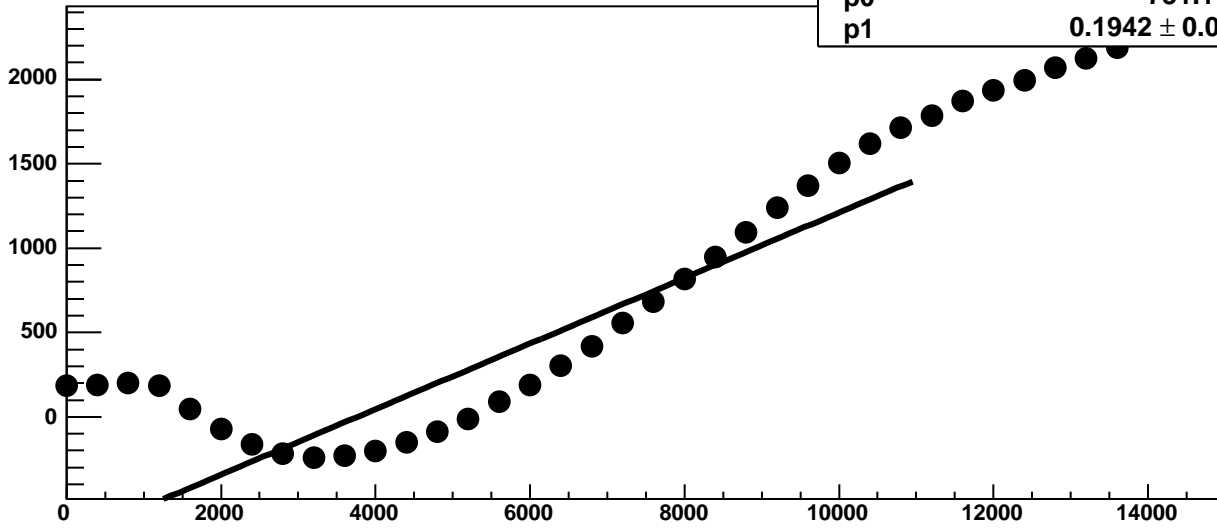


Chip 10, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

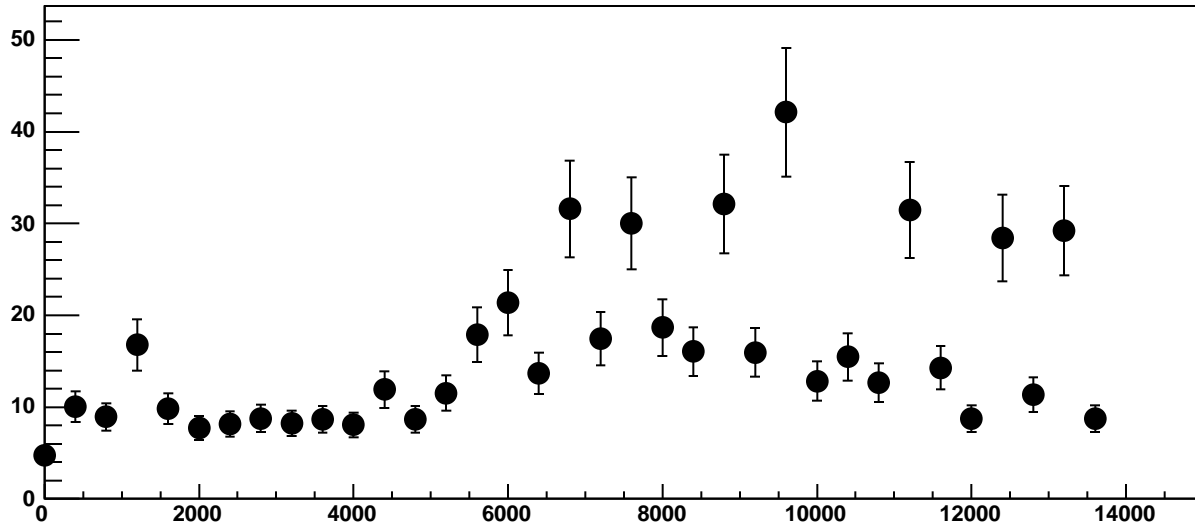


Chip 10, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

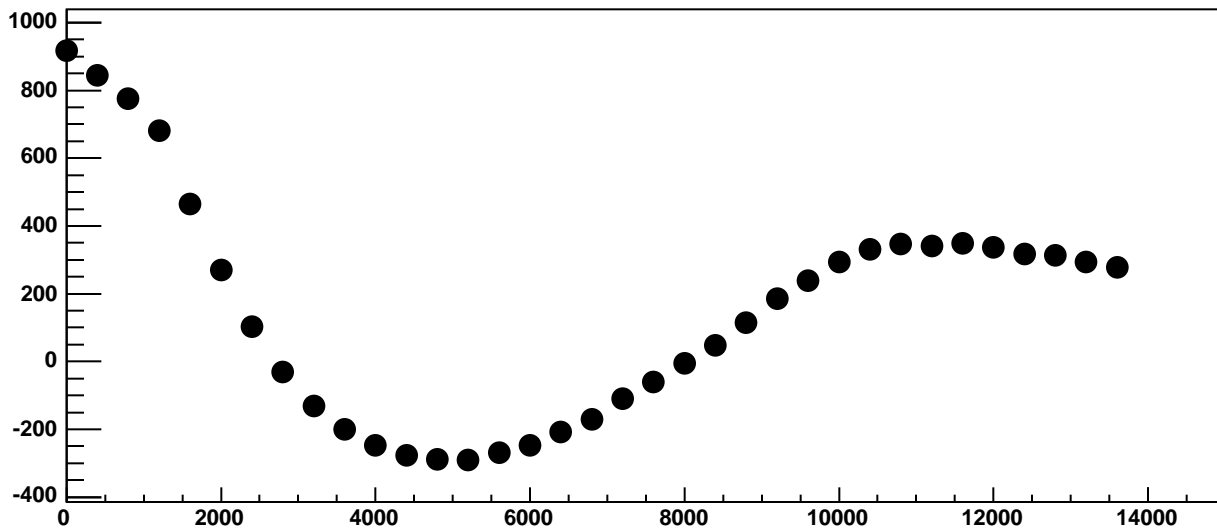
χ^2 / ndf 2.254e+05 / 23
p0 -731.1 ± 1.092
p1 0.1942 ± 0.0002064



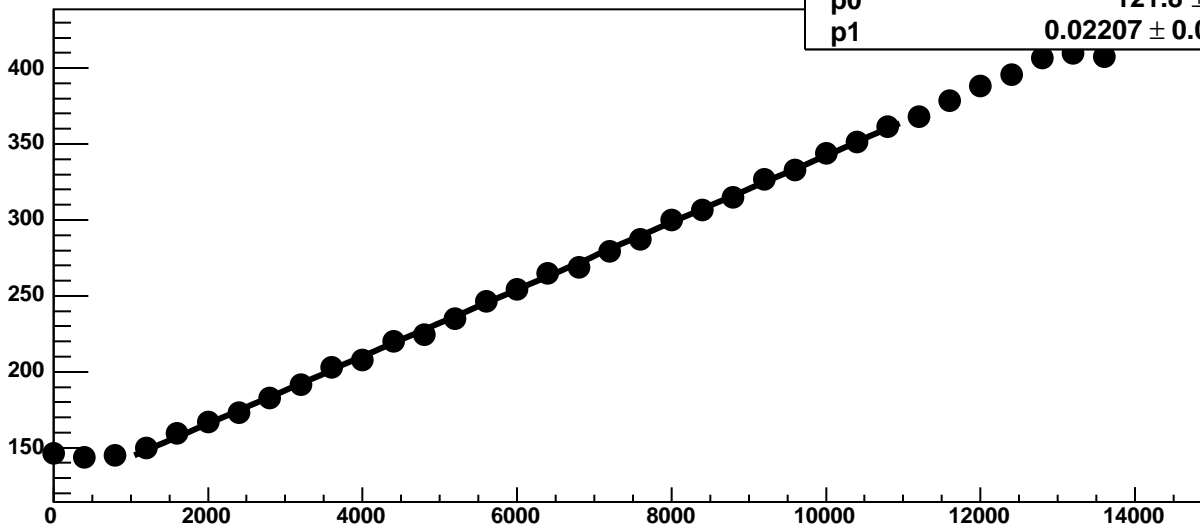
Chip 10, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

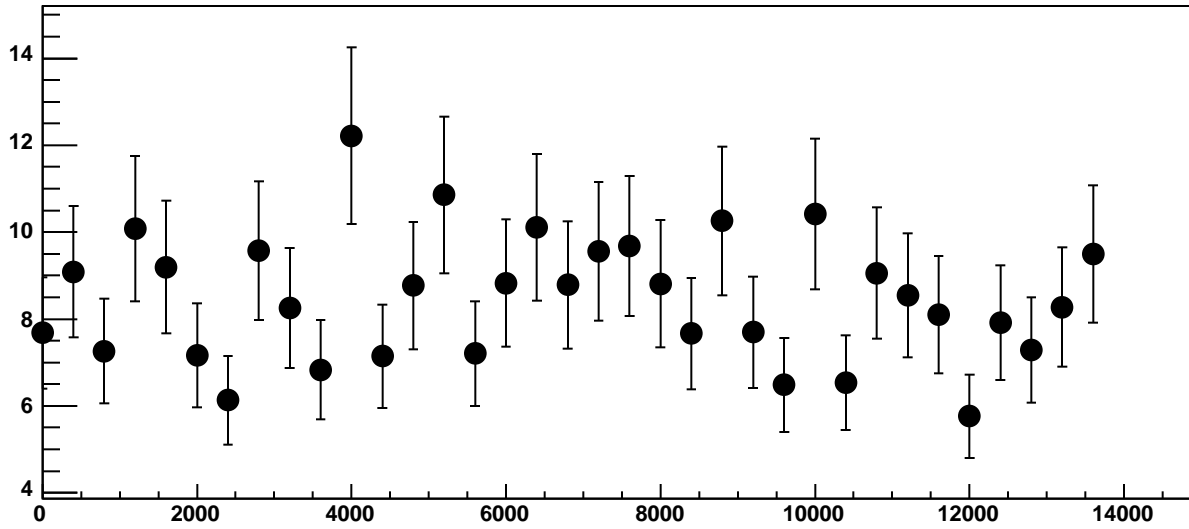


Chip 10, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

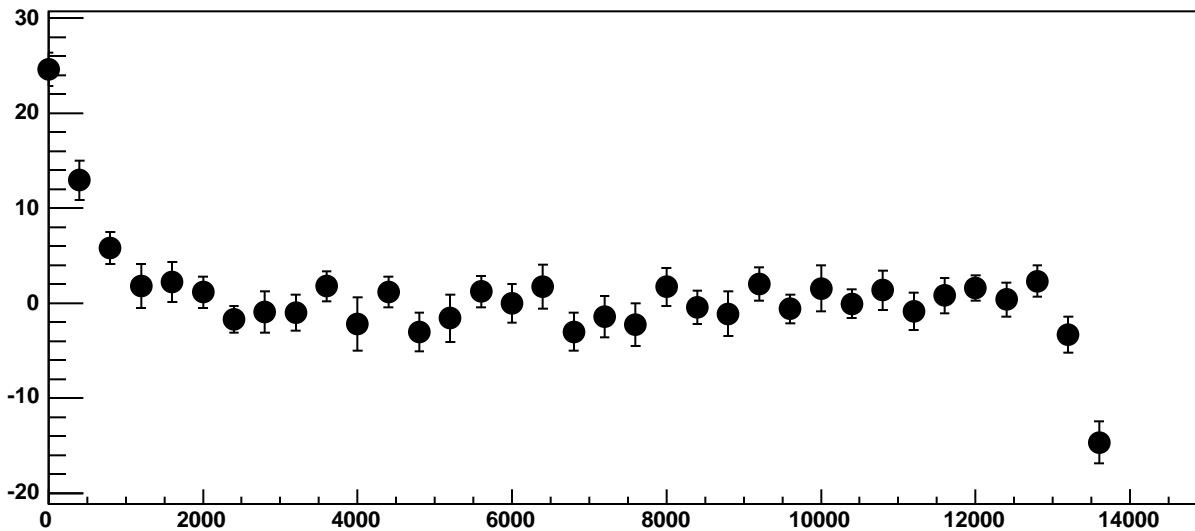


χ^2 / ndf 17.44 / 23
p0 121.8 ± 0.8577
p1 0.02207 ± 0.0001281

Chip 10, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

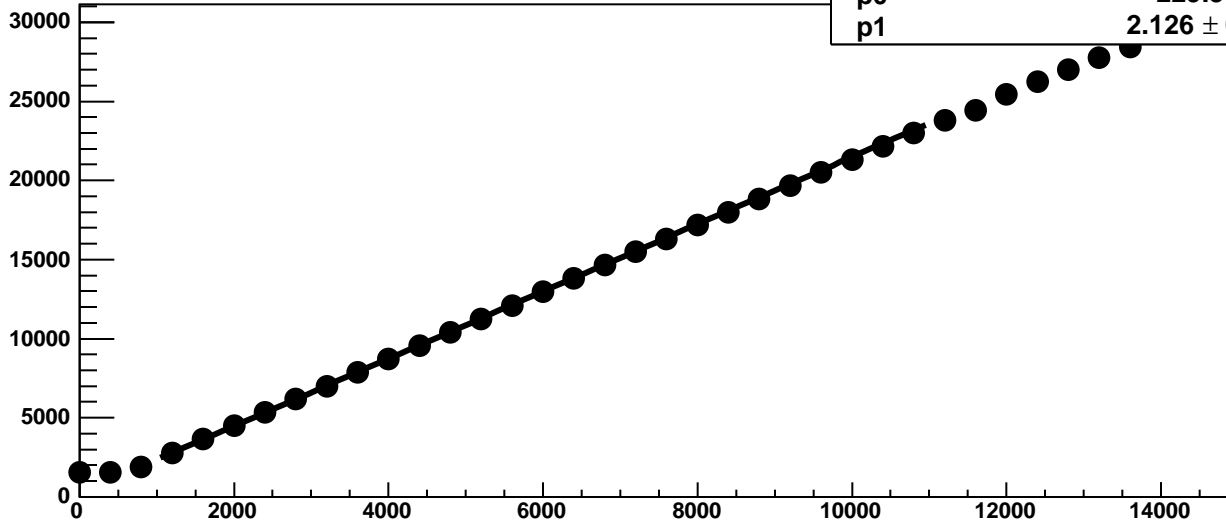


Chip 10, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

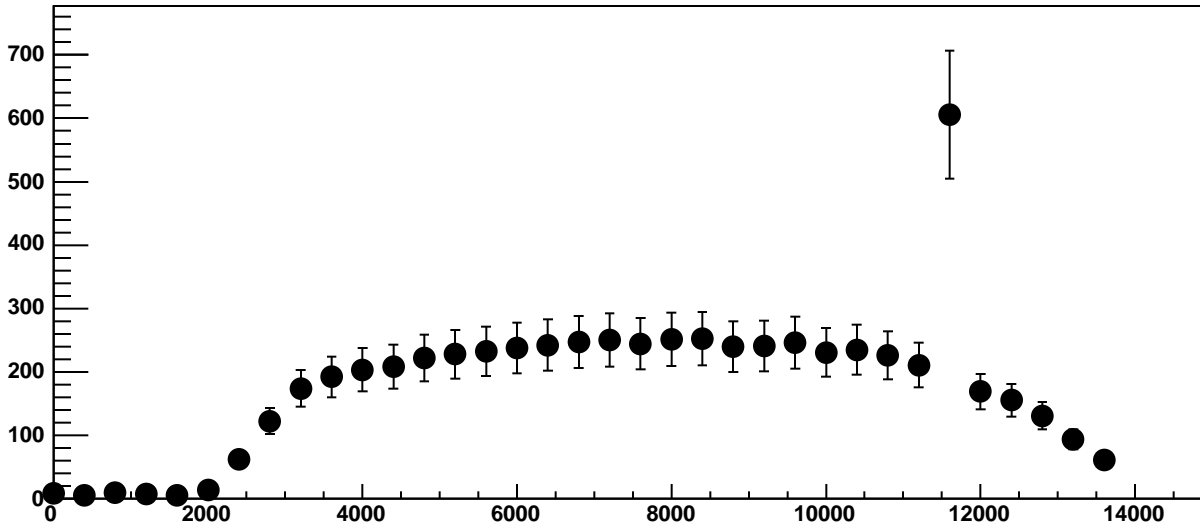


Chip 10, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC

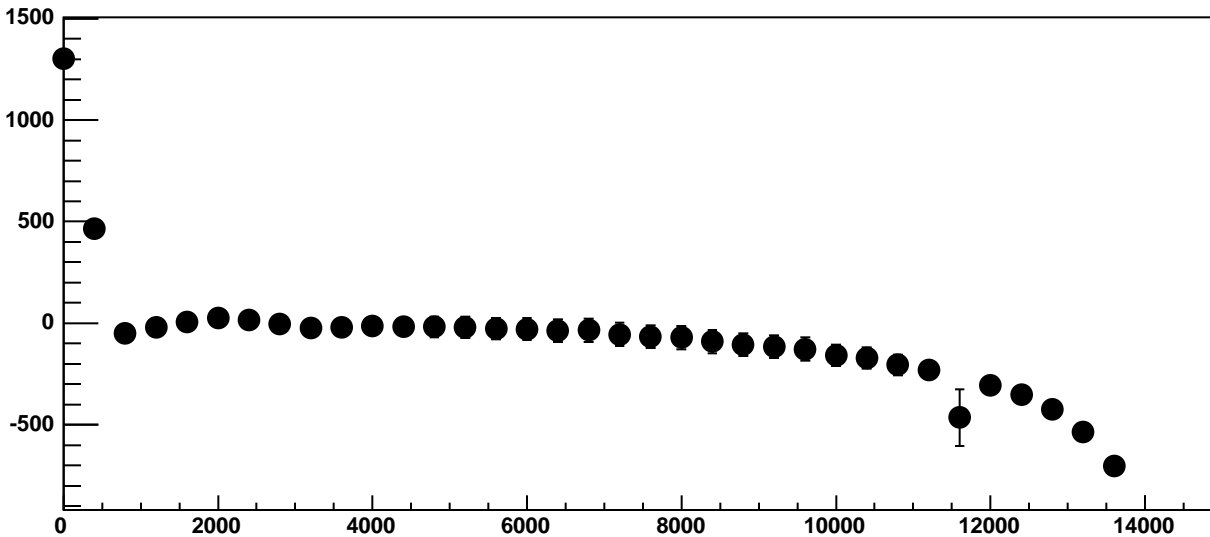
χ^2 / ndf 264.8 / 23
p0 229.3 ± 3.025
p1 2.126 ± 0.00183



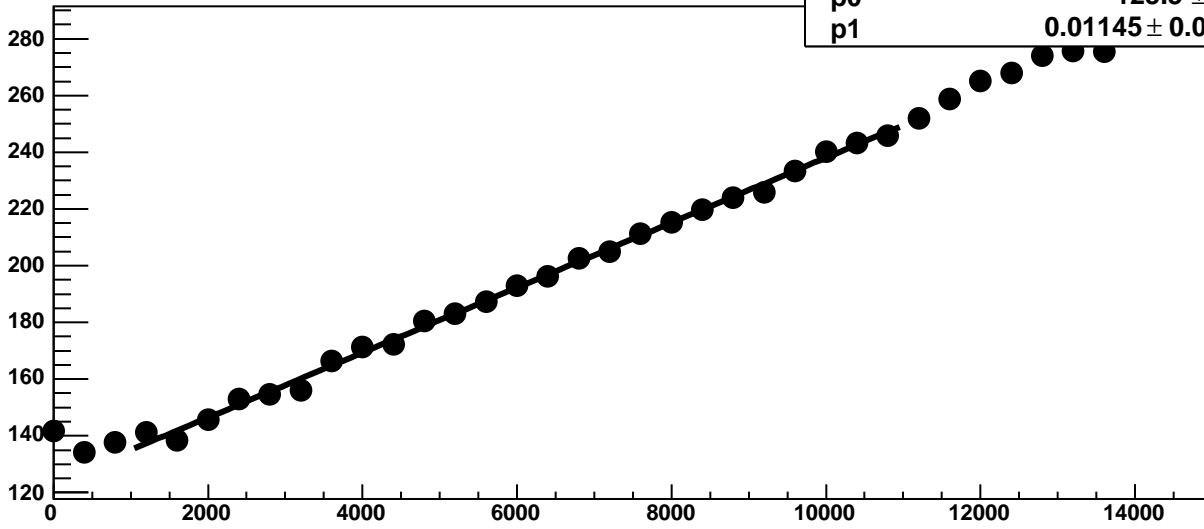
Chip 10, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC

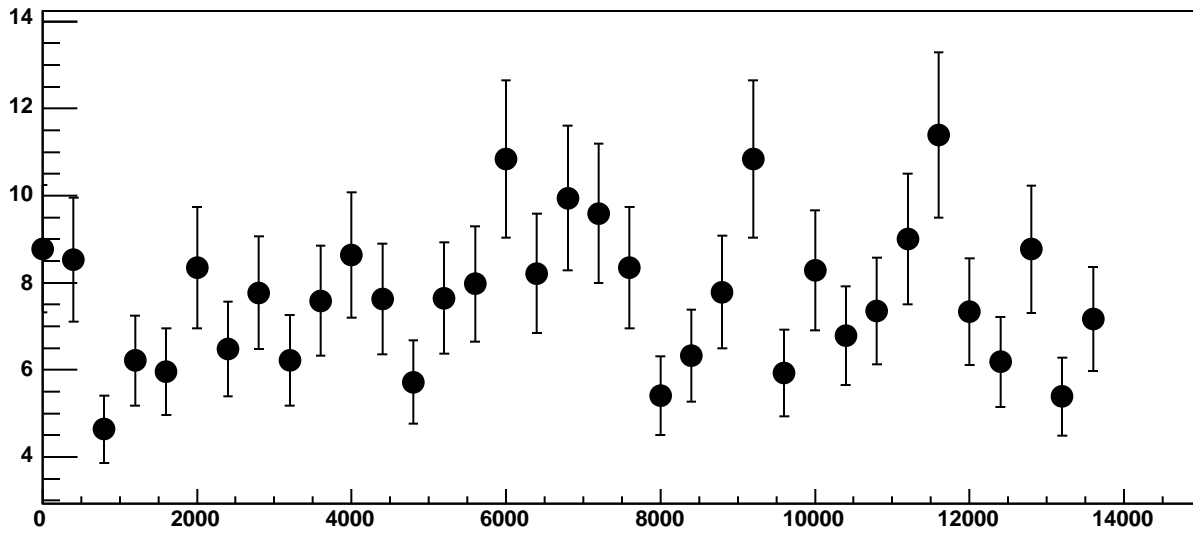


Chip 10, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

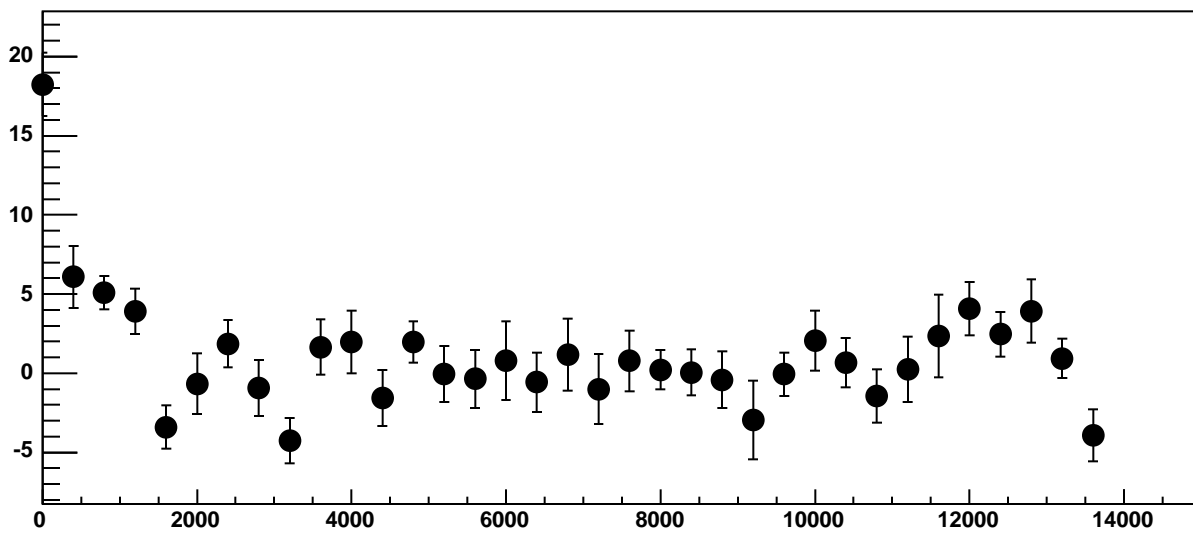


χ^2 / ndf 33.88 / 23
p0 123.5 ± 0.7276
p1 0.01145 ± 0.0001106

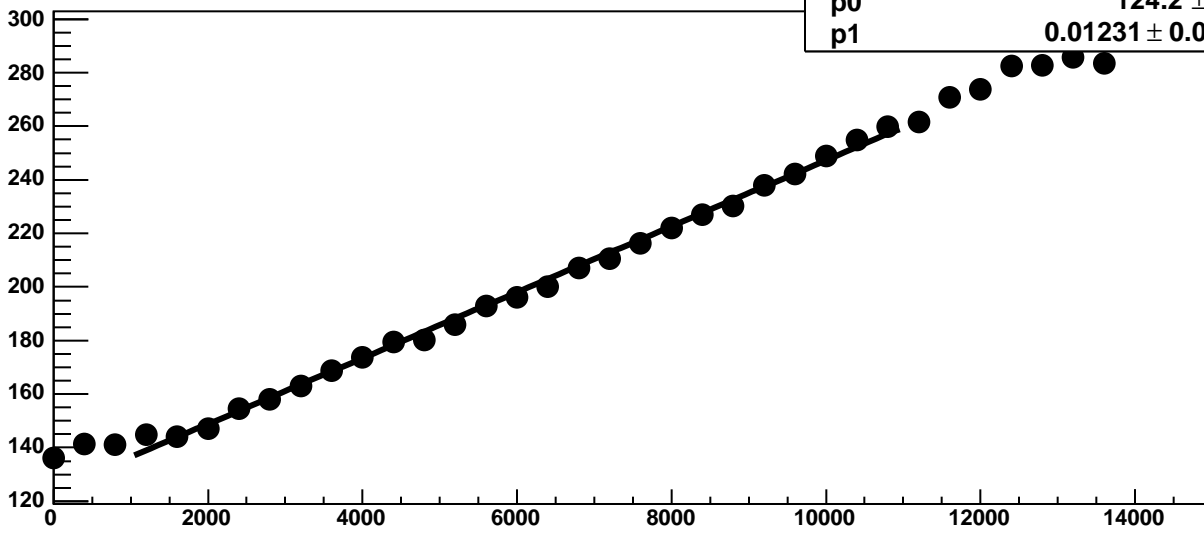
Chip 10, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



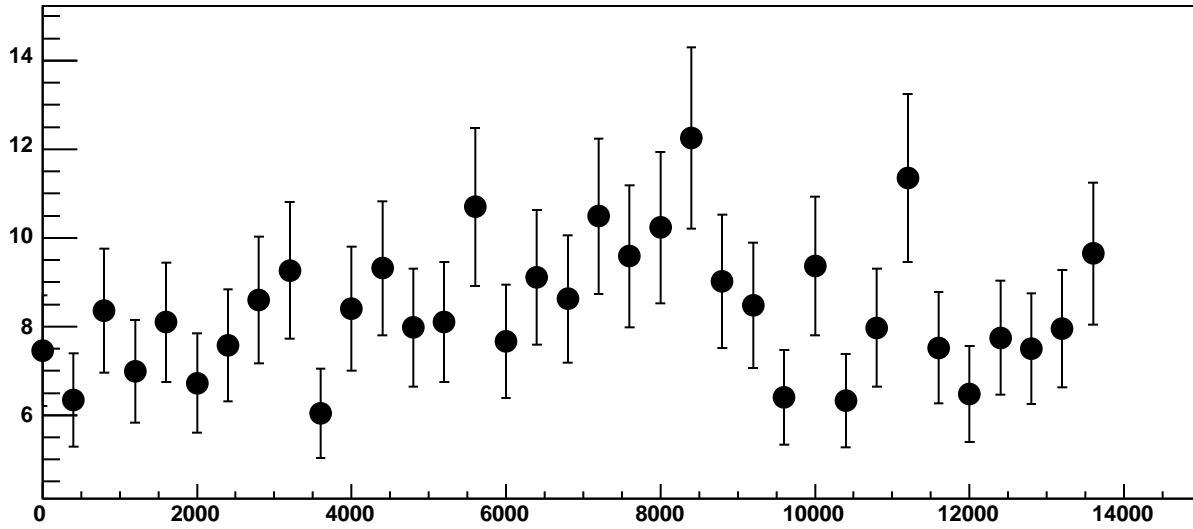
Chip 10, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



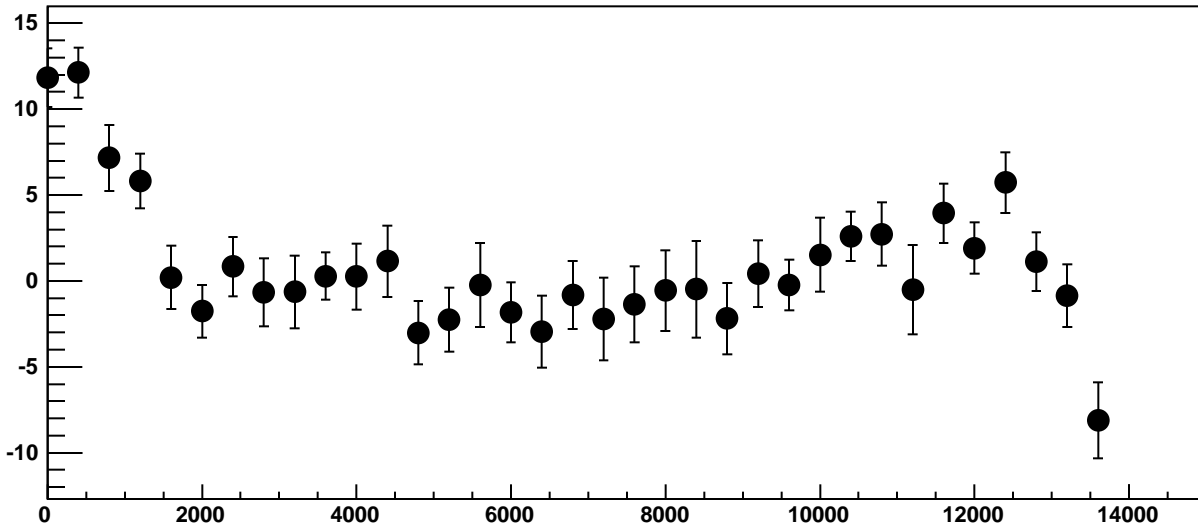
Chip 10, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



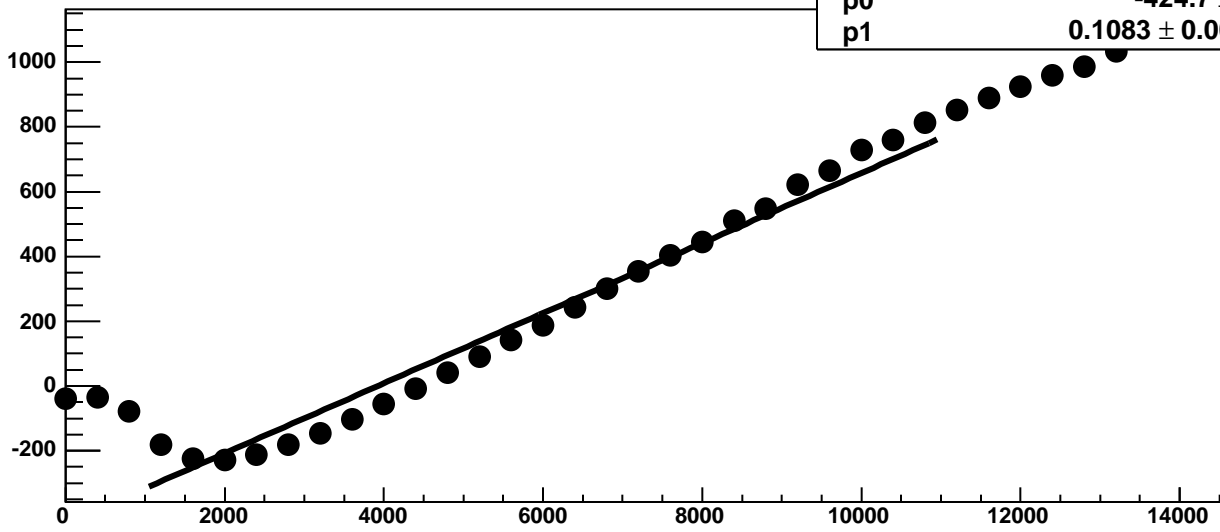
Chip 10, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



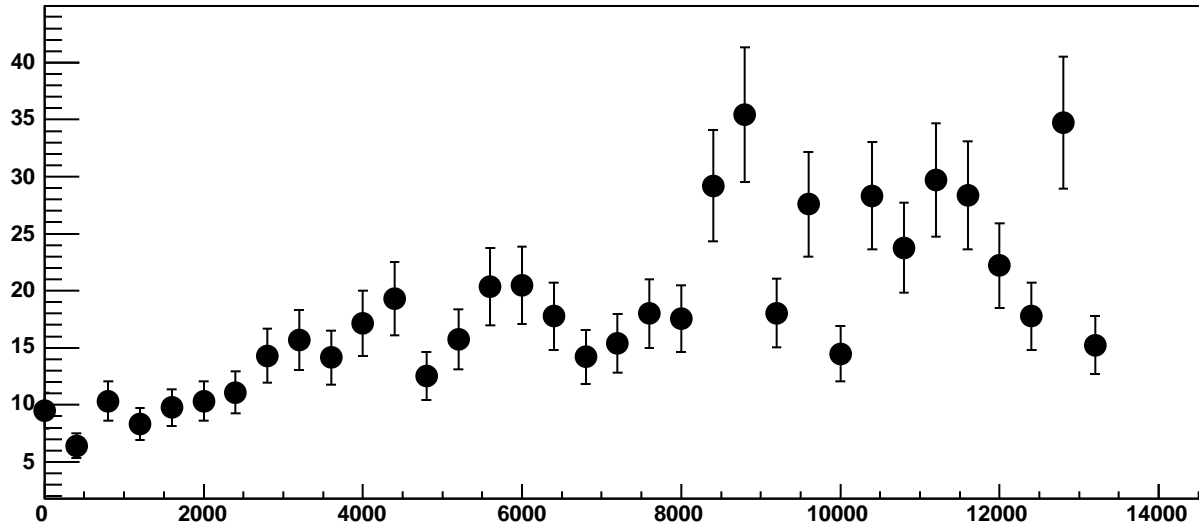
Chip 10, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



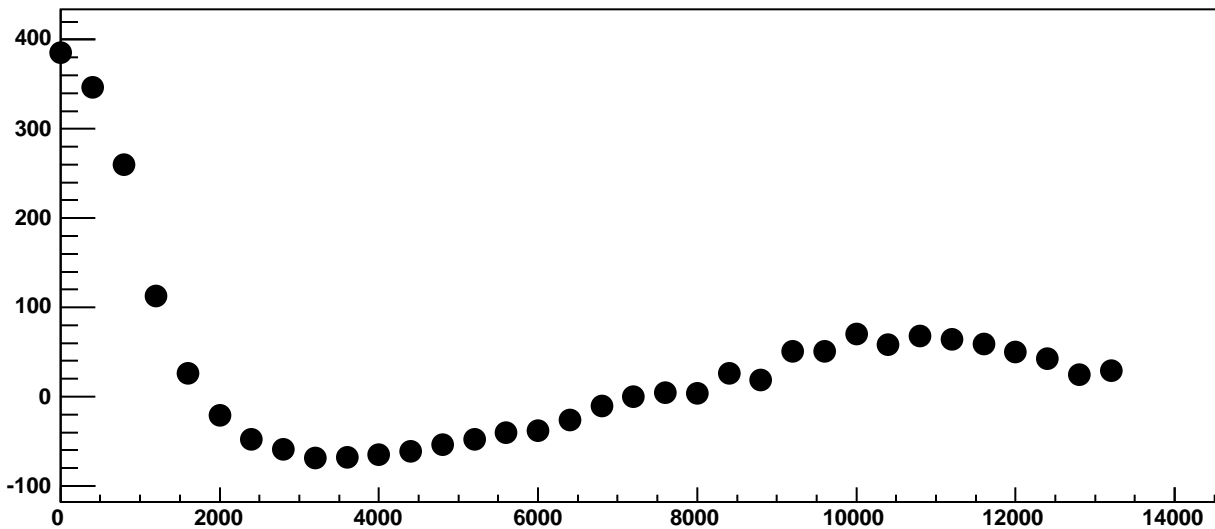
Chip 10, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC



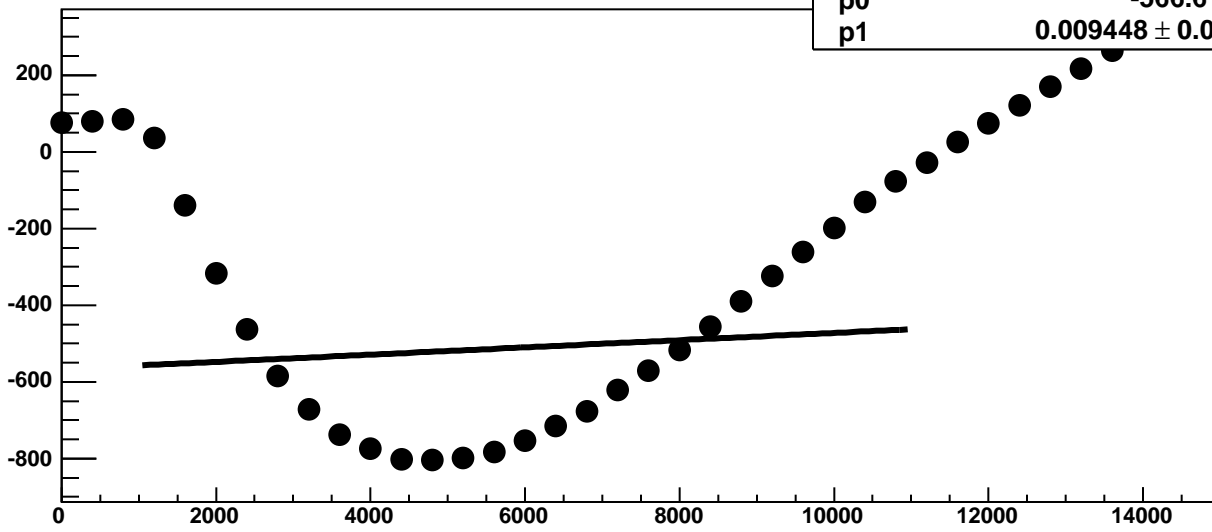
Chip 10, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC

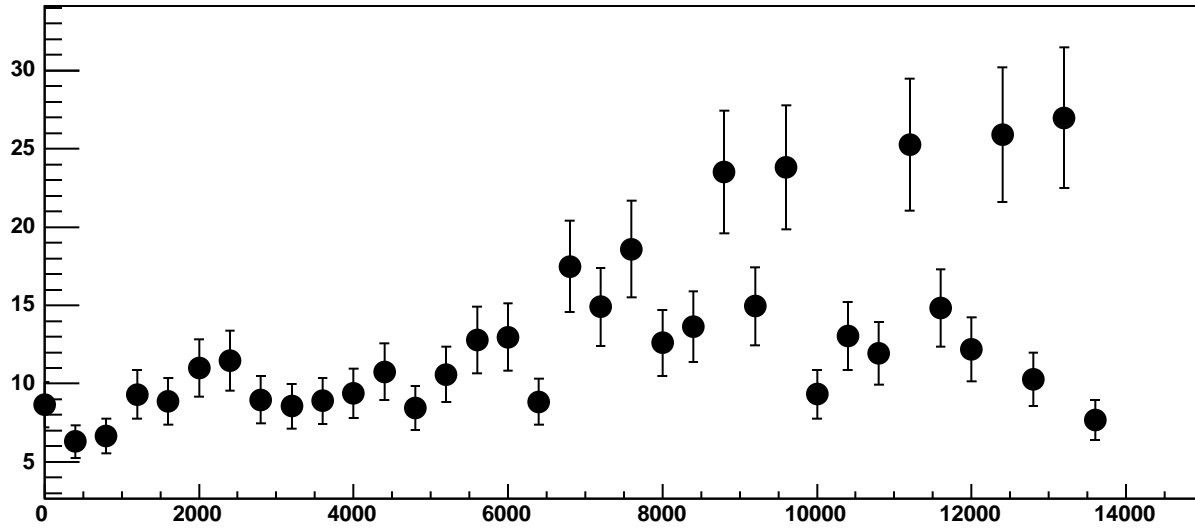


Chip 10, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

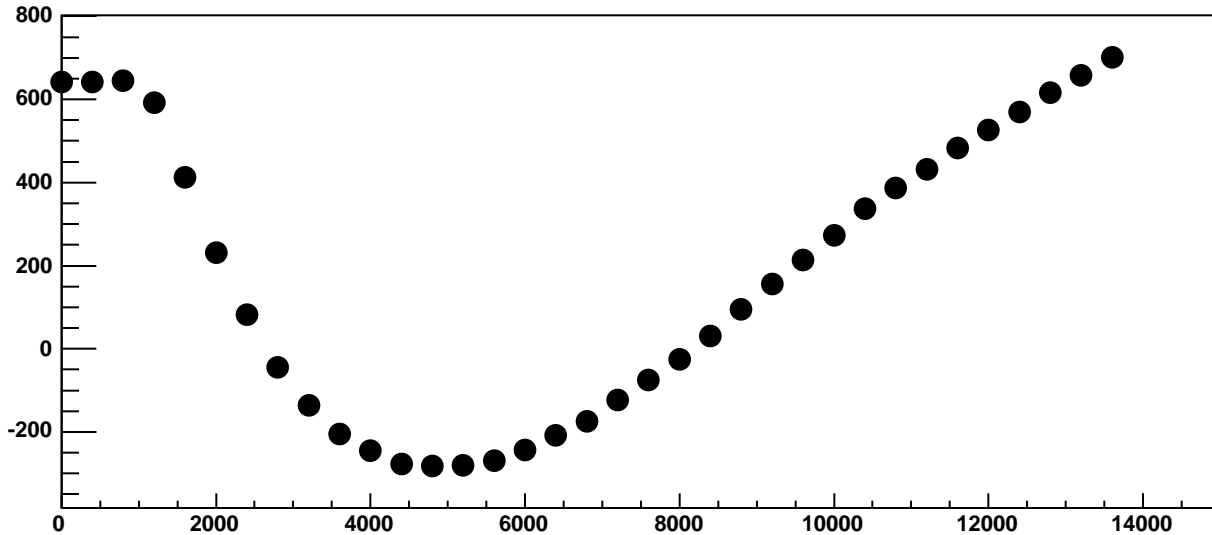


χ^2 / ndf 2.849e+05 / 23
p0 -566.6 ± 1.062
p1 0.009448 ± 0.0001803

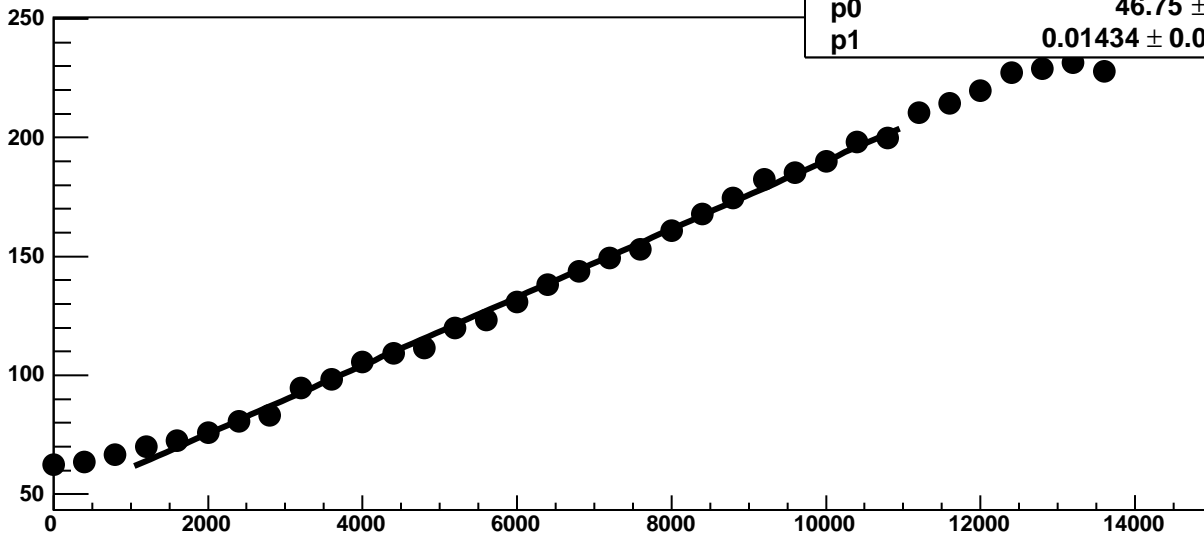
Chip 10, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

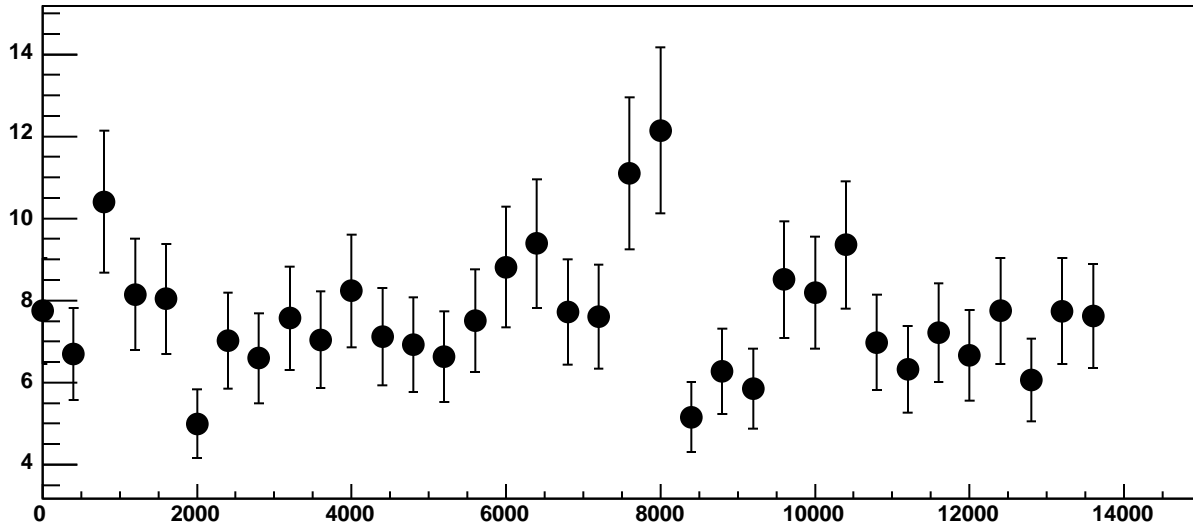


Chip 10, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

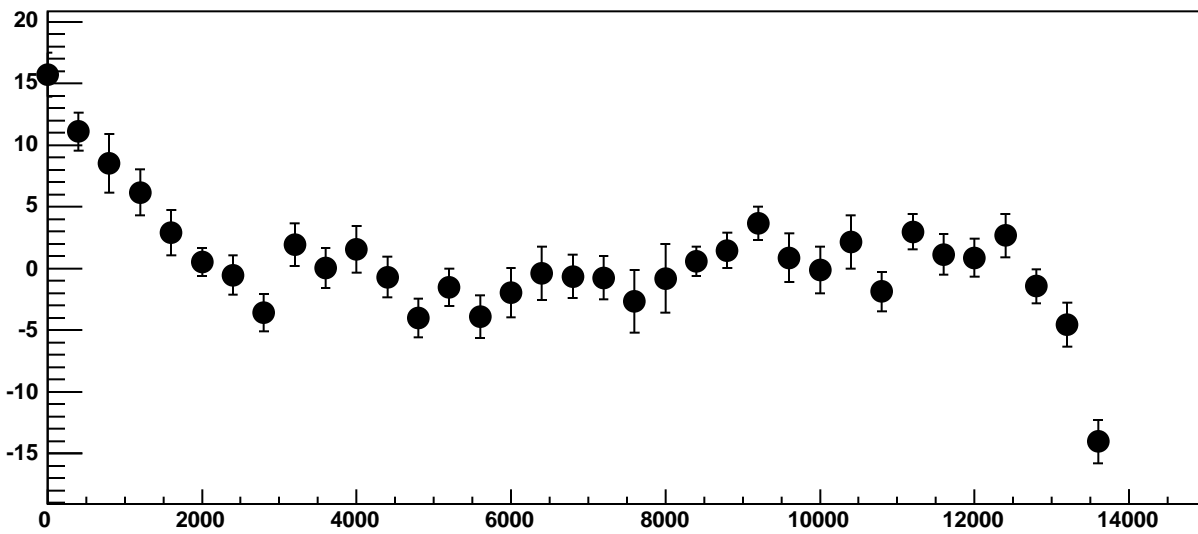


χ^2 / ndf 47.61 / 23
p0 46.75 ± 0.7408
p1 0.01434 ± 0.0001134

Chip 10, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

χ^2 / ndf

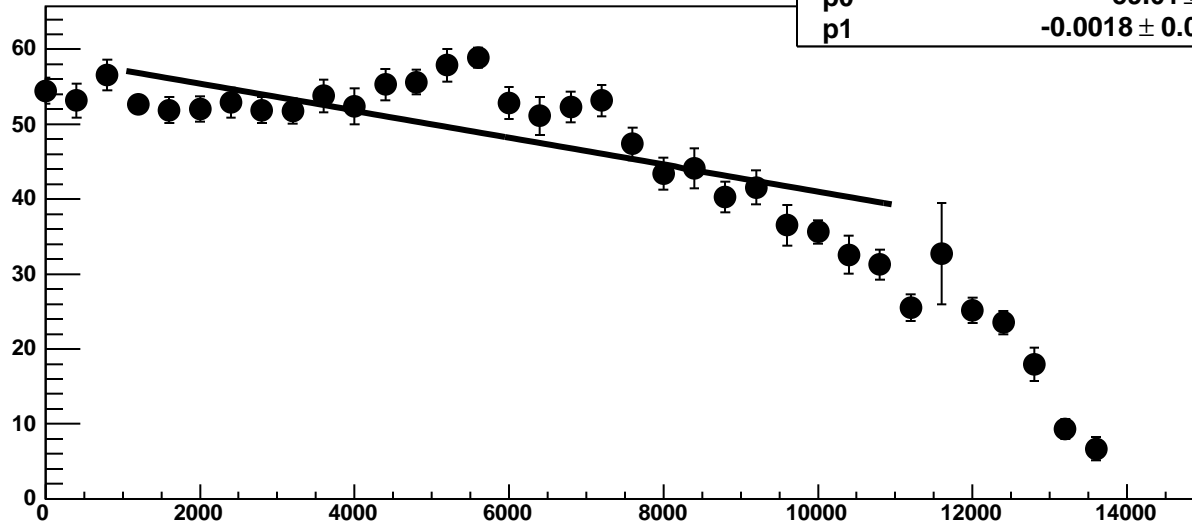
183.8 / 23

p0

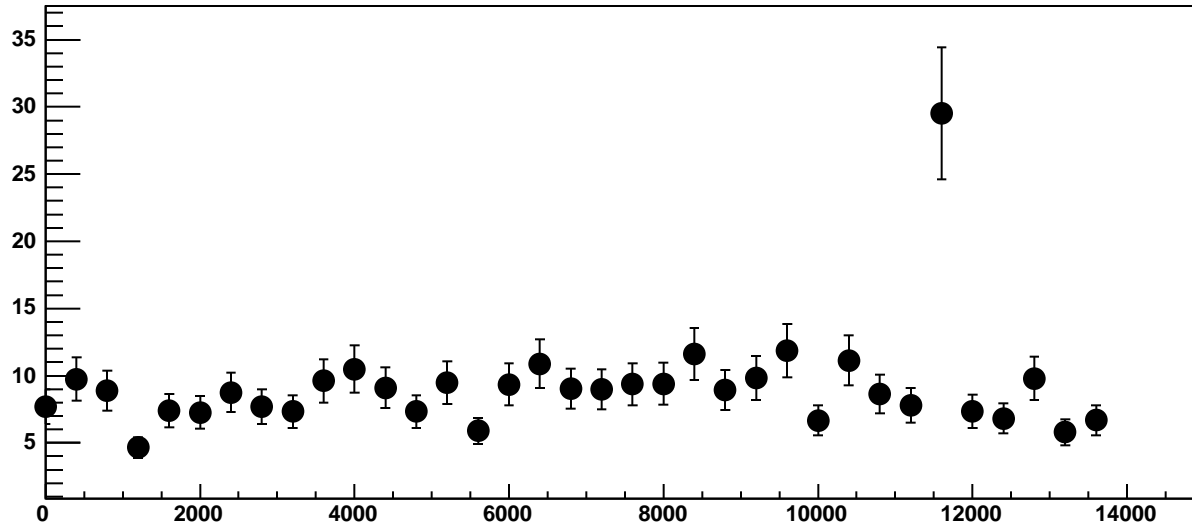
59.01 ± 0.7533

p1

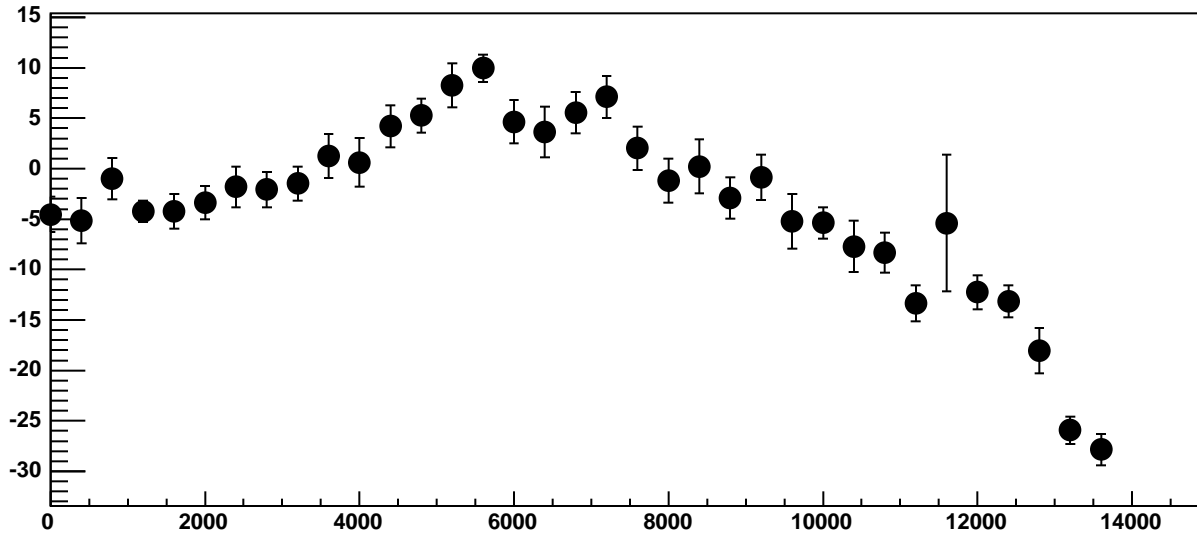
-0.0018 ± 0.0001235



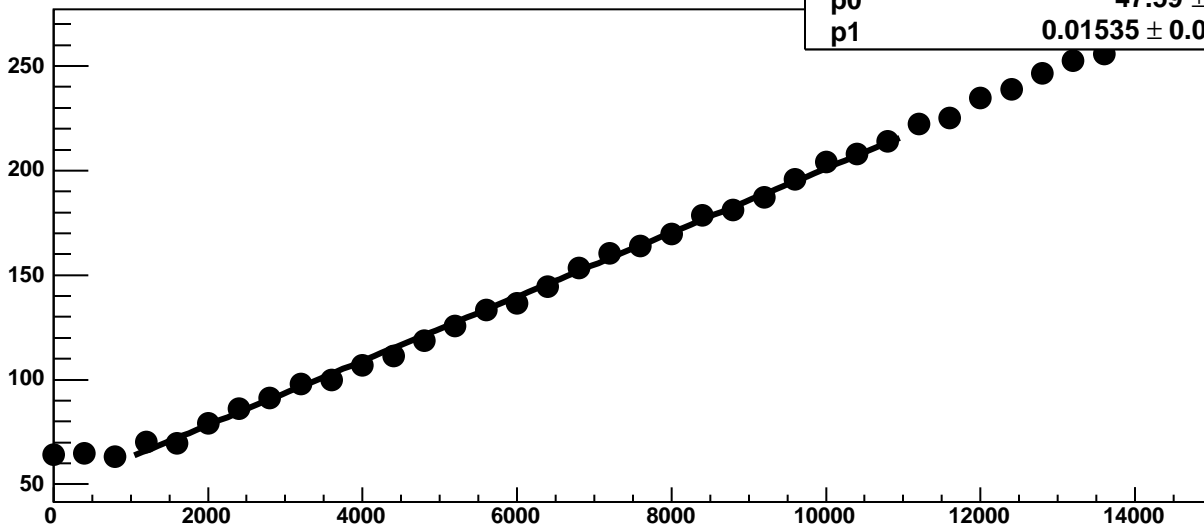
Chip 10, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

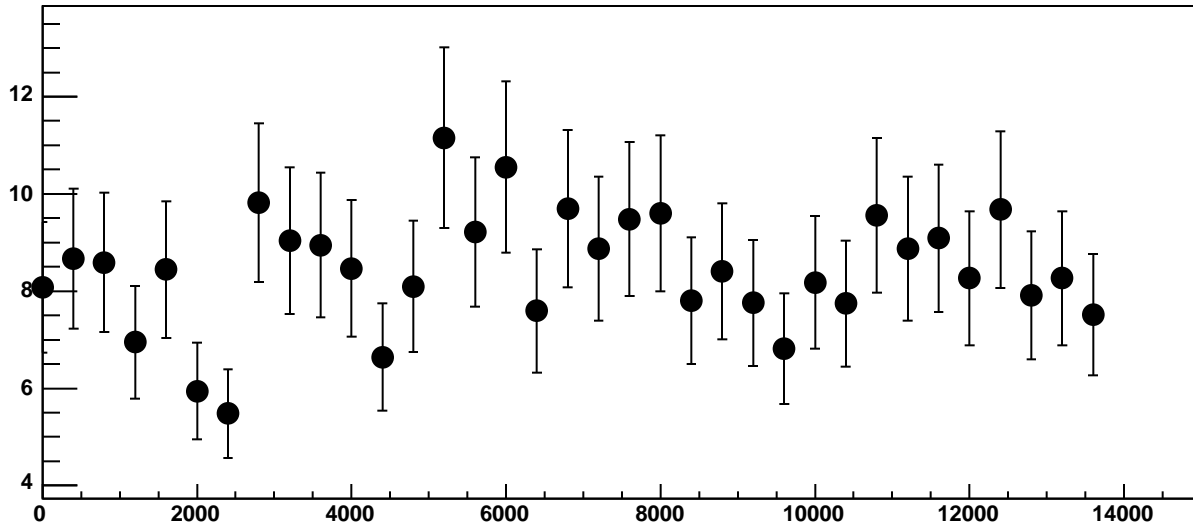


Chip 10, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

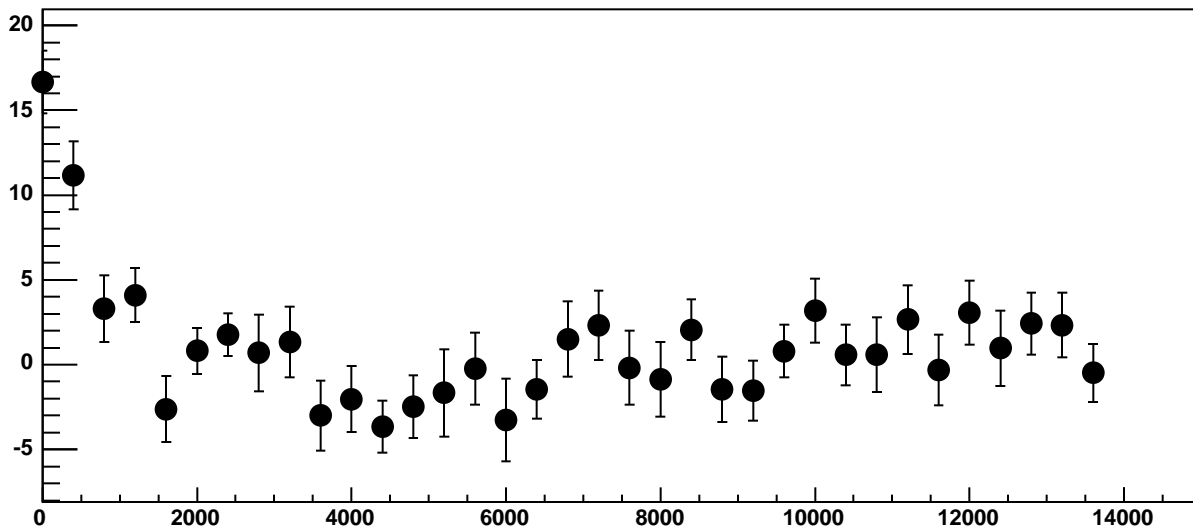


χ^2 / ndf 32.89 / 23
p0 47.59 ± 0.7825
p1 0.01535 ± 0.0001212

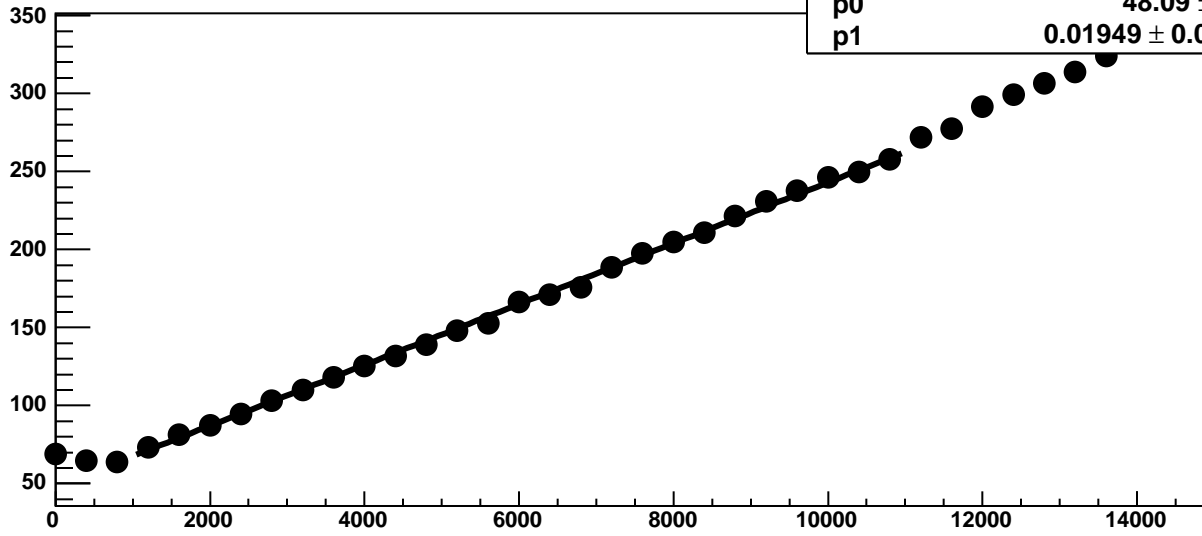
Chip 10, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

27.35 / 23

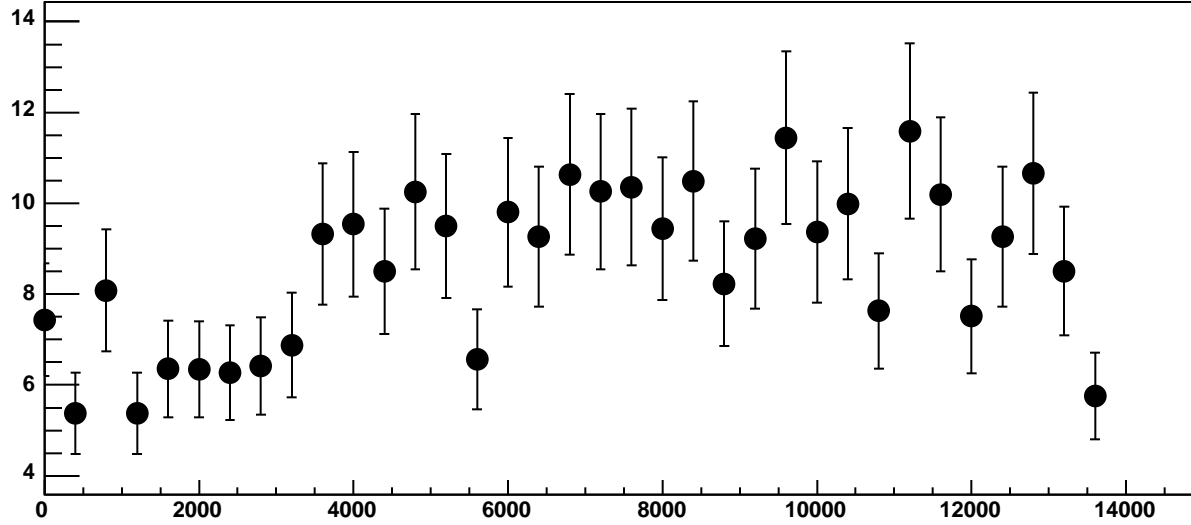
p0

48.09 ± 0.7251

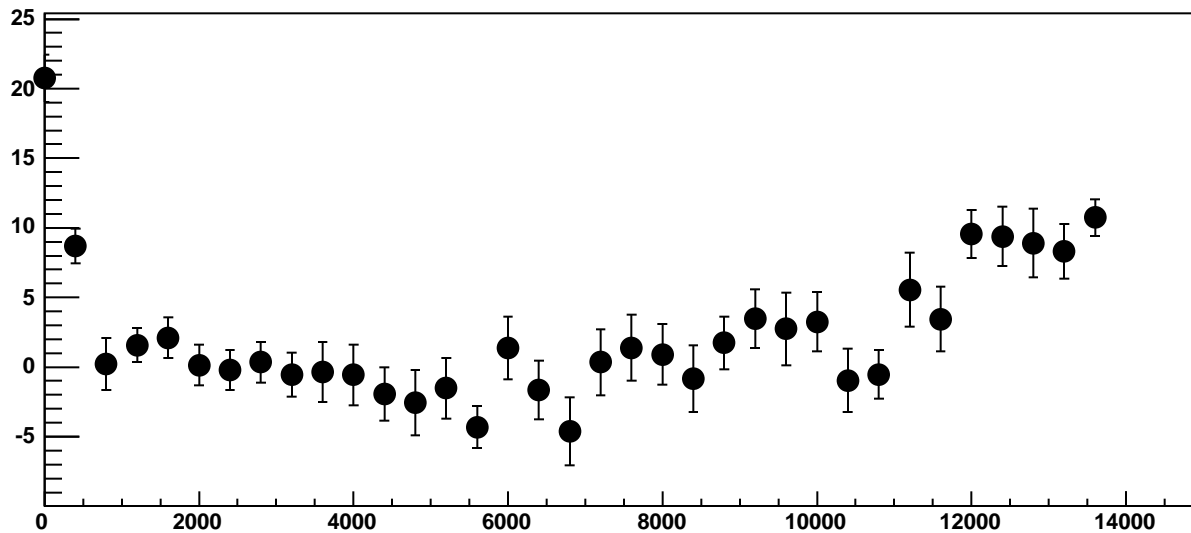
p1

0.01949 ± 0.0001222

Chip 10, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC

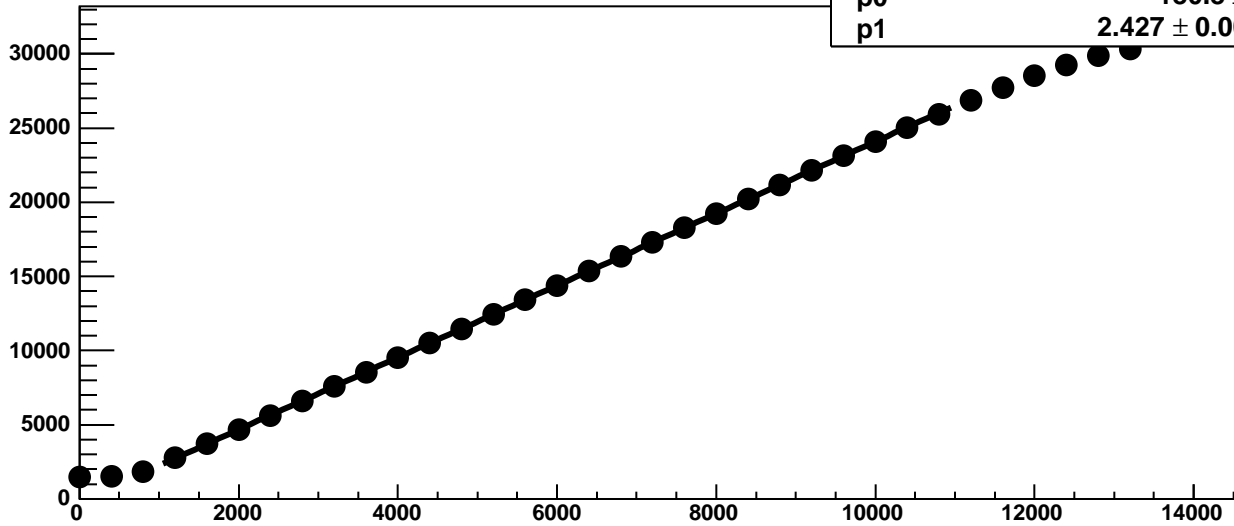


Chip 10, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

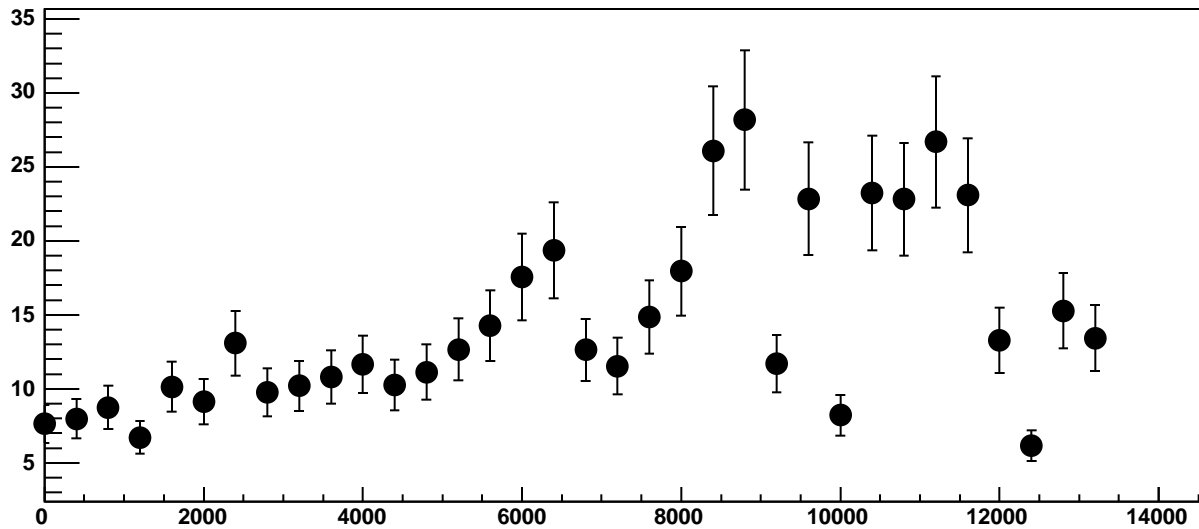


Chip 10, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC

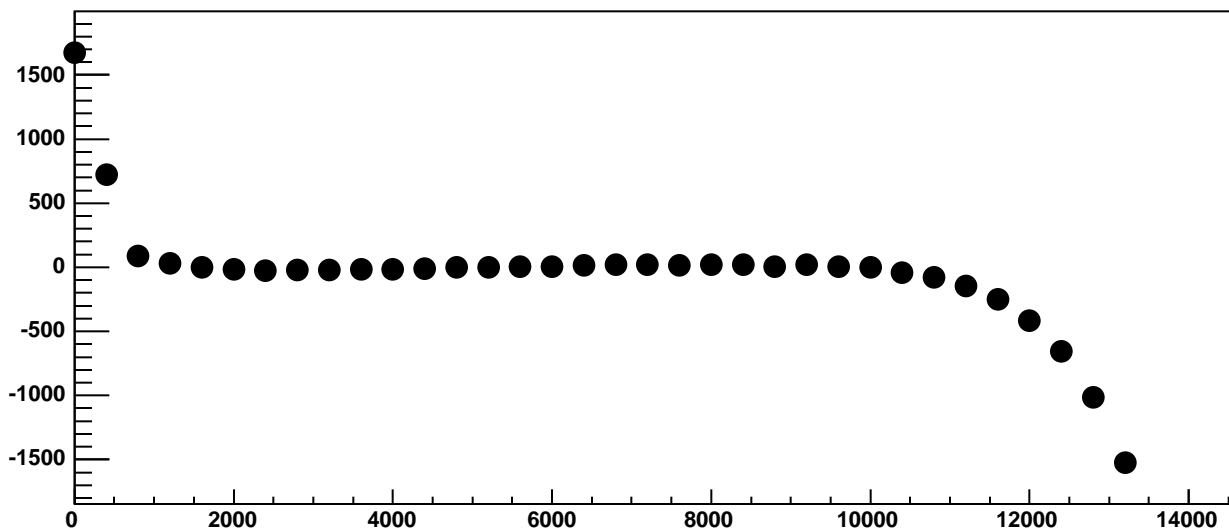
χ^2 / ndf 1339 / 23
p0 -180.8 ± 1.058
p1 2.427 ± 0.0001872



Chip 10, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC

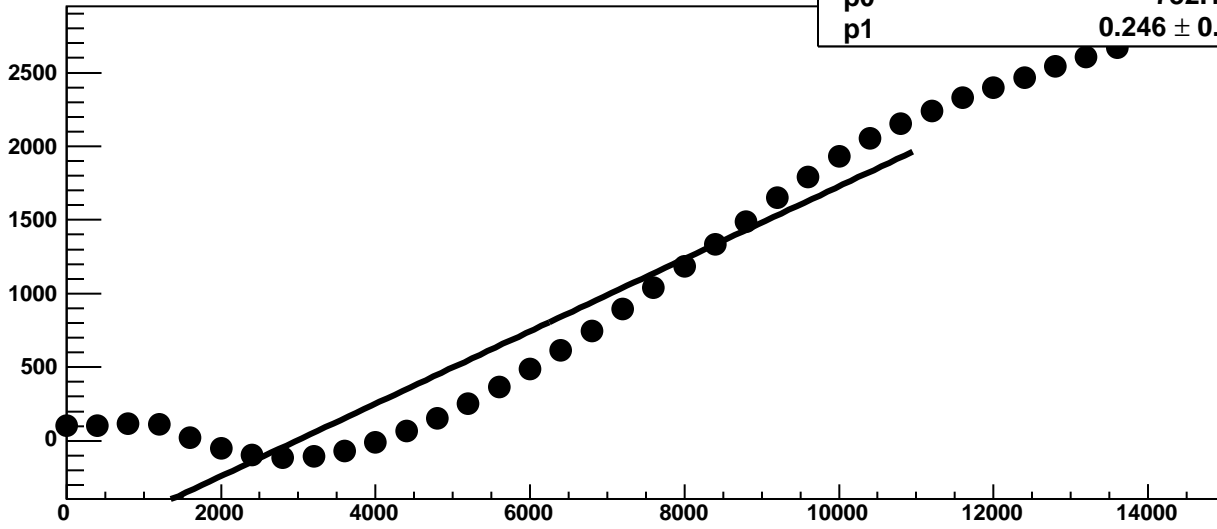


Chip 10, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC

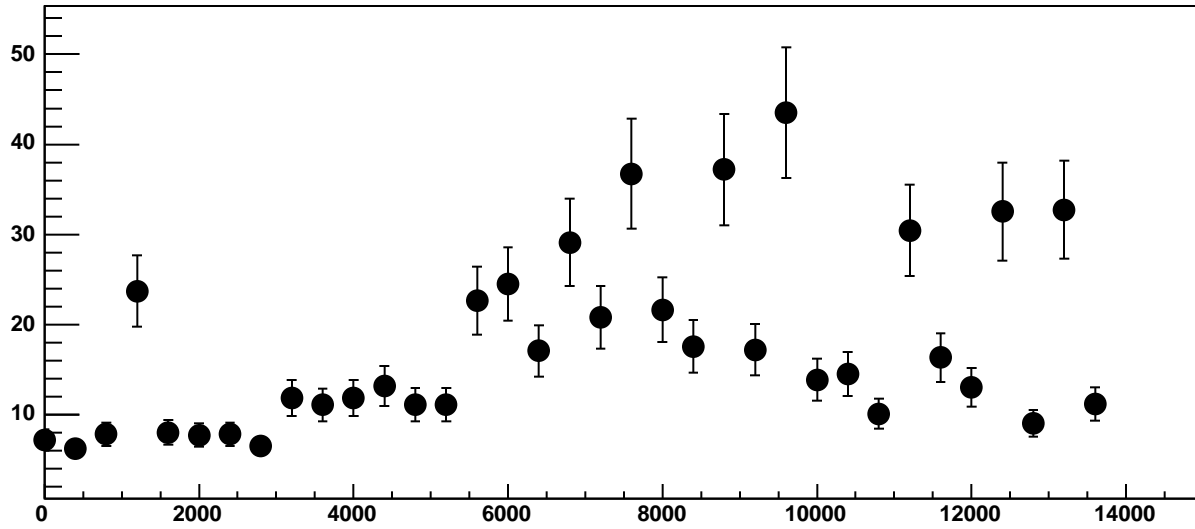


Chip 10, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

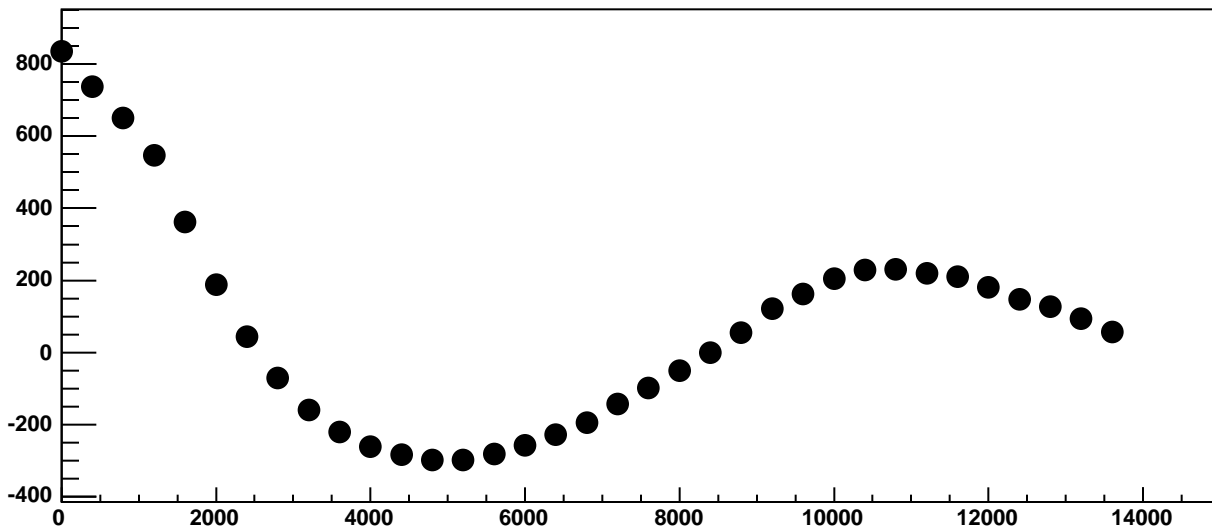
χ^2 / ndf 1.494e+05 / 23
p0 -732.1 ± 1.06
p1 0.246 ± 0.000197



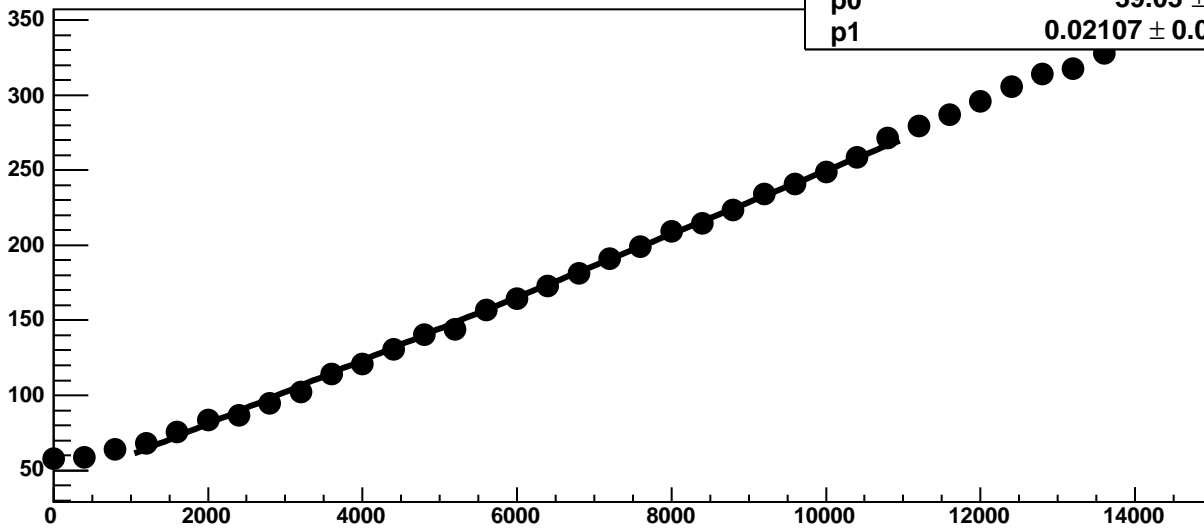
Chip 10, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

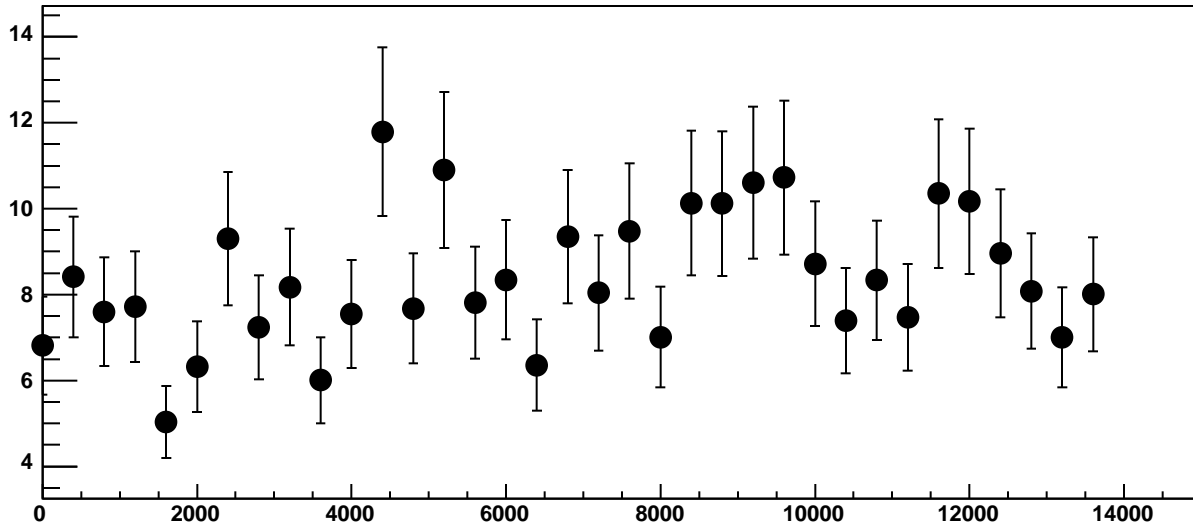


Chip 10, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

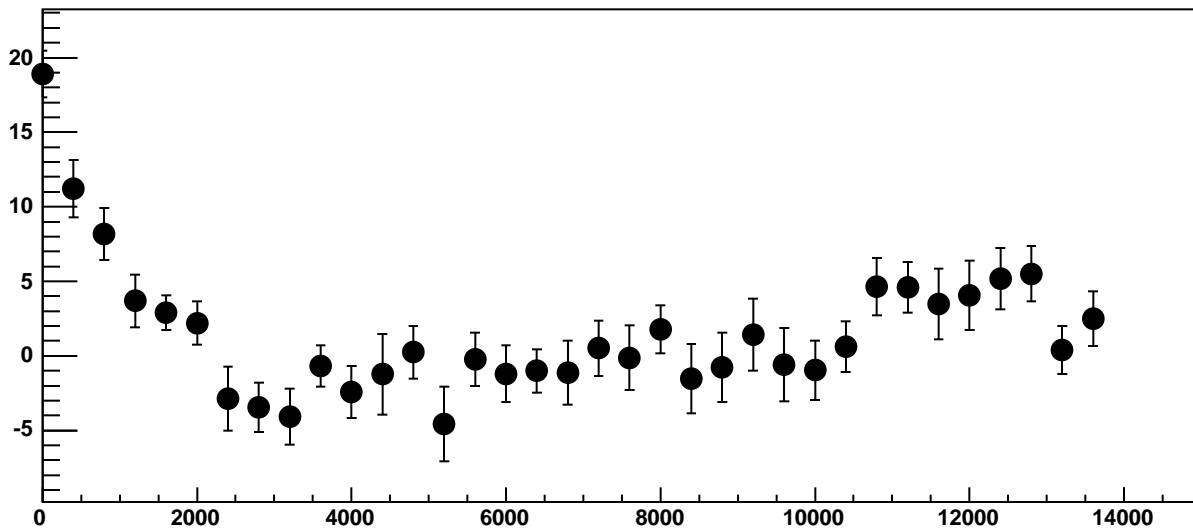


χ^2 / ndf 39.12 / 23
p0 39.03 ± 0.7534
p1 0.02107 ± 0.0001221

Chip 10, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

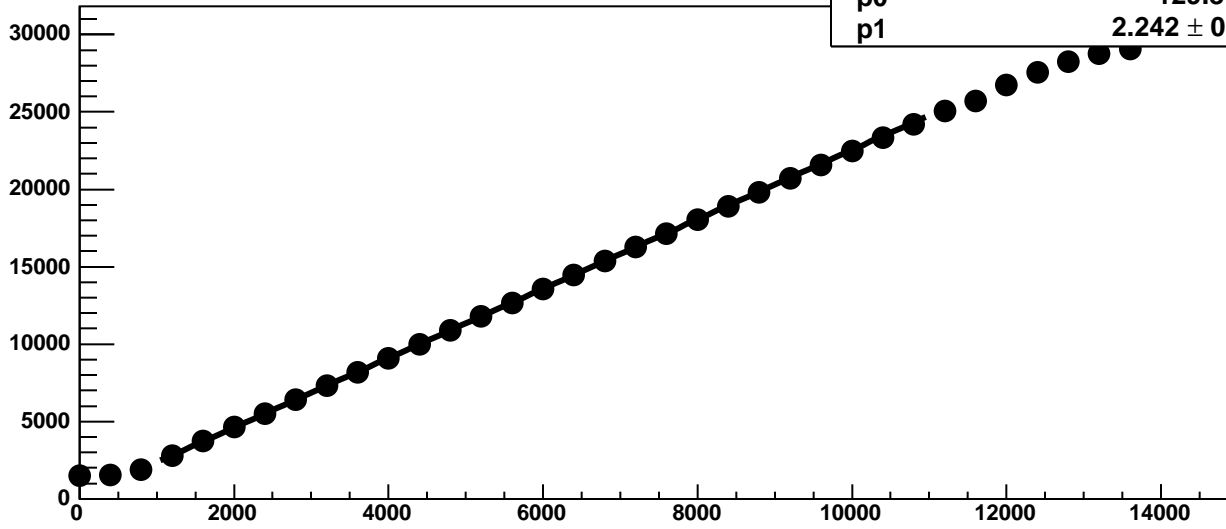


Chip 10, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

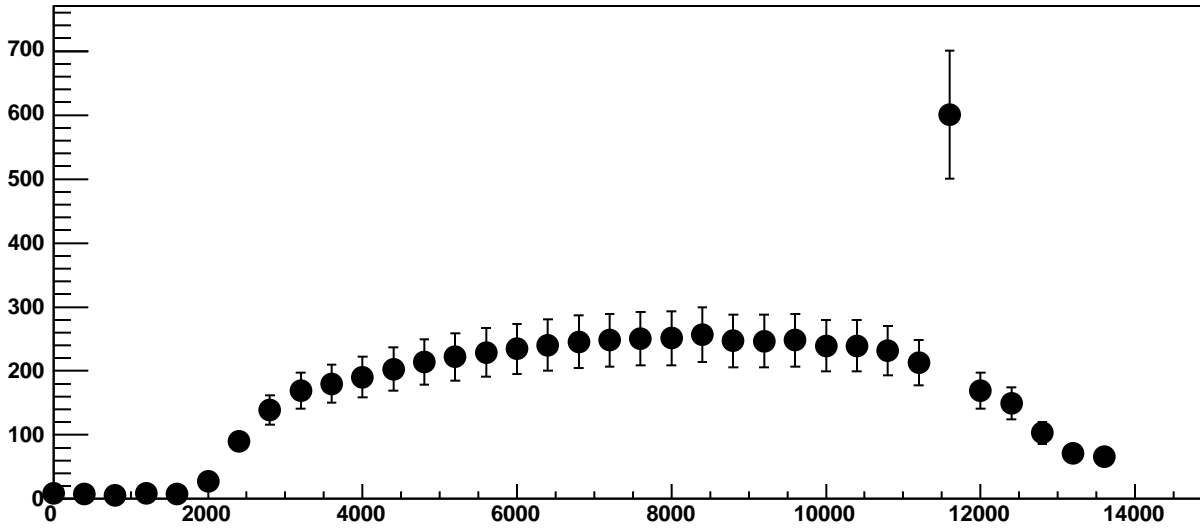


Chip 10, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC

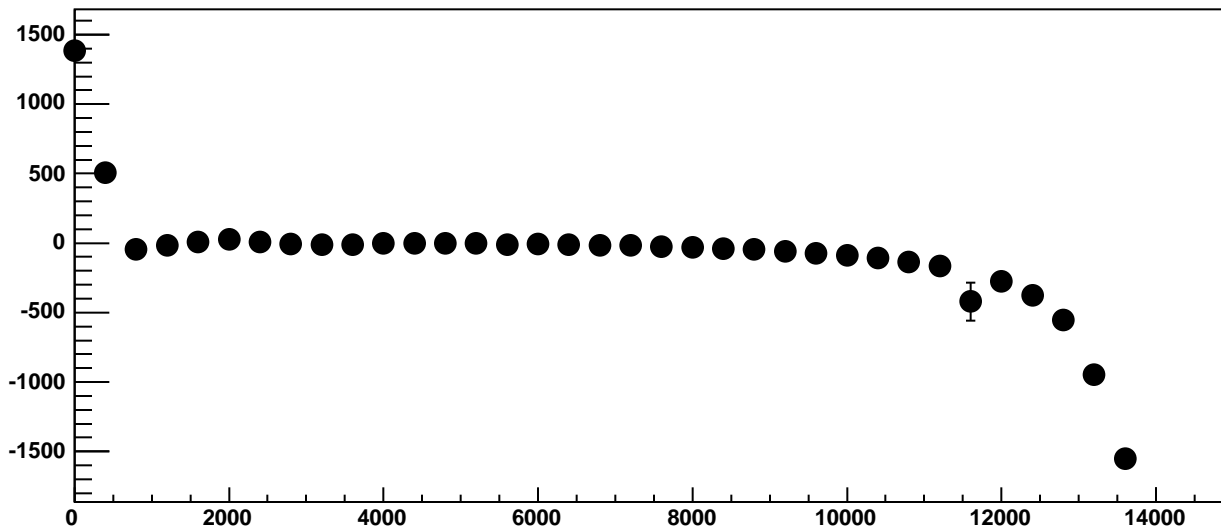
χ^2 / ndf 121.3 / 23
p0 129.8 ± 3.196
p1 2.242 ± 0.001925



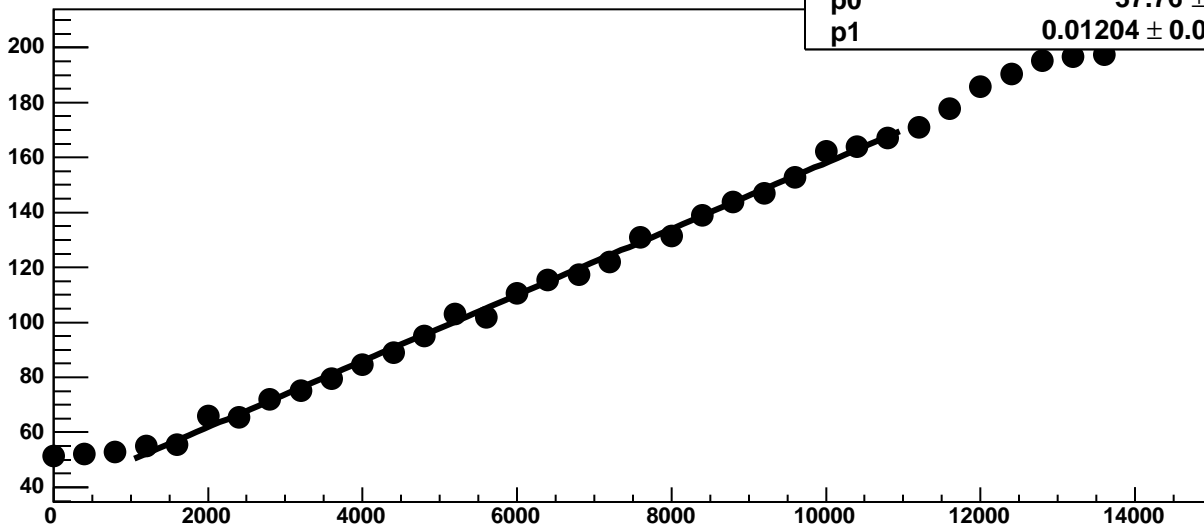
Chip 10, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



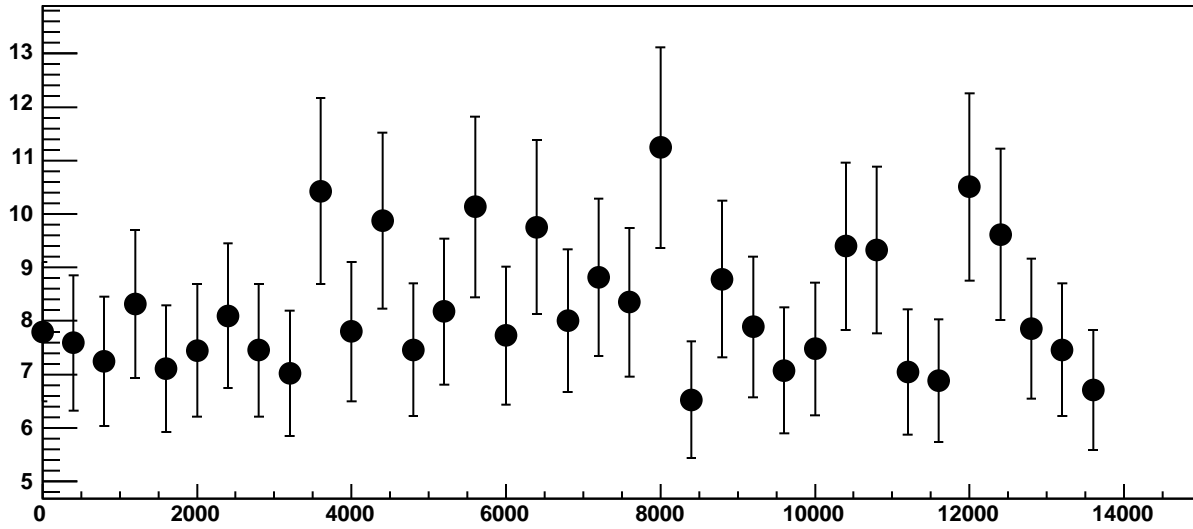
Chip 10, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC



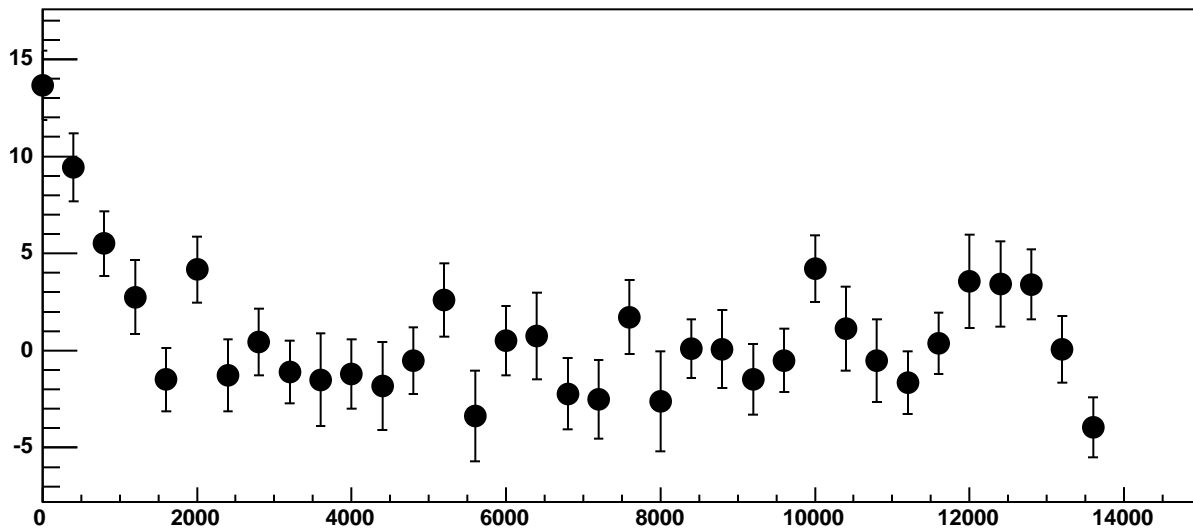
Chip 10, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



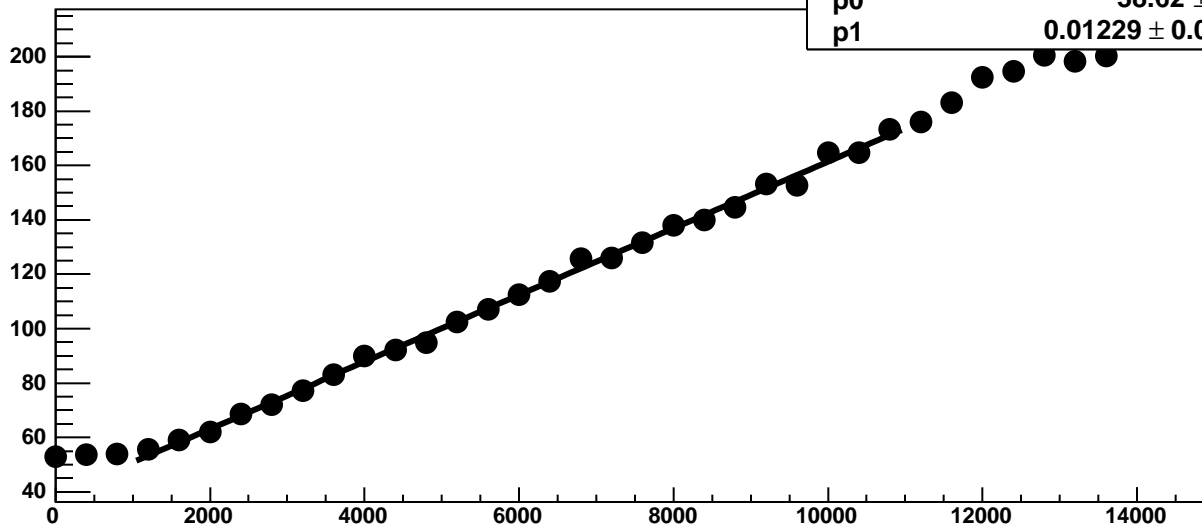
Chip 10, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

23.13 / 23

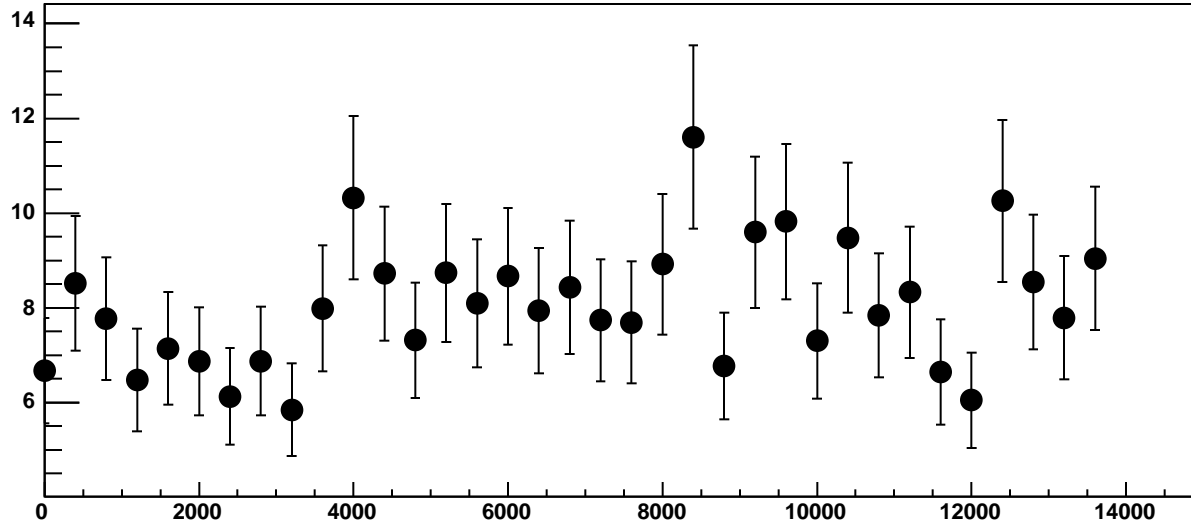
p0

38.62 ± 0.7522

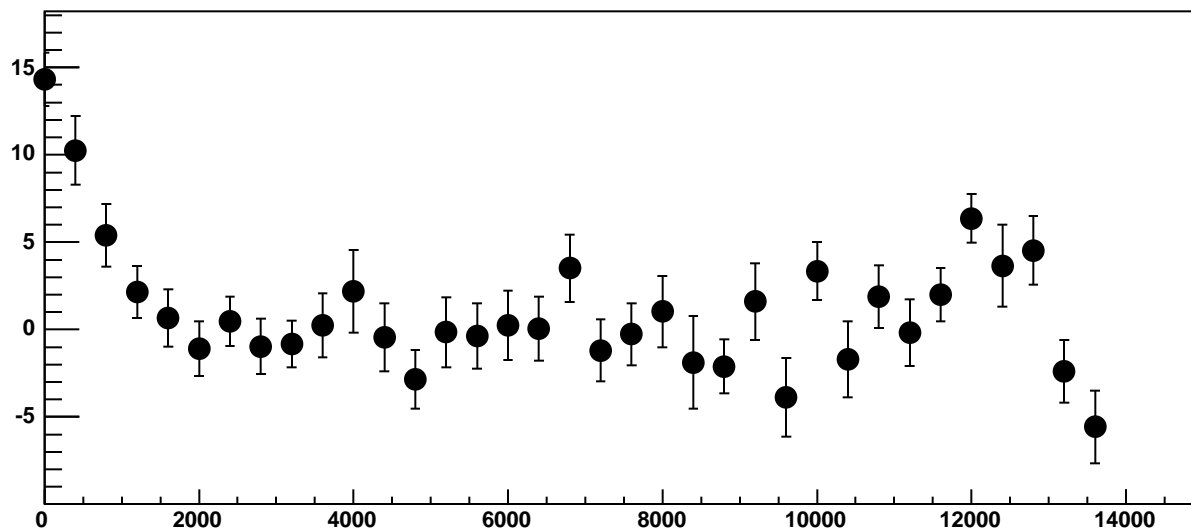
p1

0.01229 ± 0.0001203

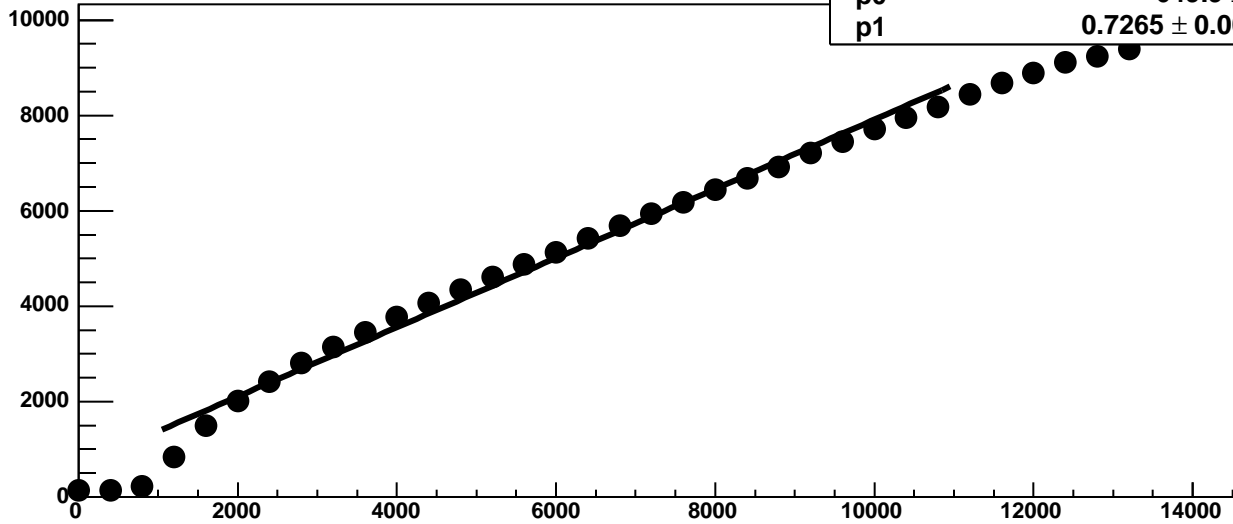
Chip 10, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



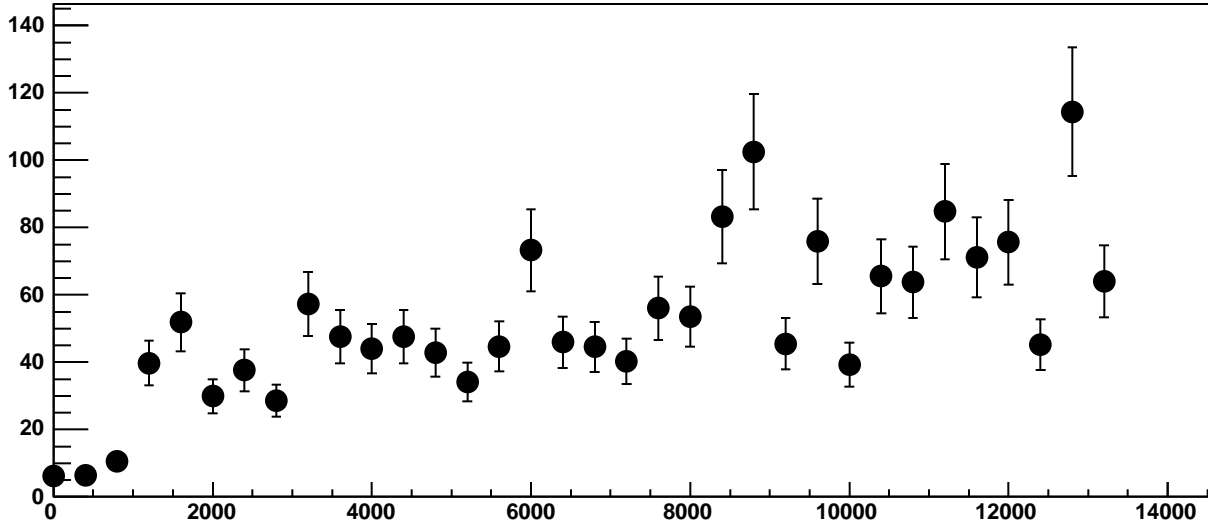
Chip 10, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



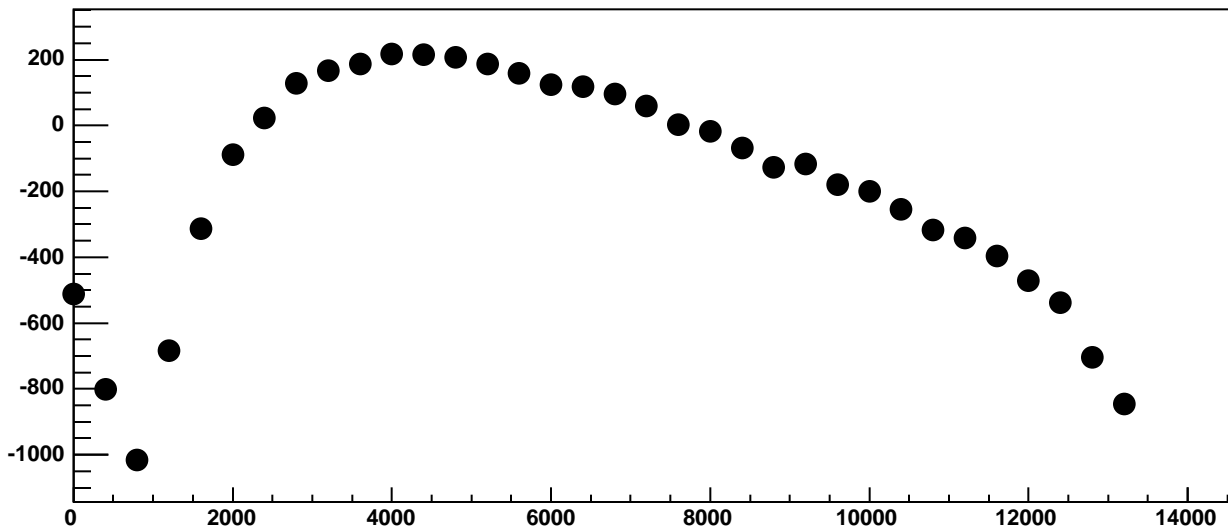
Chip 10, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC



Chip 10, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC

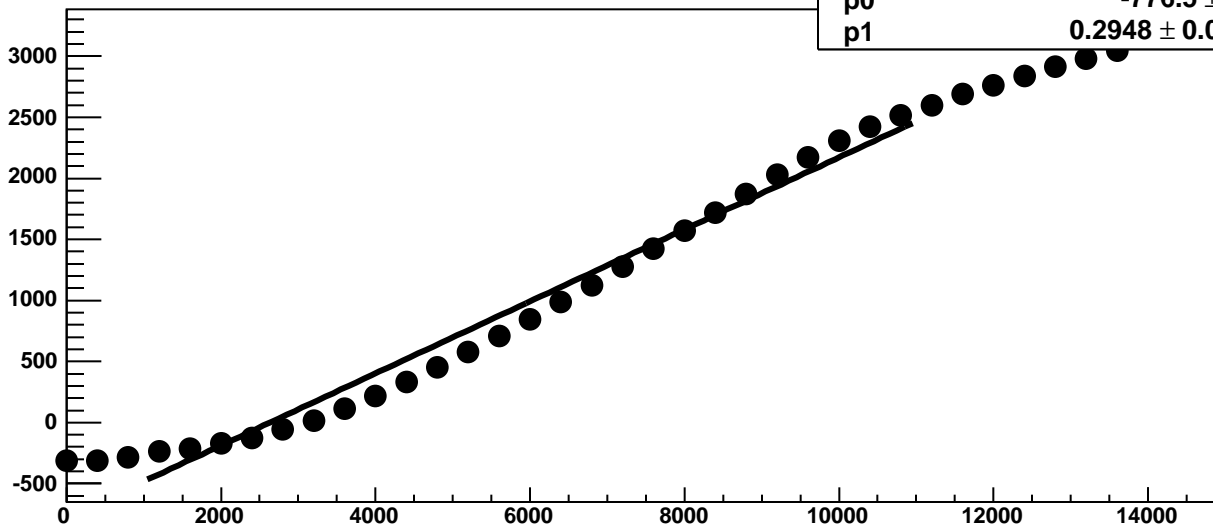


Chip 10, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC

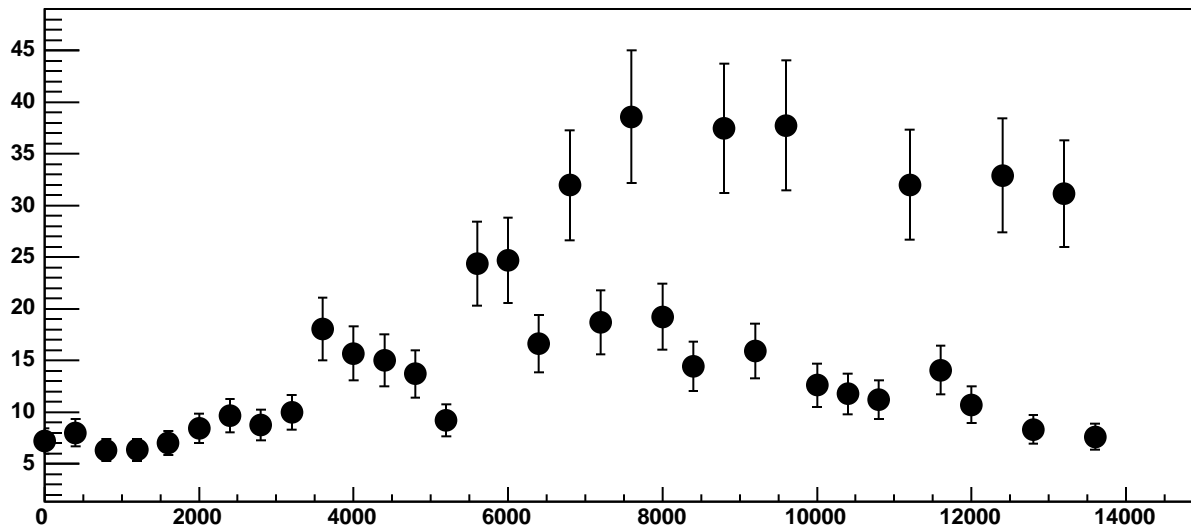


Chip 11, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

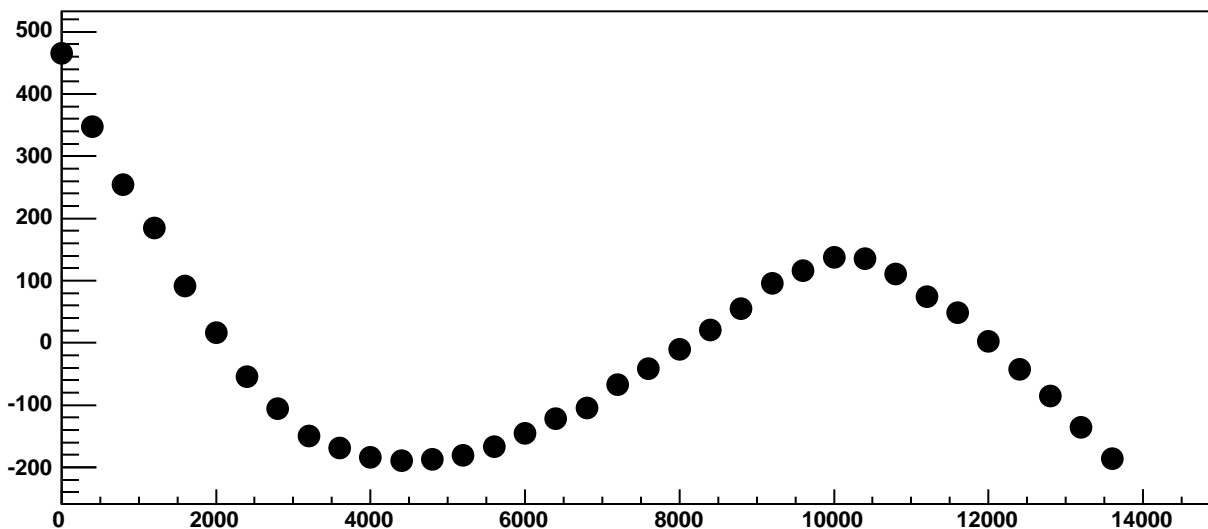
χ^2 / ndf 5.57e+04 / 23
p0 -776.5 ± 0.9546
p1 0.2948 ± 0.0001775



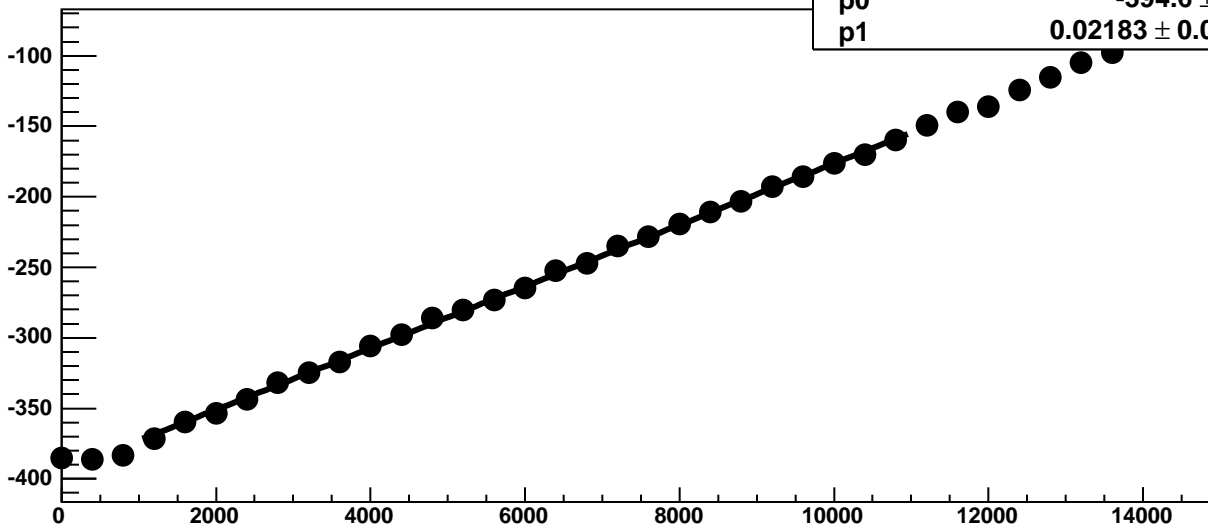
Chip 11, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

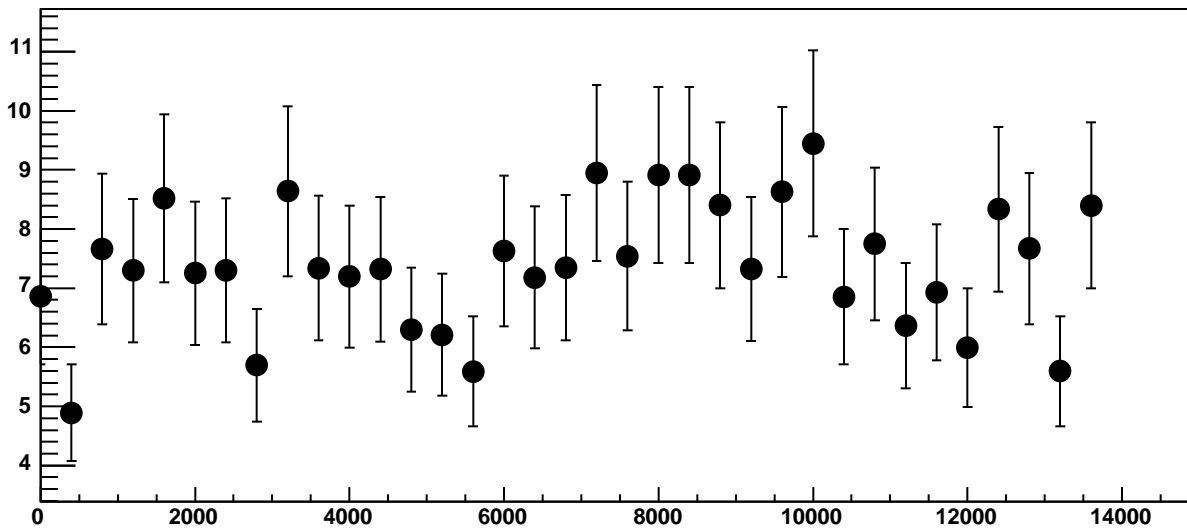


Chip 11, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

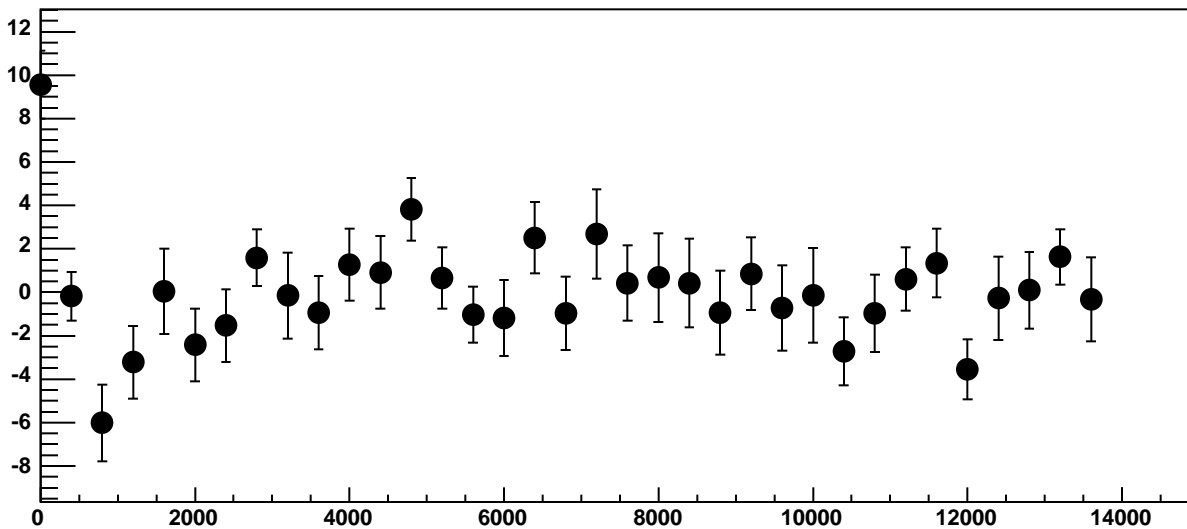


χ^2 / ndf 26.07 / 23
p0 -394.6 ± 0.7738
p1 0.02183 ± 0.0001213

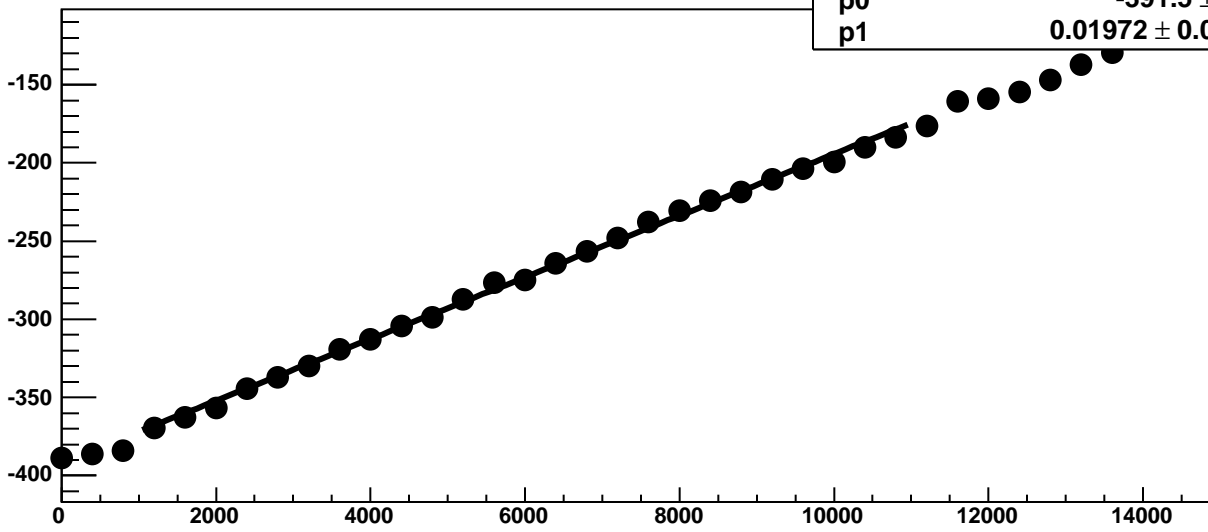
Chip 11, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

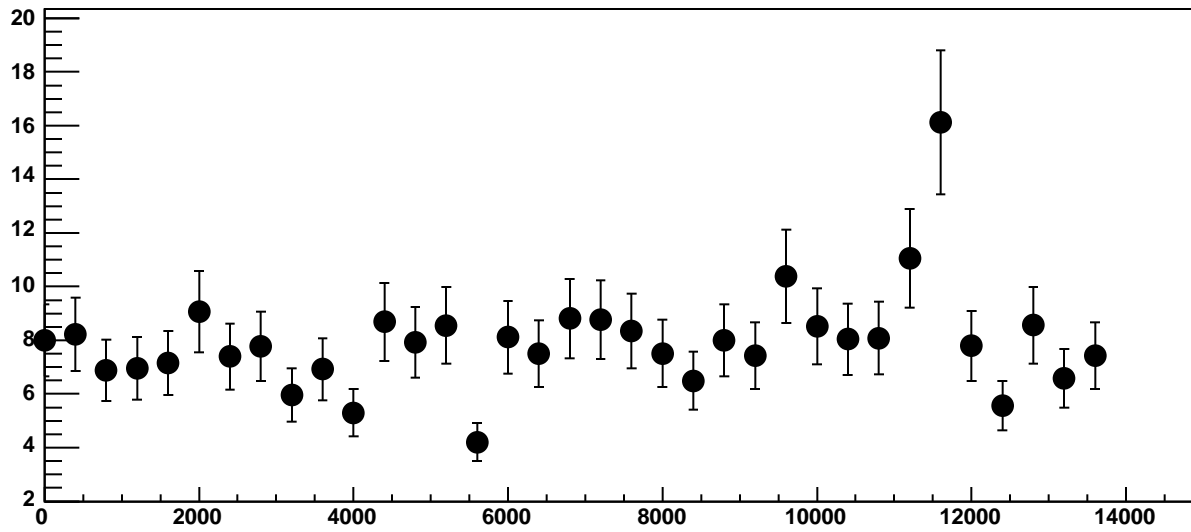


Chip 11, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

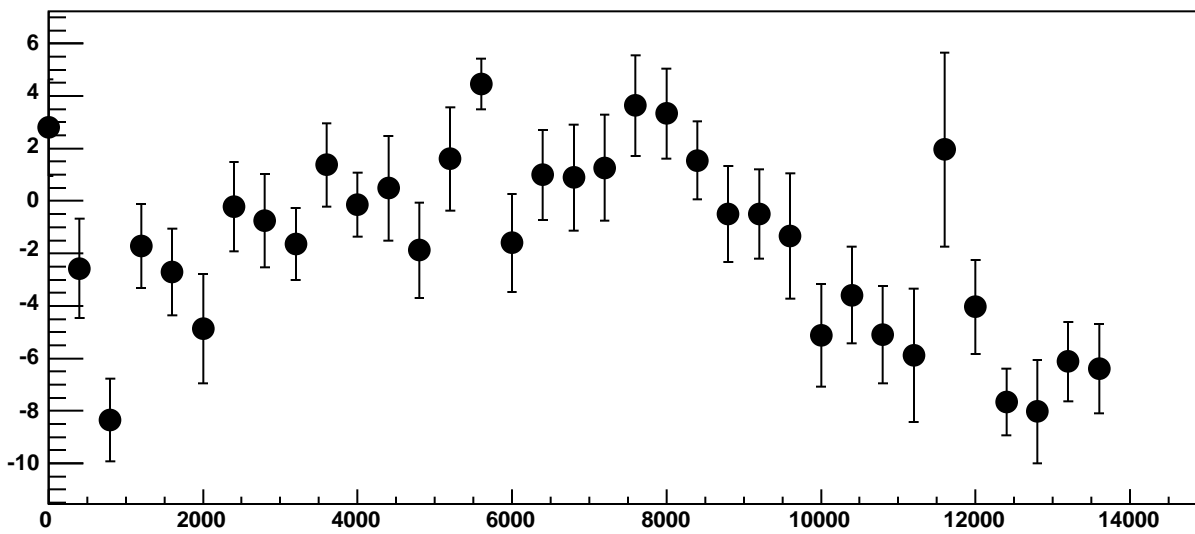


χ^2 / ndf 63.54 / 23
p0 -391.5 ± 0.7697
p1 0.01972 ± 0.0001219

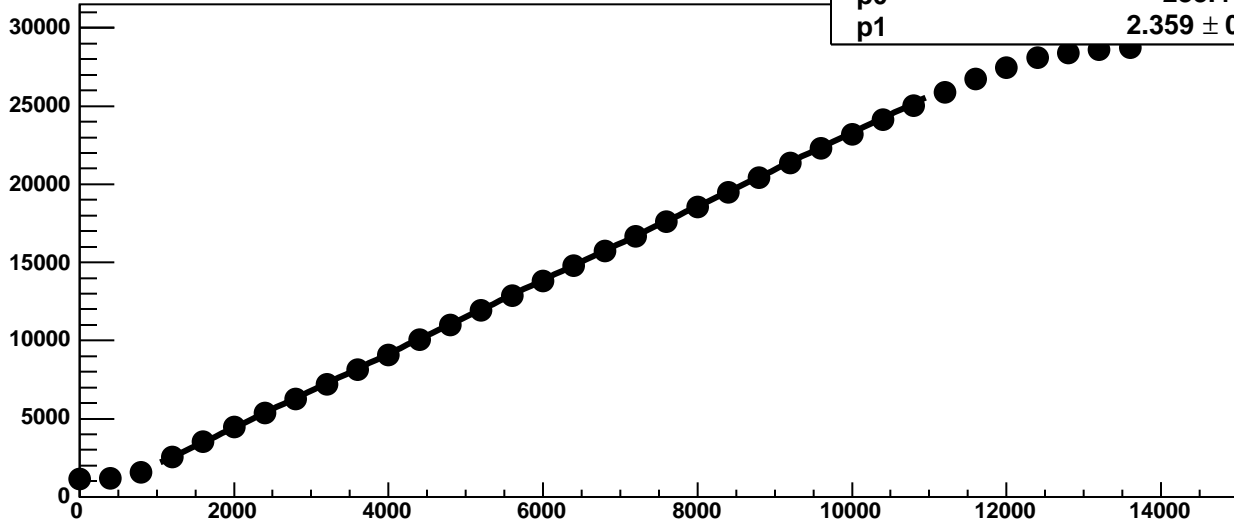
Chip 11, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



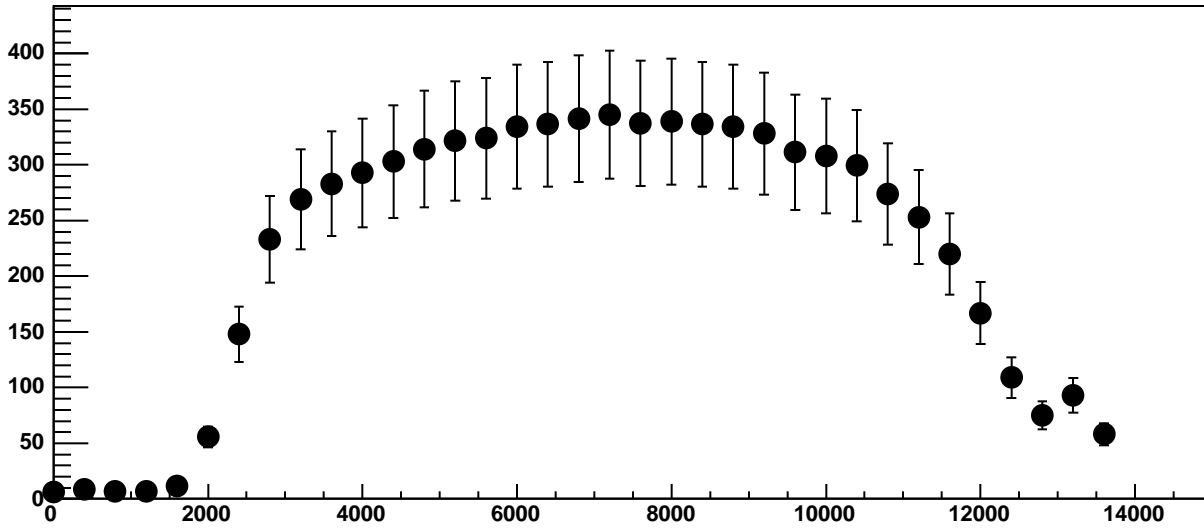
Chip 11, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



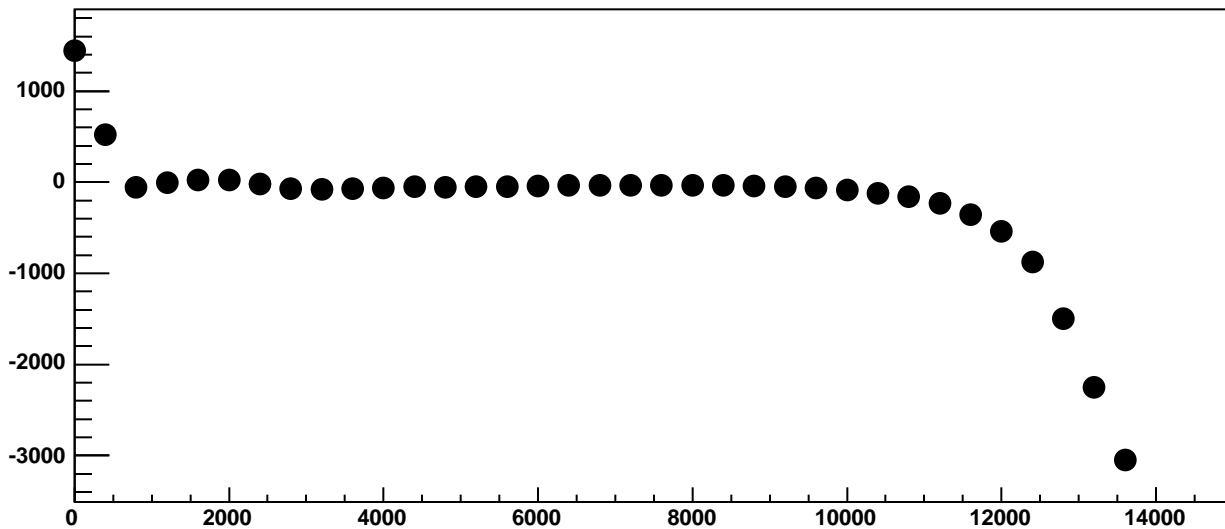
Chip 11, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC



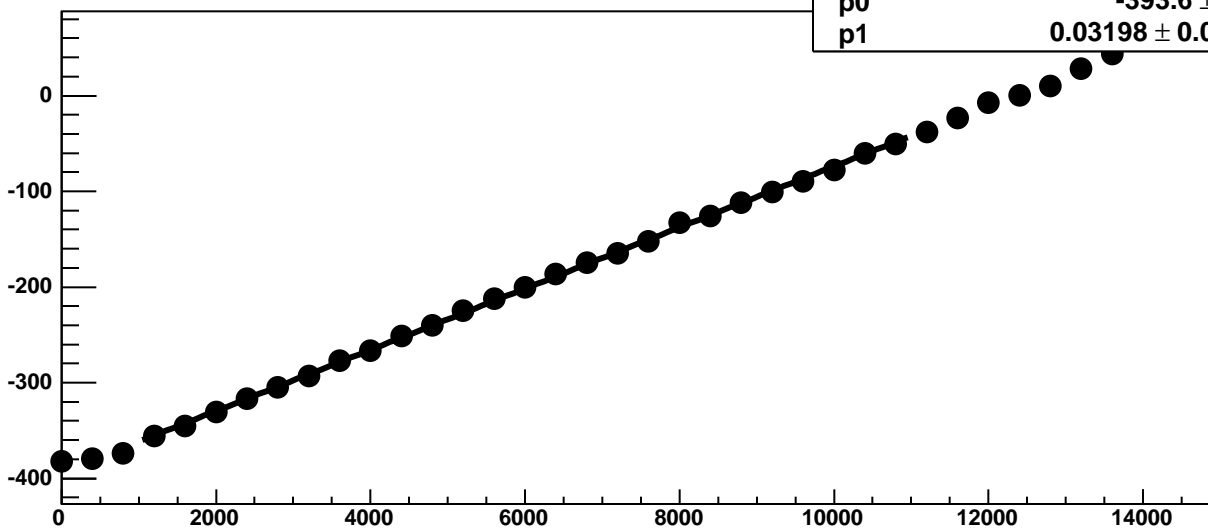
Chip 11, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC

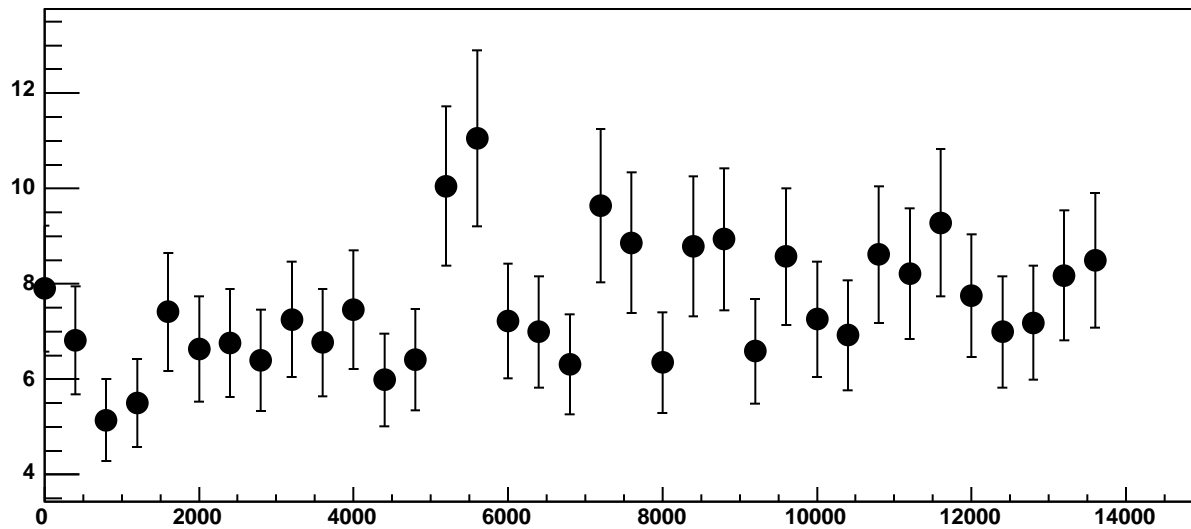


Chip 11, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

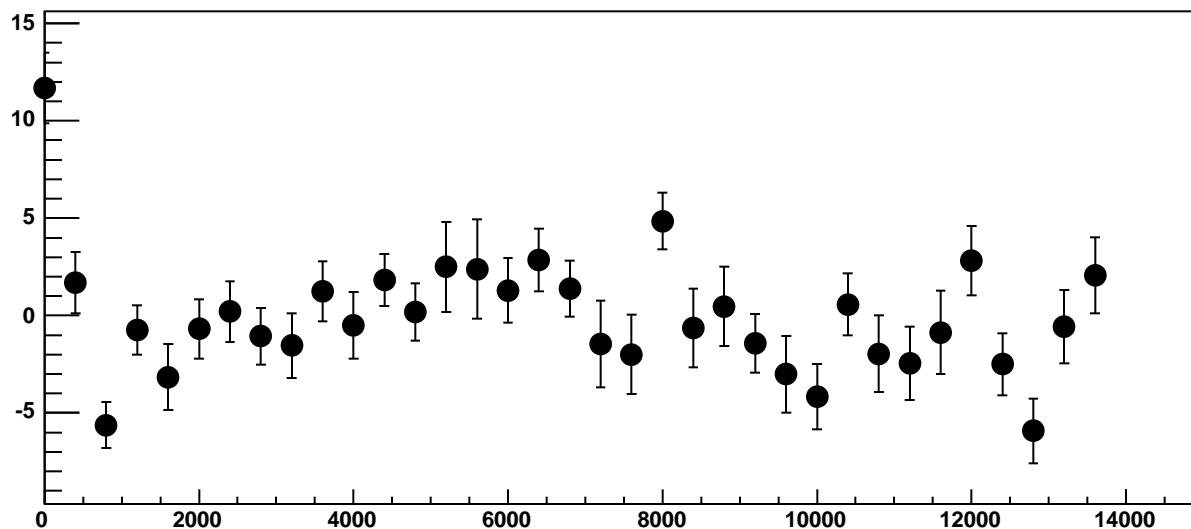


χ^2 / ndf 37.96 / 23
p0 -393.6 ± 0.7125
p1 0.03198 ± 0.0001123

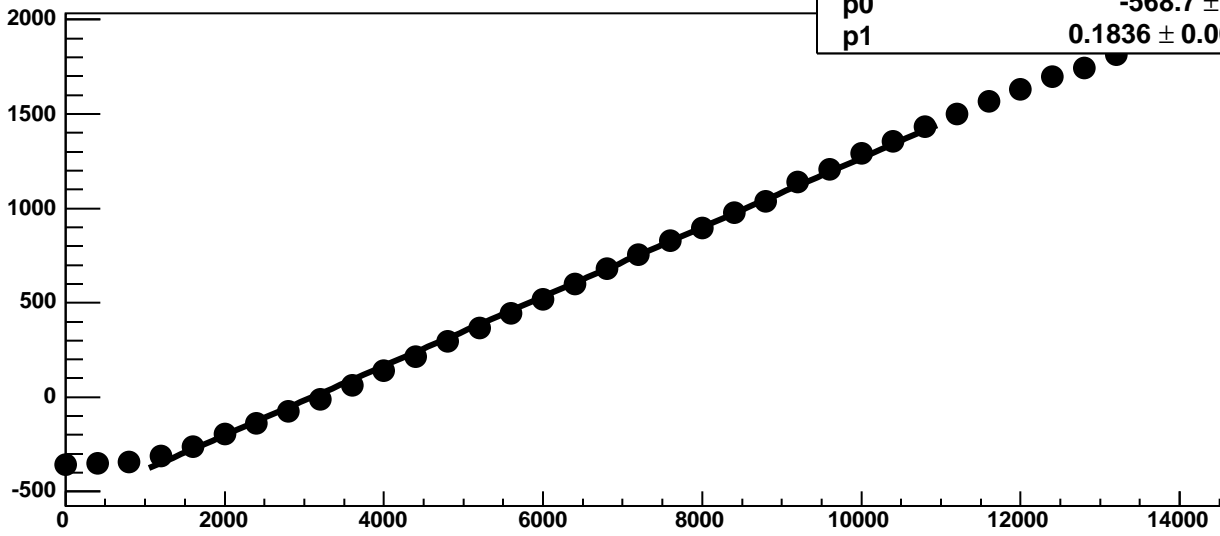
Chip 11, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



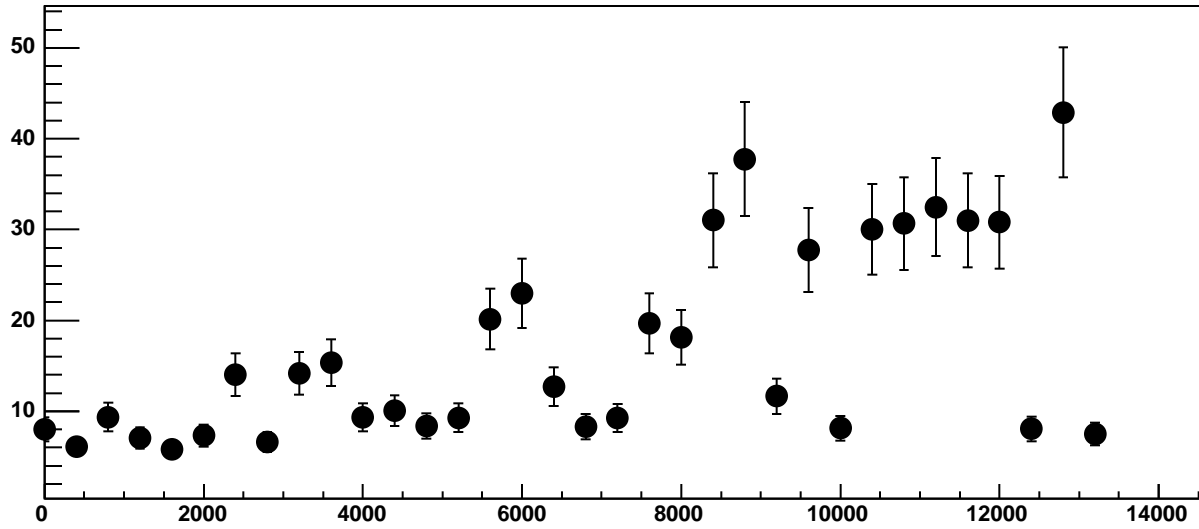
Chip 11, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



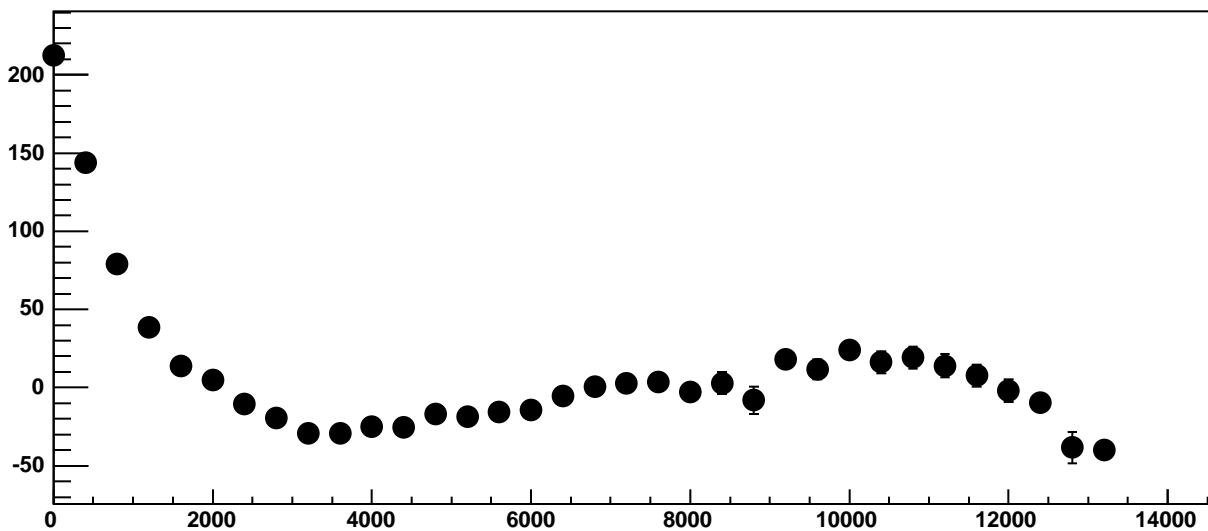
Chip 11, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



Chip 11, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



Chip 11, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

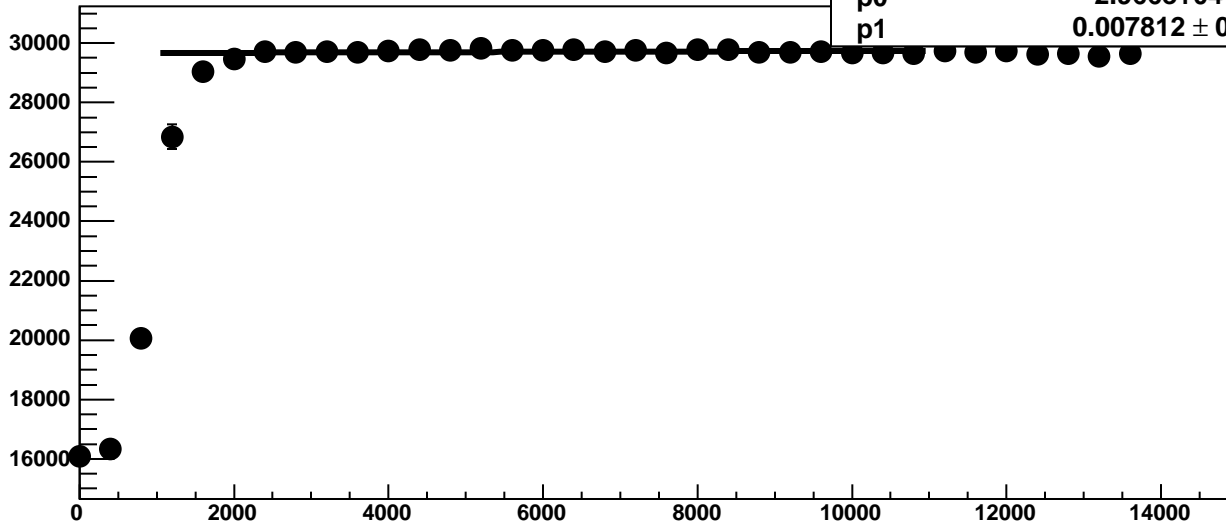
236.7 / 23

p0

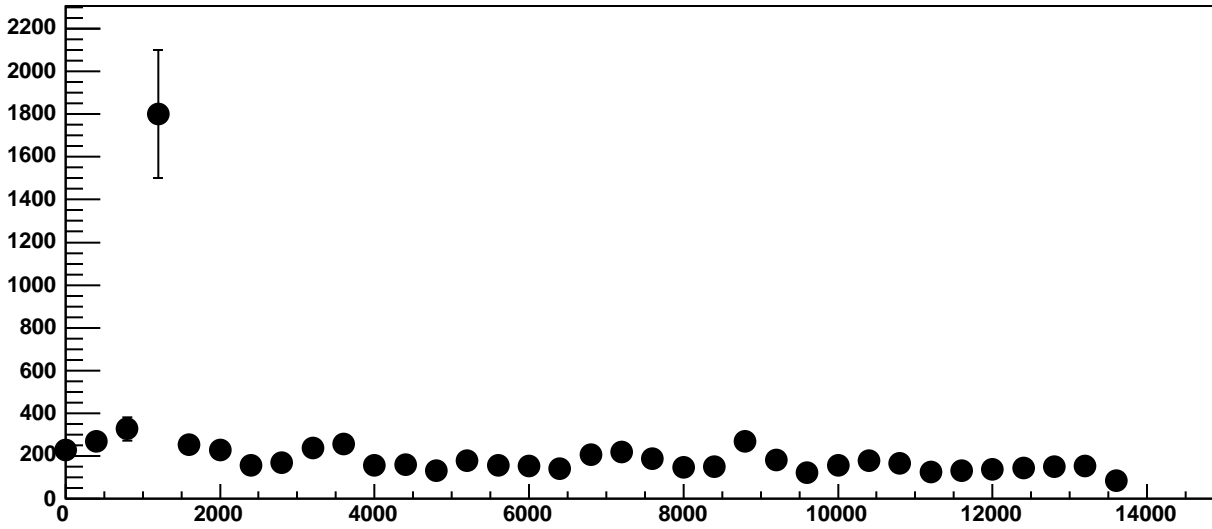
$2.966\text{e}+04 \pm 21.14$

p1

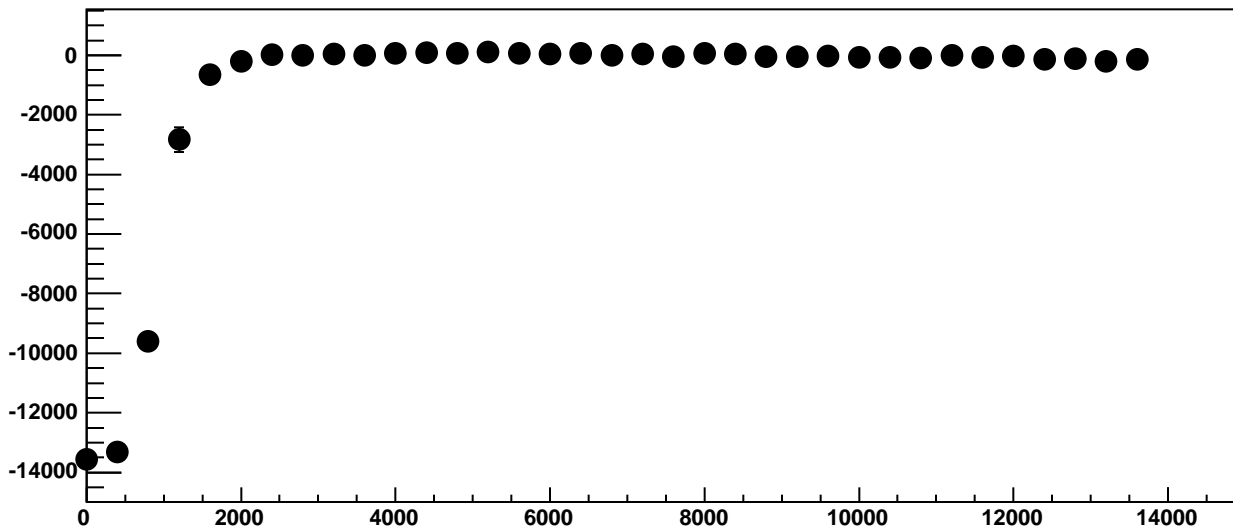
0.007812 ± 0.003011



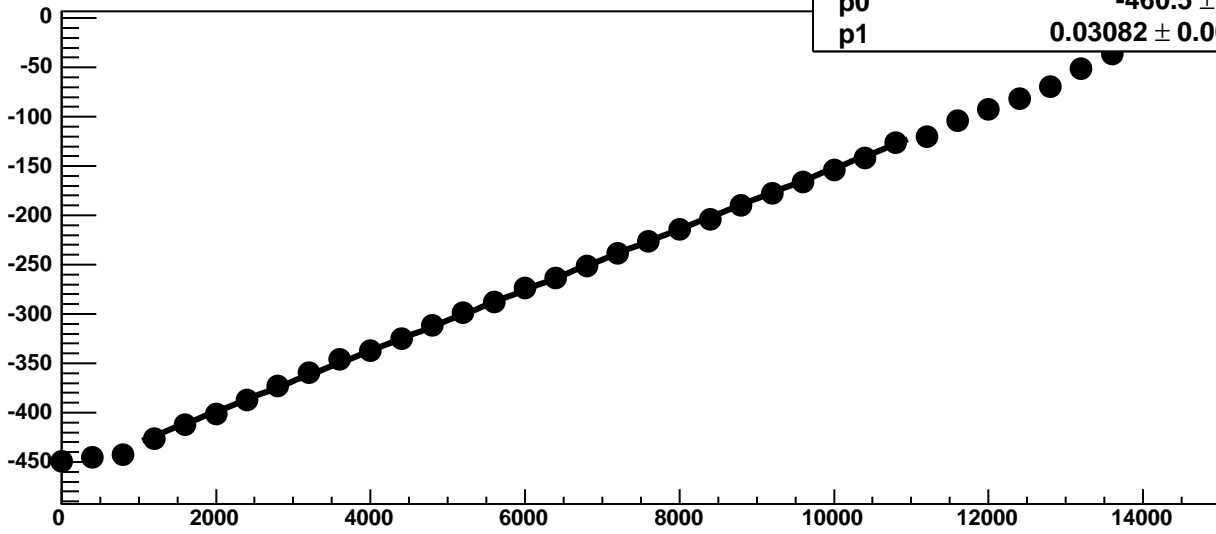
Chip 11, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



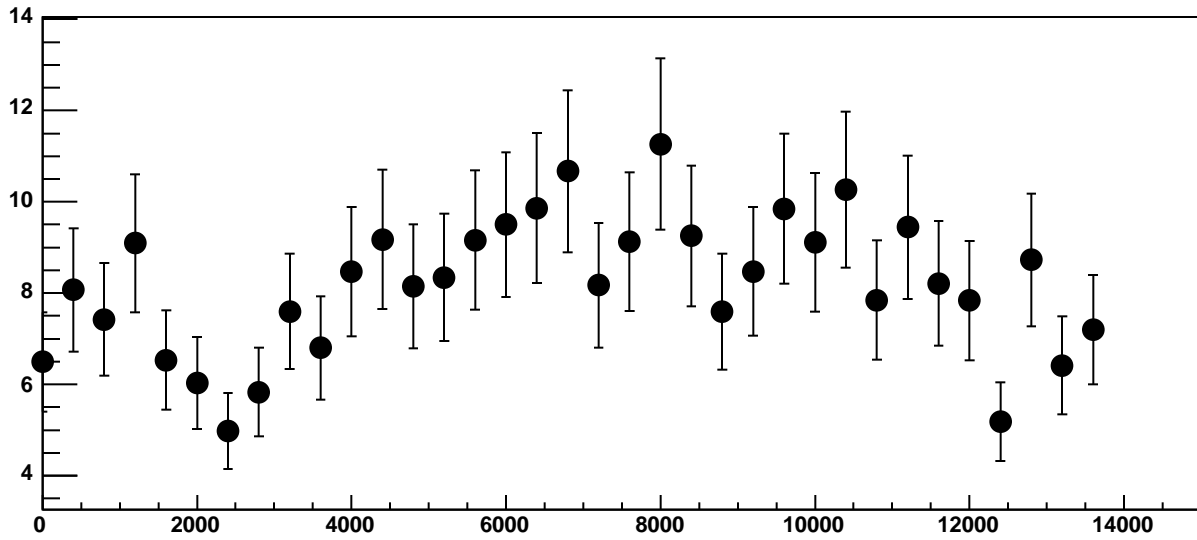
Chip 11, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC



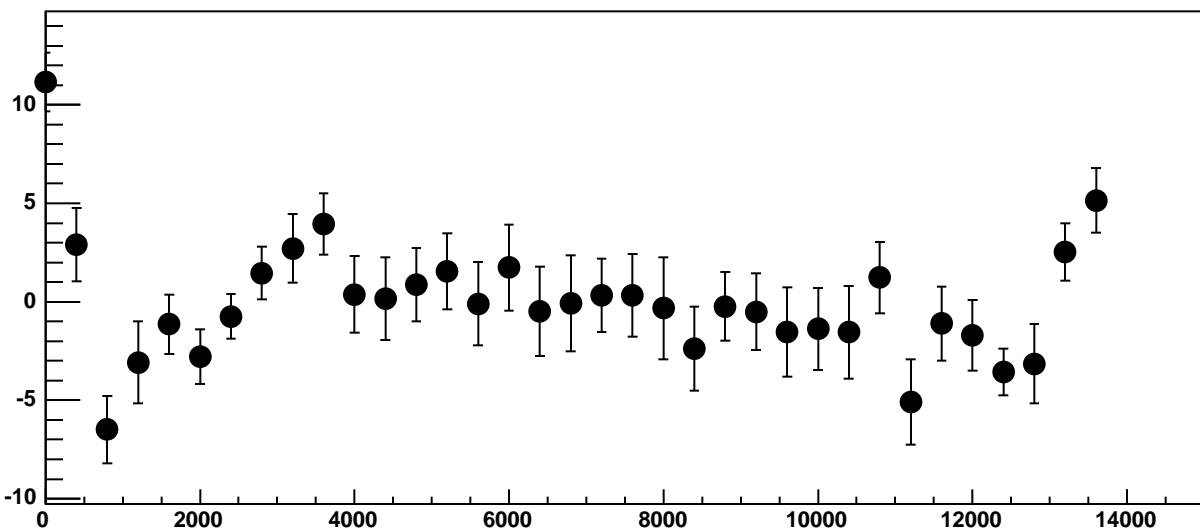
Chip 11, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



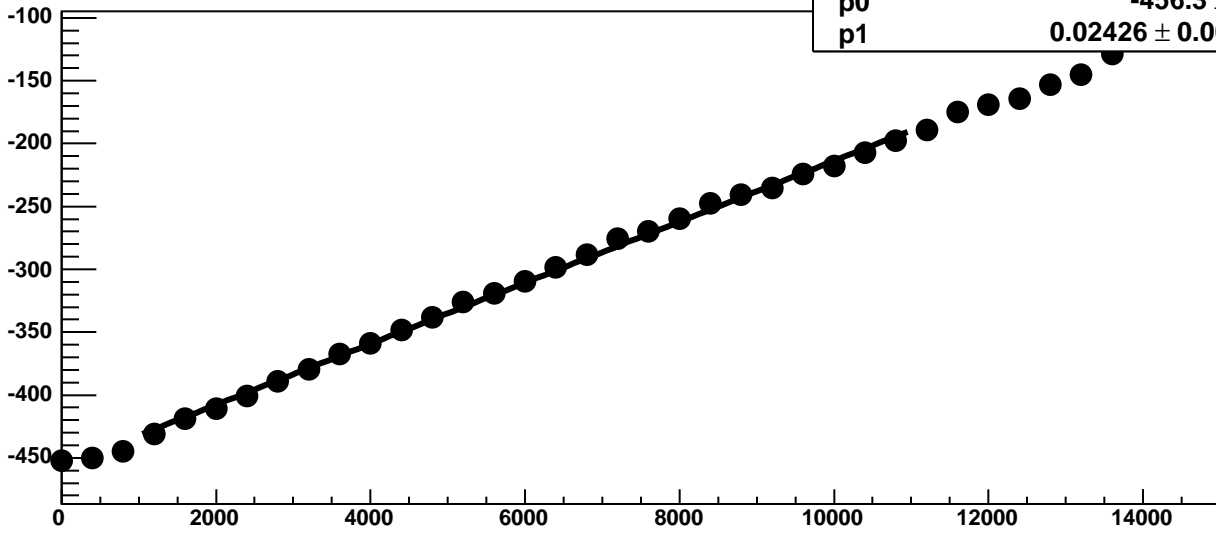
Chip 11, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



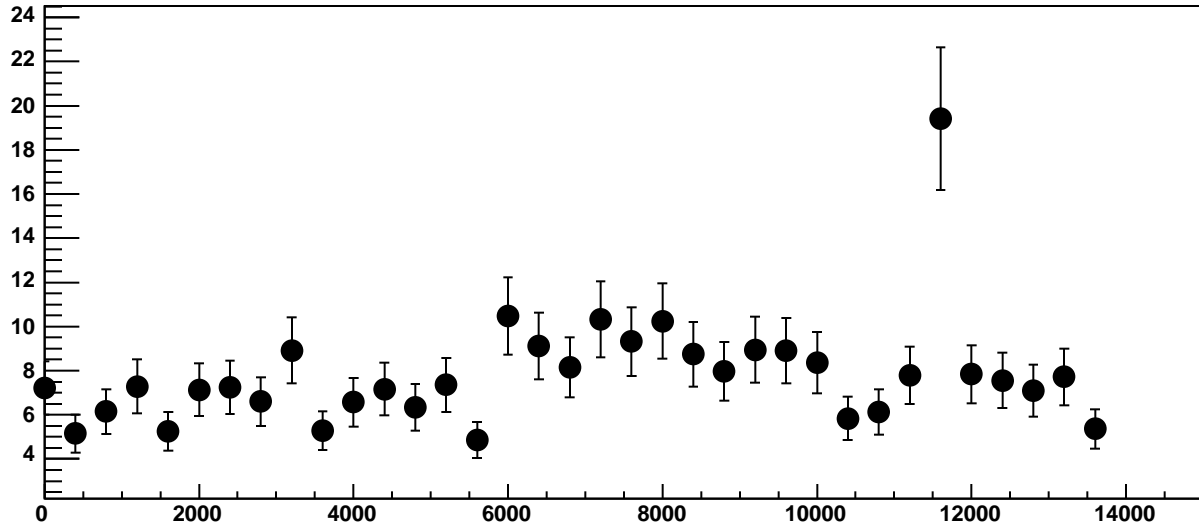
Chip 11, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



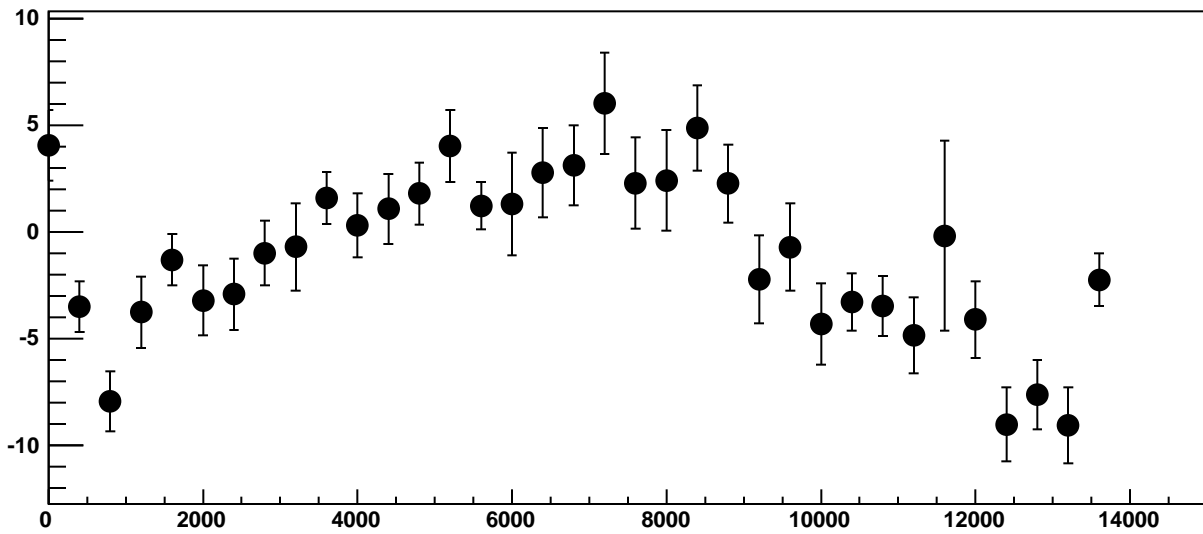
Chip 11, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



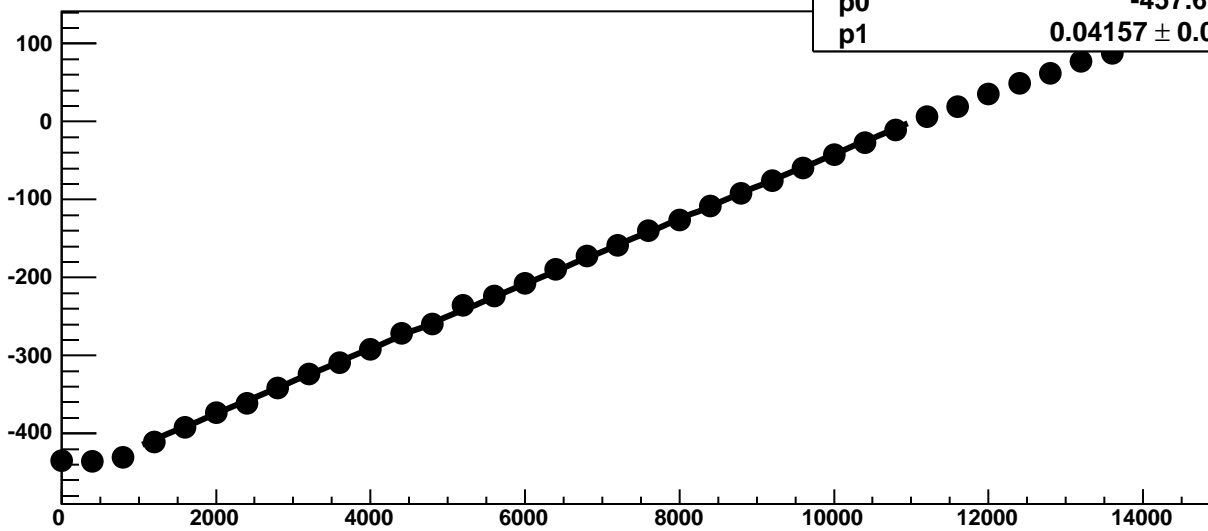
Chip 11, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



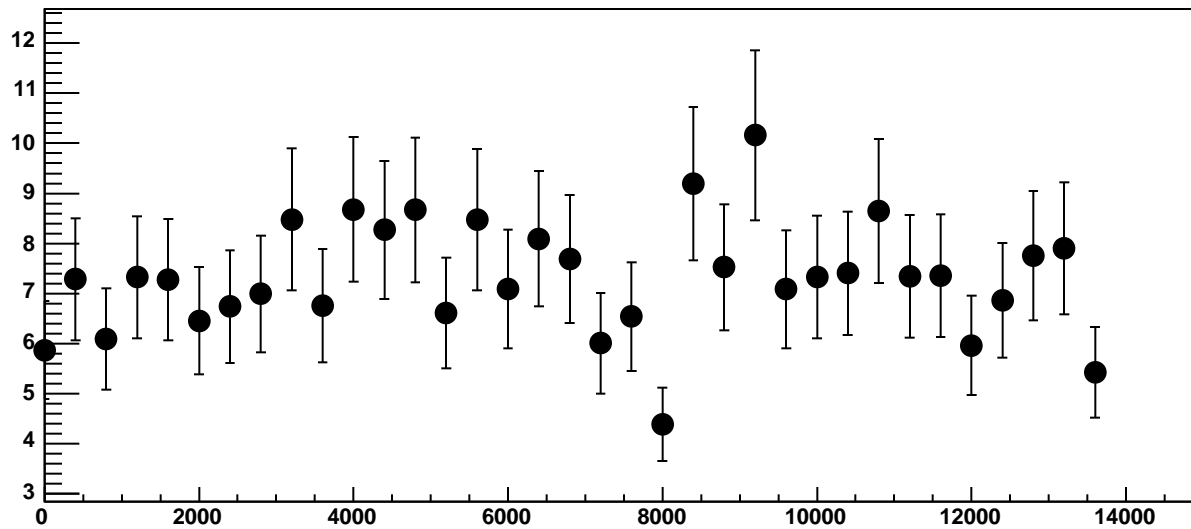
Chip 11, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



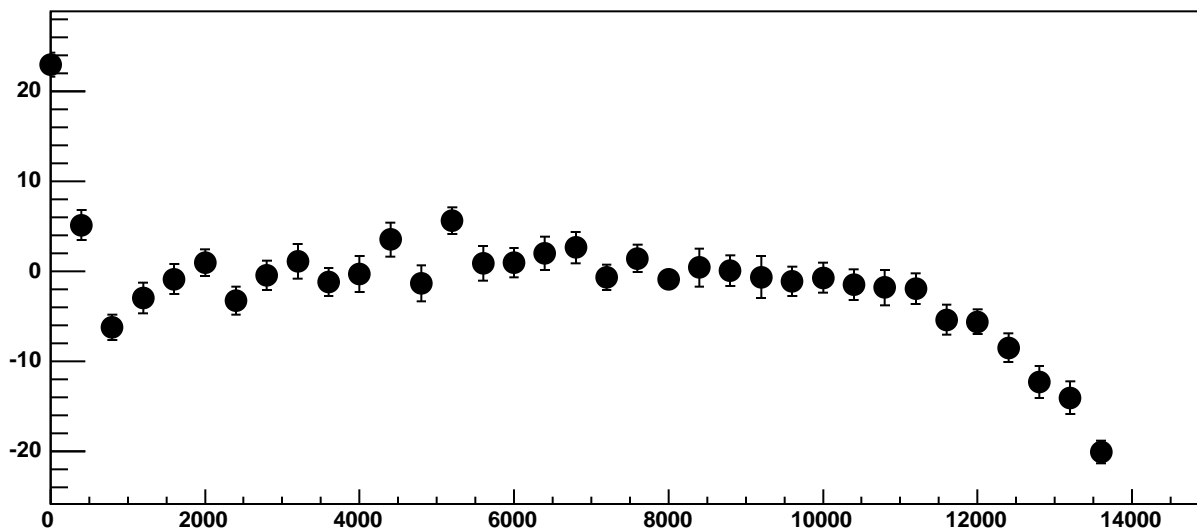
Chip 11, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC



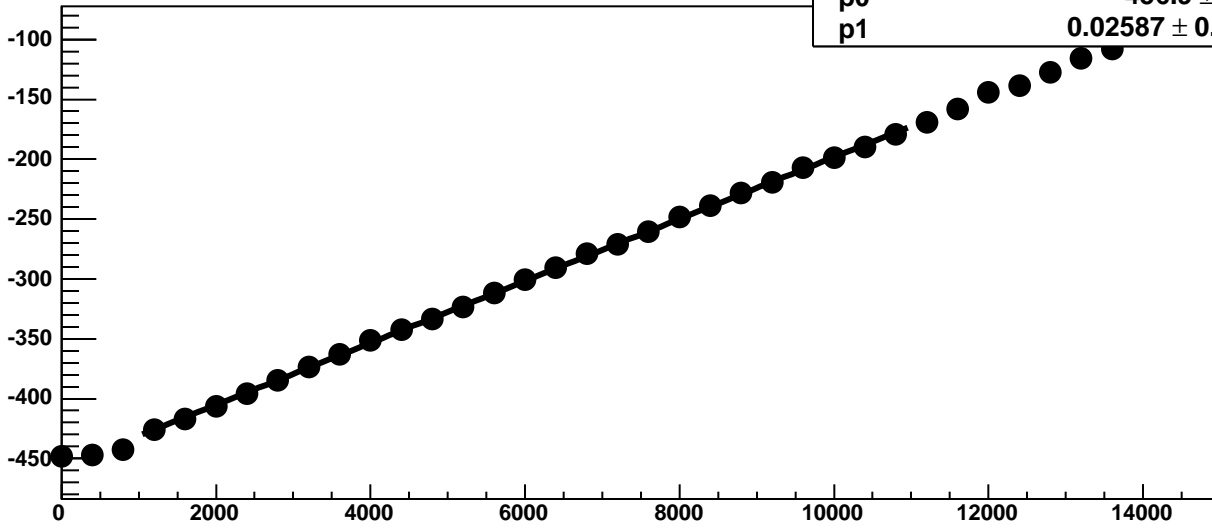
Chip 11, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC

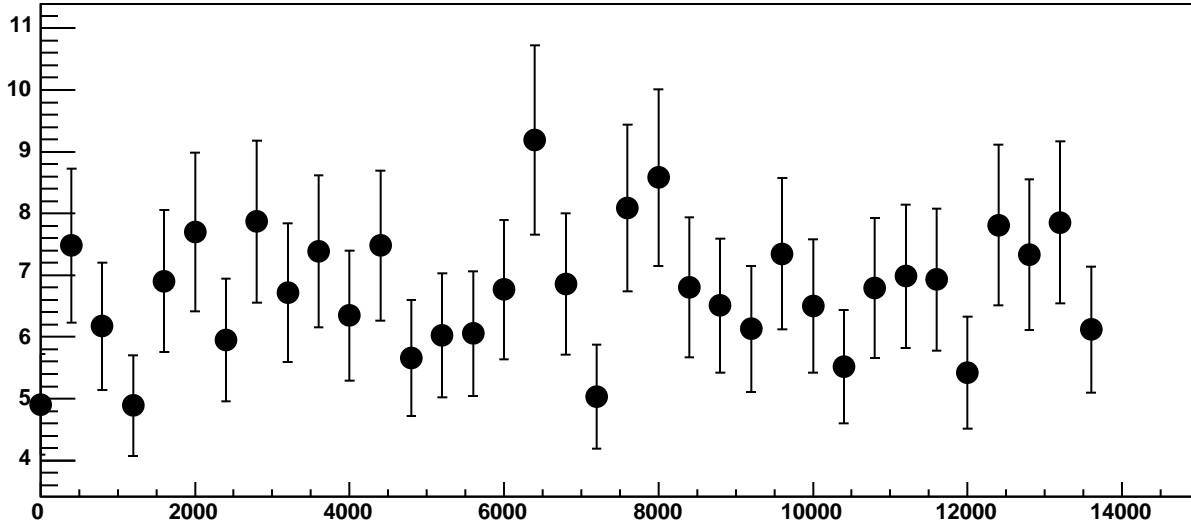


Chip 11, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

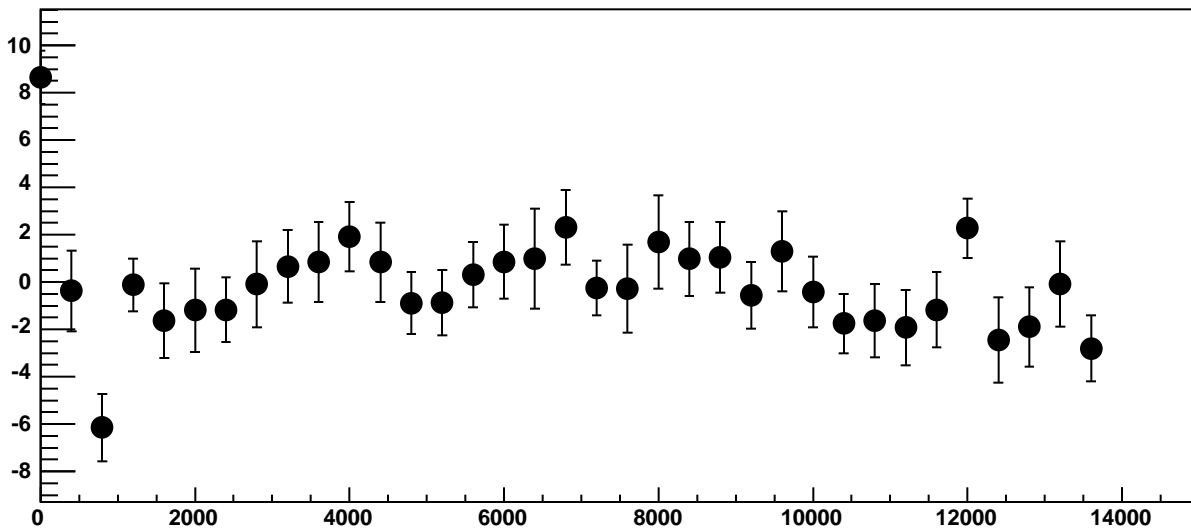


χ^2 / ndf 13.84 / 23
p0 -456.9 ± 0.6702
p1 0.02587 ± 0.000101

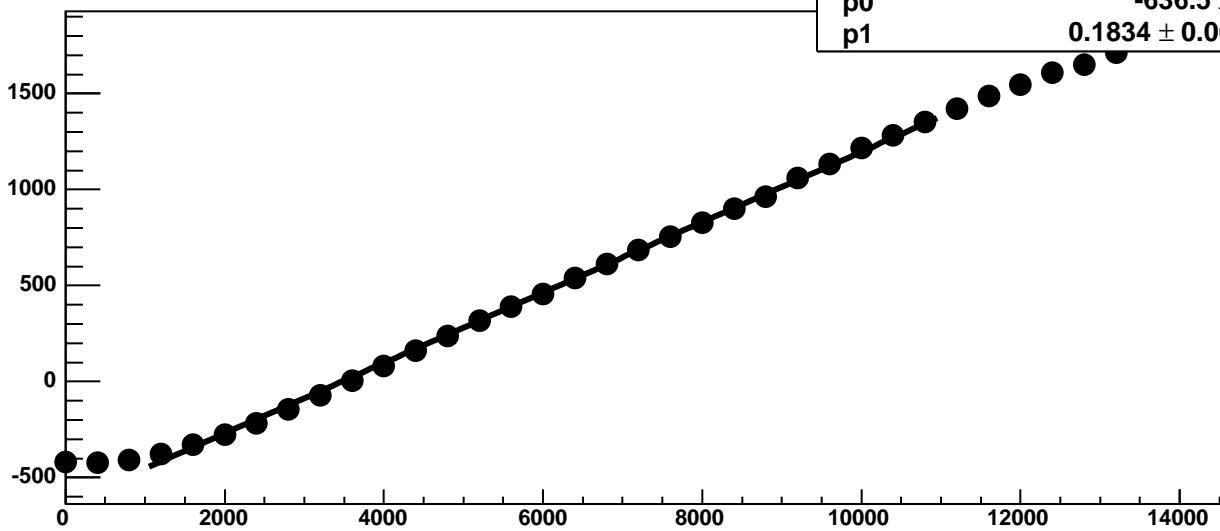
Chip 11, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



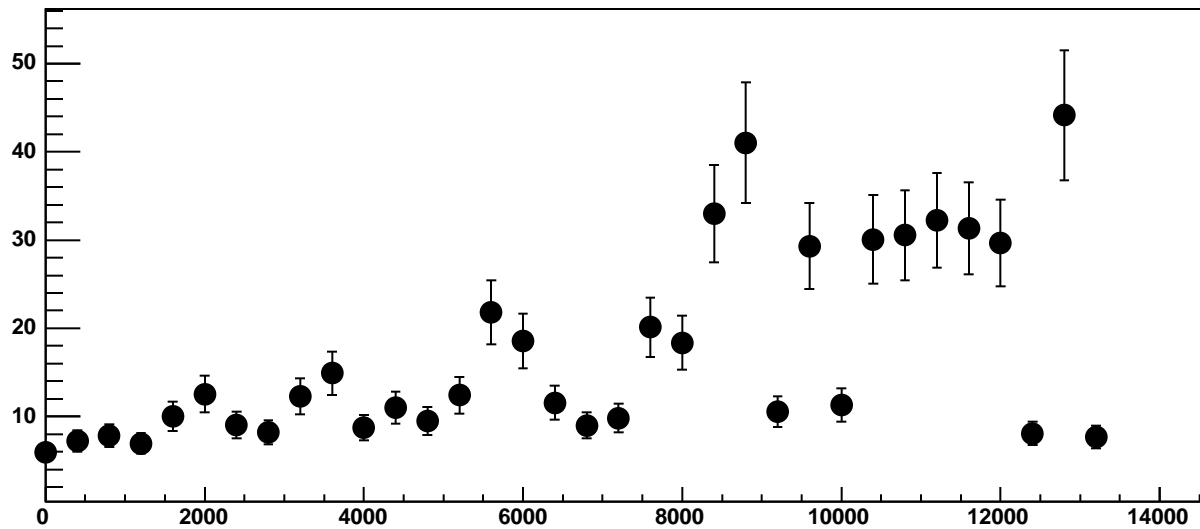
Chip 11, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



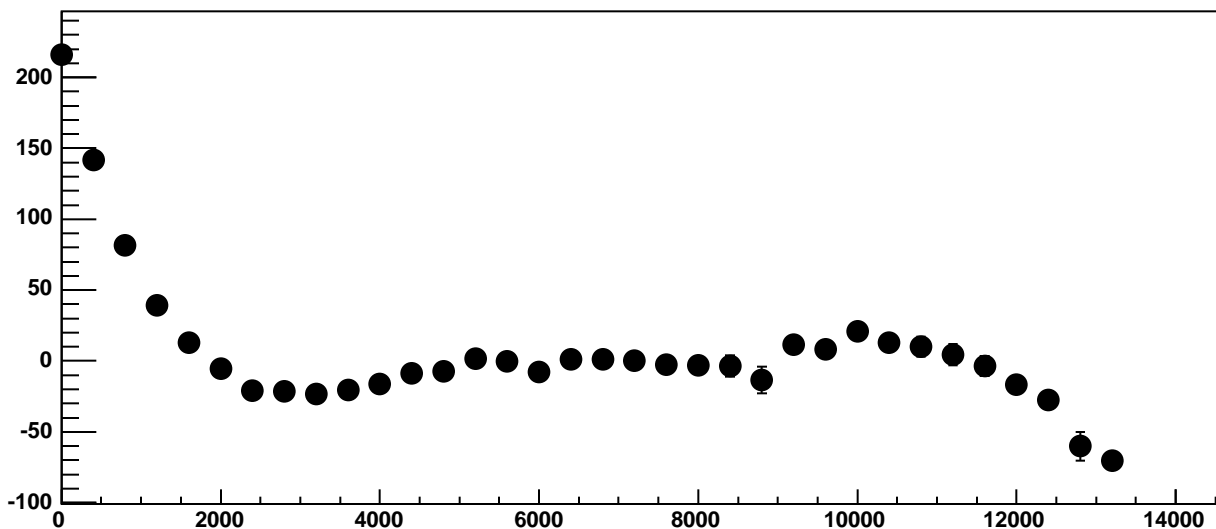
Chip 11, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



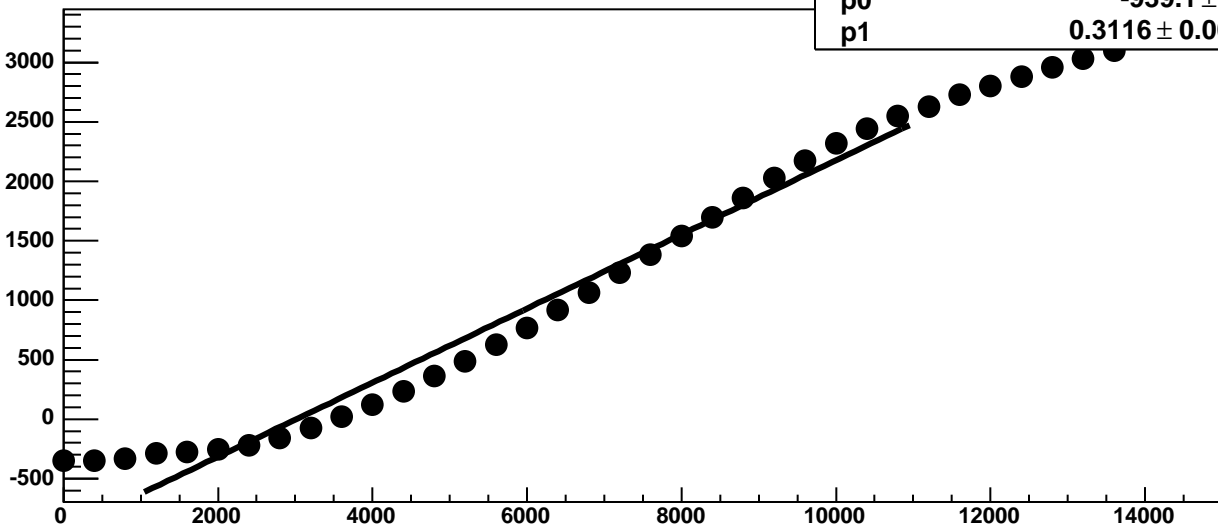
Chip 11, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



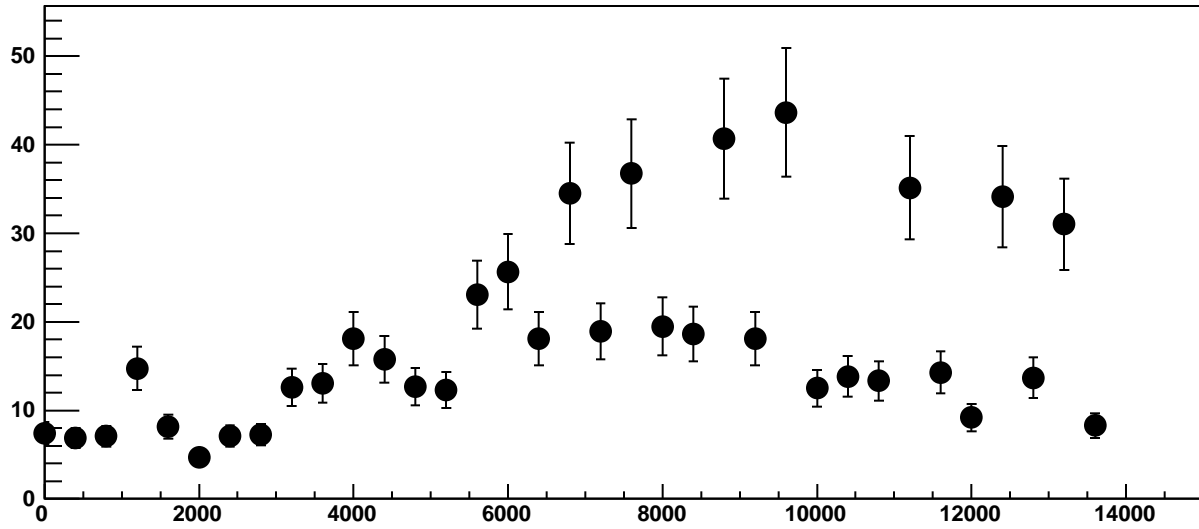
Chip 11, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



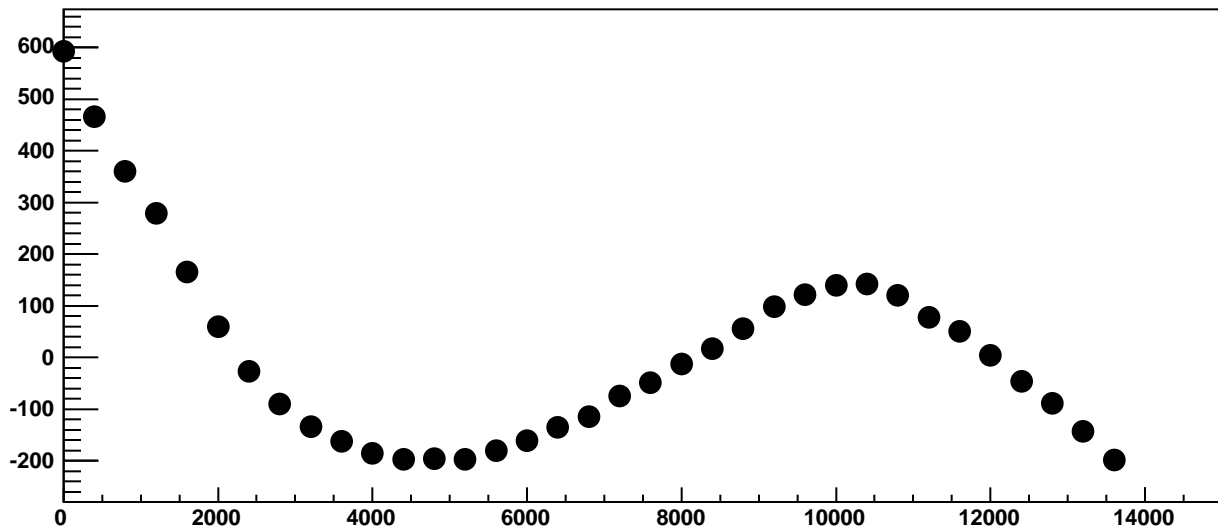
Chip 11, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC



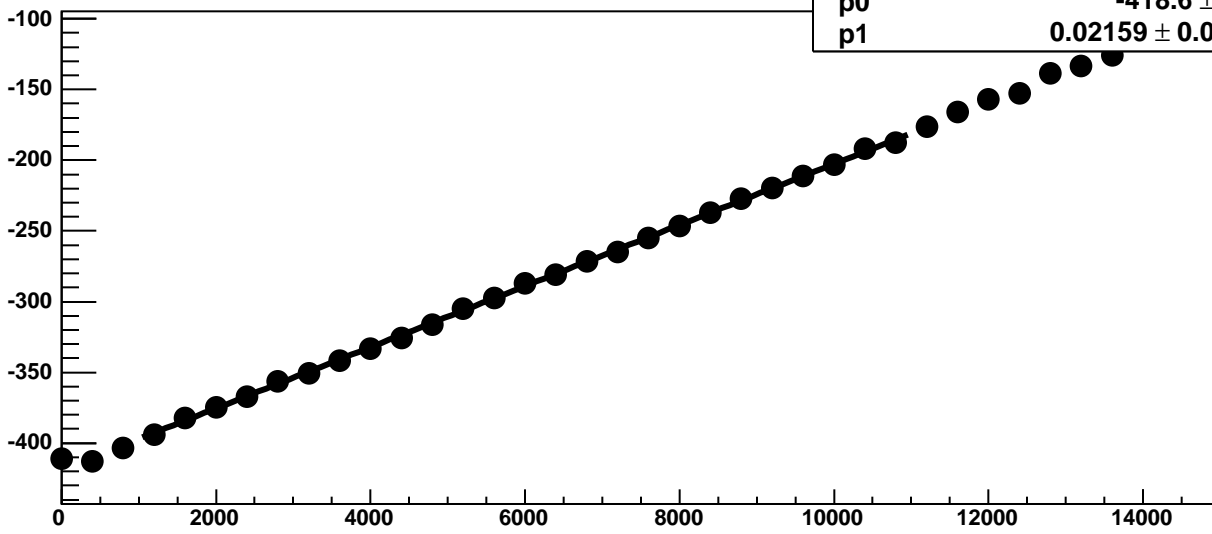
Chip 11, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

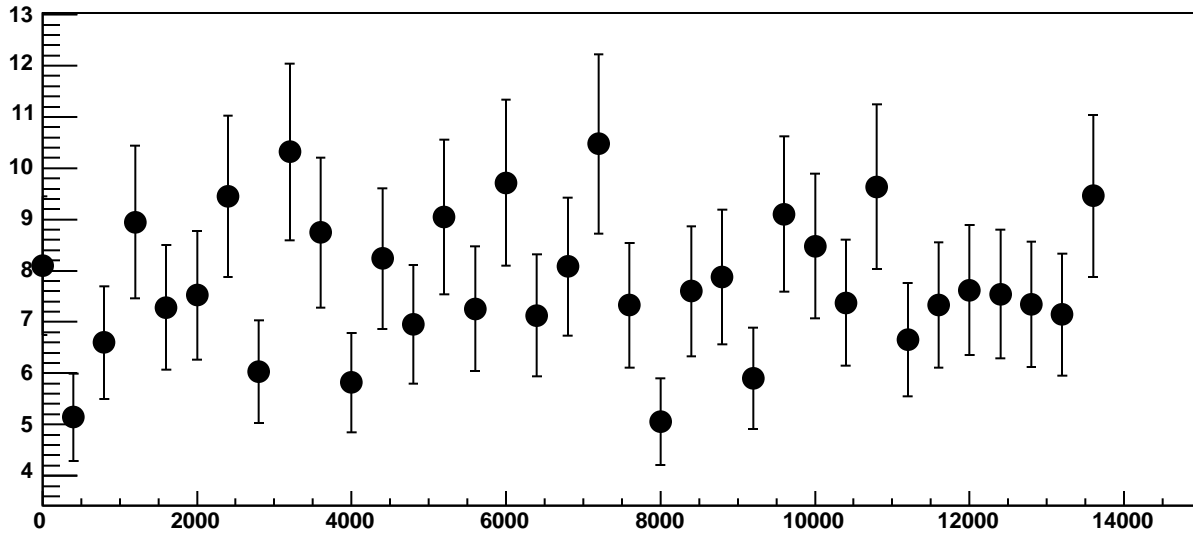


Chip 11, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

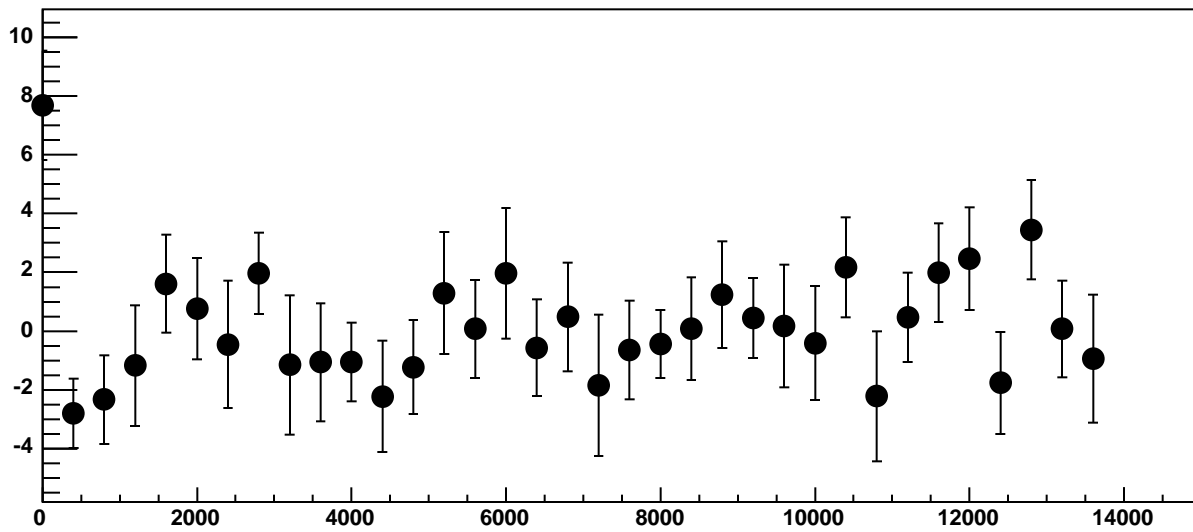


χ^2 / ndf 12.11 / 23
p0 -418.6 ± 0.8258
p1 0.02159 ± 0.0001237

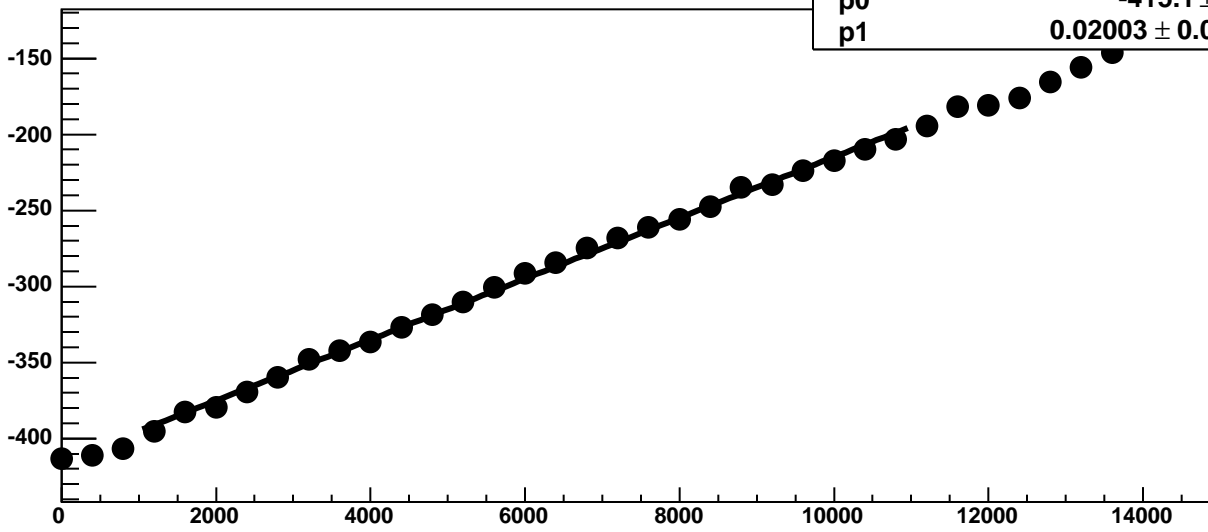
Chip 11, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

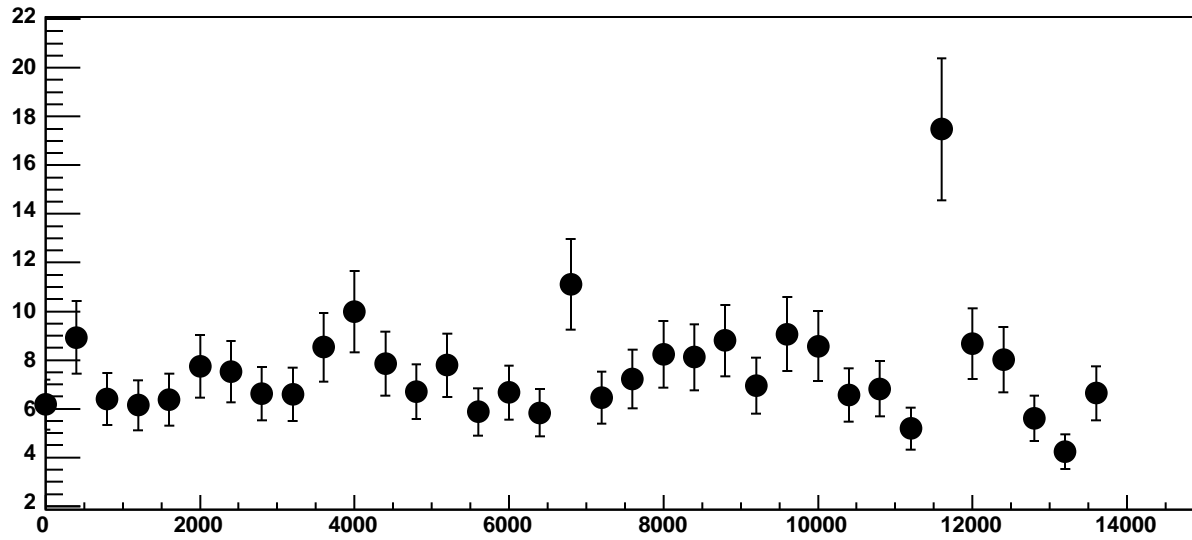


Chip 11, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

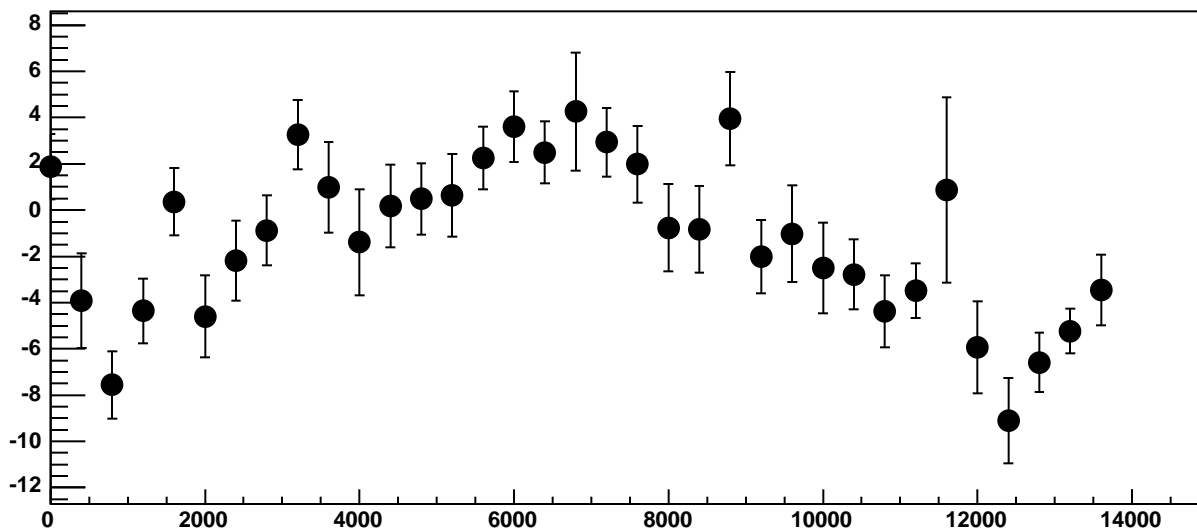


χ^2 / ndf 62.64 / 23
p0 -415.1 ± 0.7416
p1 0.02003 ± 0.0001138

Chip 11, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

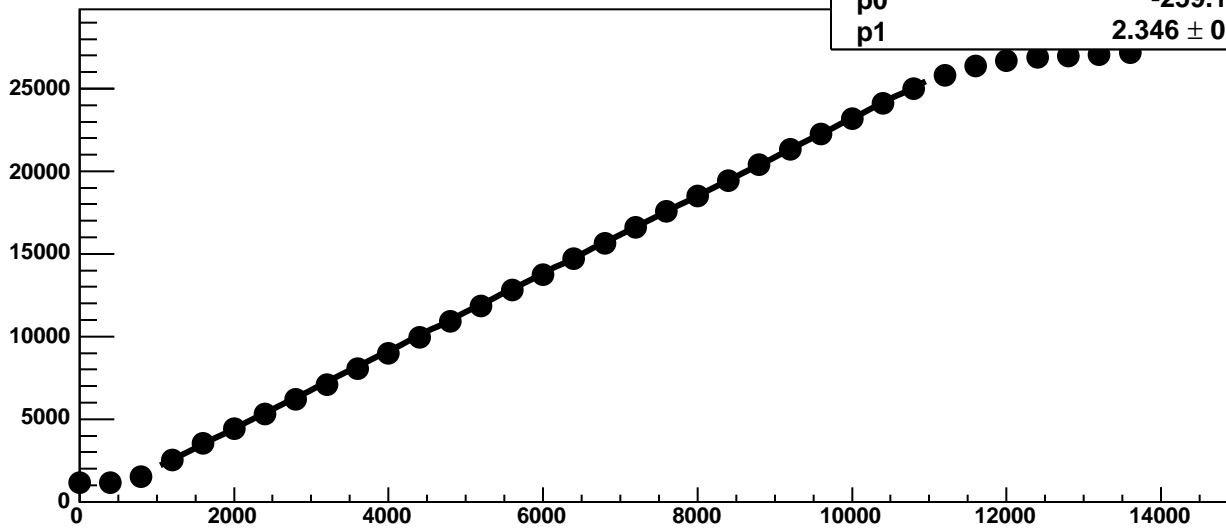


Chip 11, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

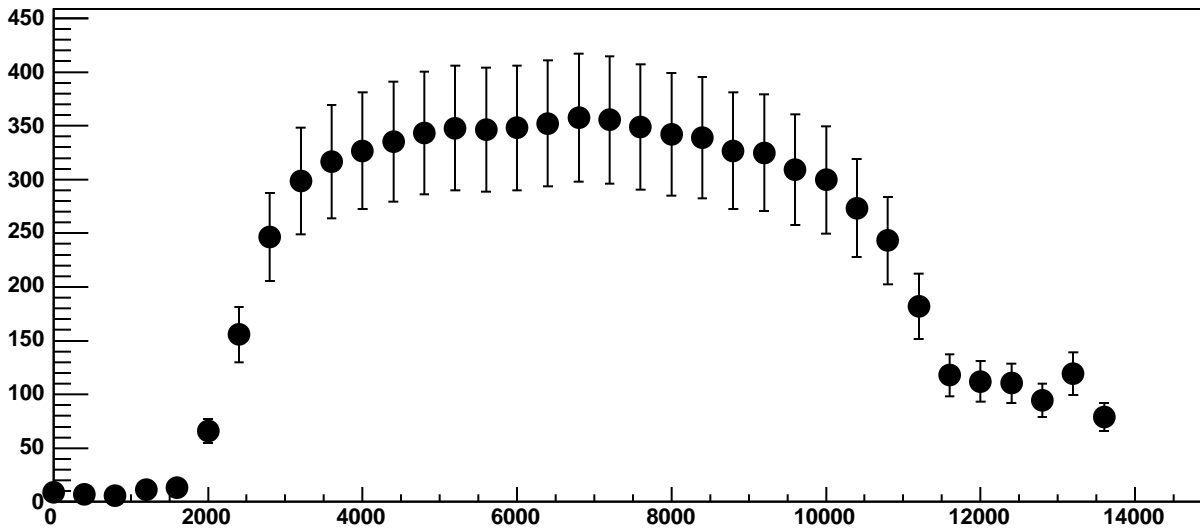


Chip 11, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC

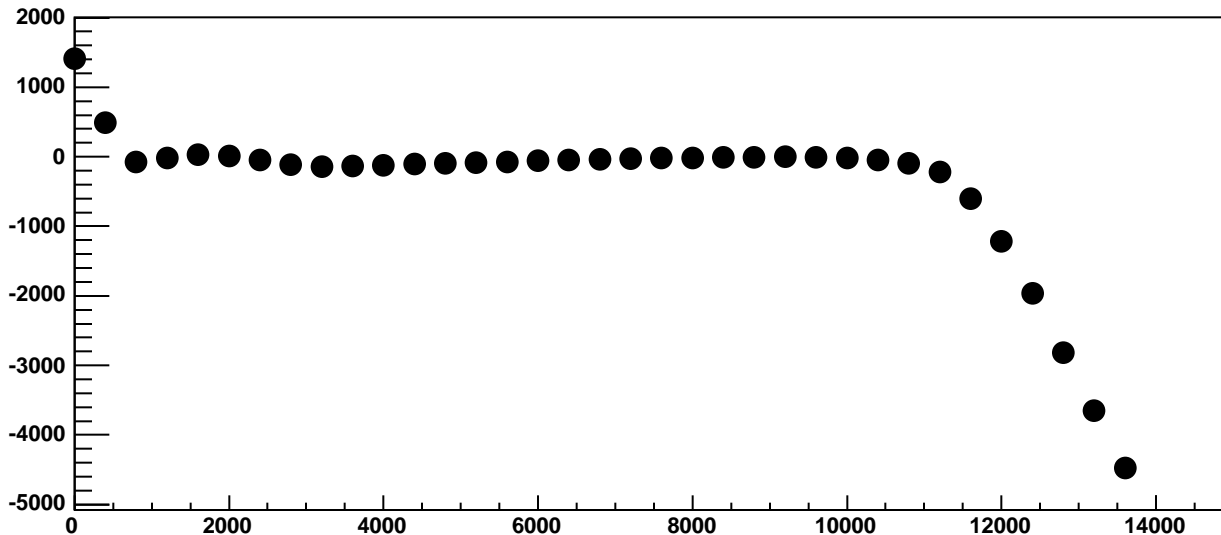
χ^2 / ndf 145.8 / 23
p0 -259.1 ± 4.184
p1 2.346 ± 0.002506



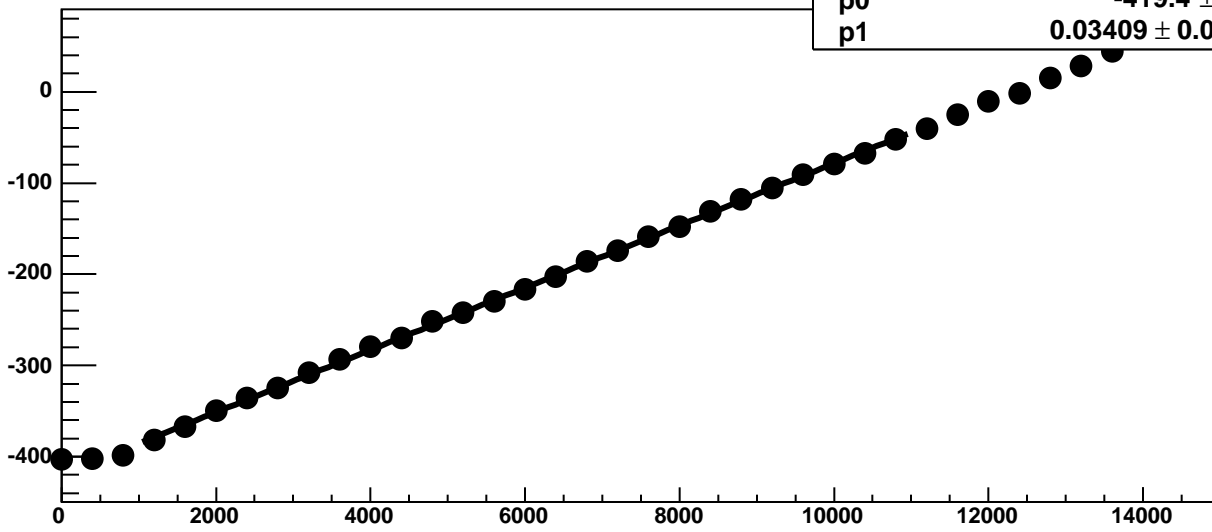
Chip 11, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC

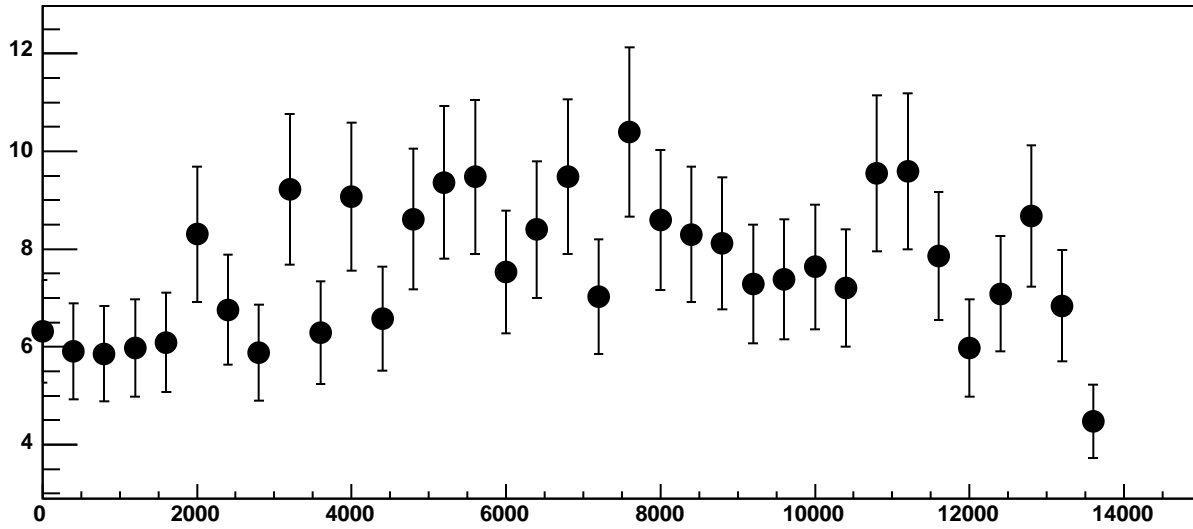


Chip 11, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

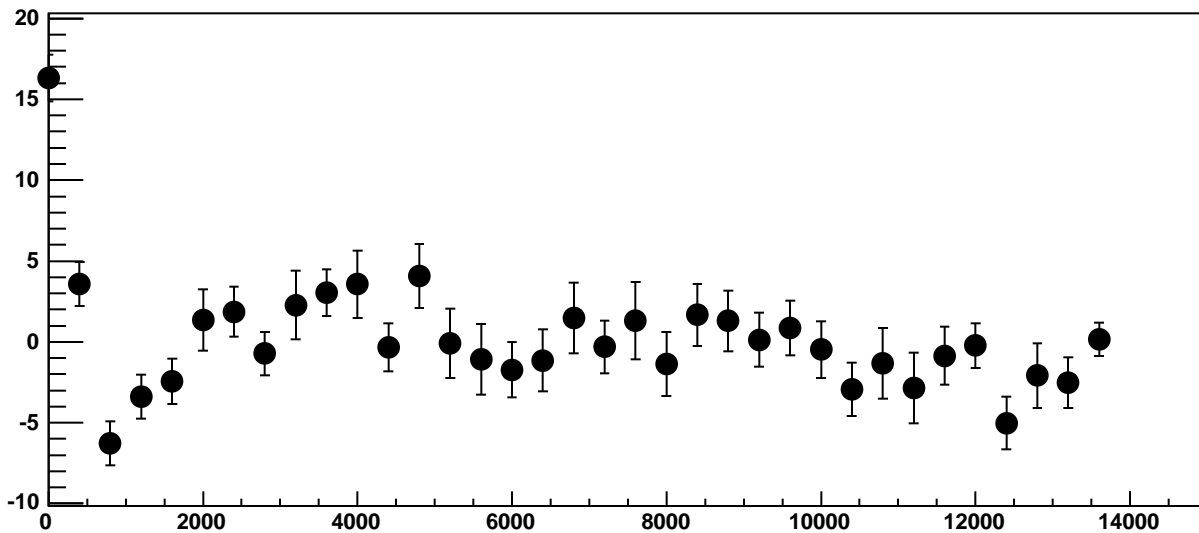


χ^2 / ndf 32.28 / 23
p0 -419.4 ± 0.7349
p1 0.03409 ± 0.0001157

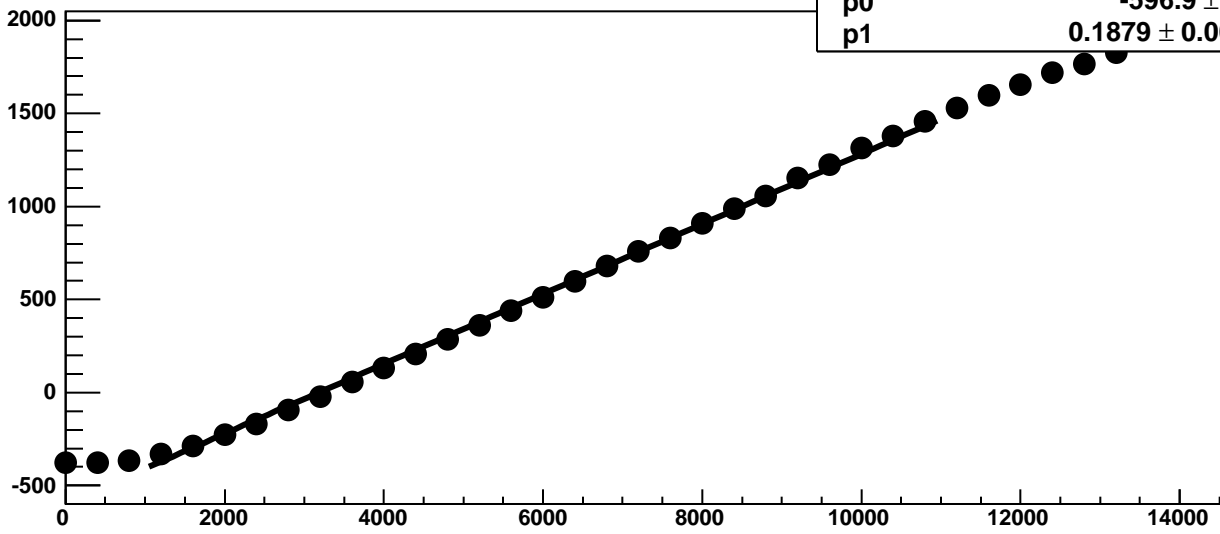
Chip 11, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 11, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

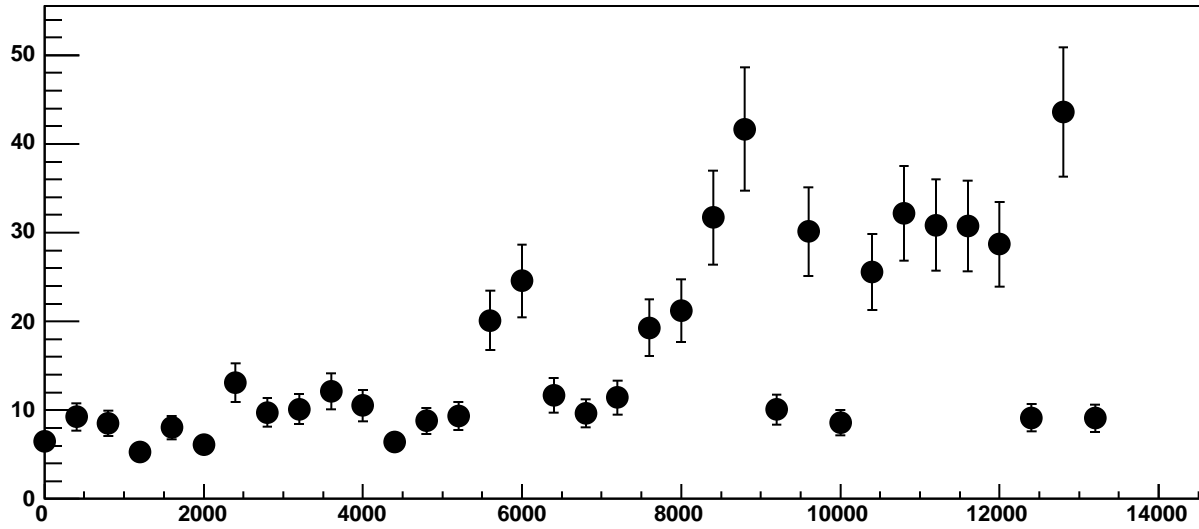


Chip 11, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

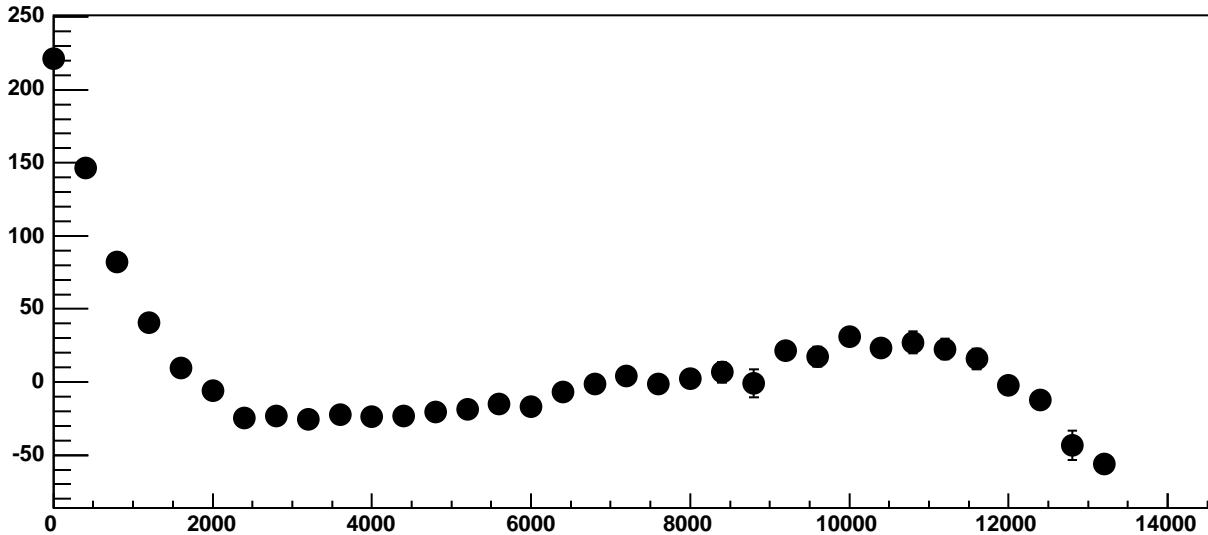


χ^2 / ndf 2434 / 23
p0 -596.9 ± 0.8902
p1 0.1879 ± 0.0001744

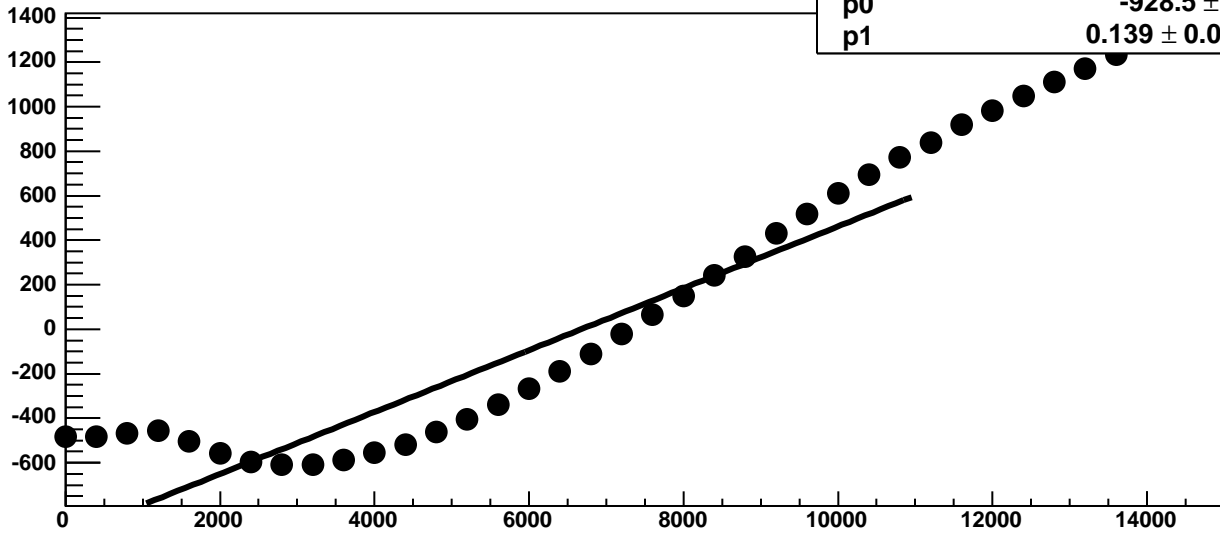
Chip 11, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



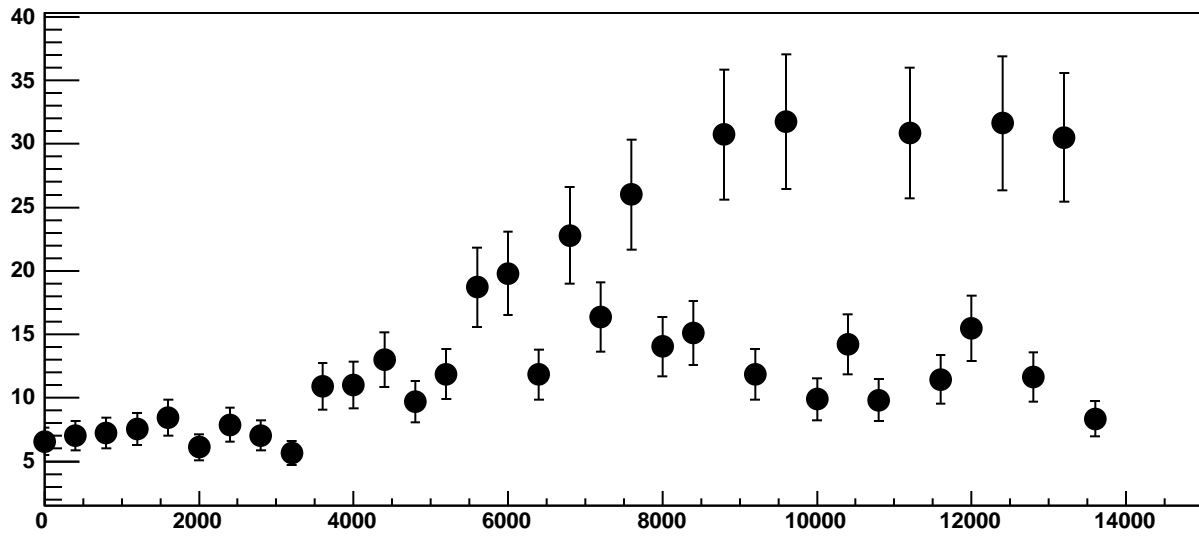
Chip 11, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



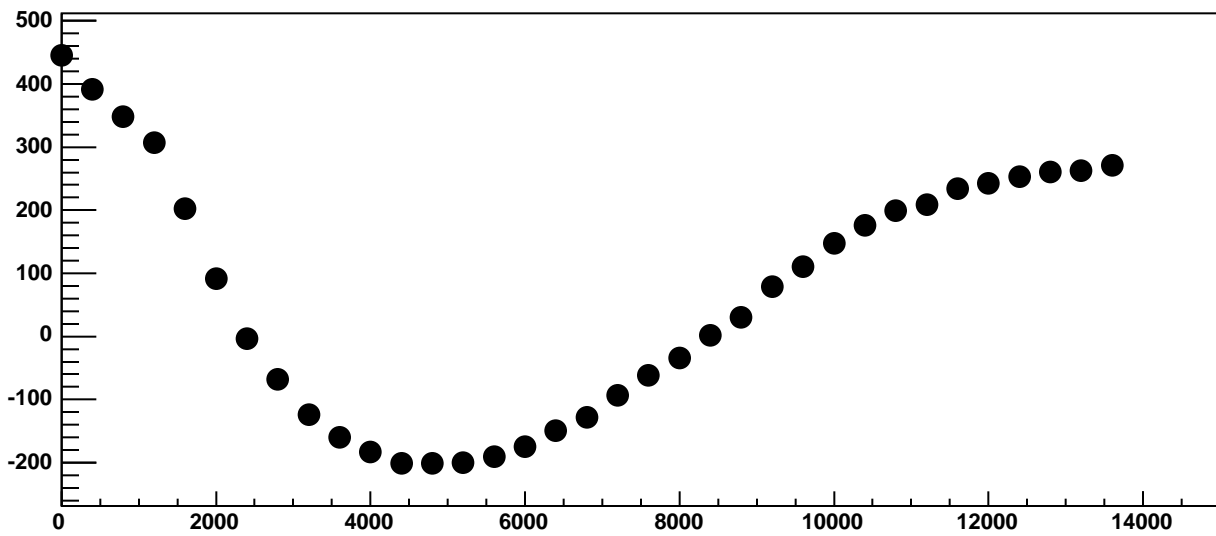
Chip 11, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



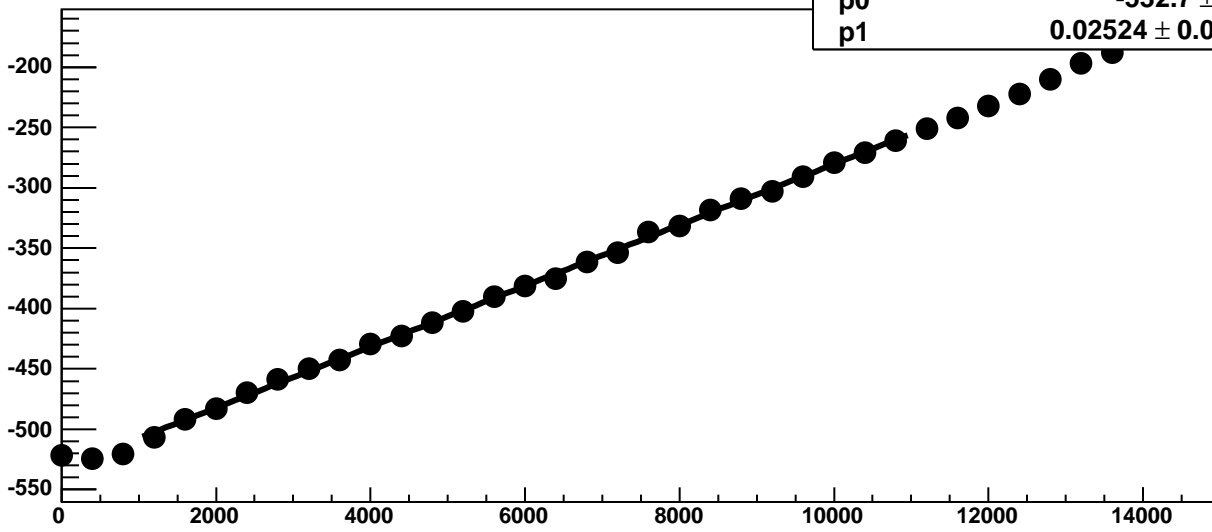
Chip 11, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



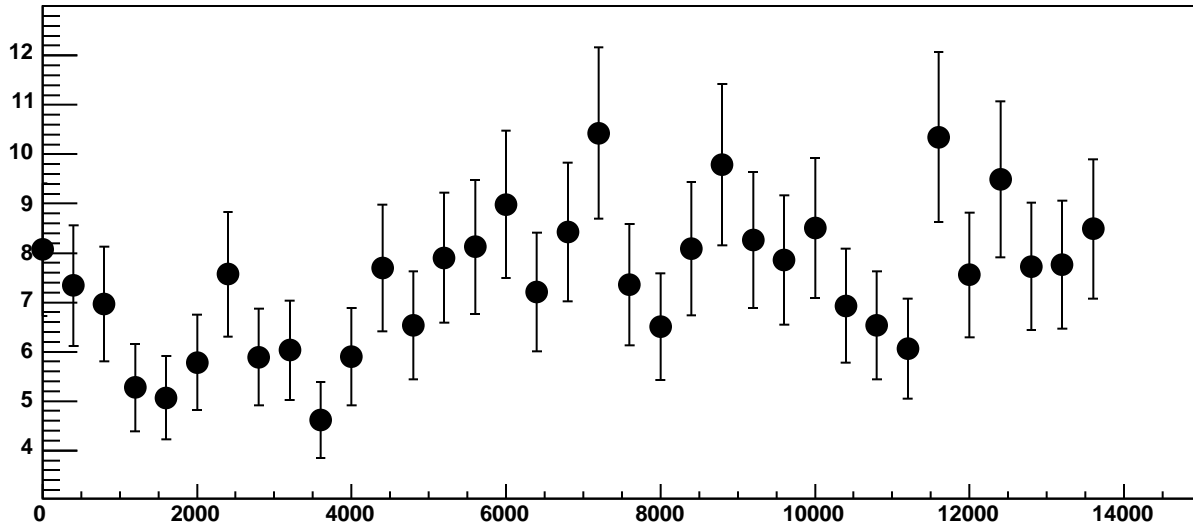
Chip 11, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



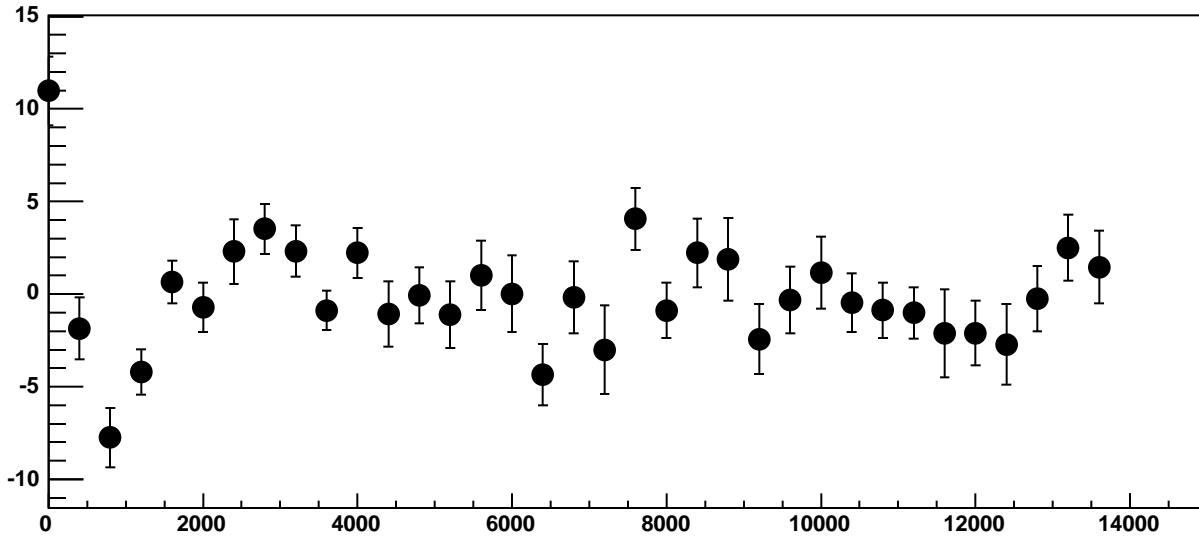
Chip 11, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC



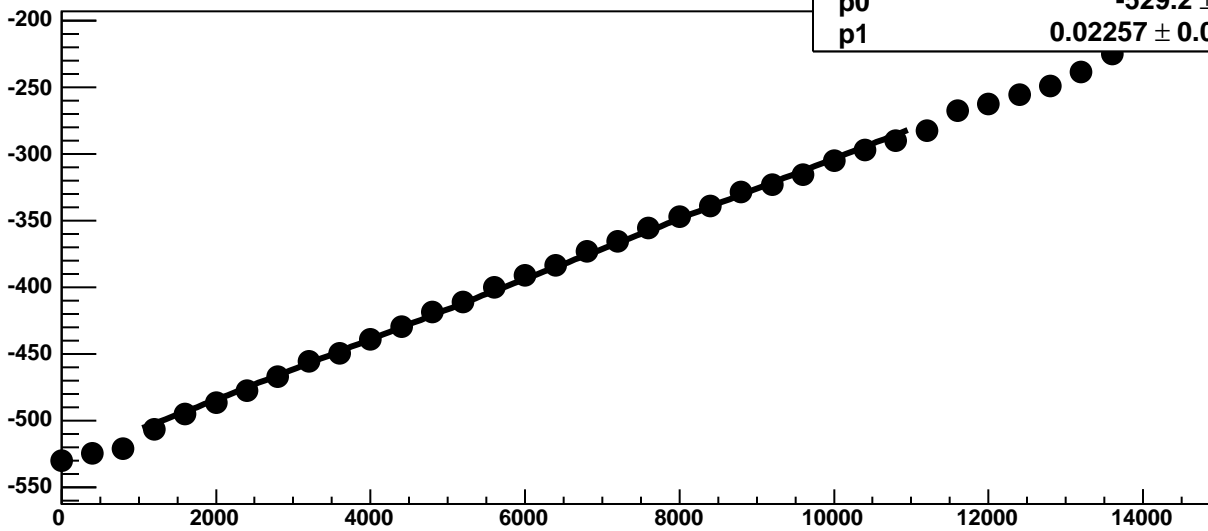
Chip 11, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



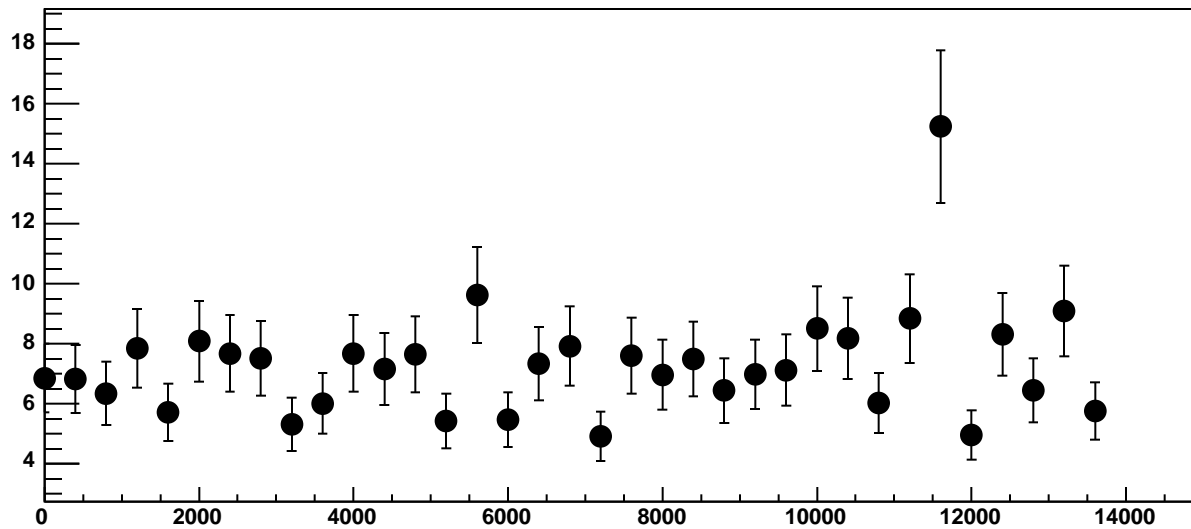
Chip 11, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC



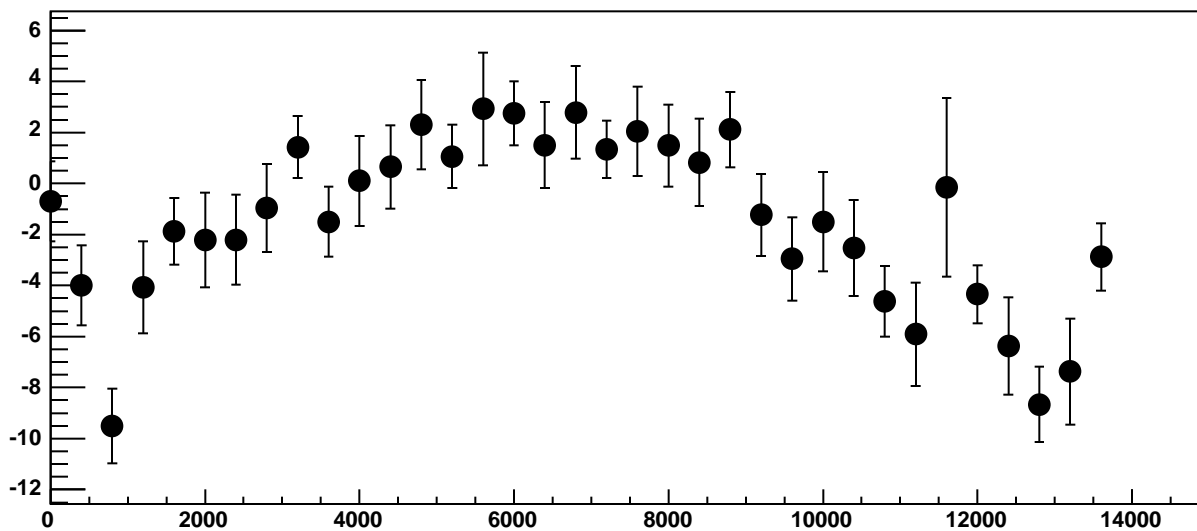
Chip 11, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



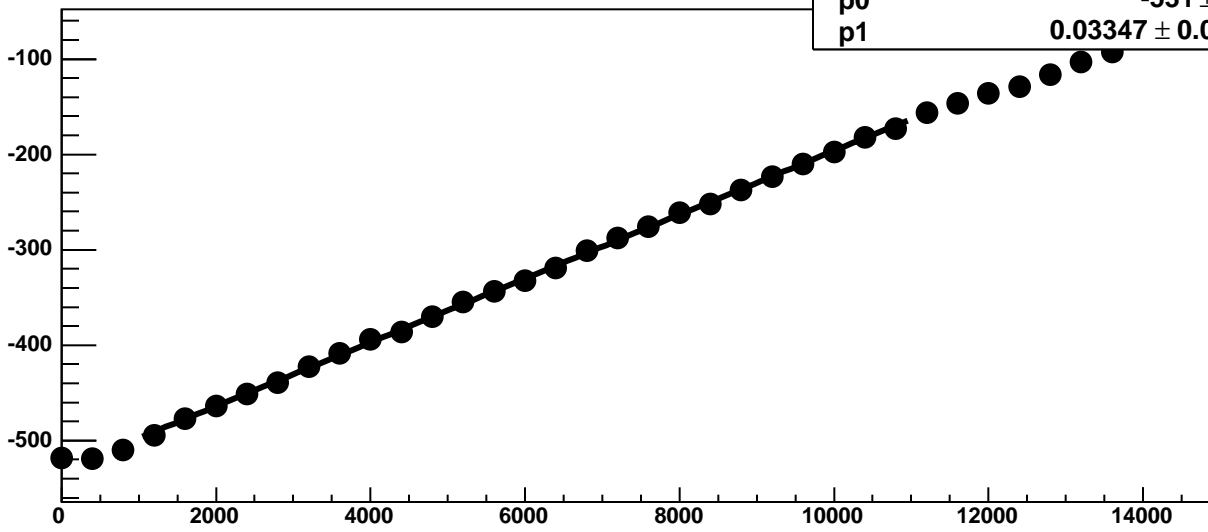
Chip 11, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

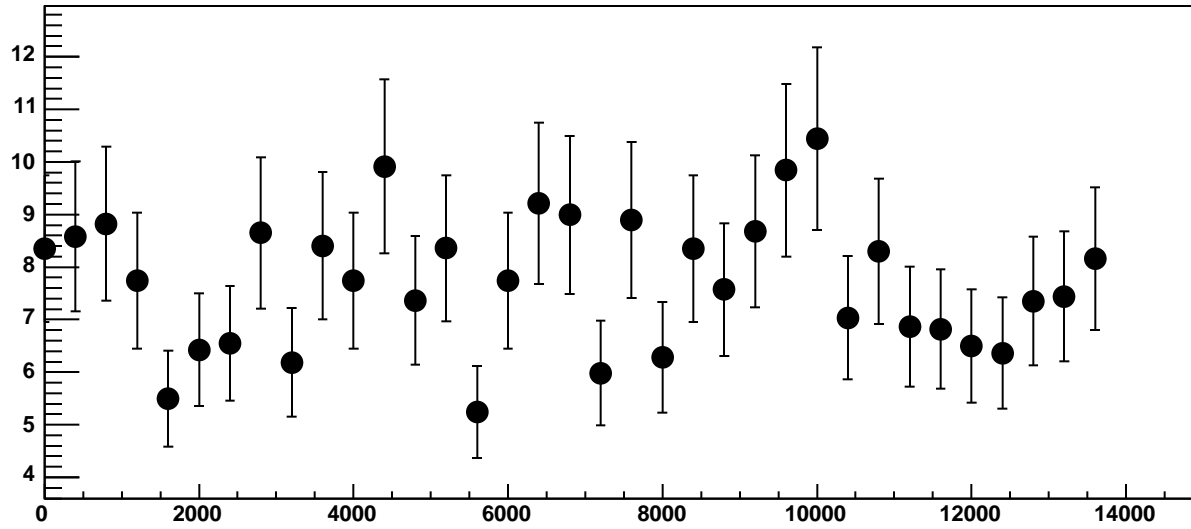


Chip 11, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

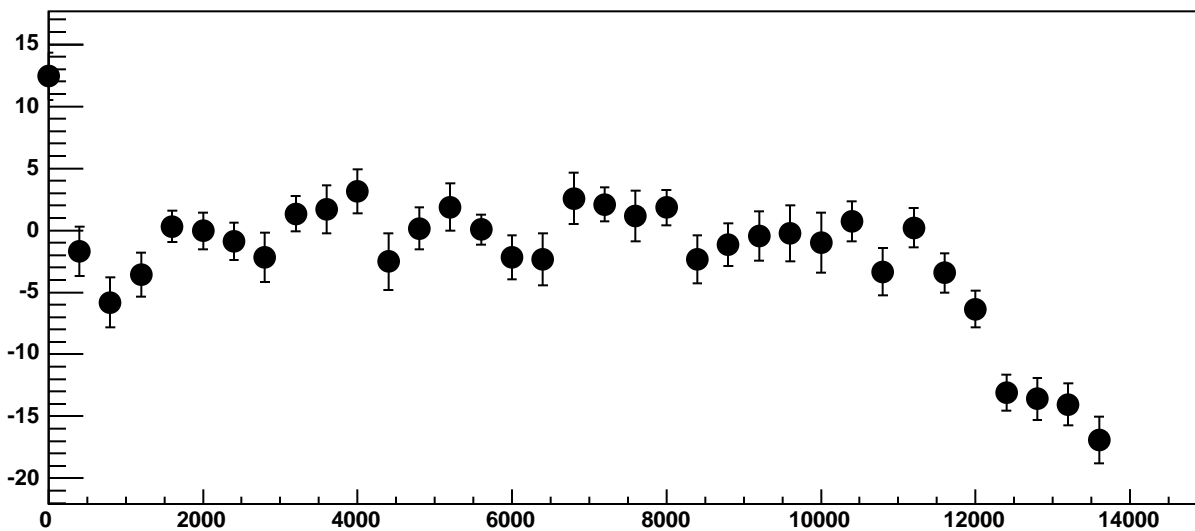


χ^2 / ndf 26.63 / 23
p0 -531 ± 0.7468
p1 0.03347 ± 0.0001185

Chip 11, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC

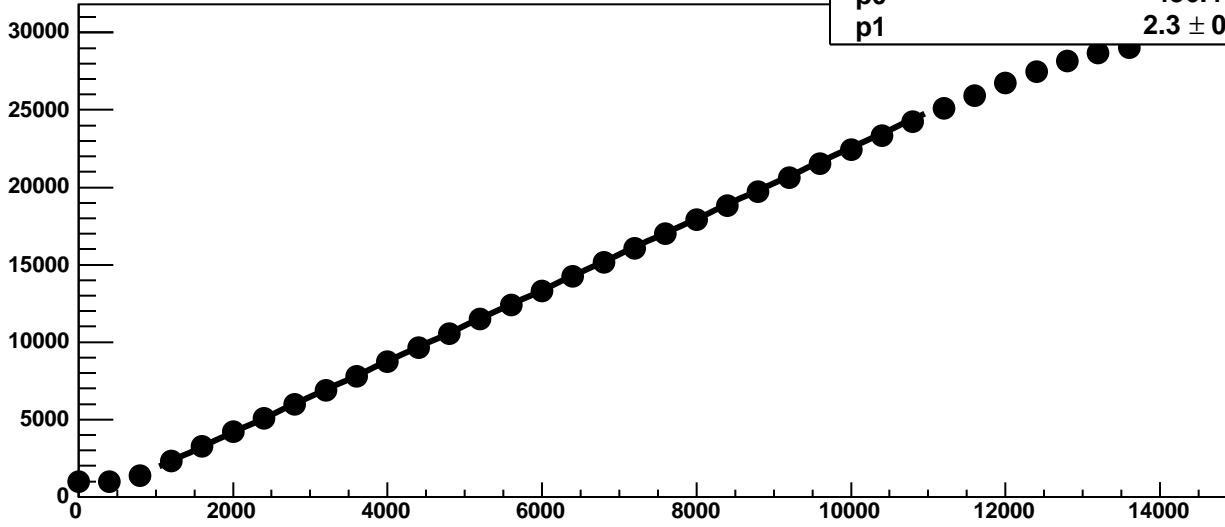


Chip 11, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

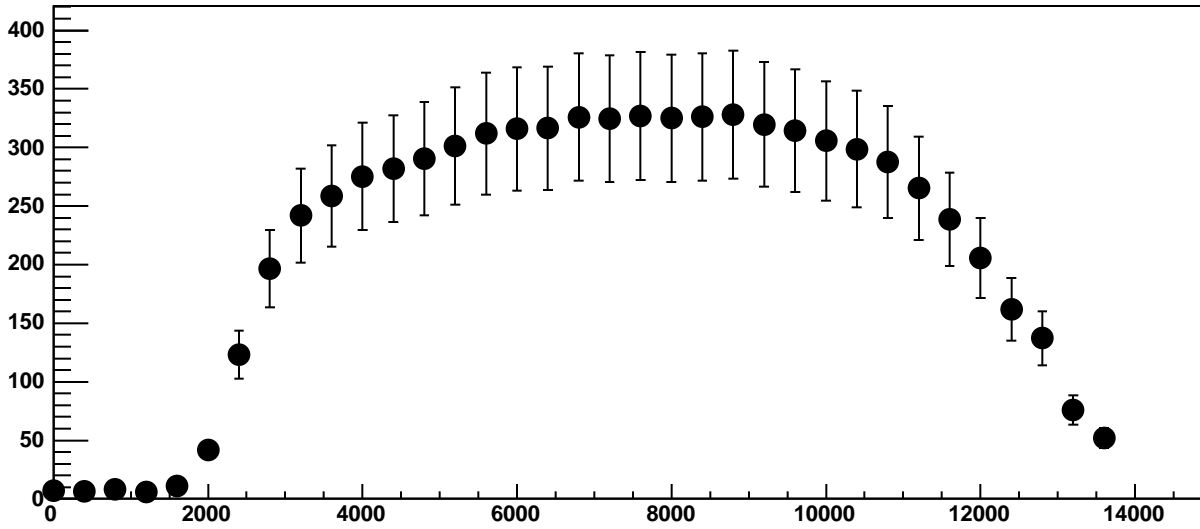


Chip 11, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC

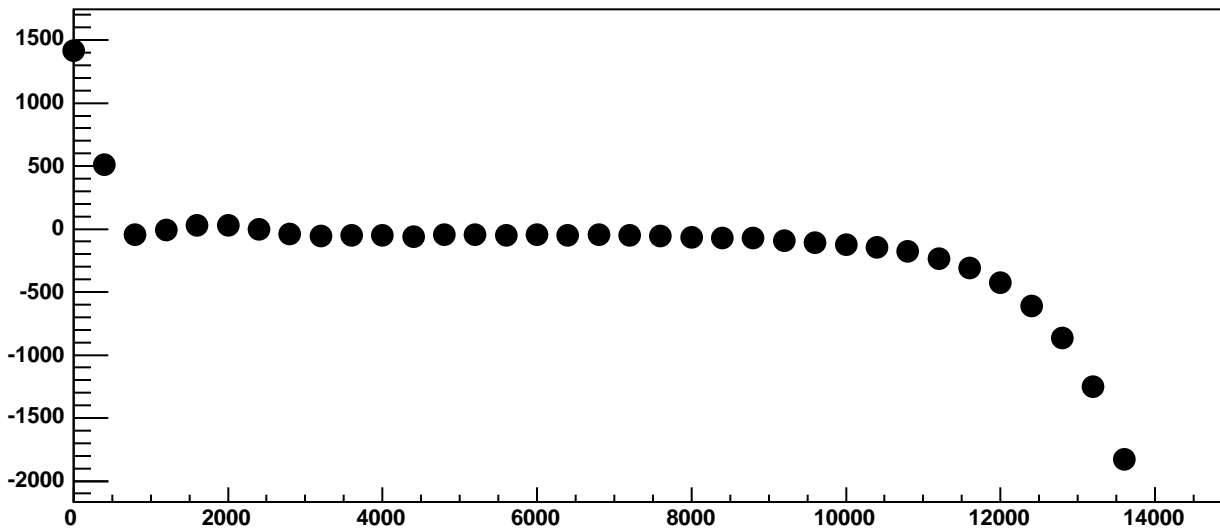
χ^2 / ndf 196.6 / 23
p0 -436.4 ± 3.407
p1 2.3 ± 0.002381



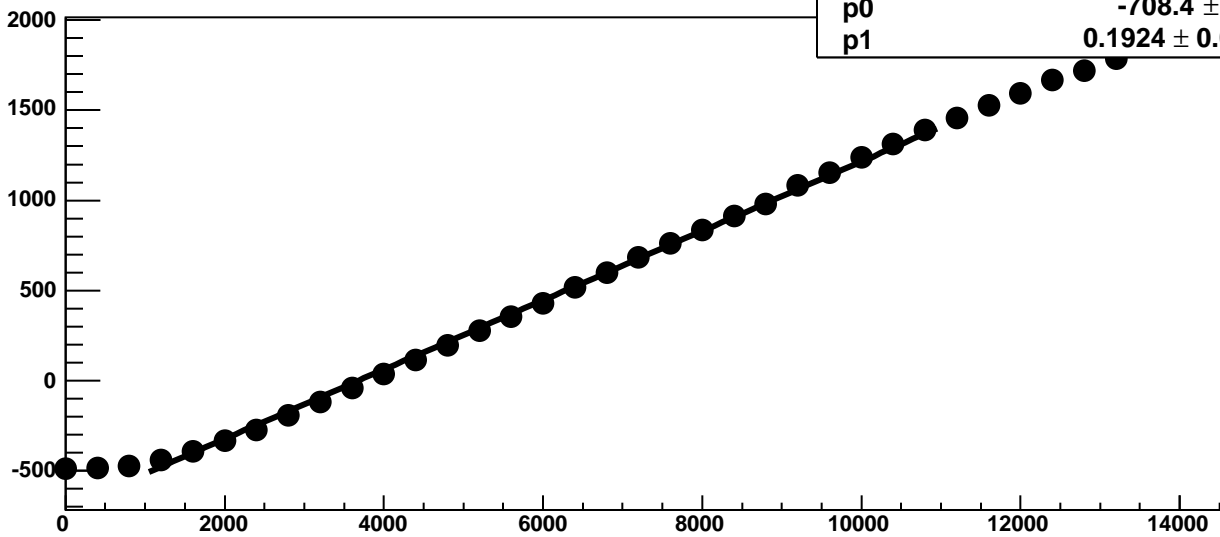
Chip 11, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



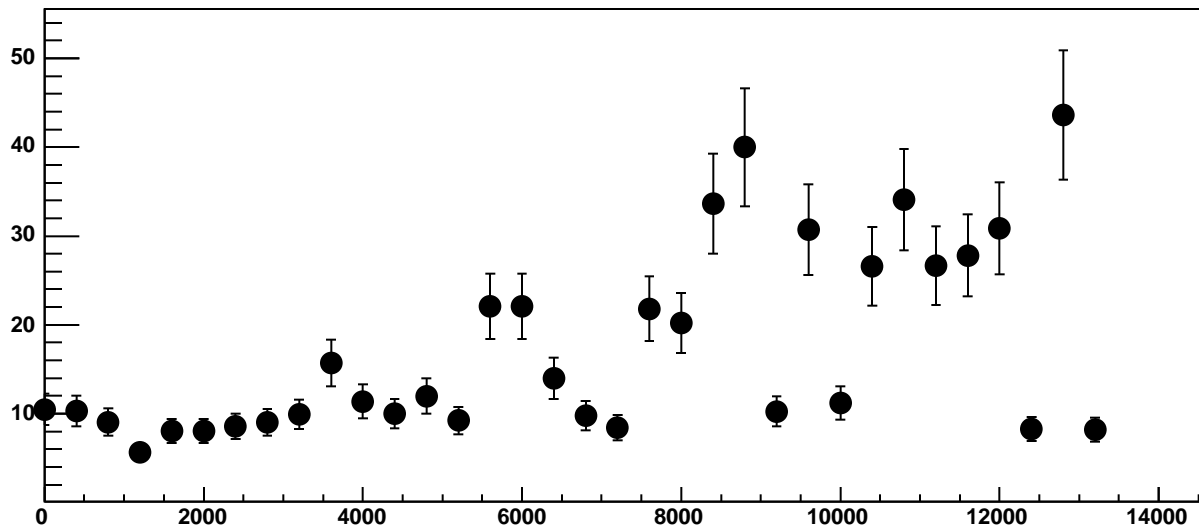
Chip 11, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC



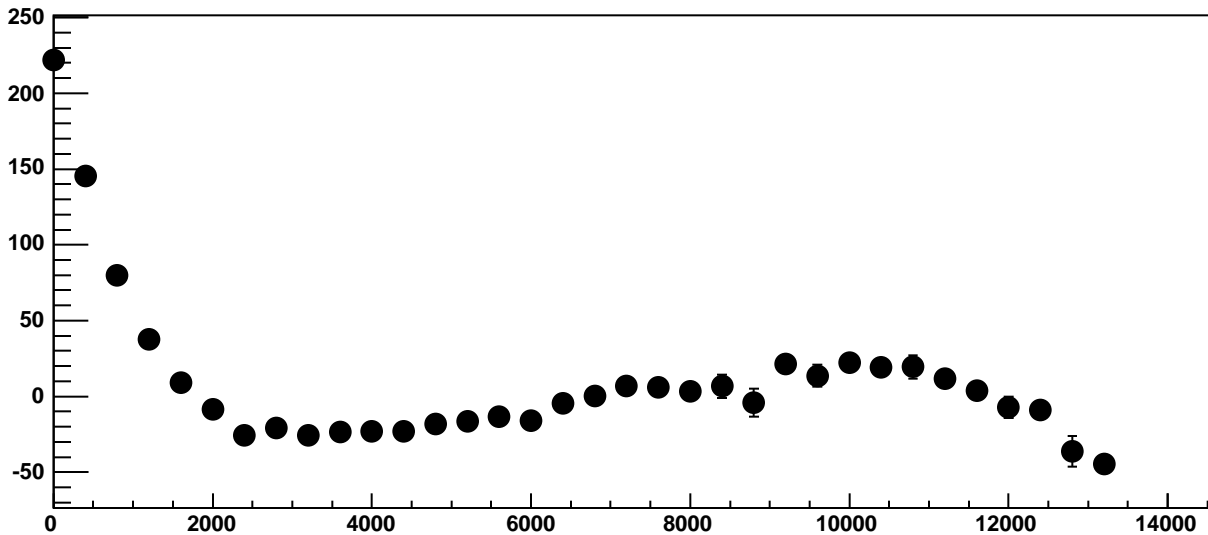
Chip 11, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



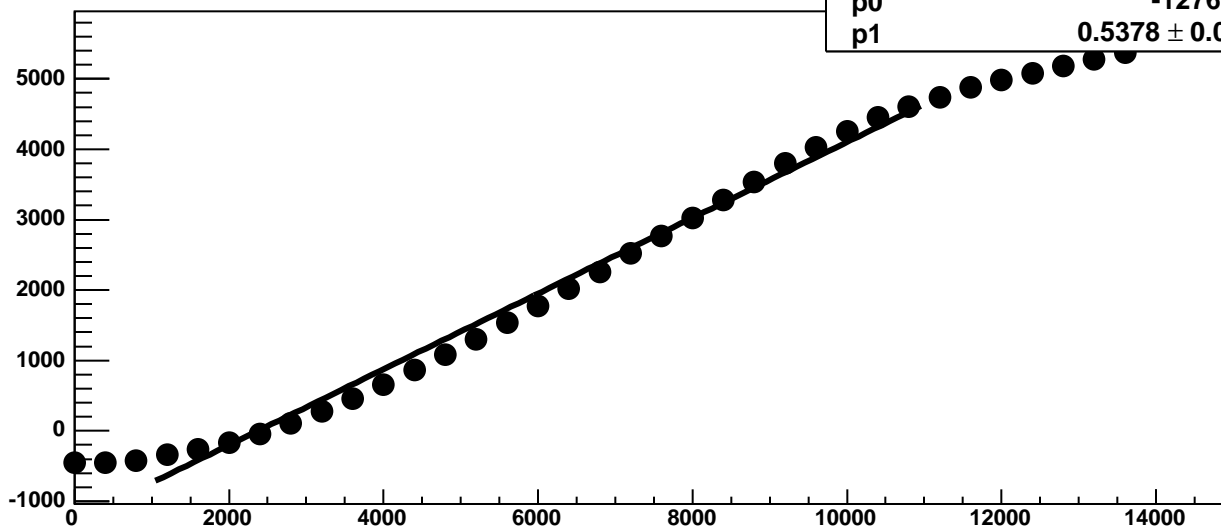
Chip 11, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



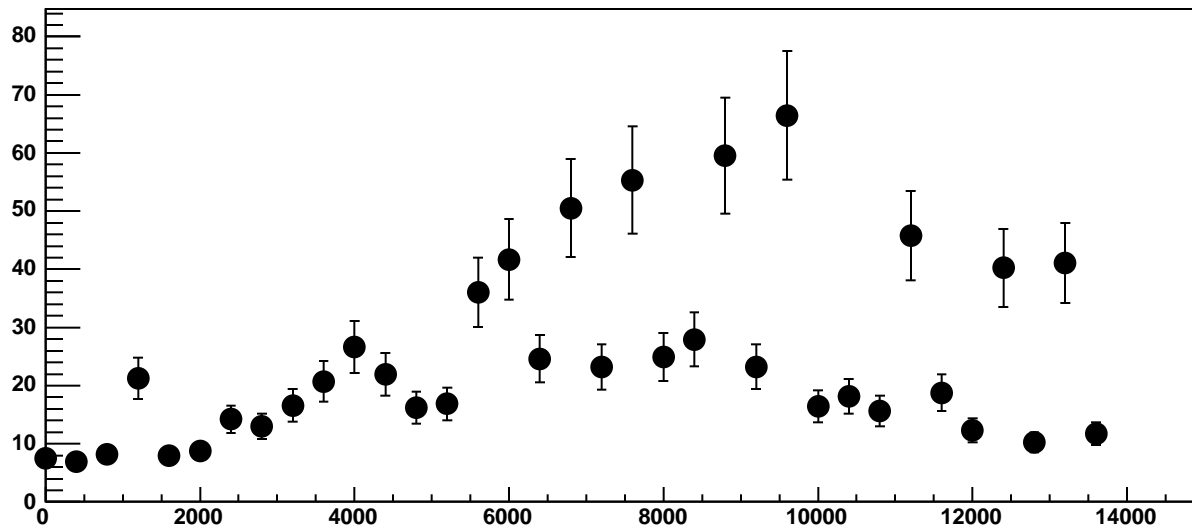
Chip 11, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



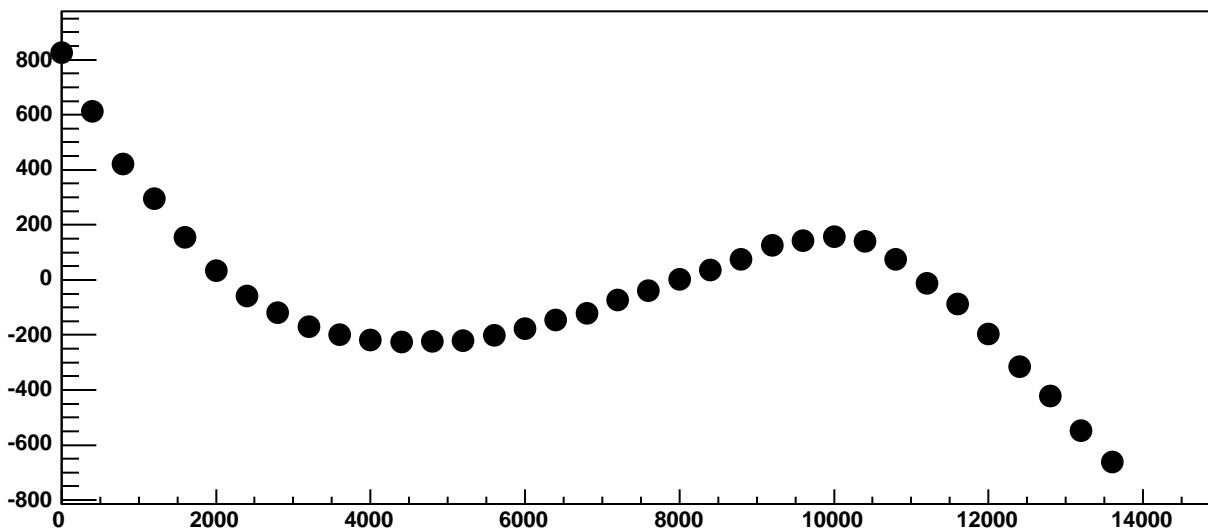
Chip 11, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



Chip 11, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

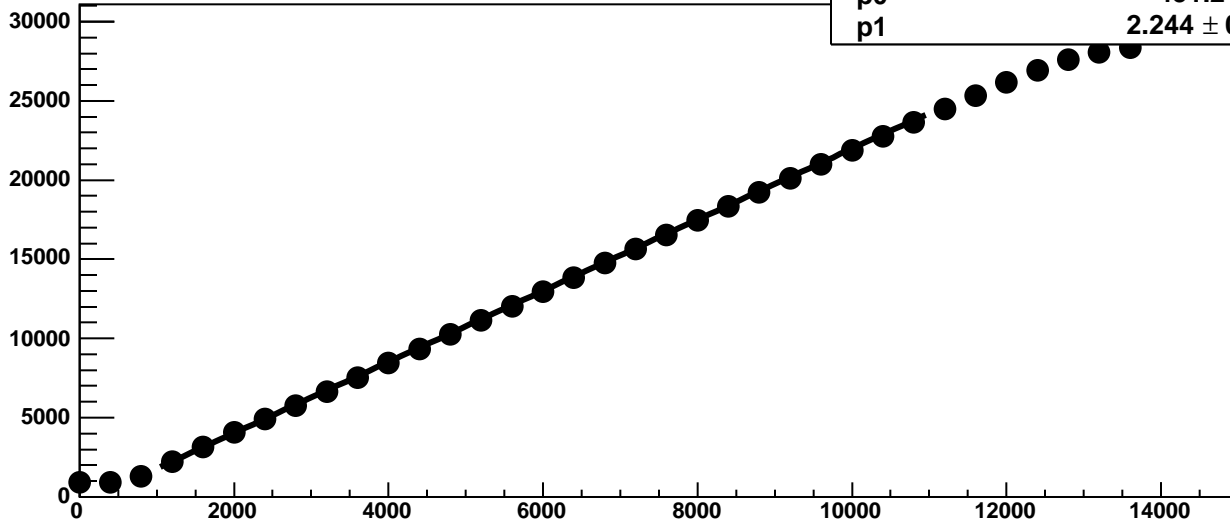
173 / 23

p0

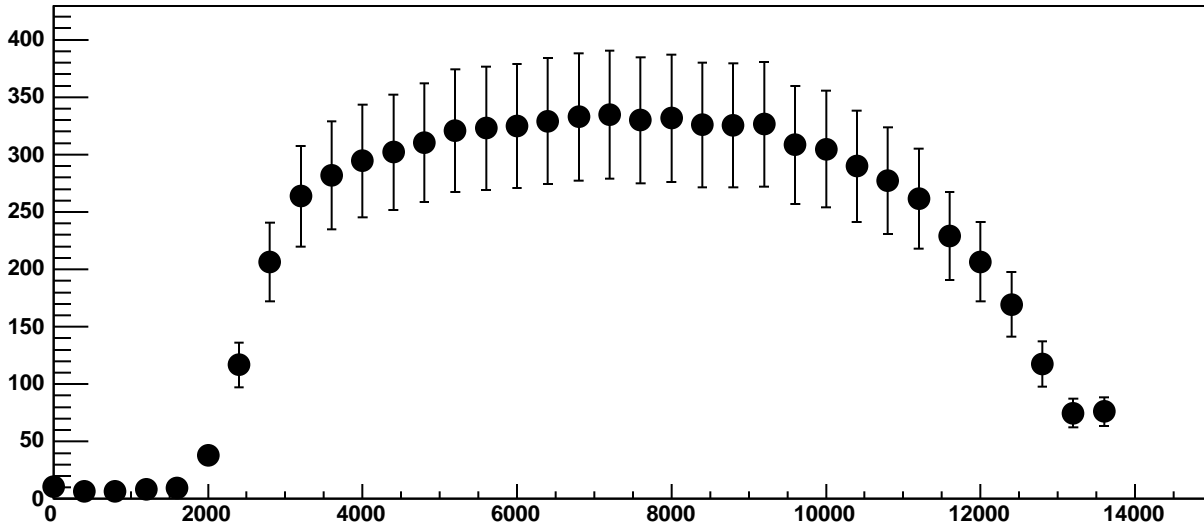
-451.2 ± 3.786

p1

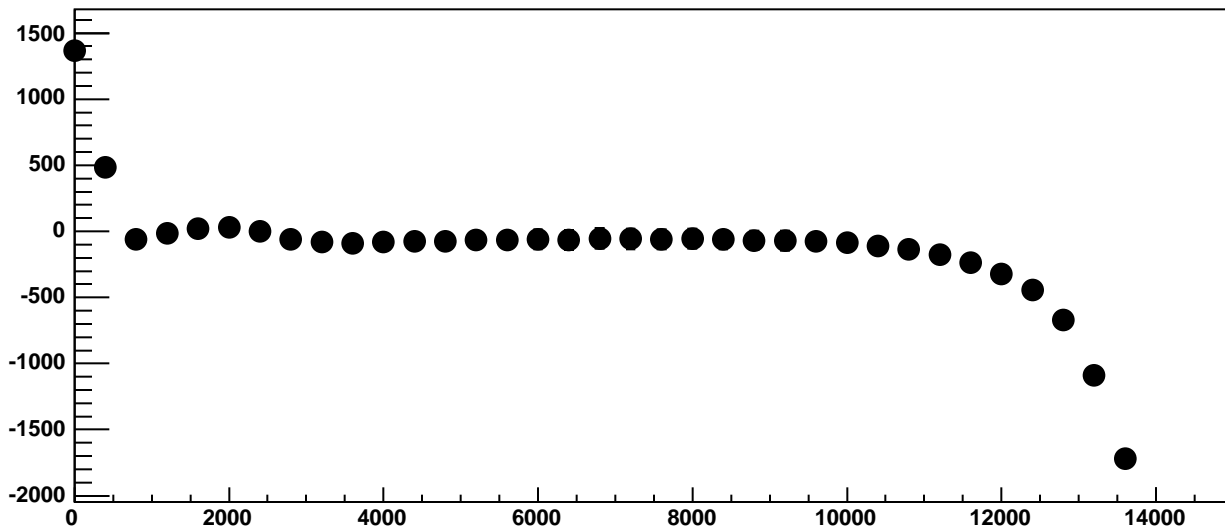
2.244 ± 0.00243



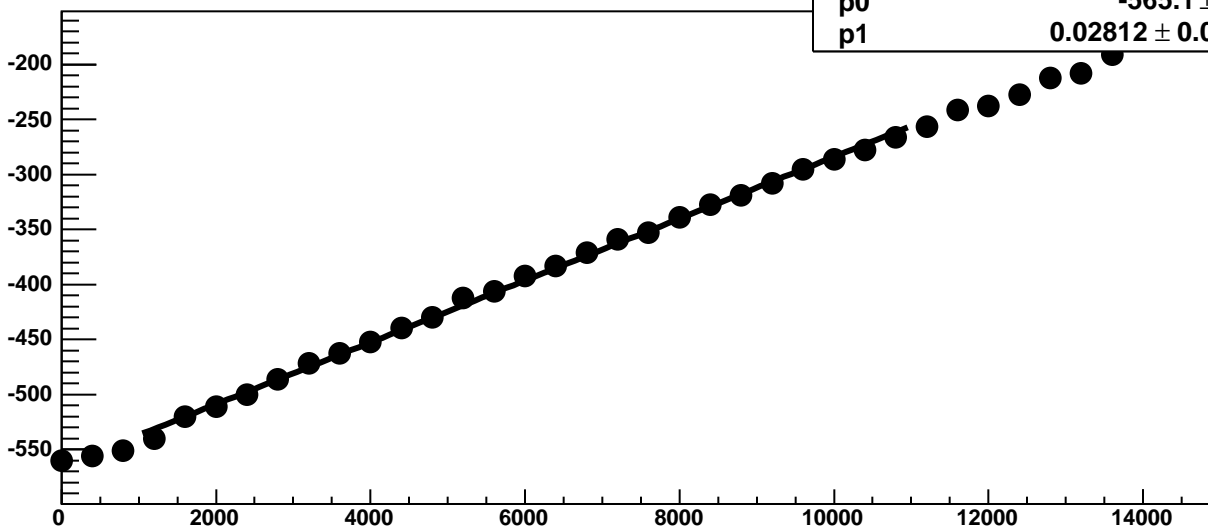
Chip 11, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC

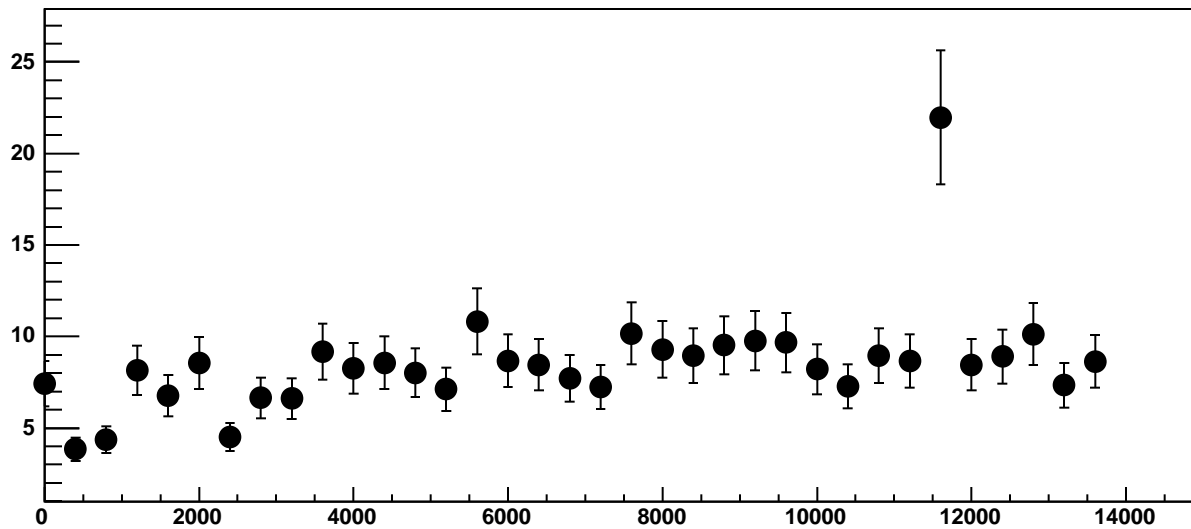


Chip 11, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

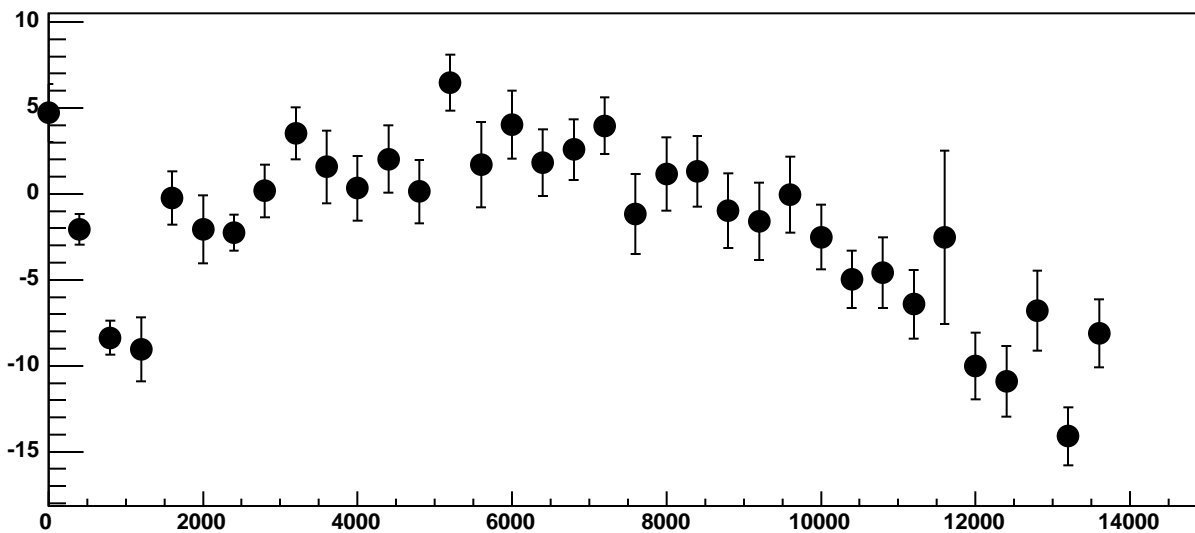


χ^2 / ndf 82.53 / 23
p0 -565.1 ± 0.7556
p1 0.02812 ± 0.0001227

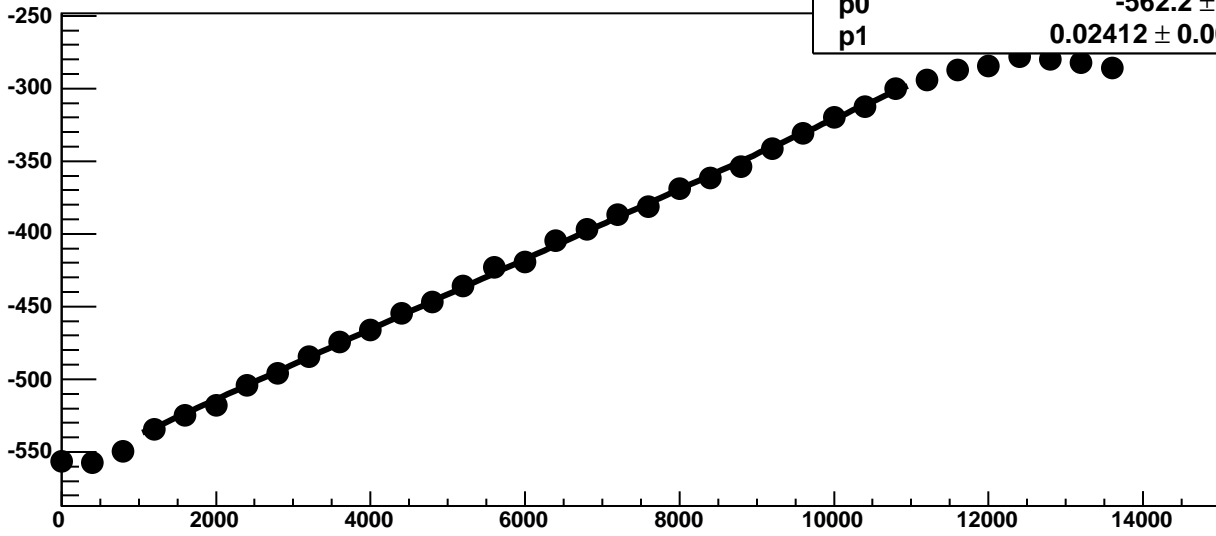
Chip 11, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

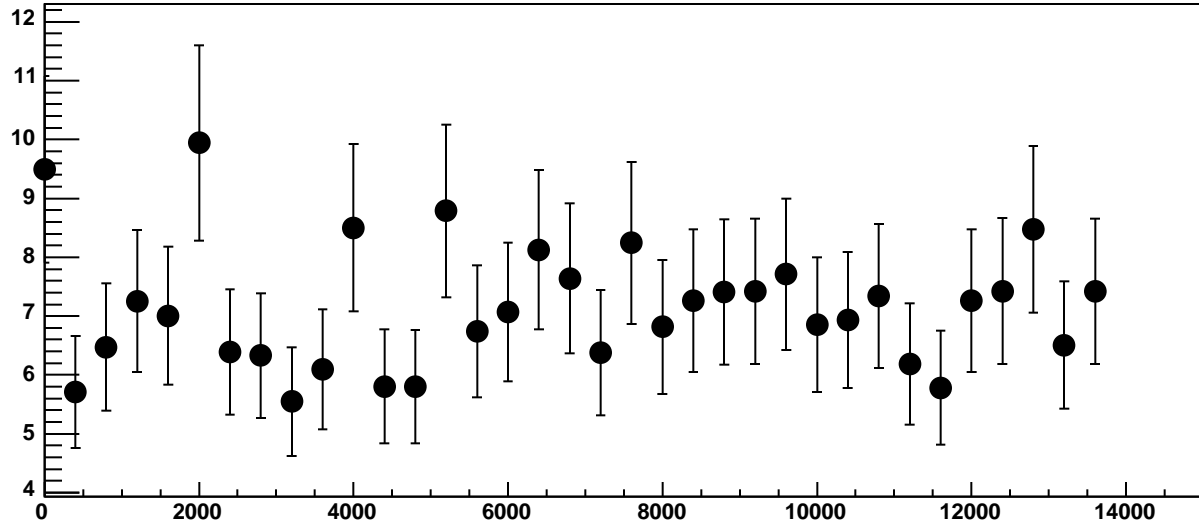


Chip 11, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

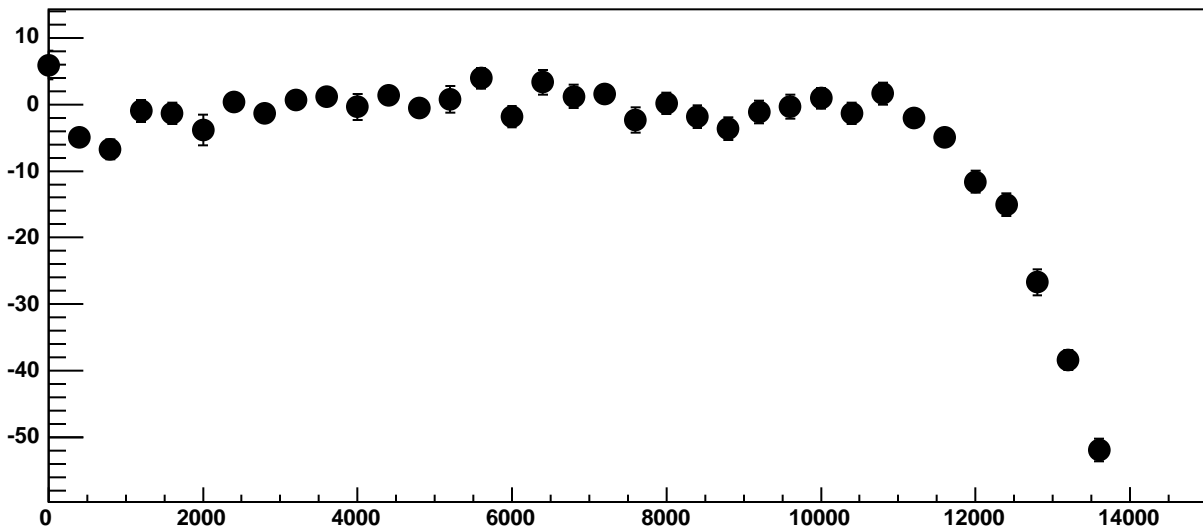


χ^2 / ndf 29.56 / 23
p0 -562.2 ± 0.7324
p1 0.02412 ± 0.0001126

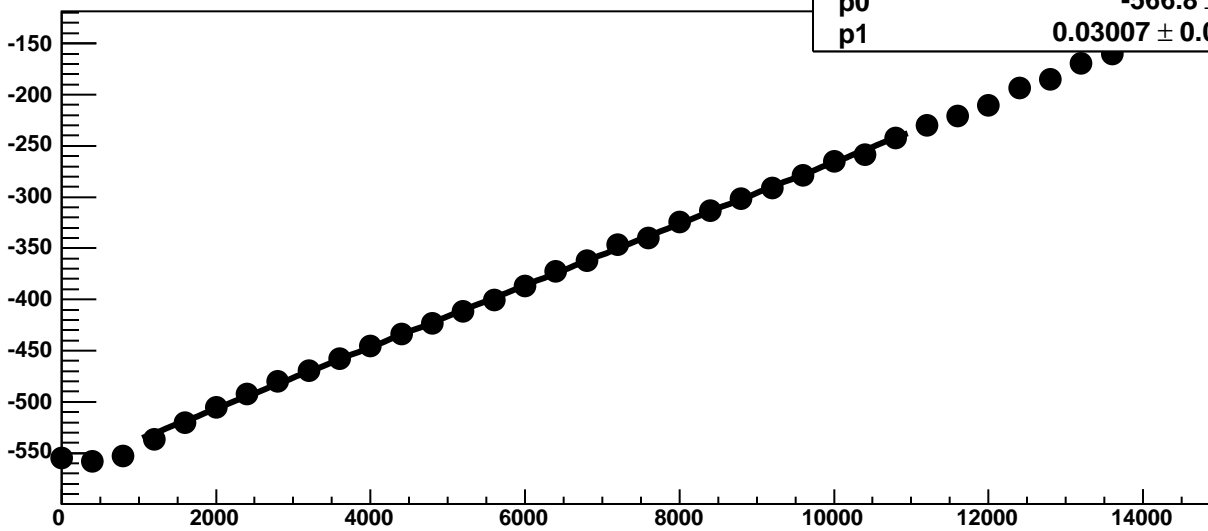
Chip 11, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

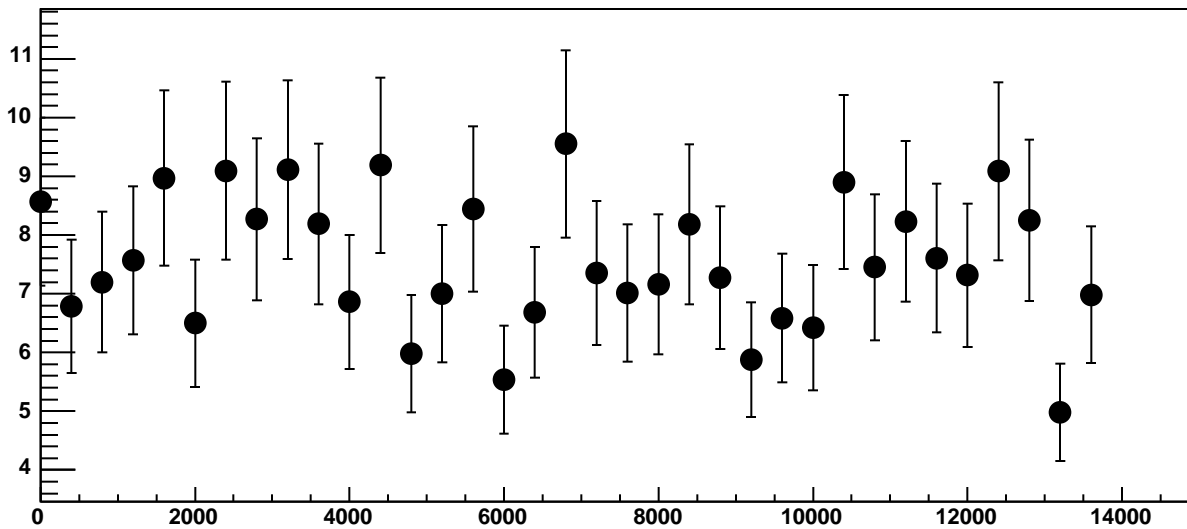


Chip 11, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

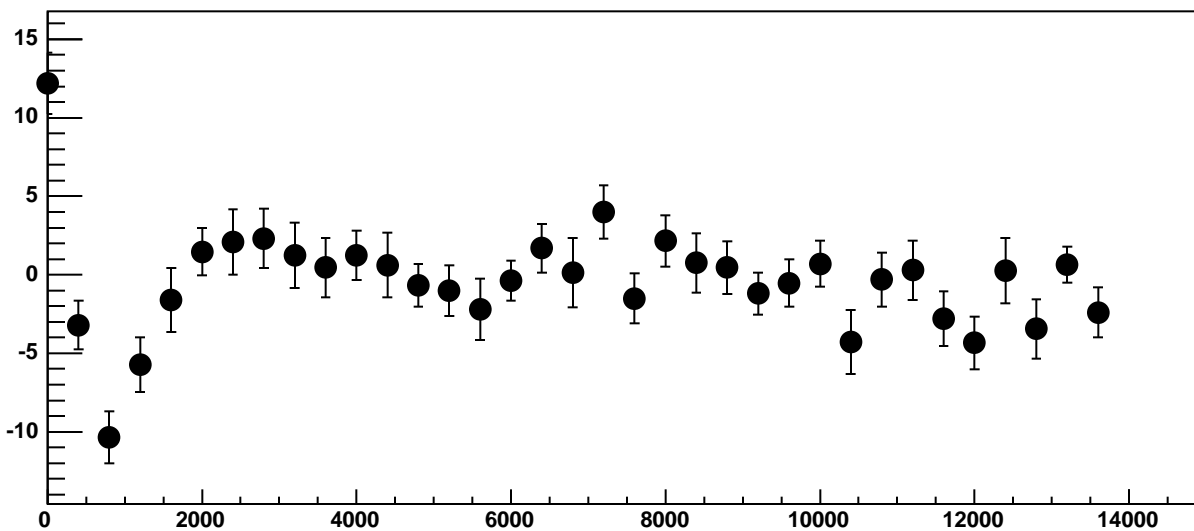


χ^2 / ndf 33.32 / 23
p0 -566.8 ± 0.8141
p1 0.03007 ± 0.0001192

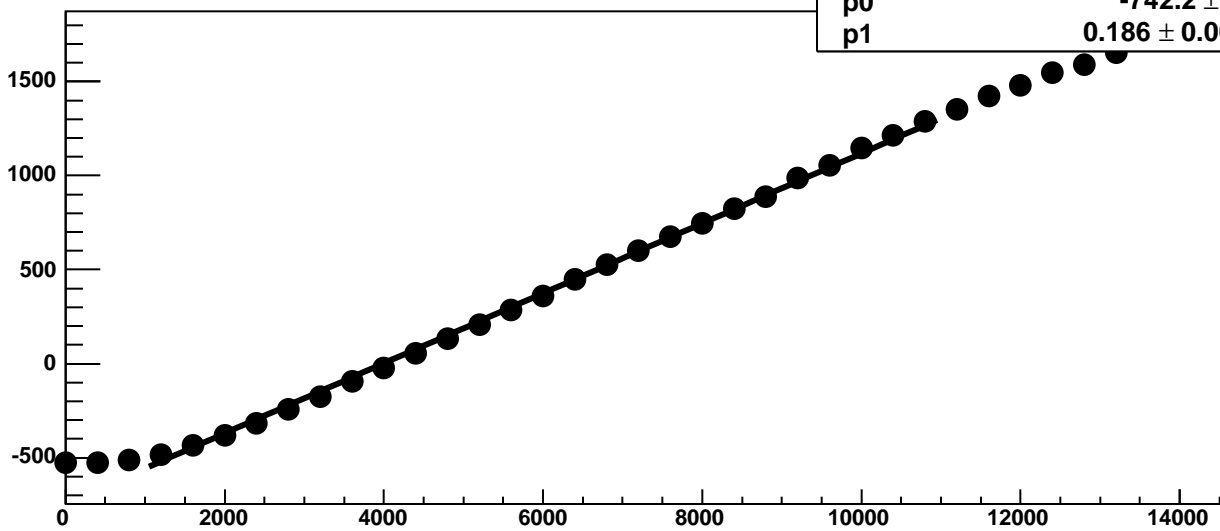
Chip 11, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



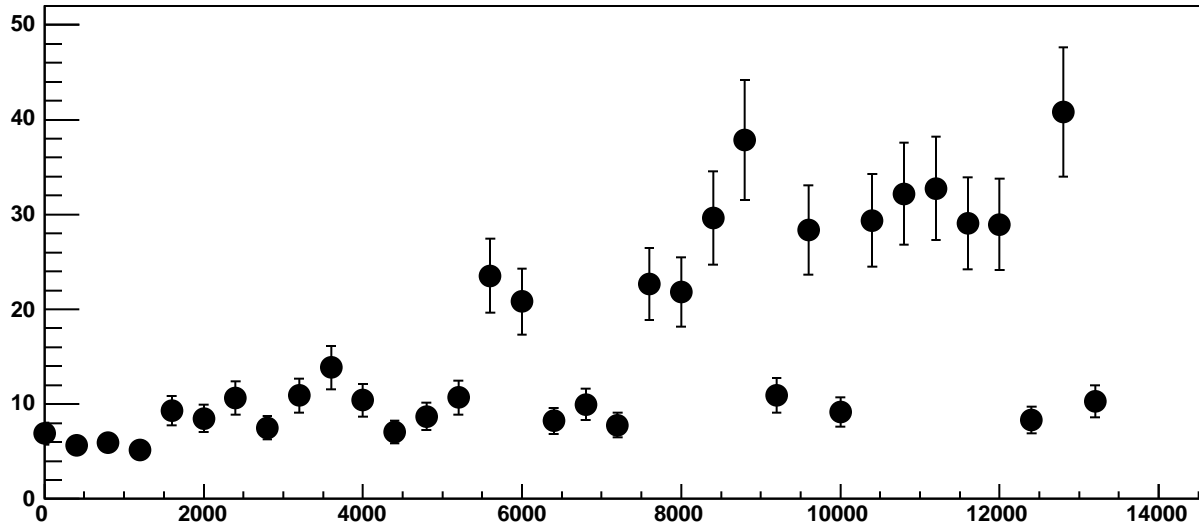
Chip 11, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC



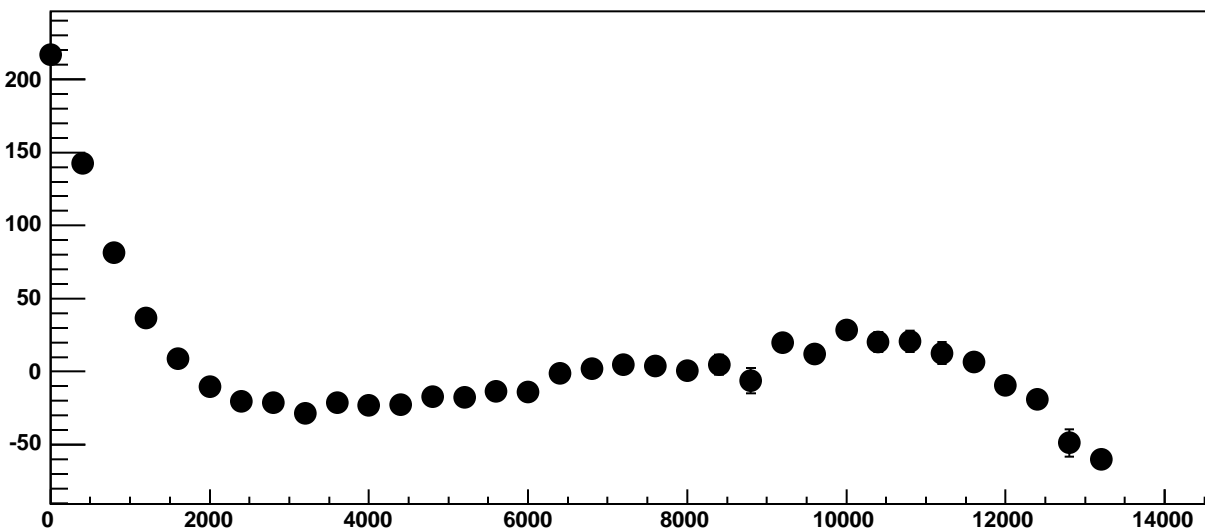
Chip 11, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



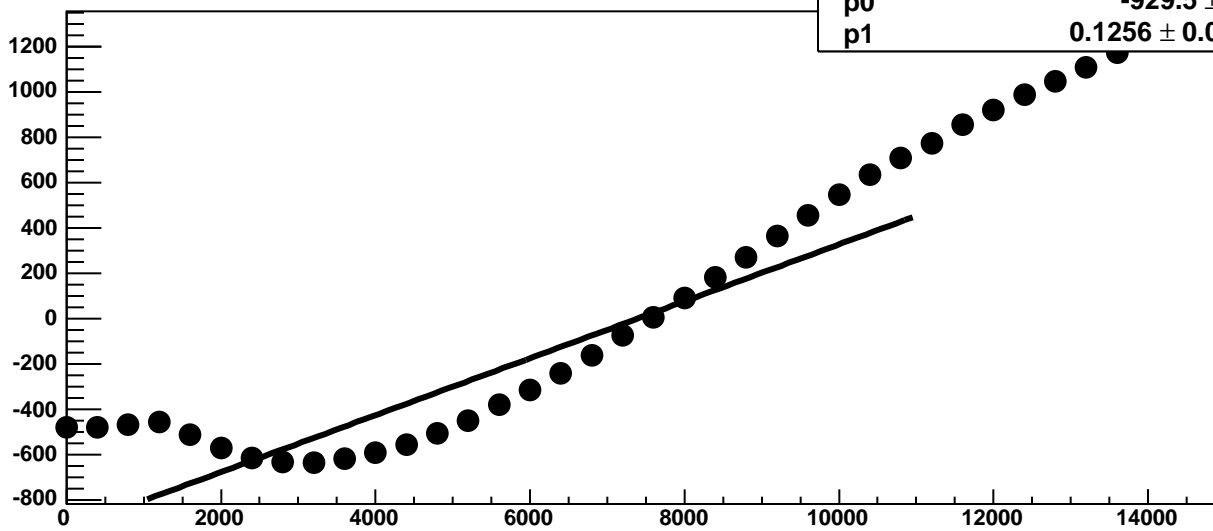
Chip 11, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 11, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

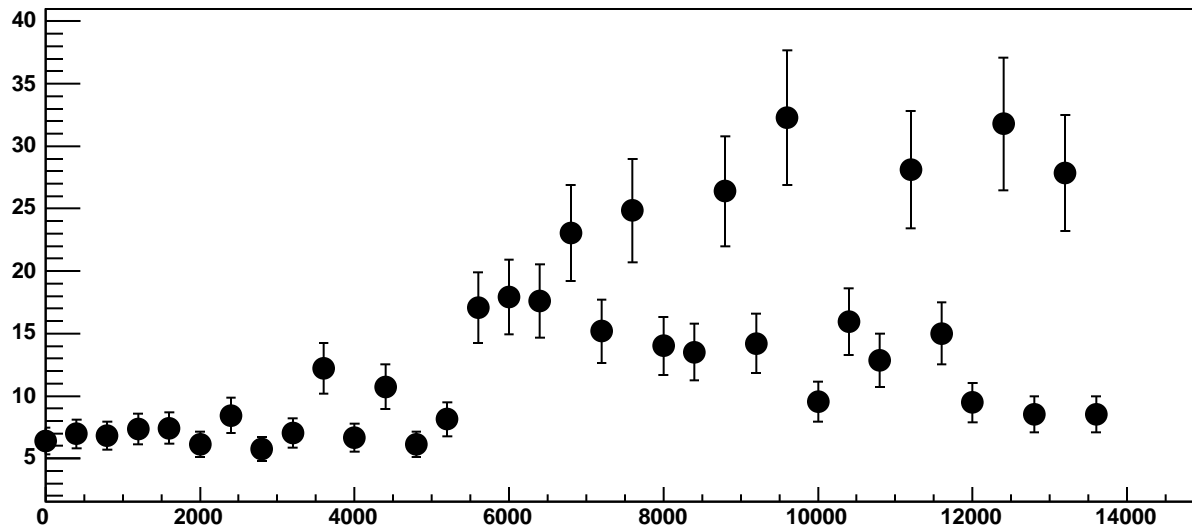


Chip 11, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

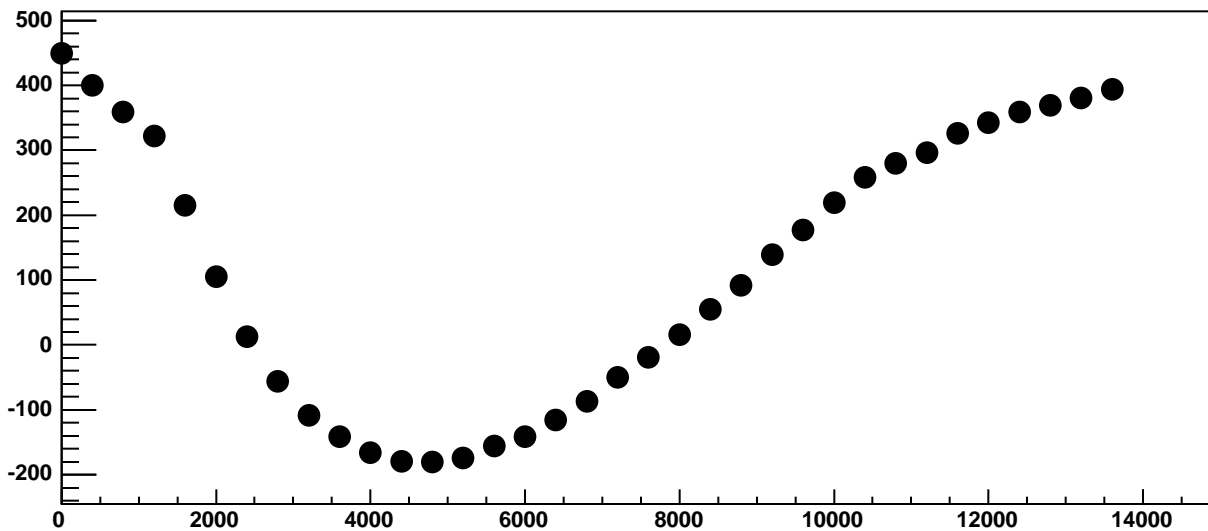


χ^2 / ndf 1.403e+05 / 23
p0 -929.5 ± 0.8576
p1 0.1256 ± 0.0001707

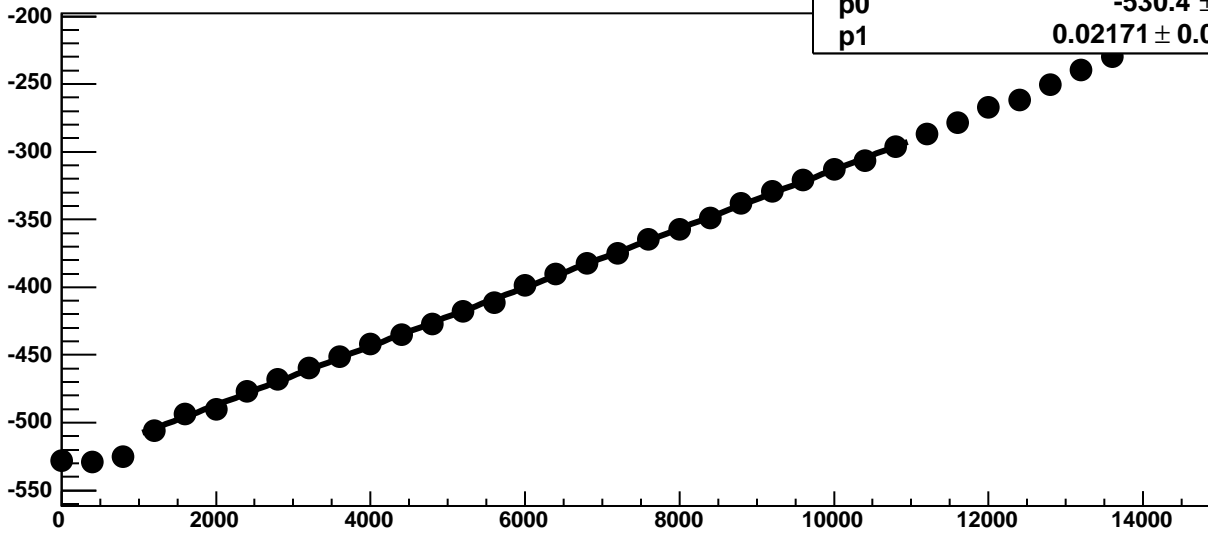
Chip 11, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

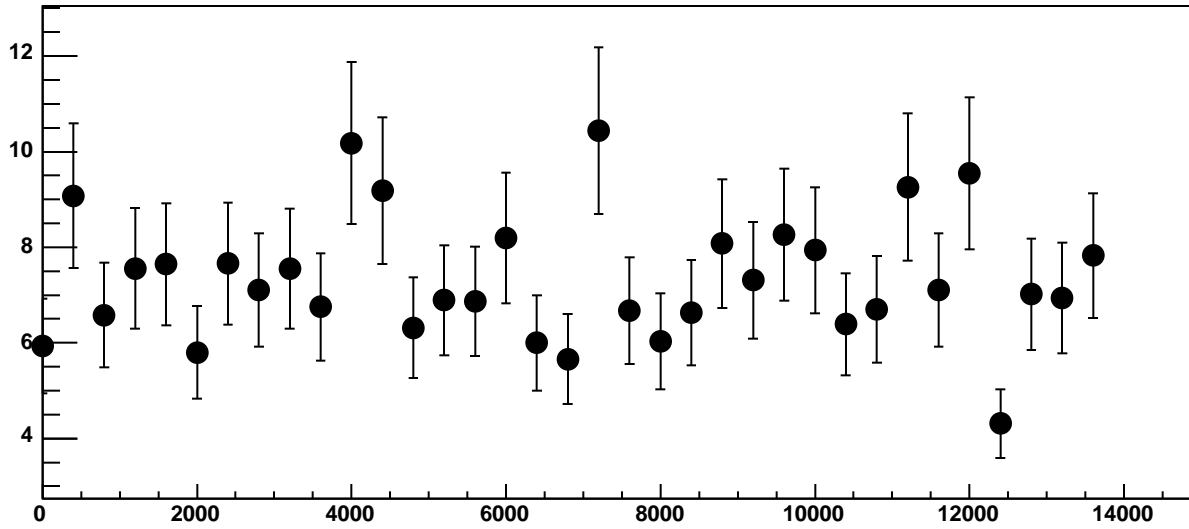


Chip 11, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

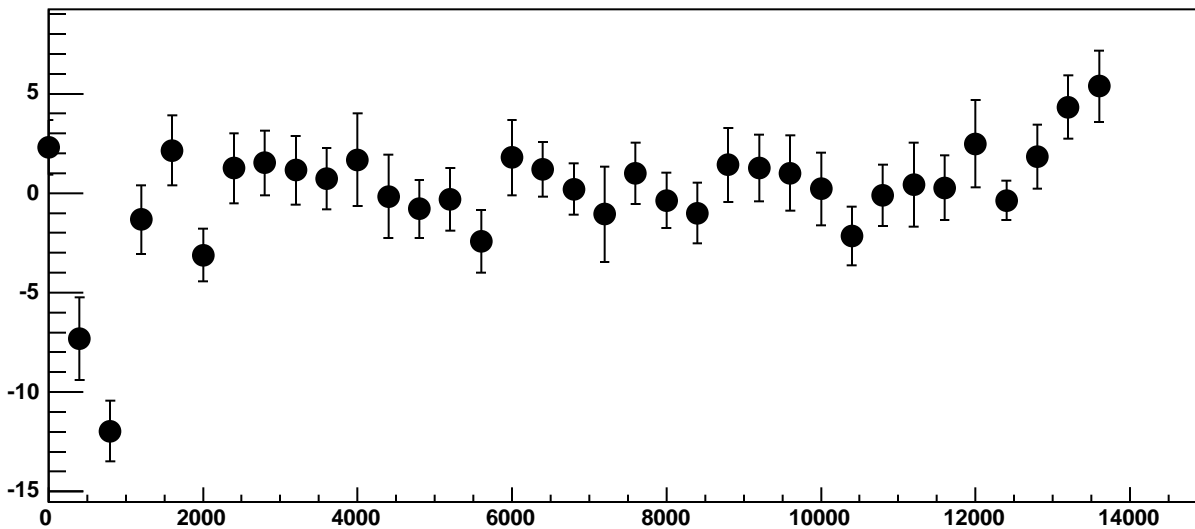


χ^2 / ndf 19.27 / 23
p0 -530.4 ± 0.7597
p1 0.02171 ± 0.0001133

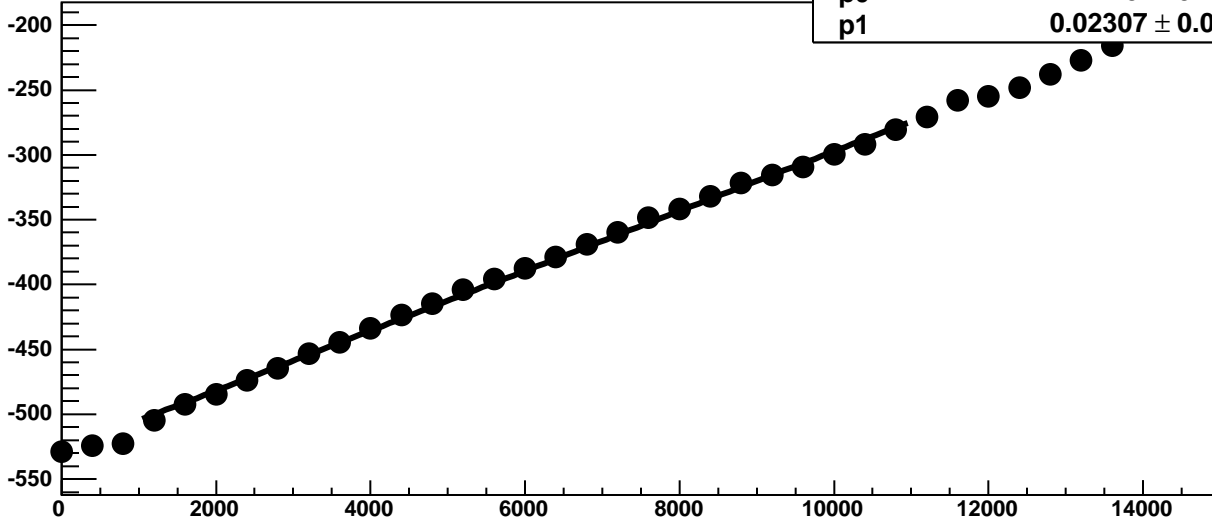
Chip 11, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



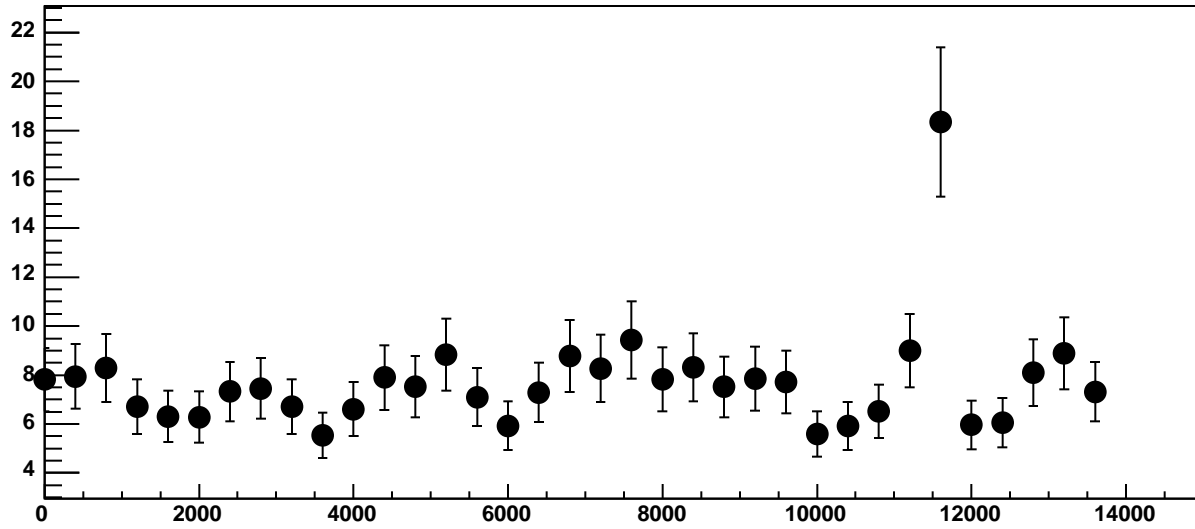
Chip 11, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC



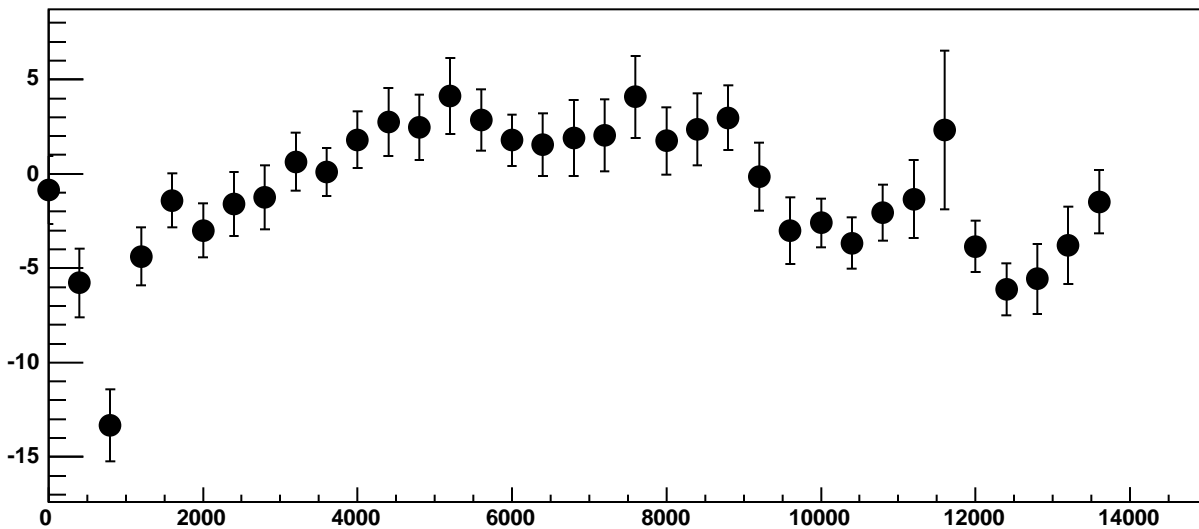
Chip 11, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



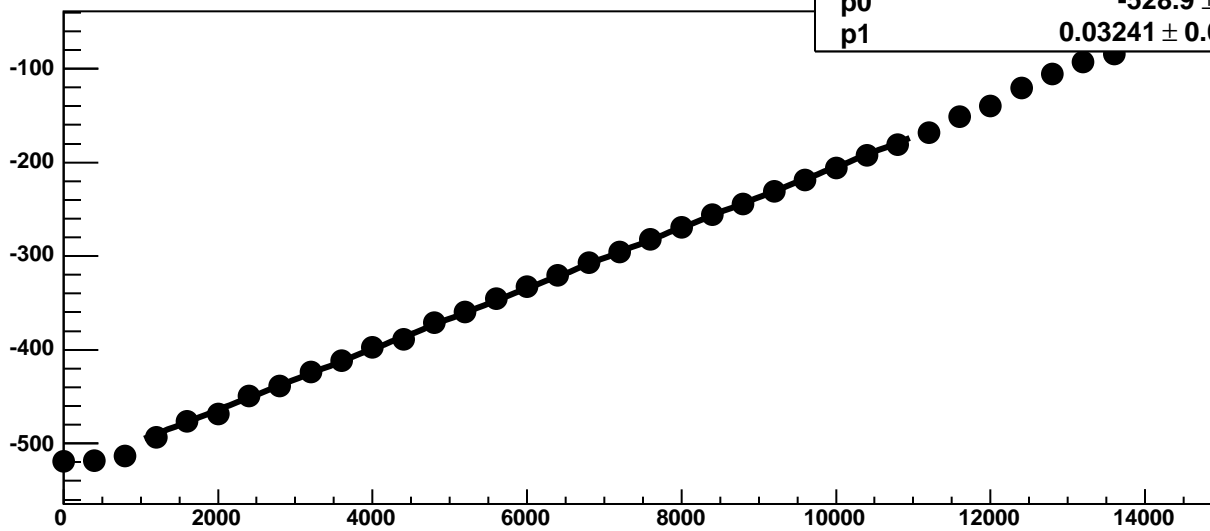
Chip 11, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

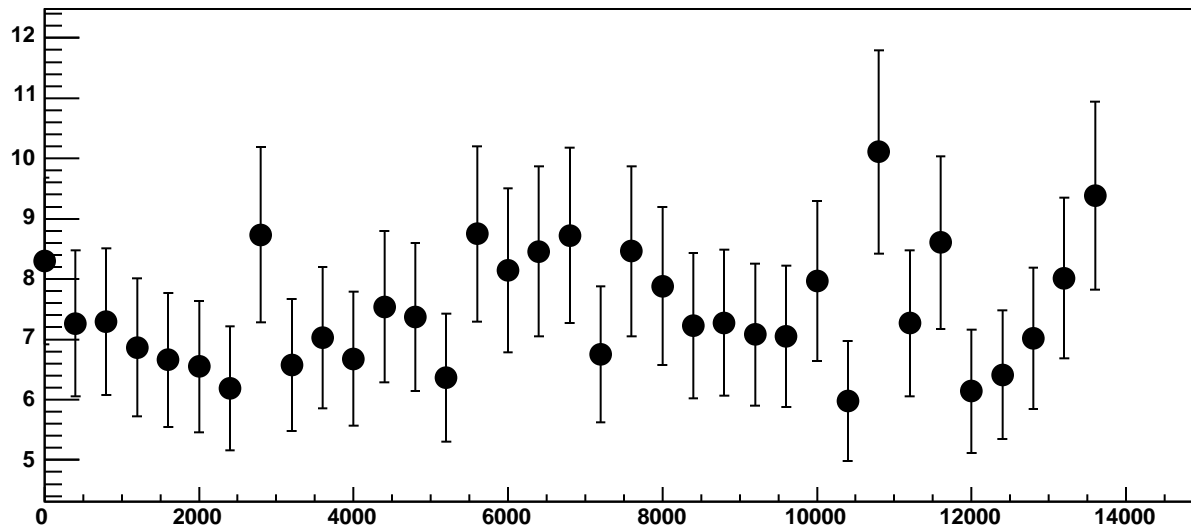


Chip 11, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

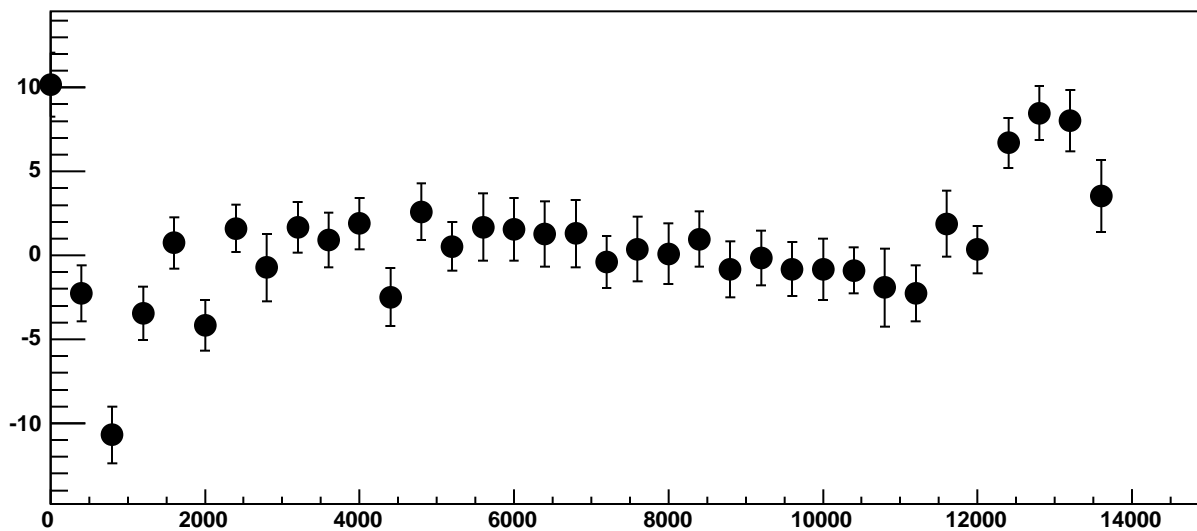


χ^2 / ndf 26.34 / 23
p0 -528.9 ± 0.7353
p1 0.03241 ± 0.0001131

Chip 11, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC

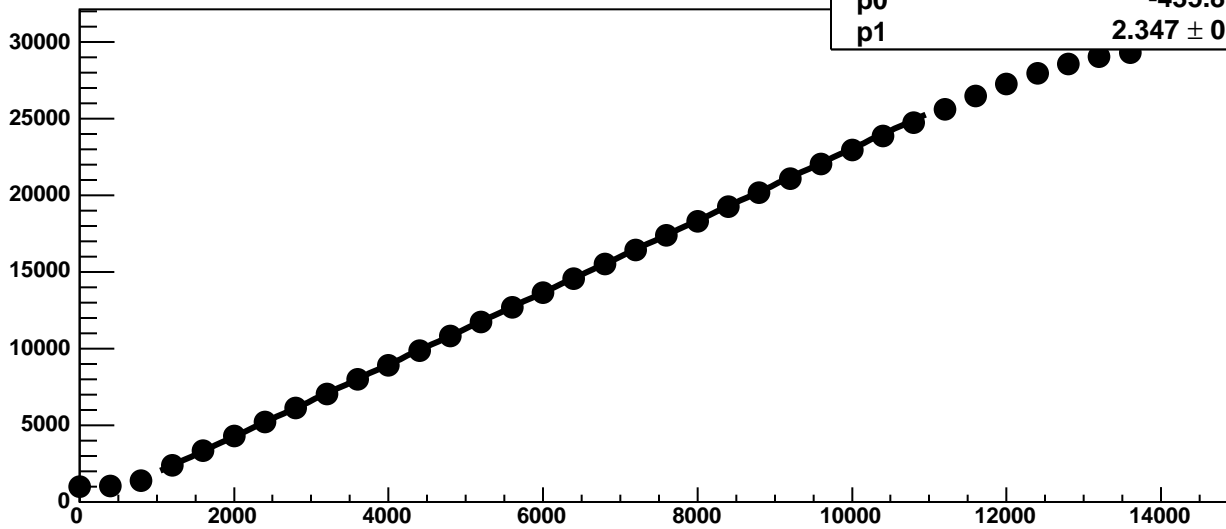


Chip 11, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

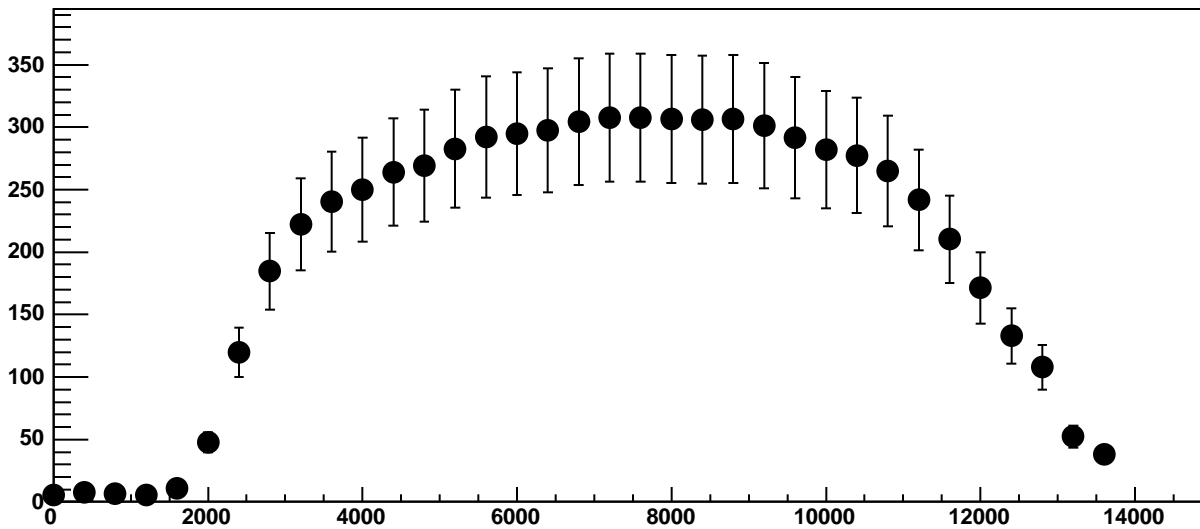


Chip 11, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC

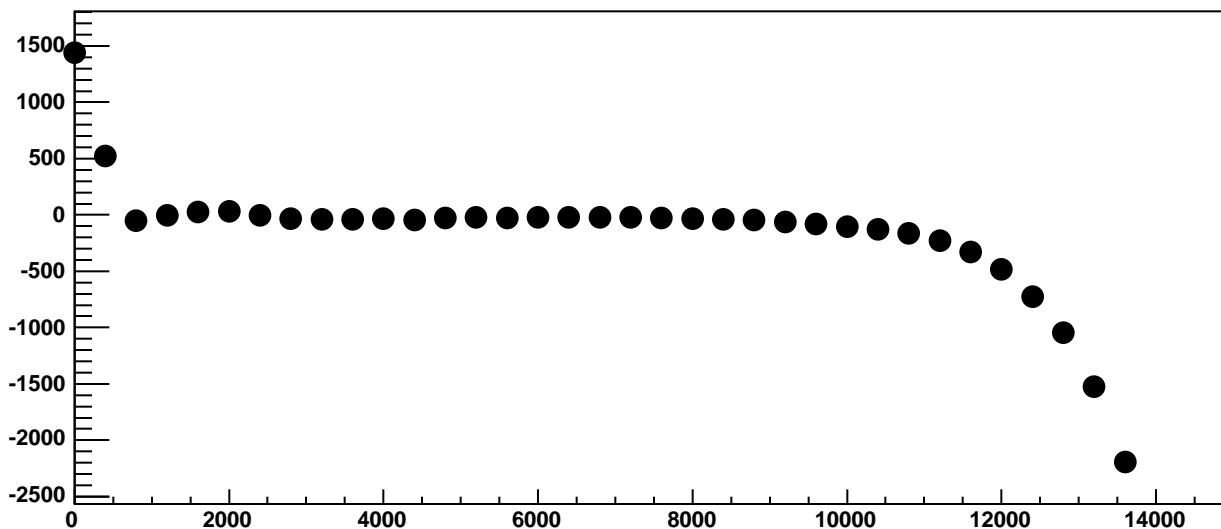
χ^2 / ndf 144.1 / 23
p0 -435.8 ± 3.155
p1 2.347 ± 0.002239



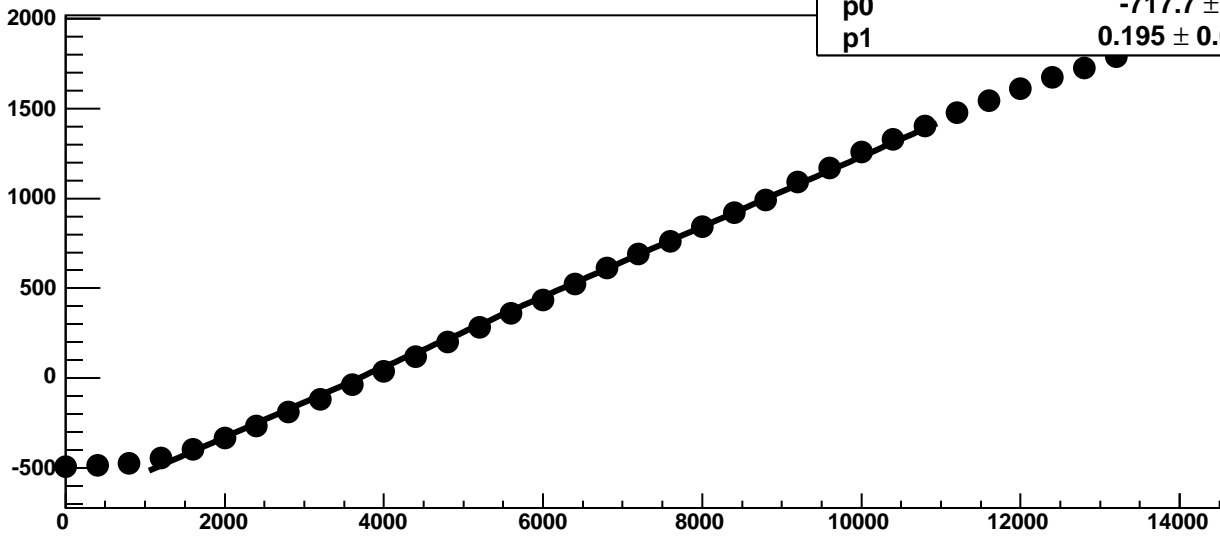
Chip 11, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC

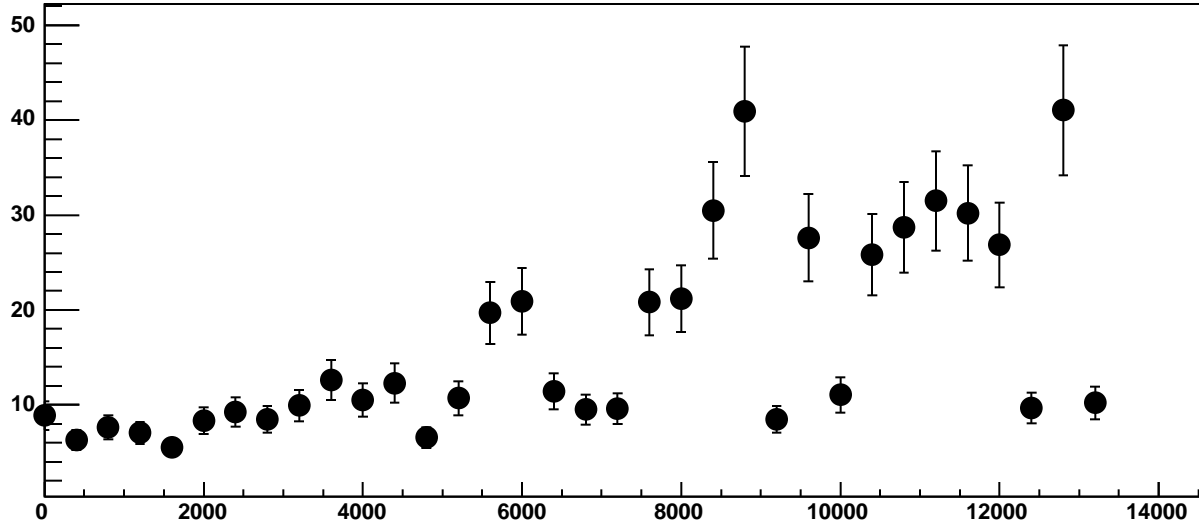


Chip 11, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

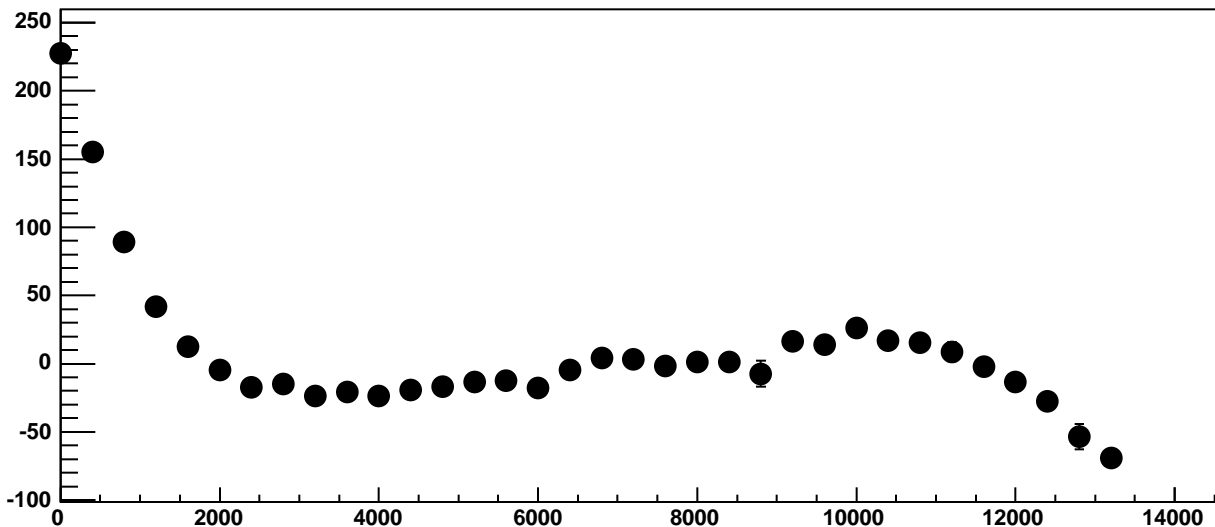


χ^2 / ndf 1573 / 23
p0 -717.7 ± 0.9233
p1 0.195 ± 0.000179

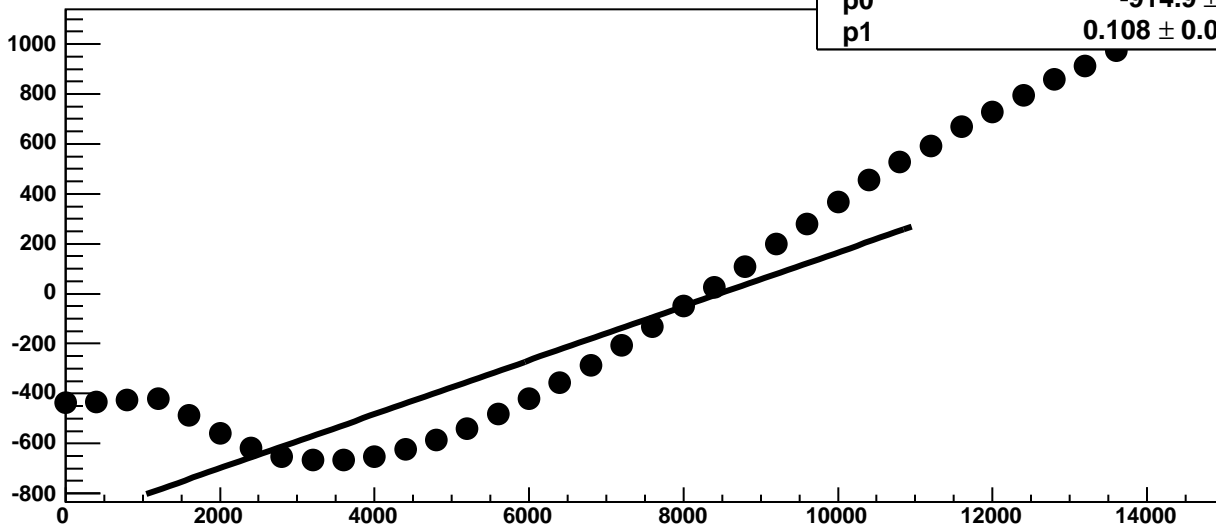
Chip 11, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 11, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

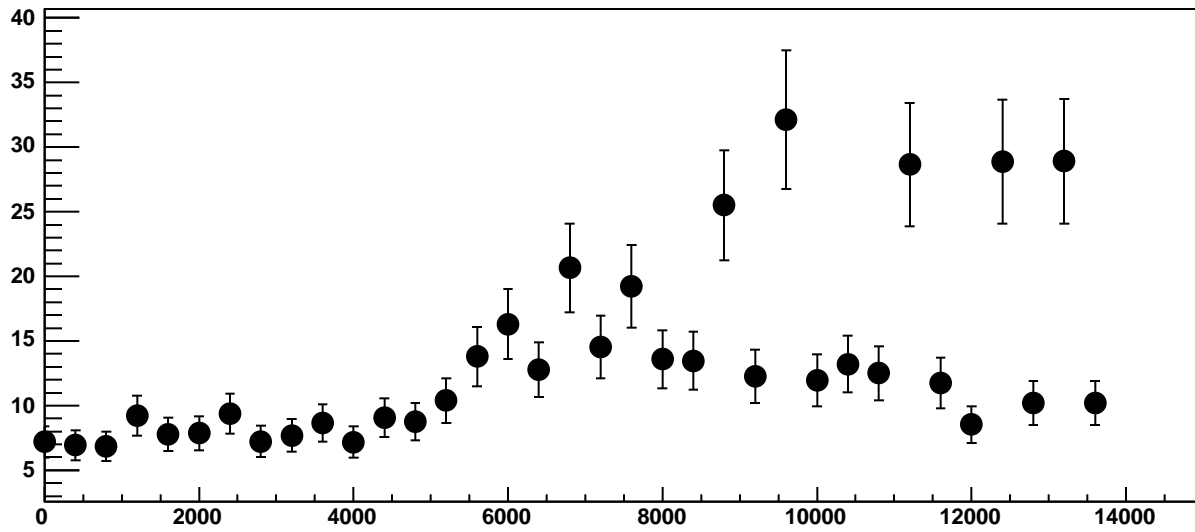


Chip 11, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

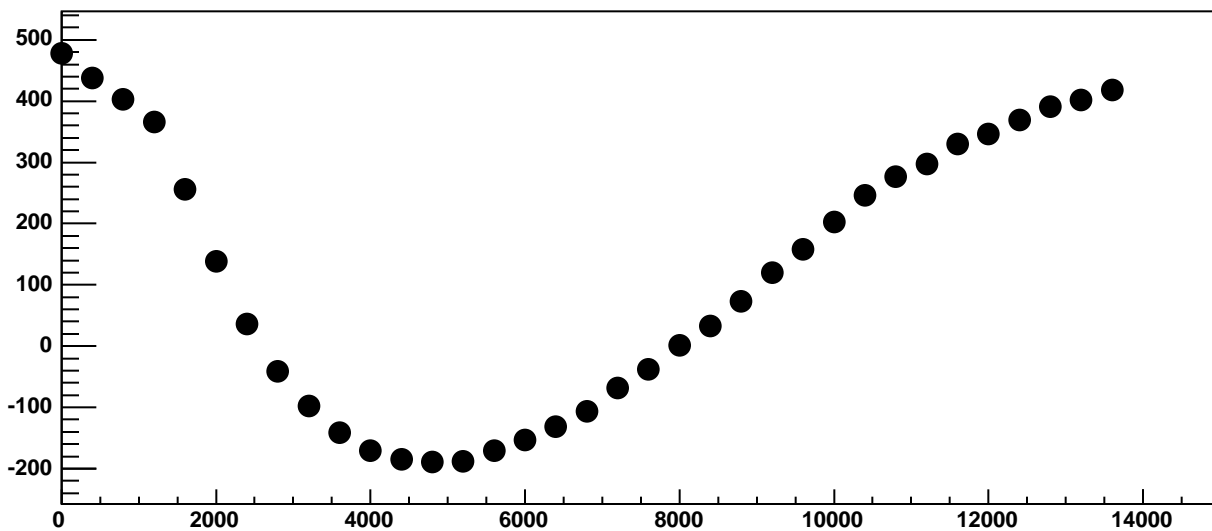


χ^2 / ndf	1.305e+05 / 23
p0	-914.9 ± 0.9592
p1	0.108 ± 0.0001789

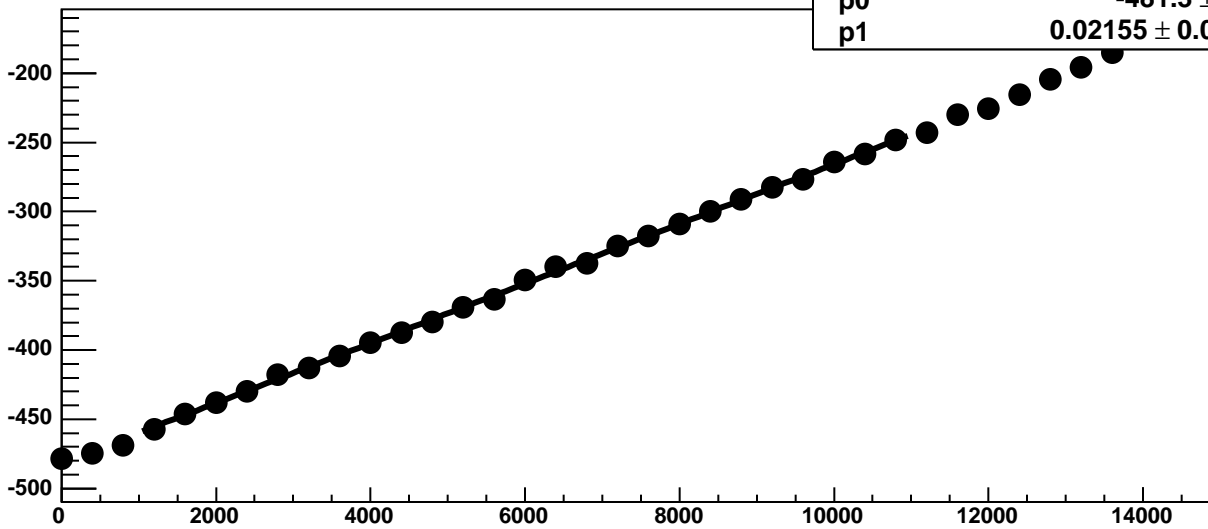
Chip 11, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

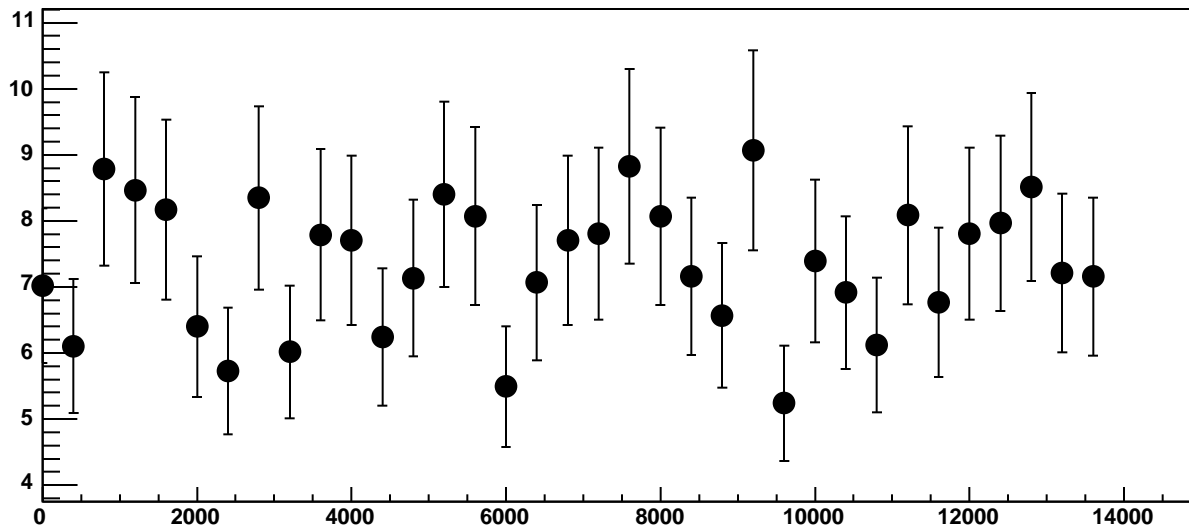


Chip 11, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

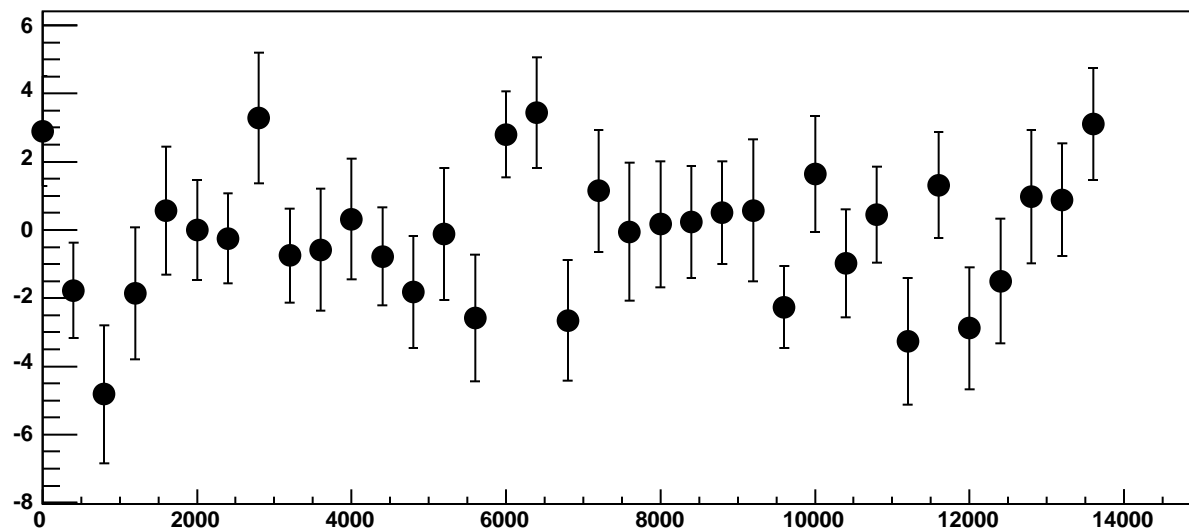


χ^2 / ndf 25.17 / 23
p0 -481.3 ± 0.7435
p1 0.02155 ± 0.0001099

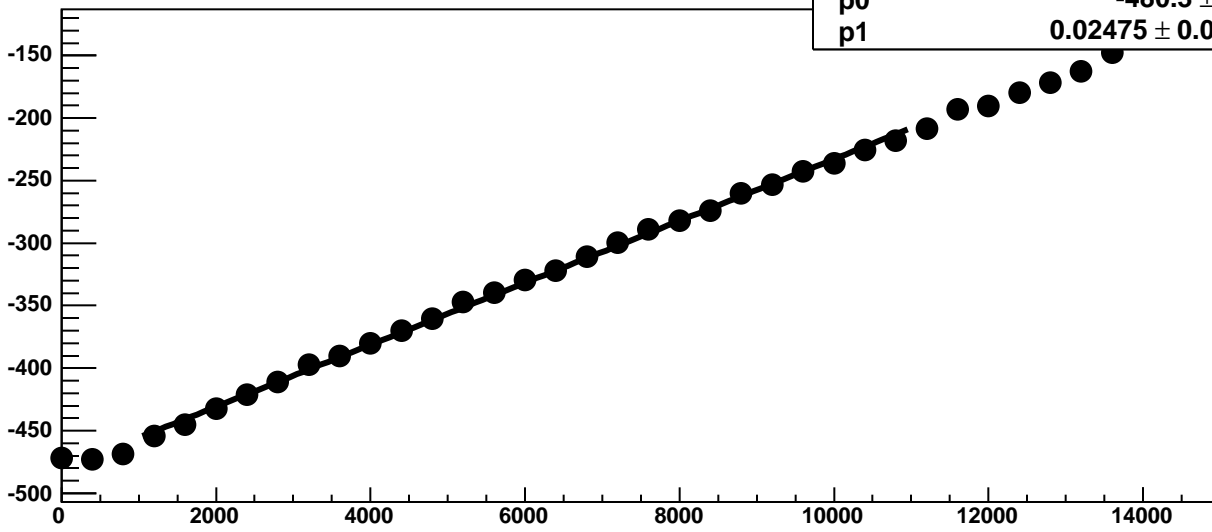
Chip 11, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



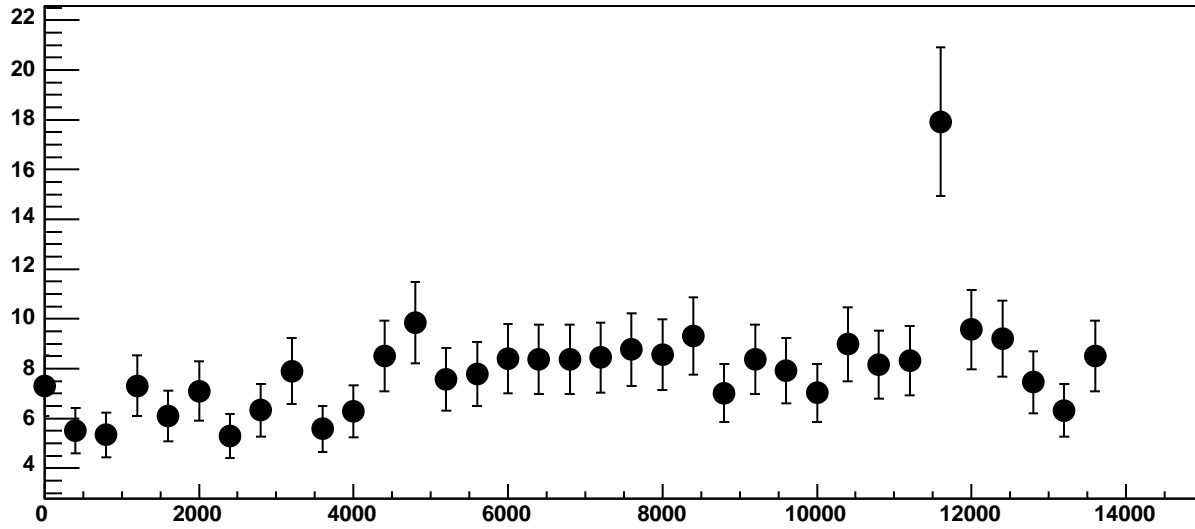
Chip 11, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



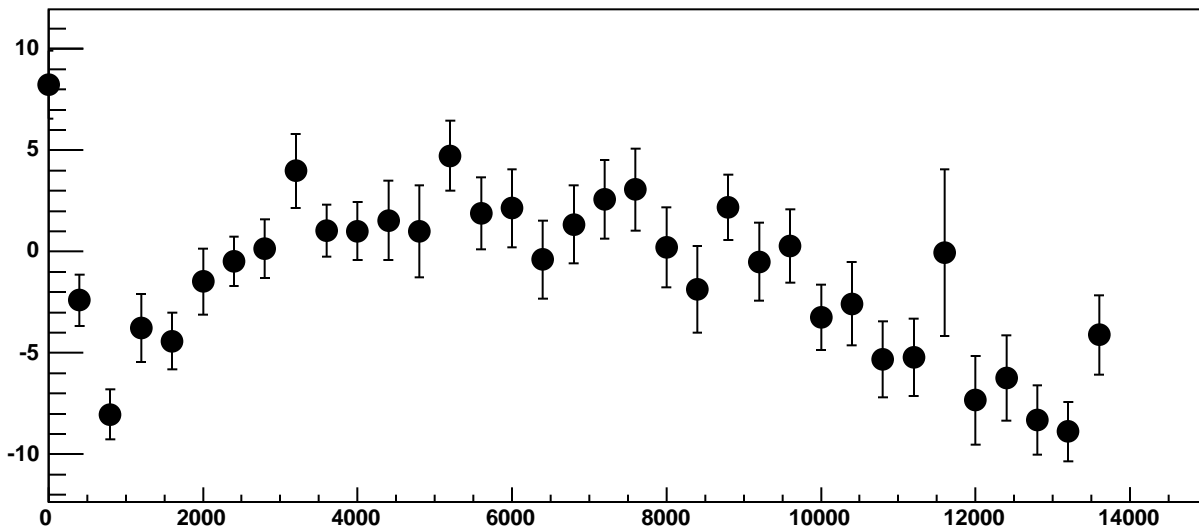
Chip 11, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC



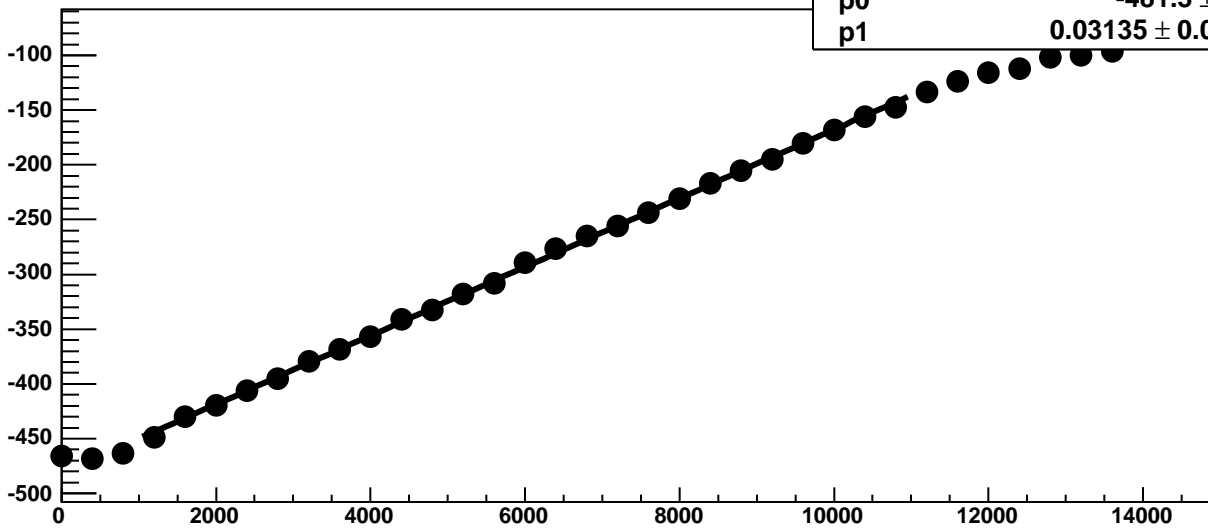
Chip 11, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC

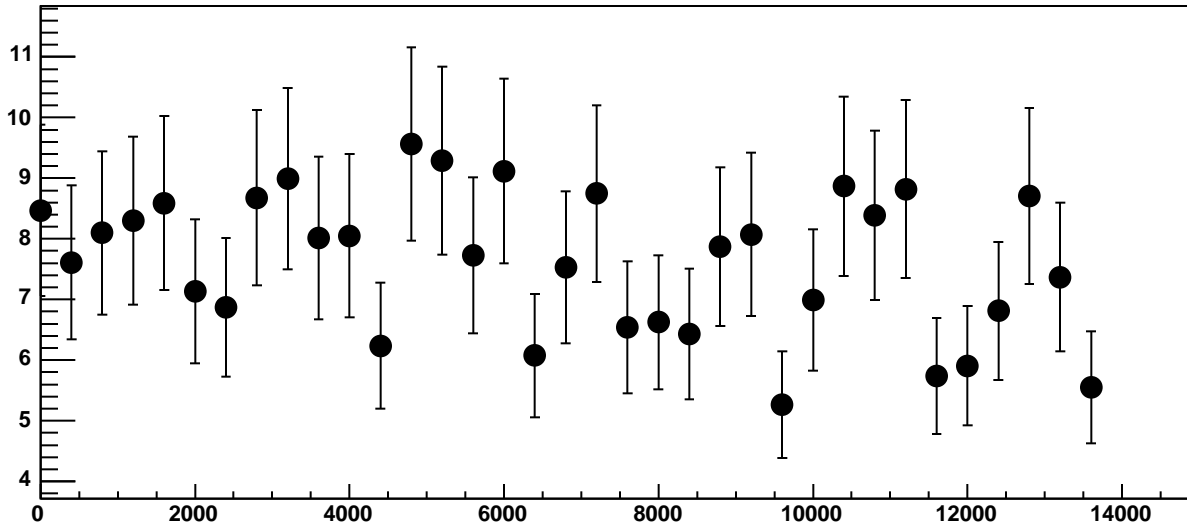


Chip 11, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

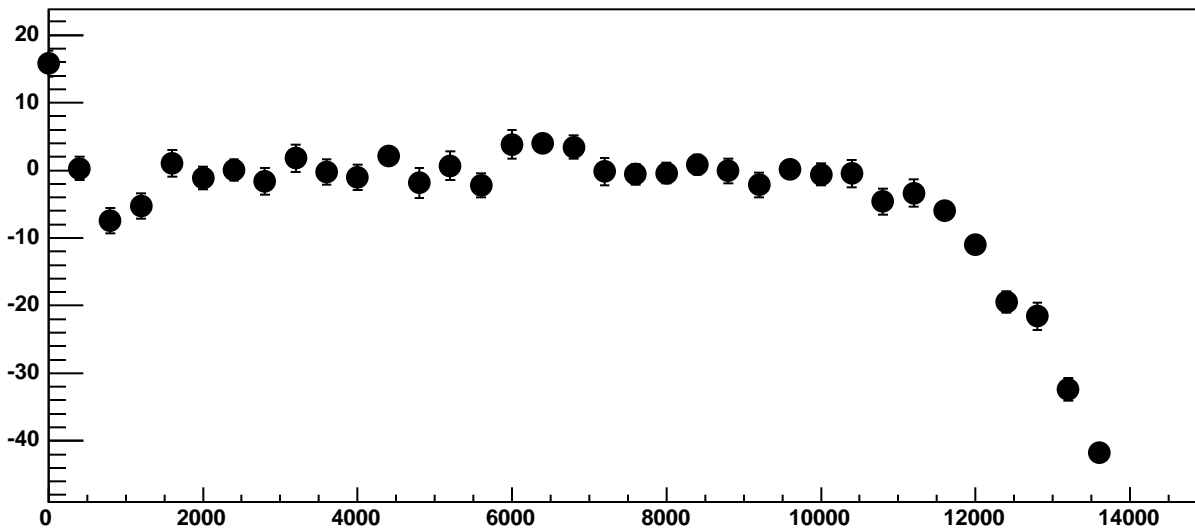


χ^2 / ndf 38.36 / 23
p0 -481.3 ± 0.8294
p1 0.03135 ± 0.0001207

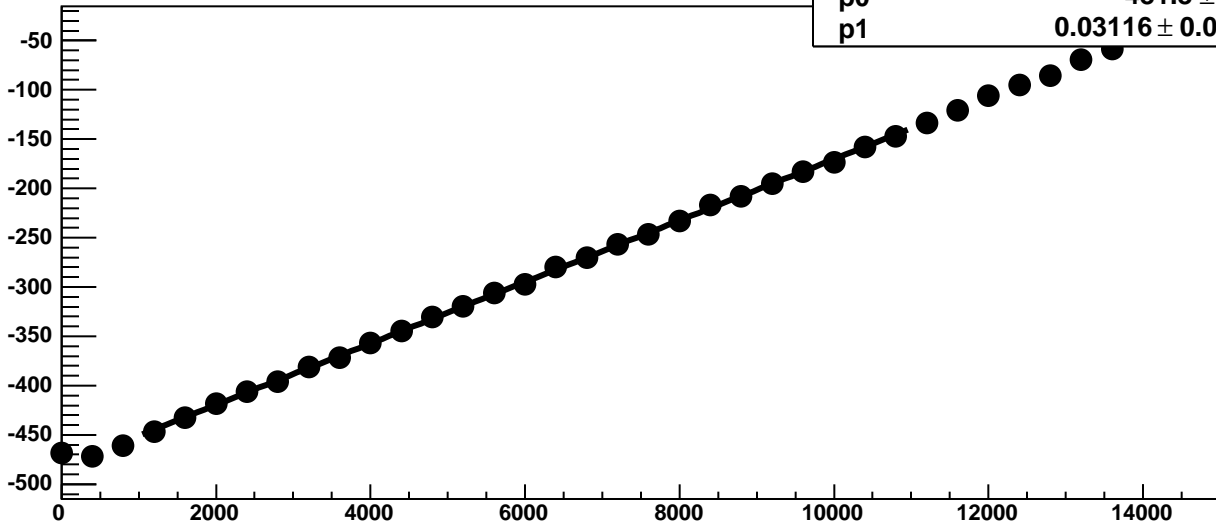
Chip 11, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

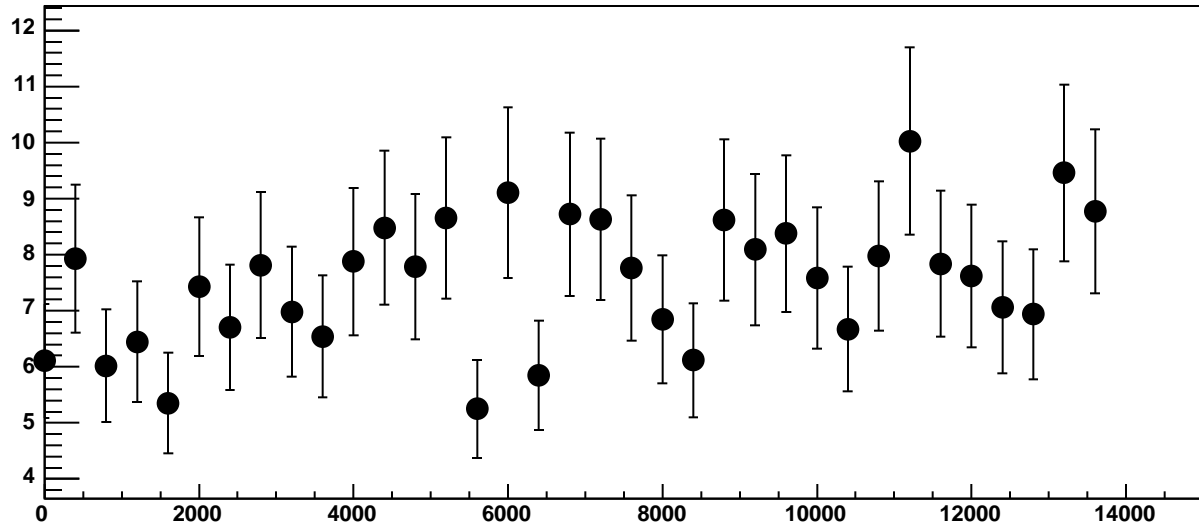


Chip 11, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

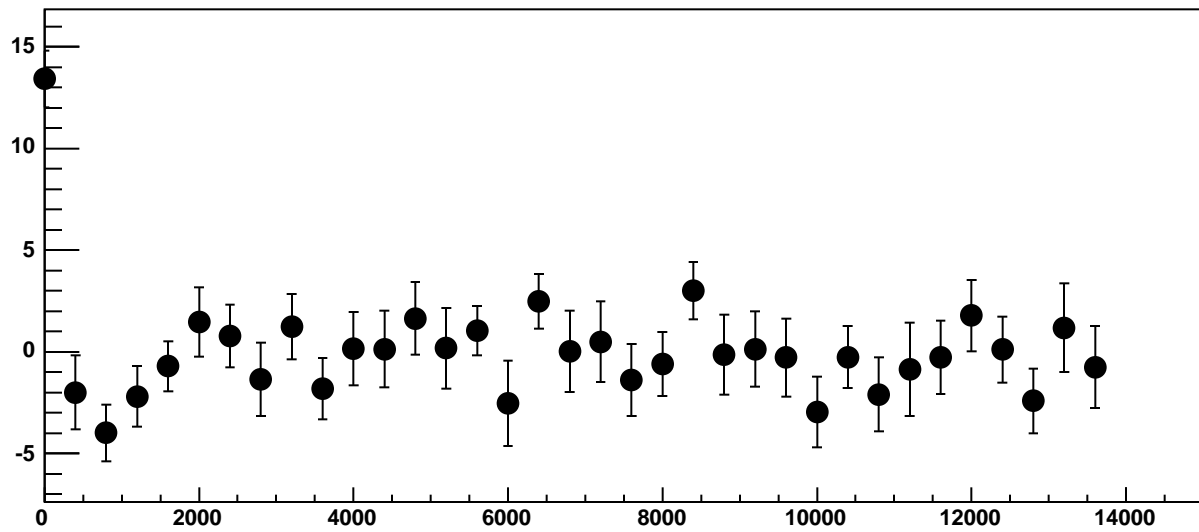


χ^2 / ndf 22.26 / 23
p0 -481.8 ± 0.7194
p1 0.03116 ± 0.0001118

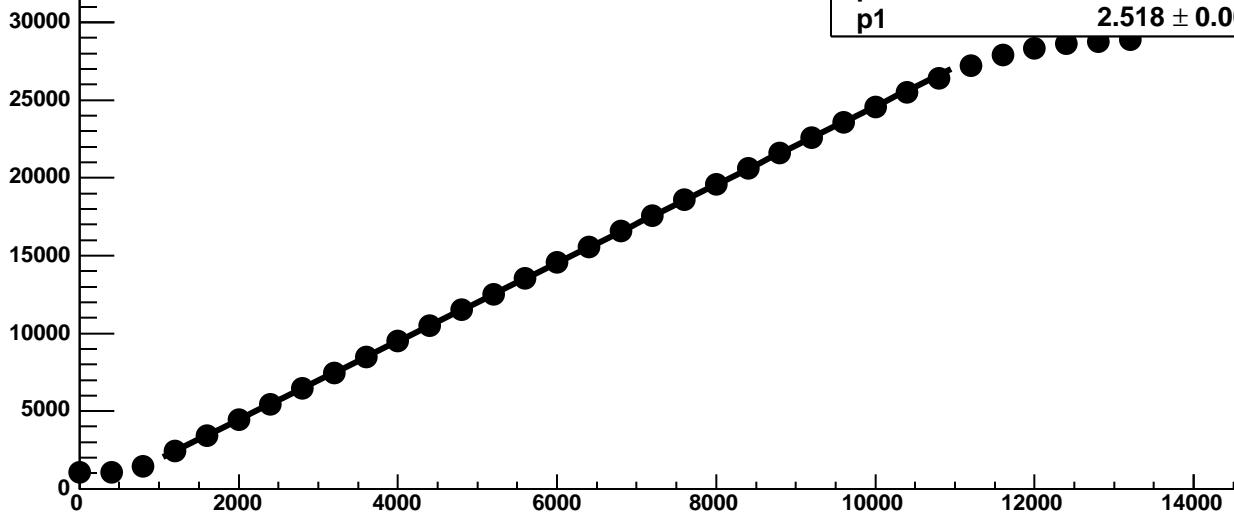
Chip 11, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



Chip 11, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

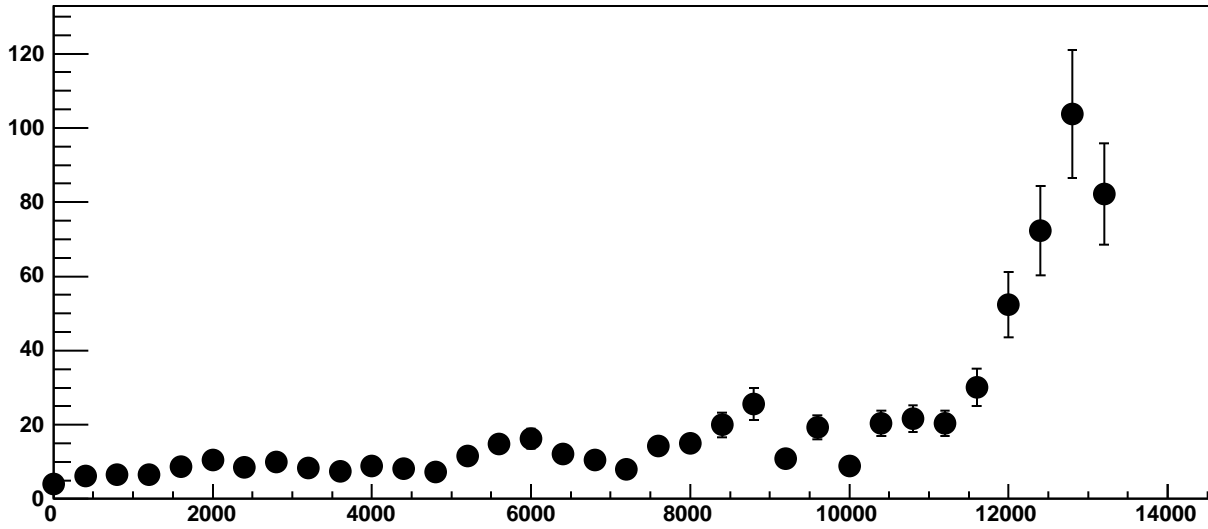


Chip 11, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC

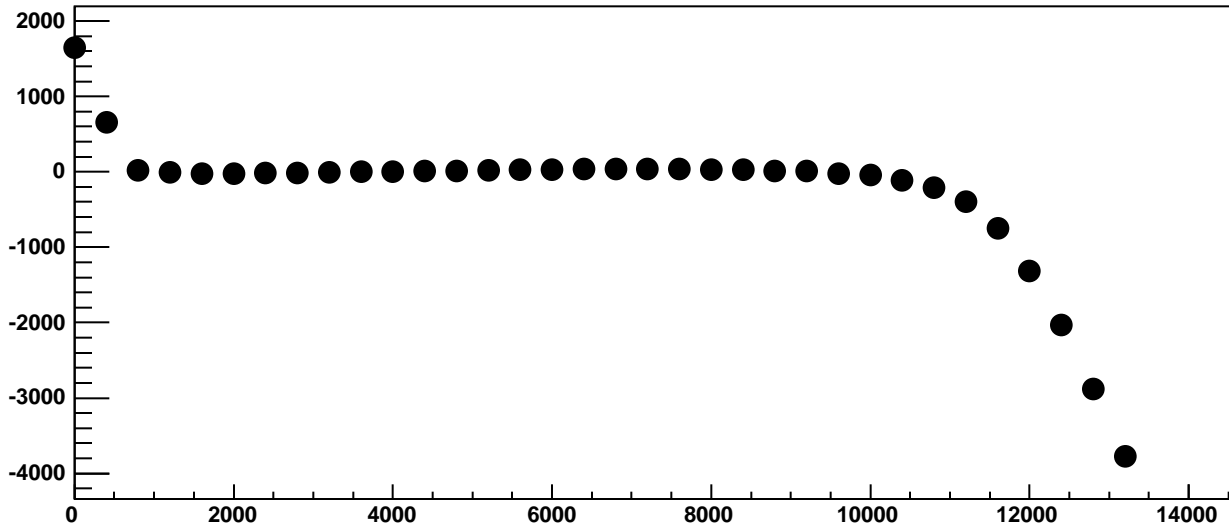


χ^2 / ndf 4762 / 23
p0 -583.3 ± 0.9646
p1 2.518 ± 0.0001756

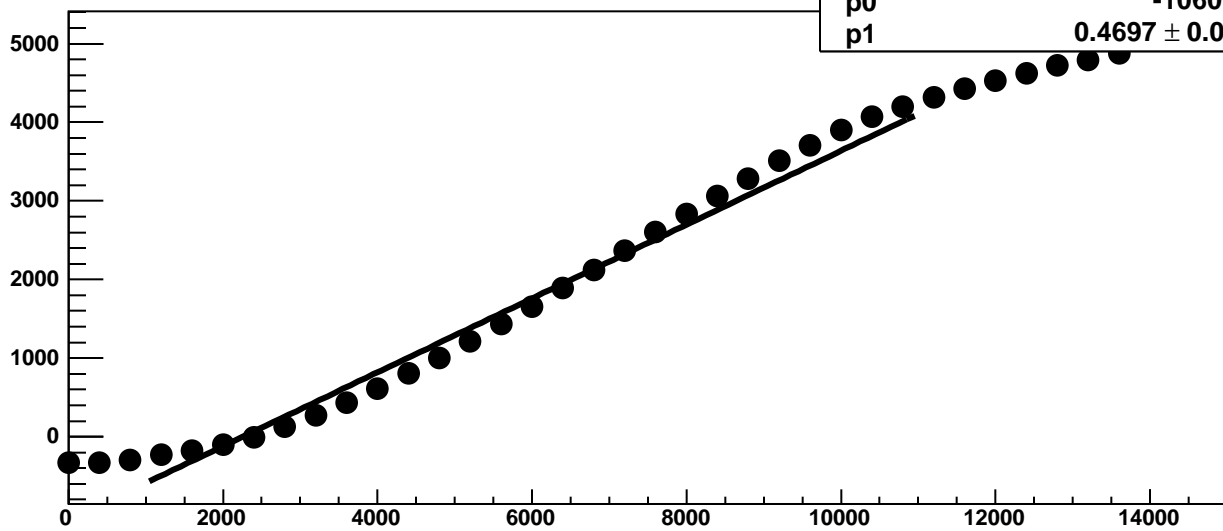
Chip 11, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC



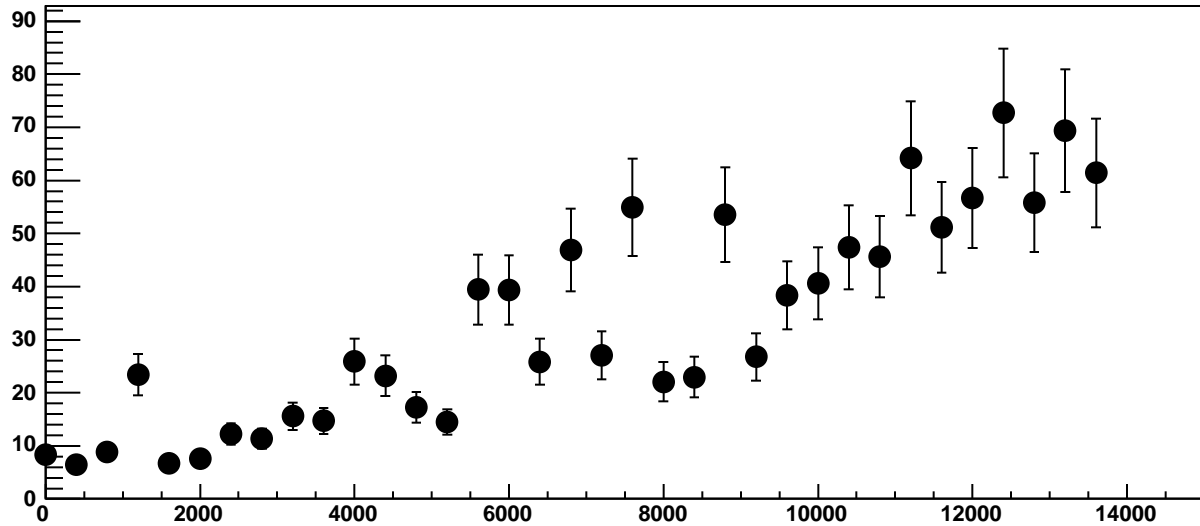
Chip 11, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC



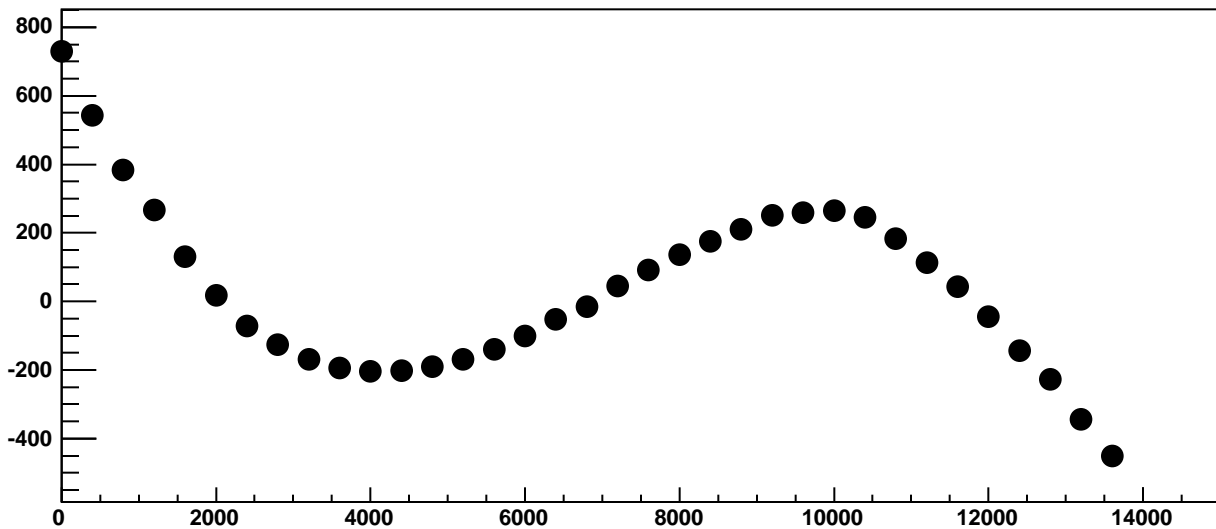
Chip 11, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



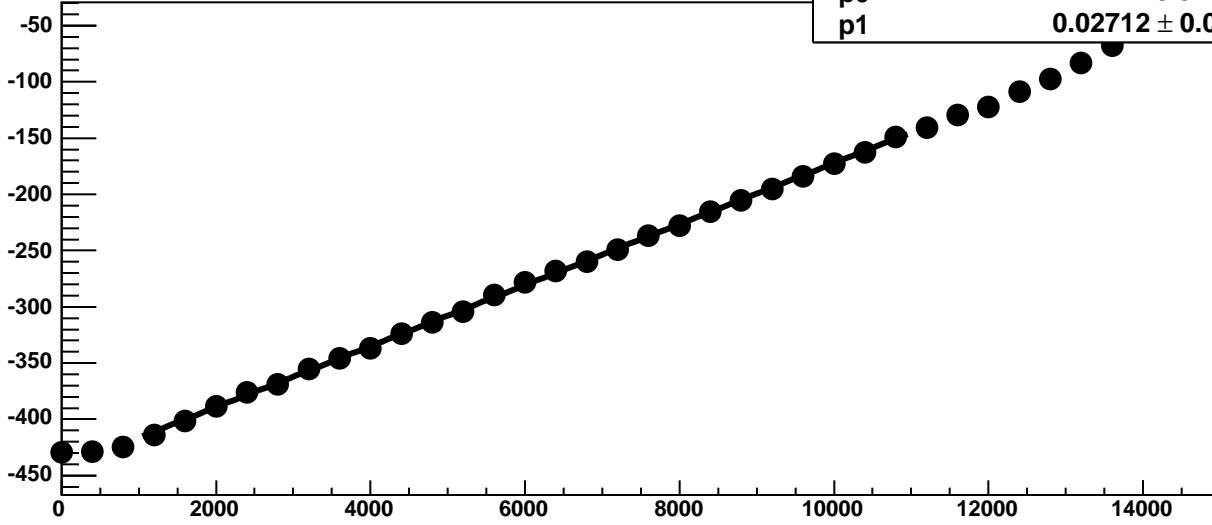
Chip 11, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



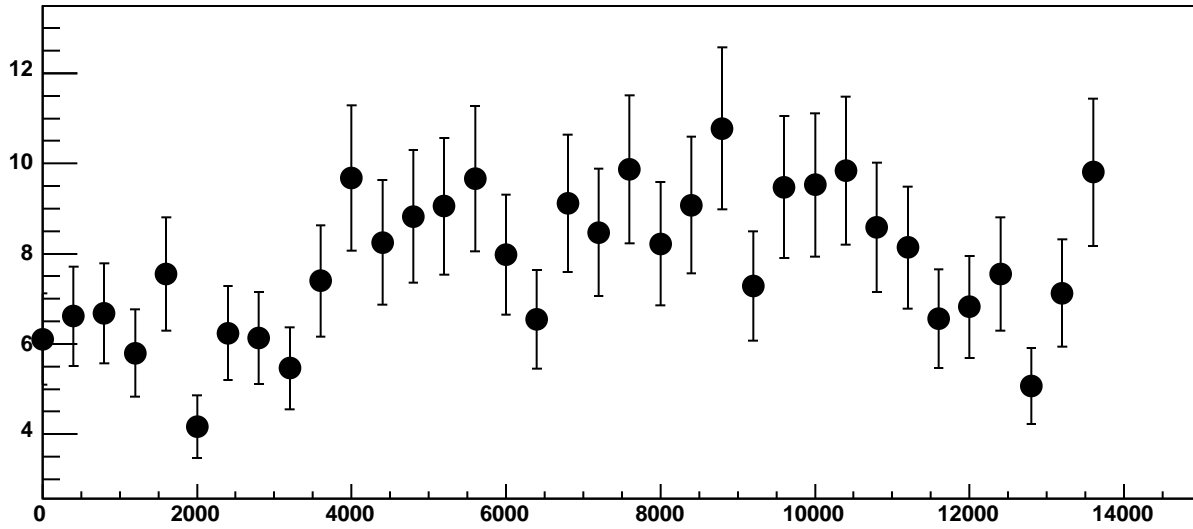
Chip 11, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



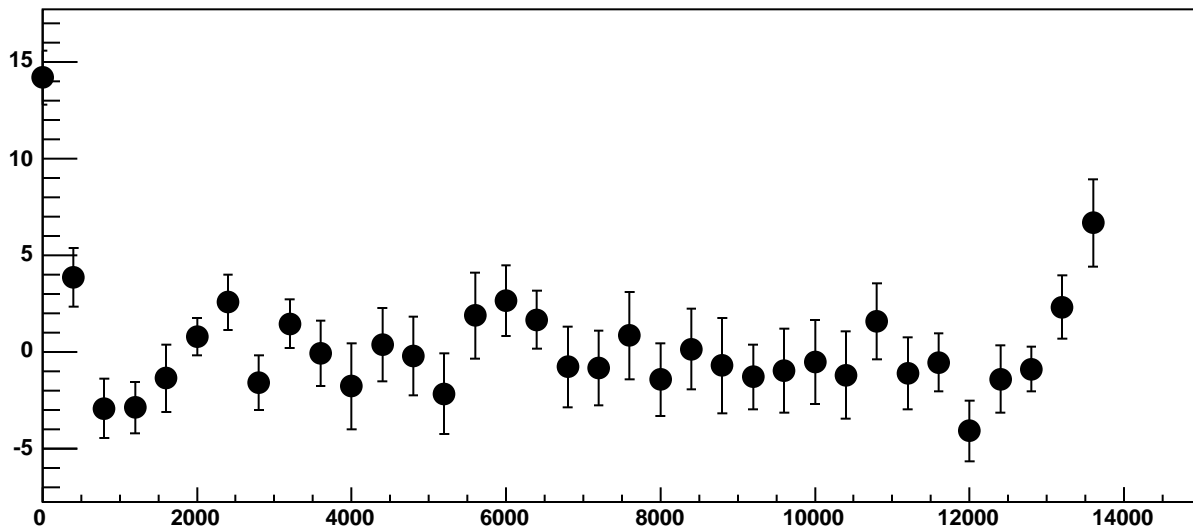
Chip 11, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



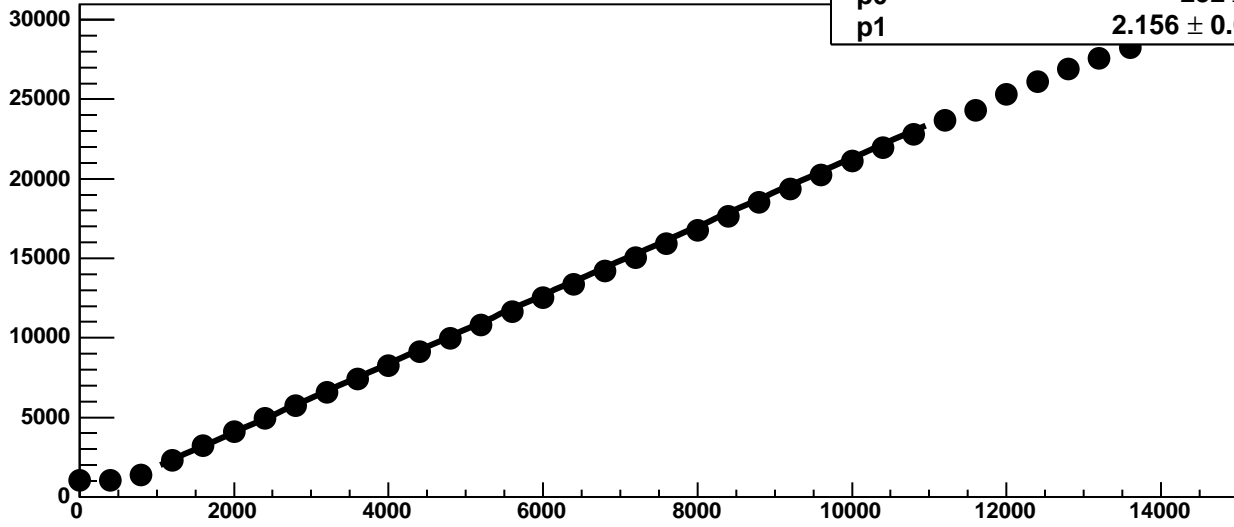
Chip 11, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



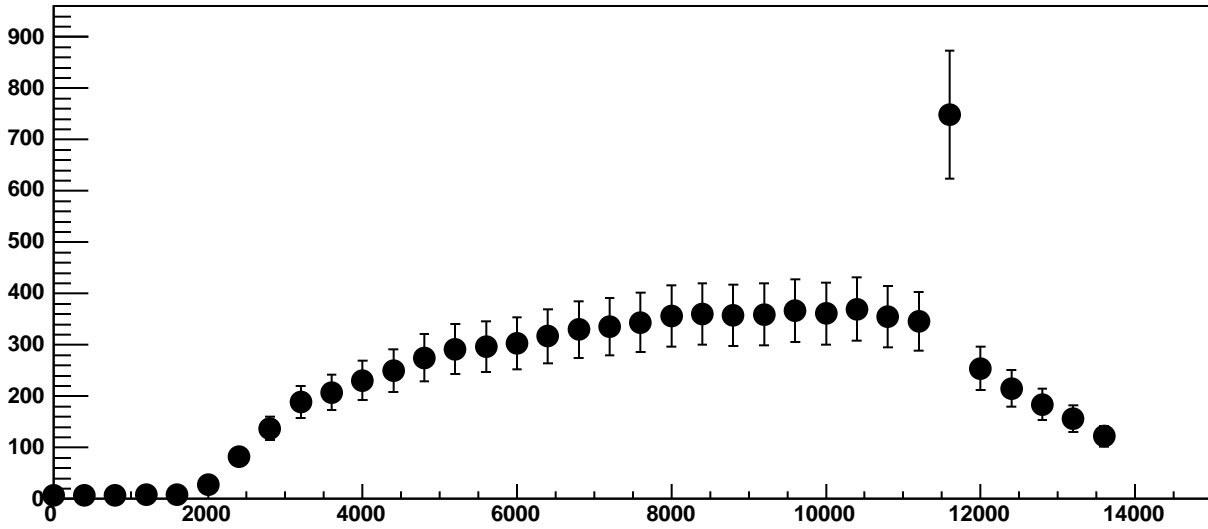
Chip 11, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



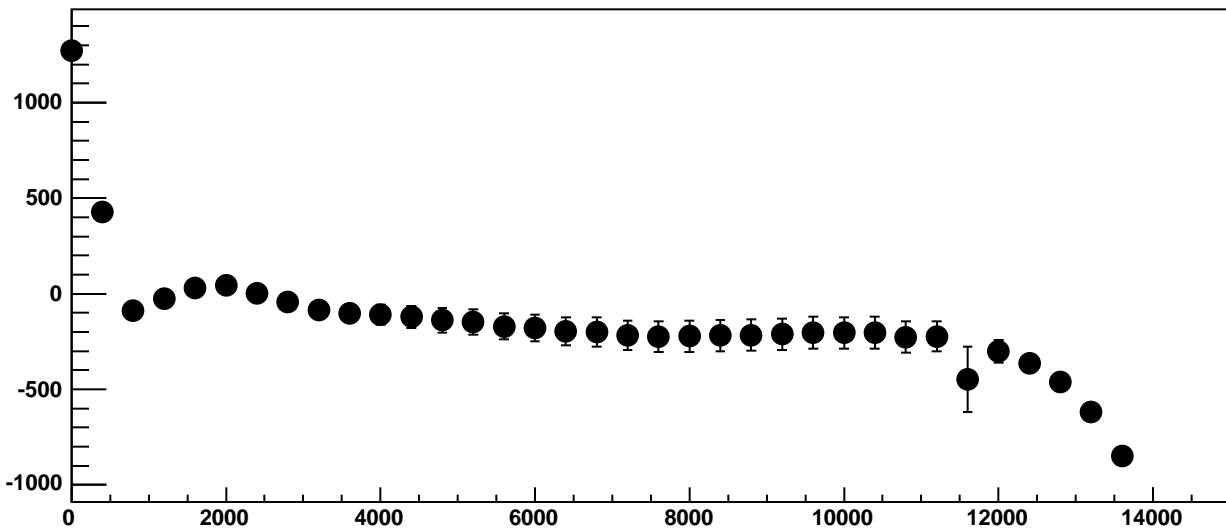
Chip 11, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC



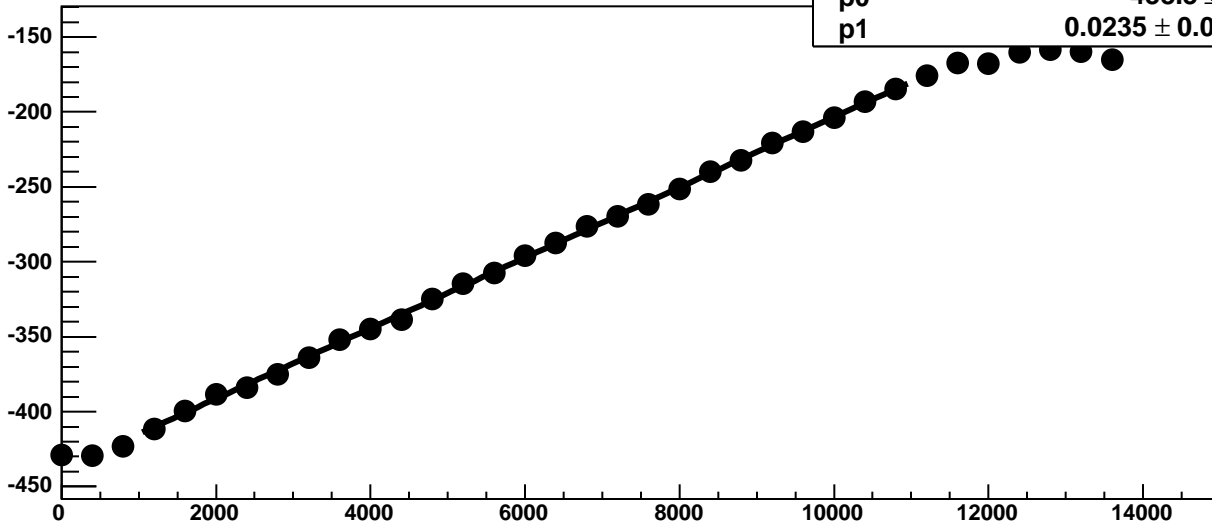
Chip 11, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC

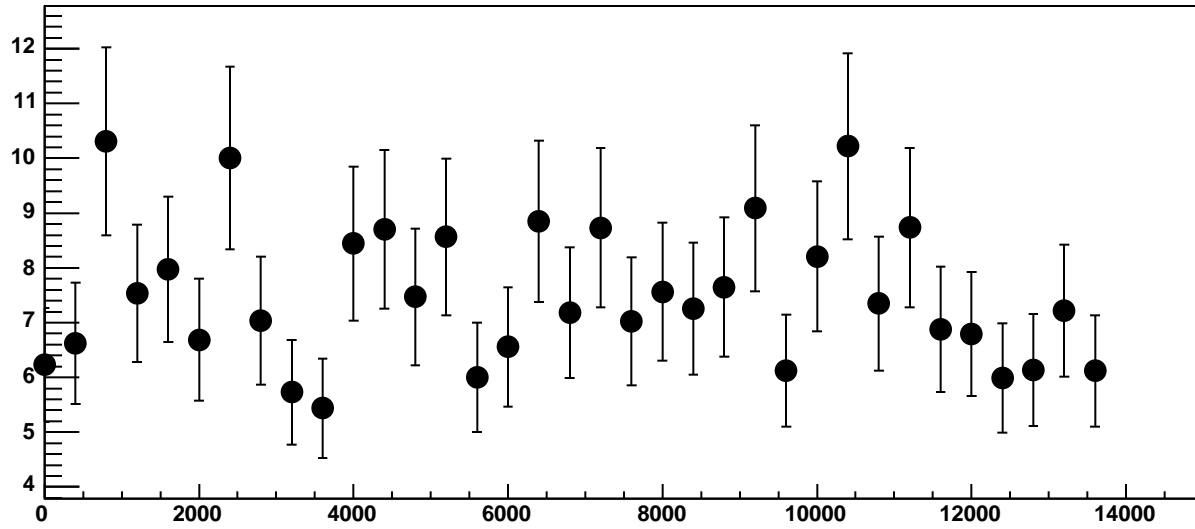


Chip 11, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

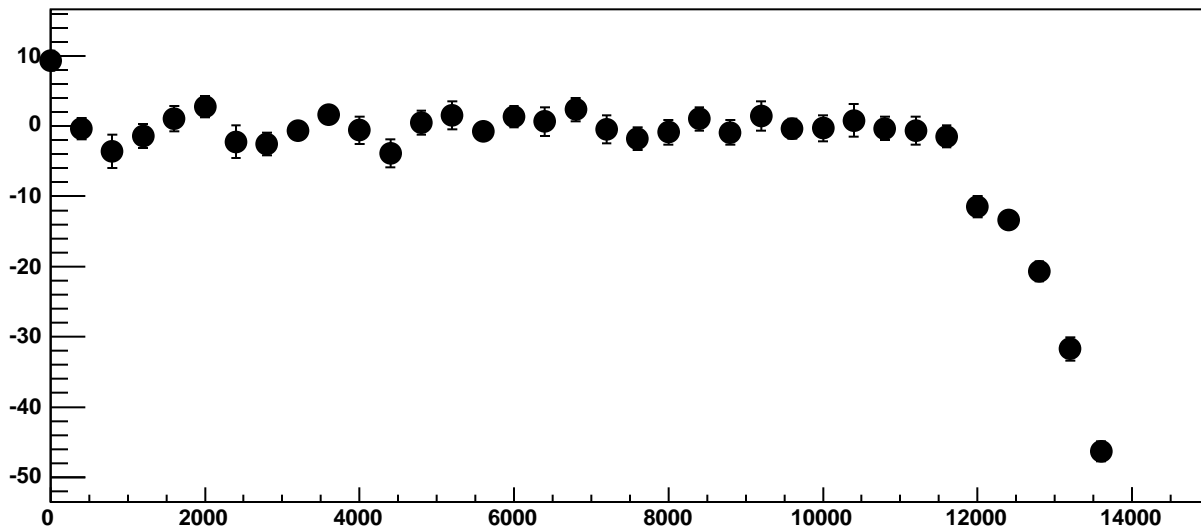


χ^2 / ndf 20.18 / 23
p0 -438.3 ± 0.7751
p1 0.0235 ± 0.0001198

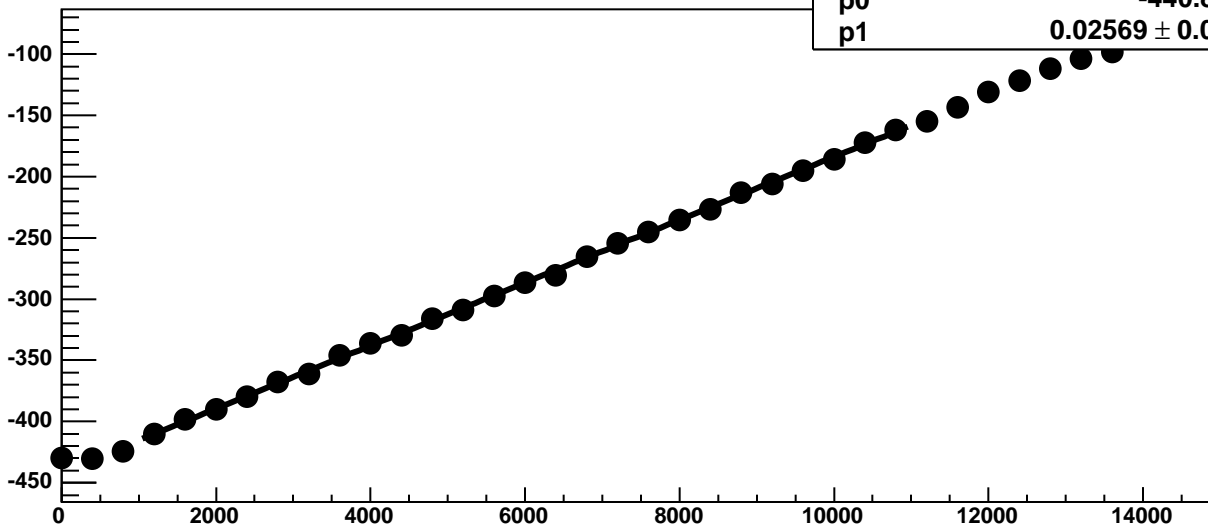
Chip 11, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

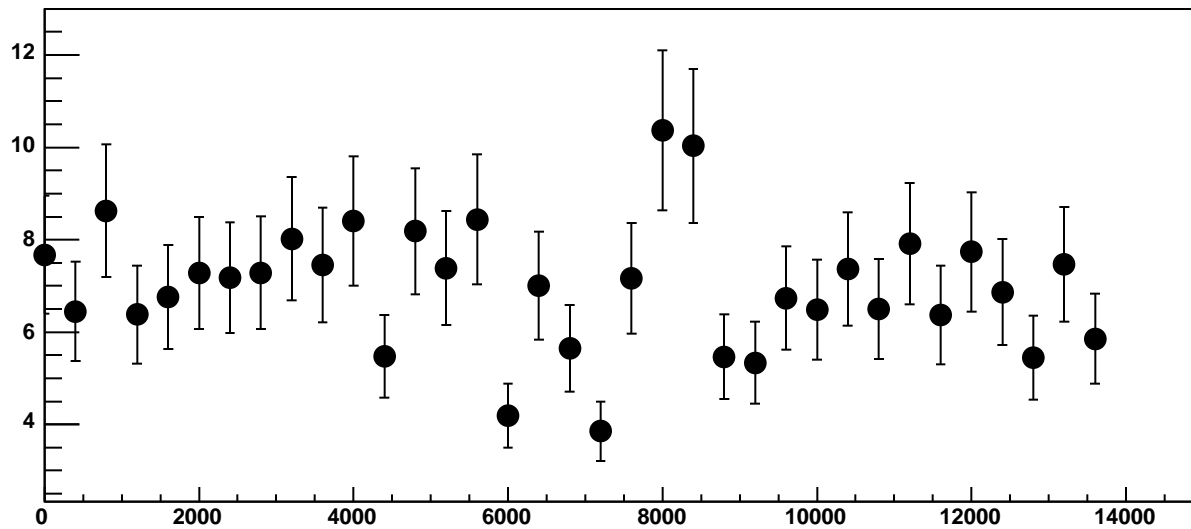


Chip 11, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

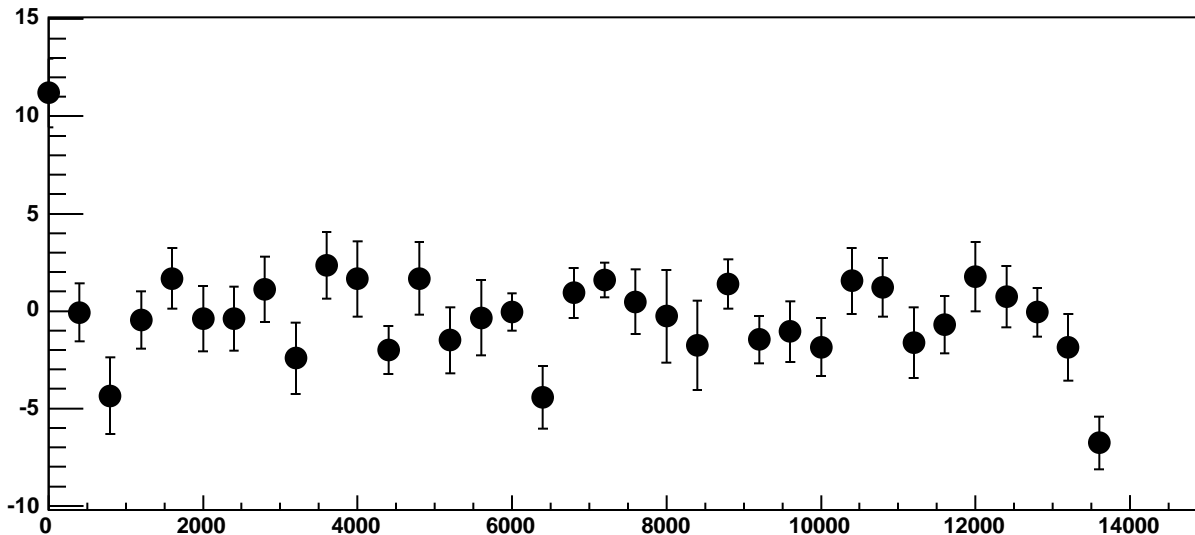


χ^2 / ndf 28.74 / 23
p0 -440.8 ± 0.73
p1 0.02569 ± 0.0001073

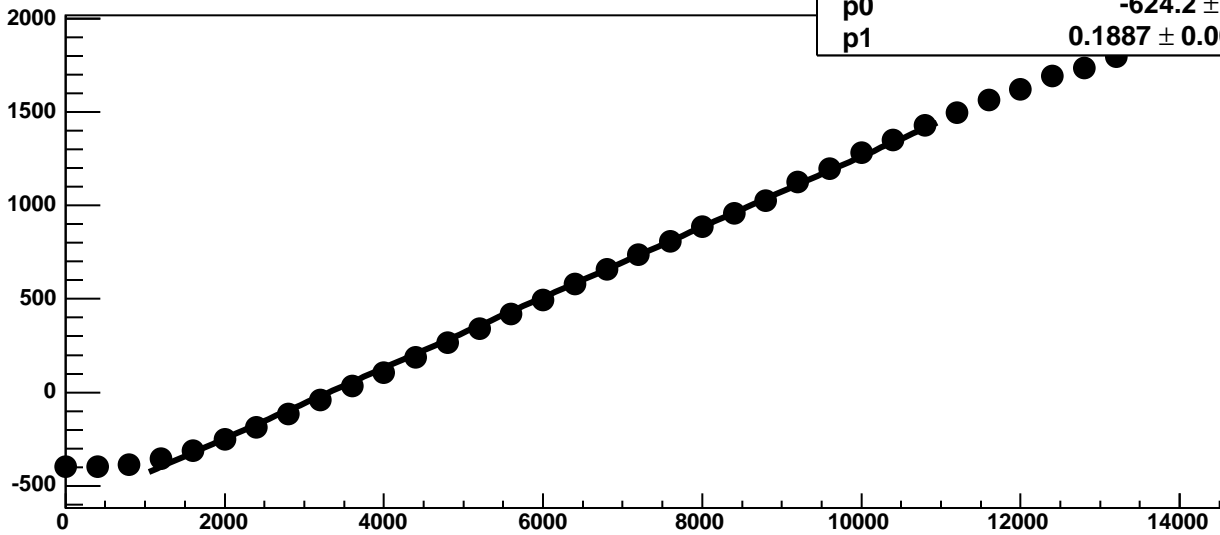
Chip 11, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



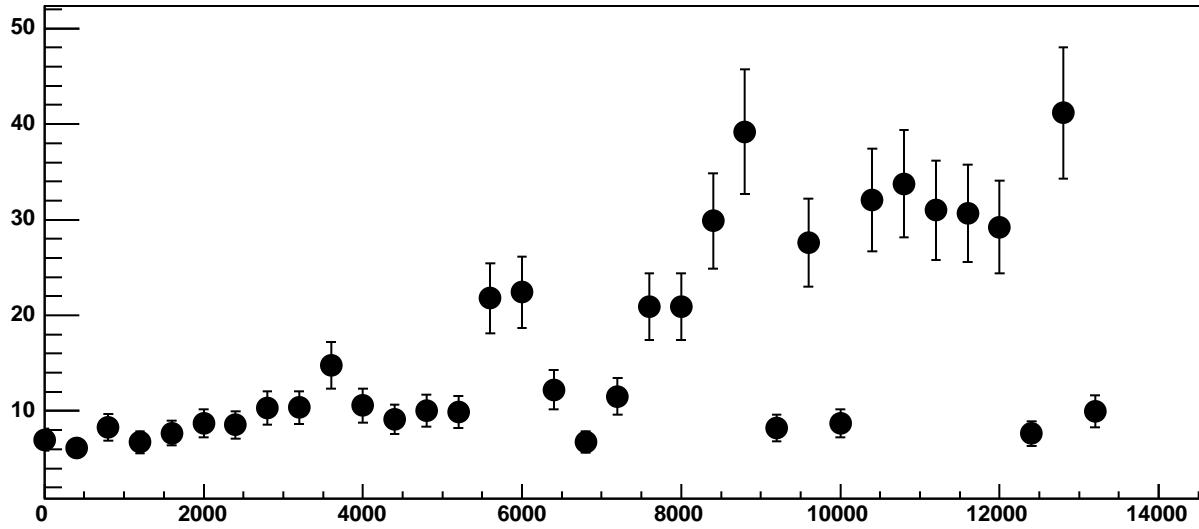
Chip 11, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



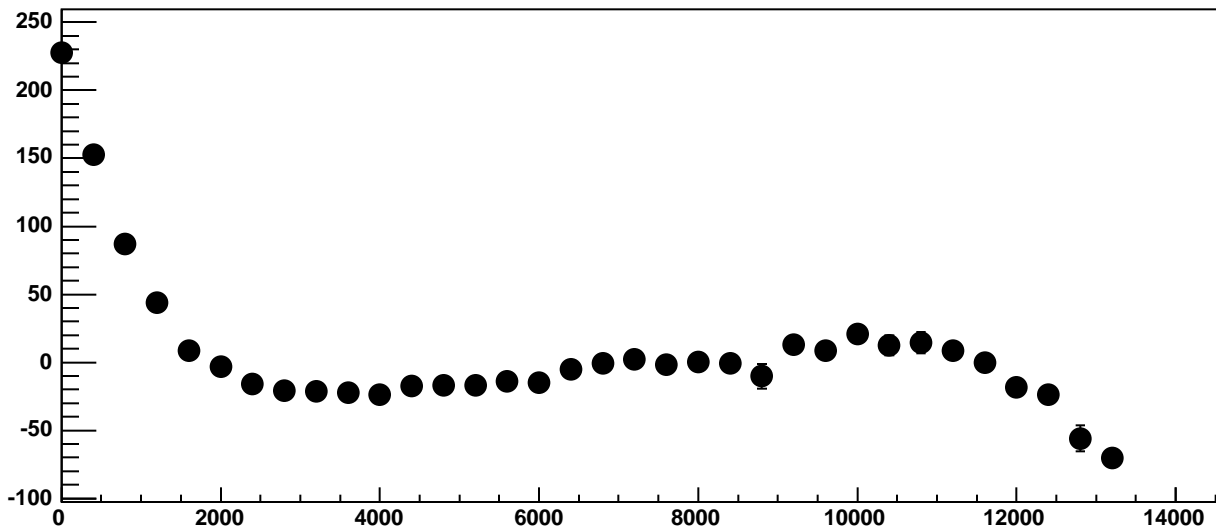
Chip 11, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC



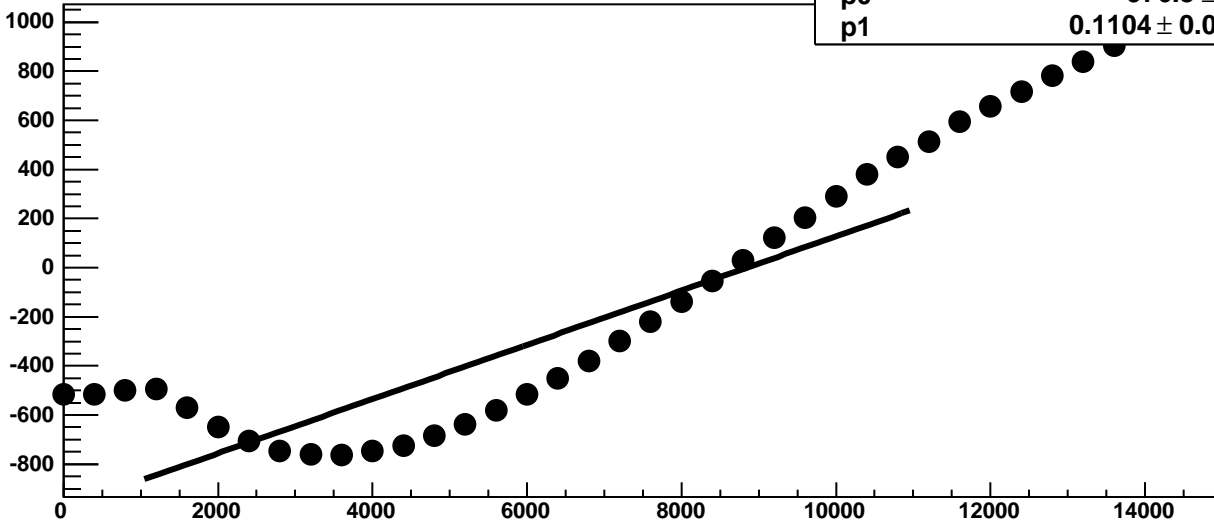
Chip 11, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



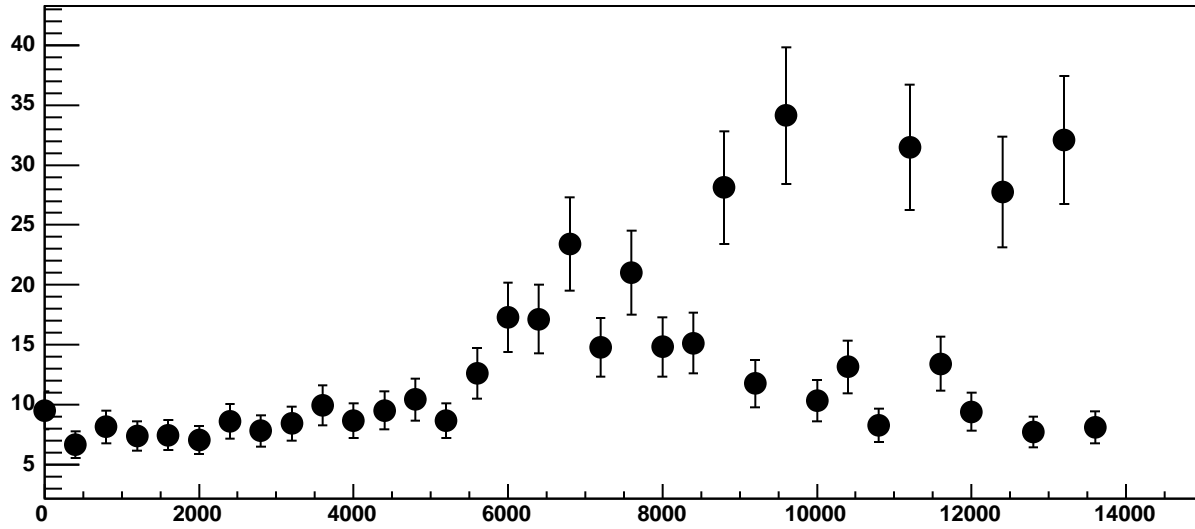
Chip 11, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC



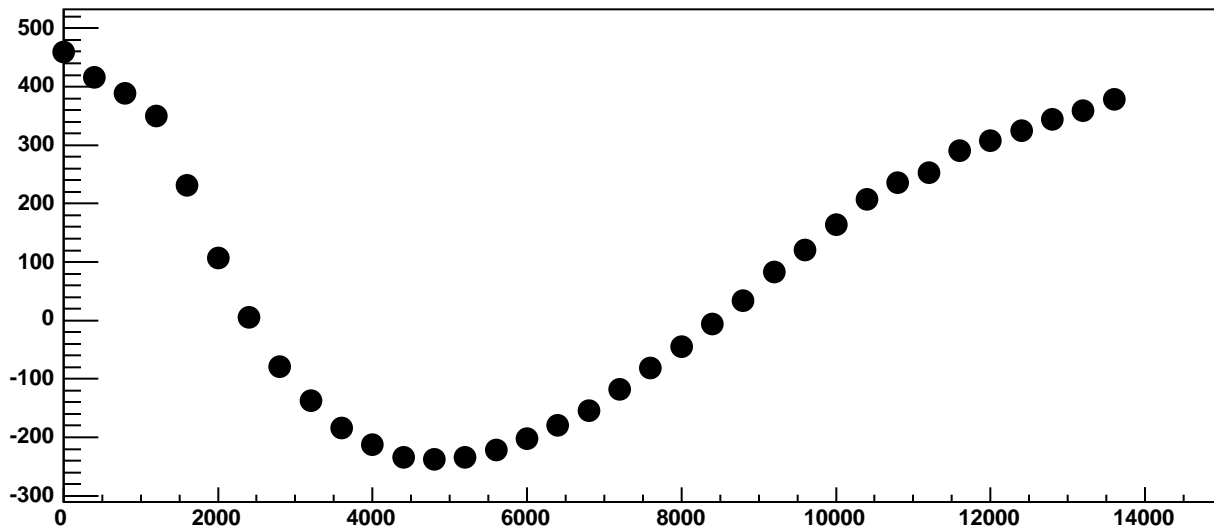
Chip 11, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



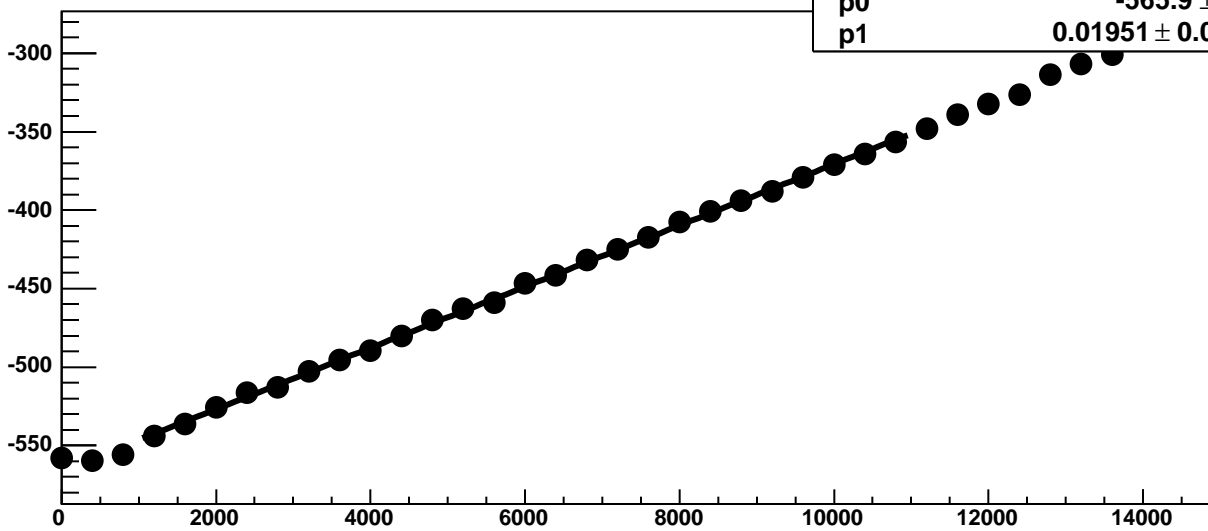
Chip 11, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

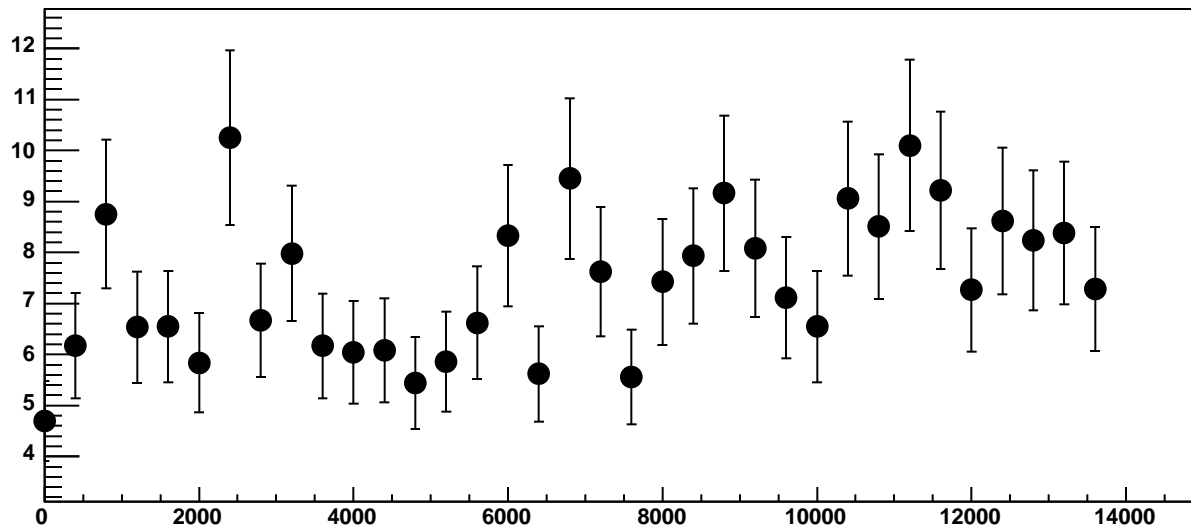


Chip 11, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

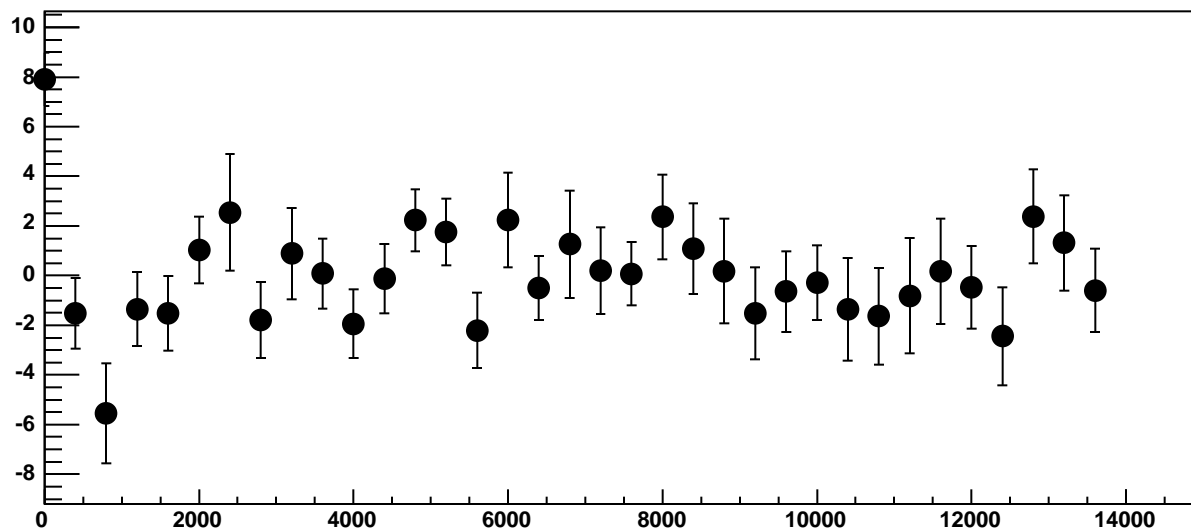


χ^2 / ndf 20.39 / 23
p0 -565.9 ± 0.7222
p1 0.01951 ± 0.0001148

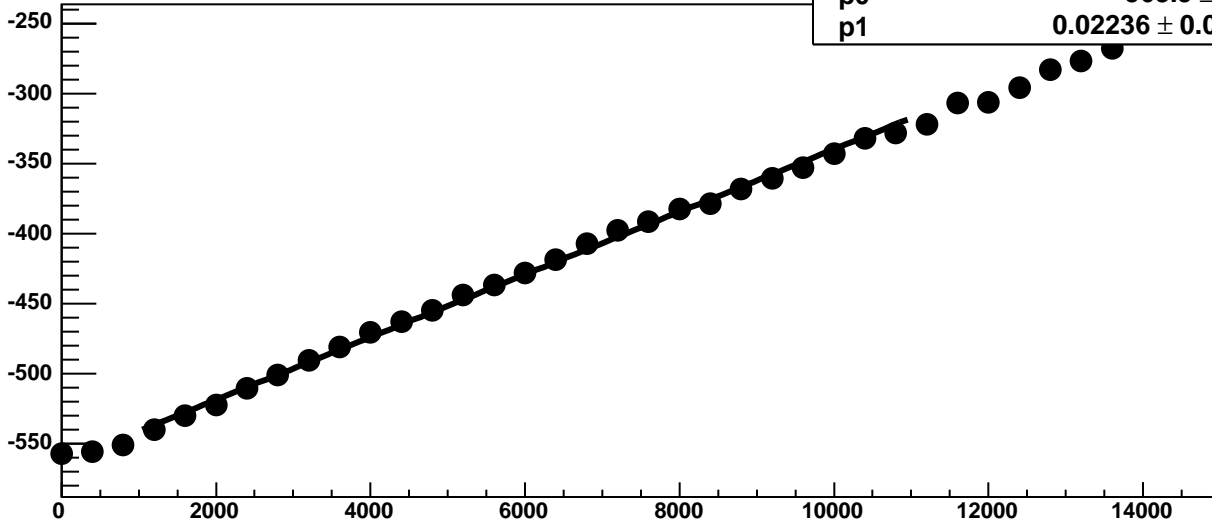
Chip 11, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



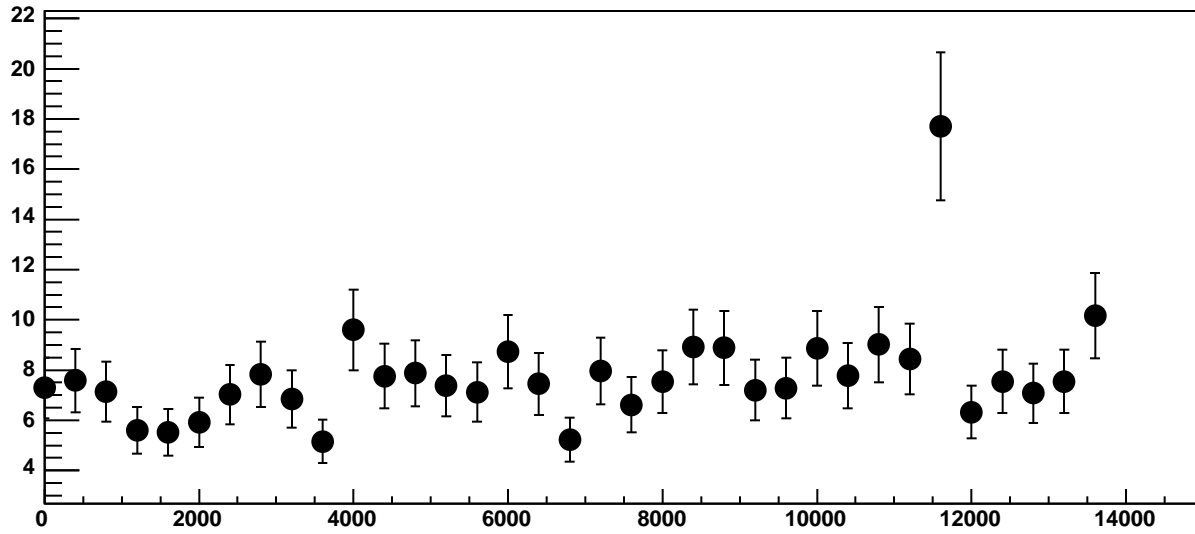
Chip 11, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



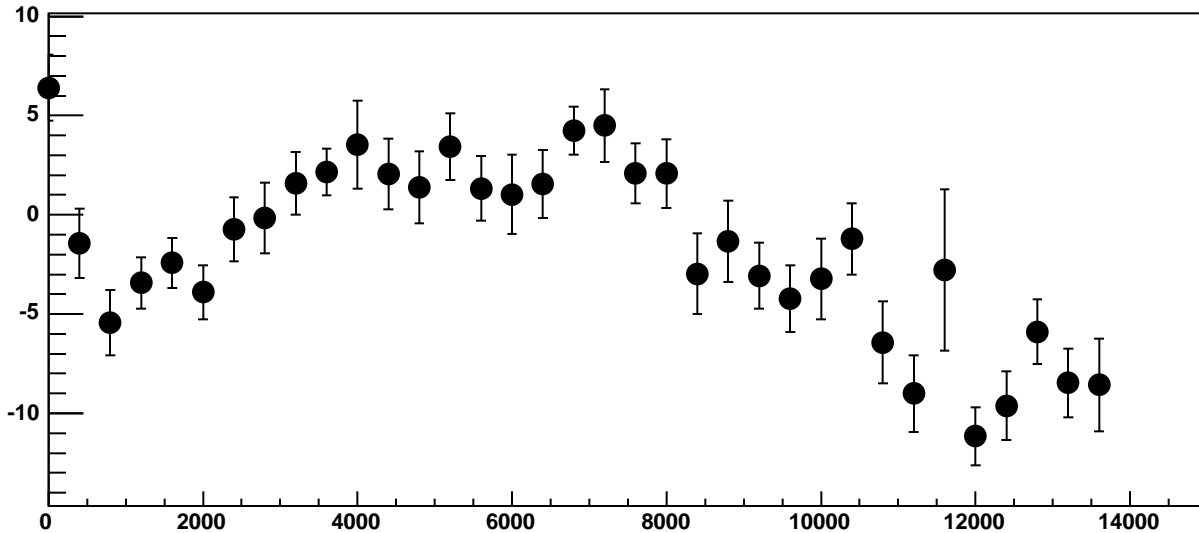
Chip 11, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC



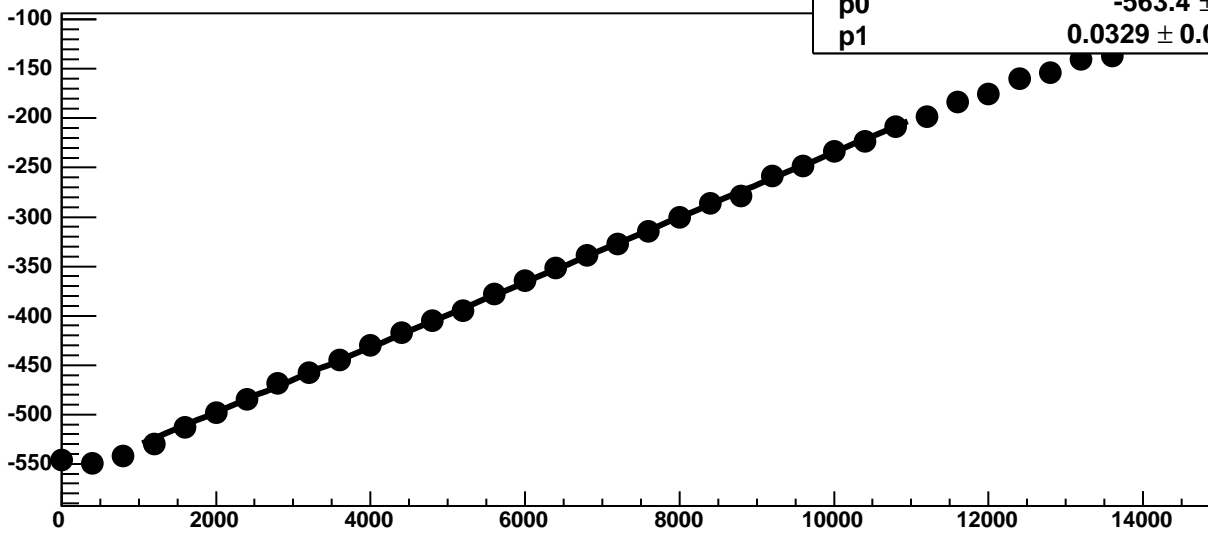
Chip 11, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



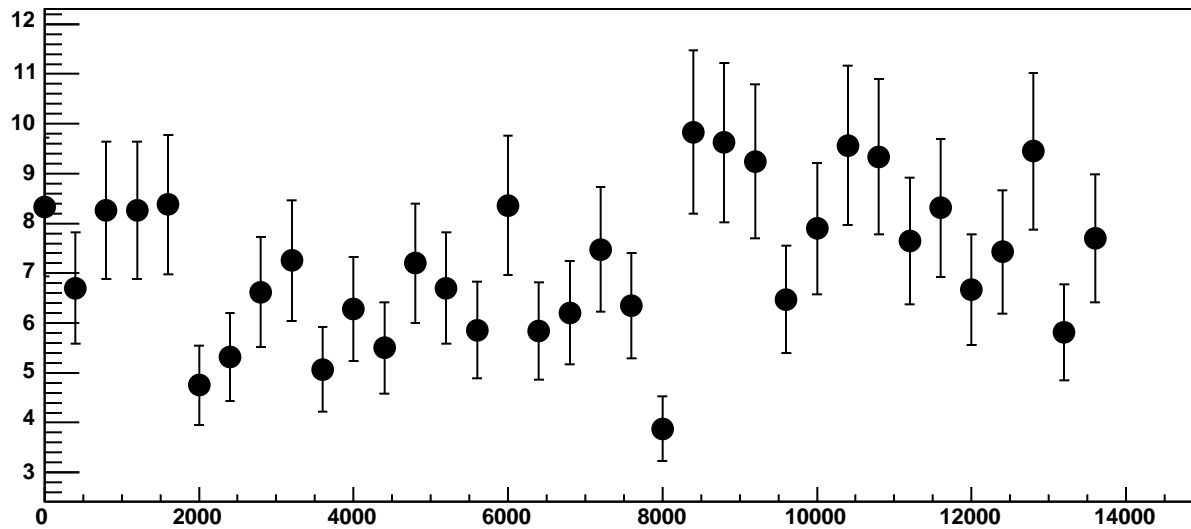
Chip 11, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC



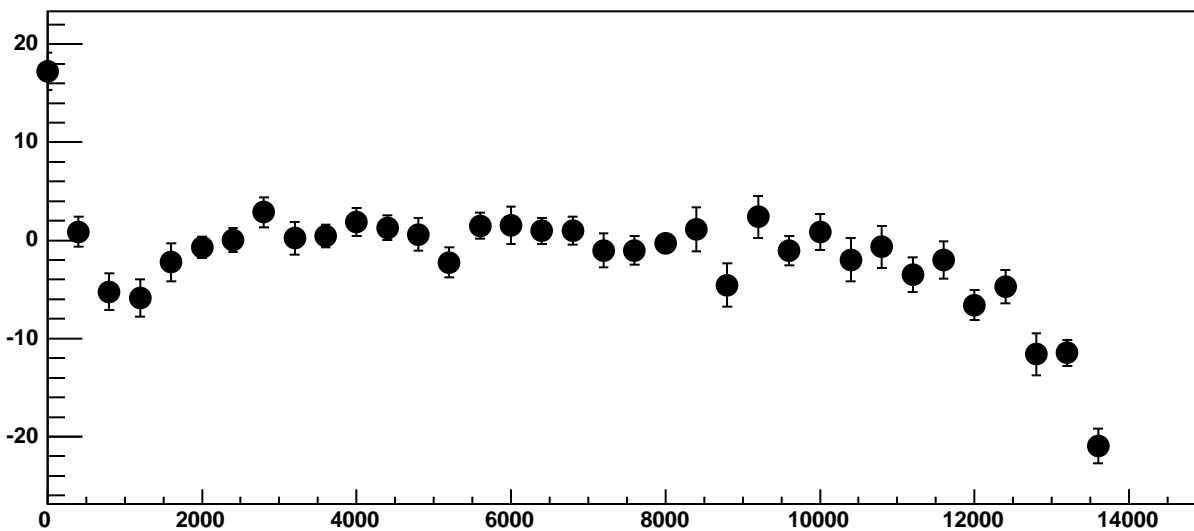
Chip 11, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



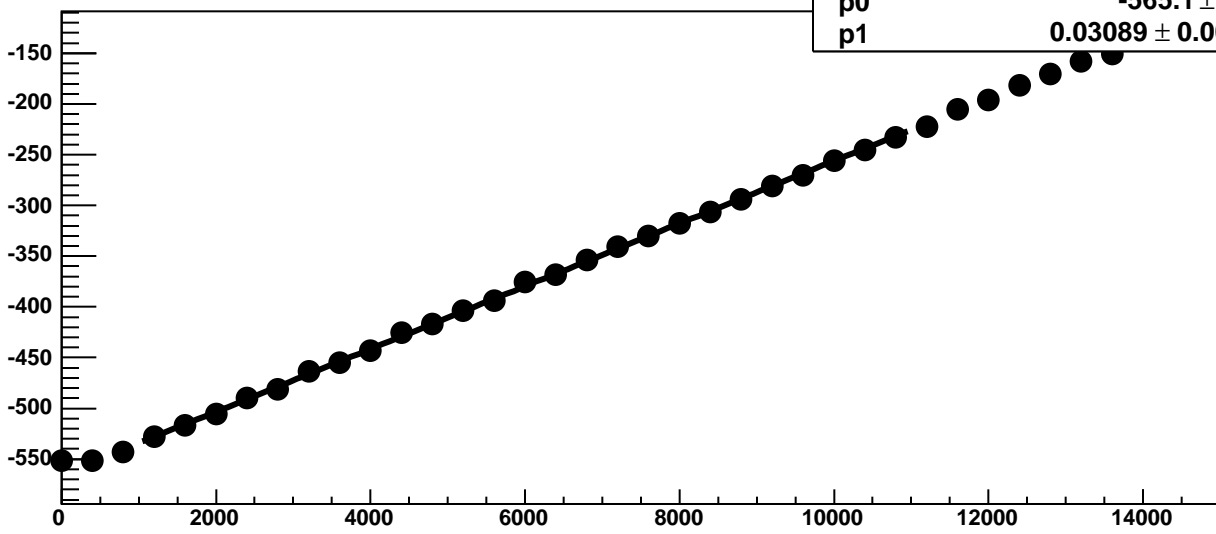
Chip 11, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

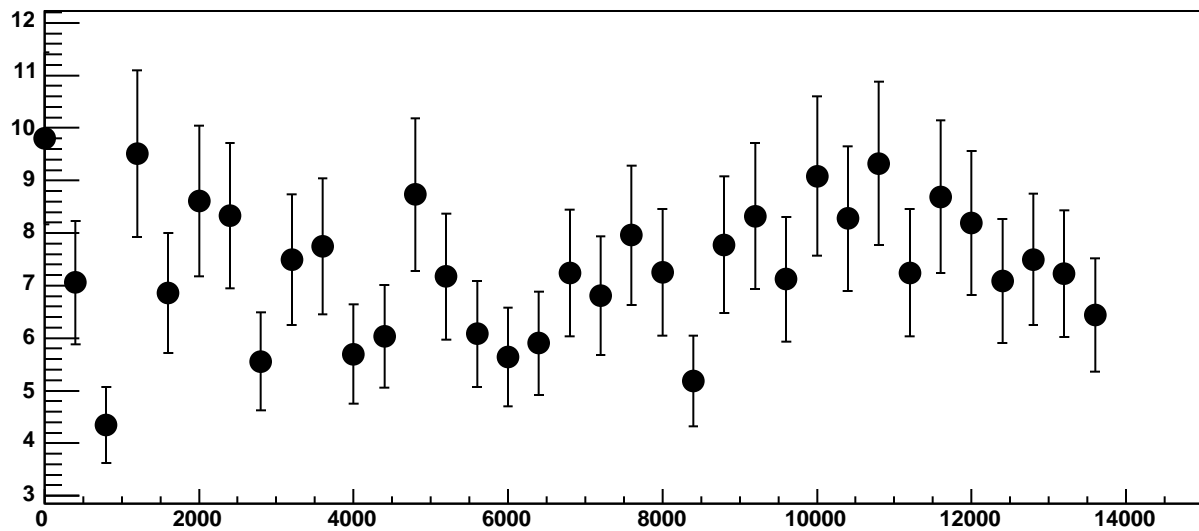


Chip 11, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

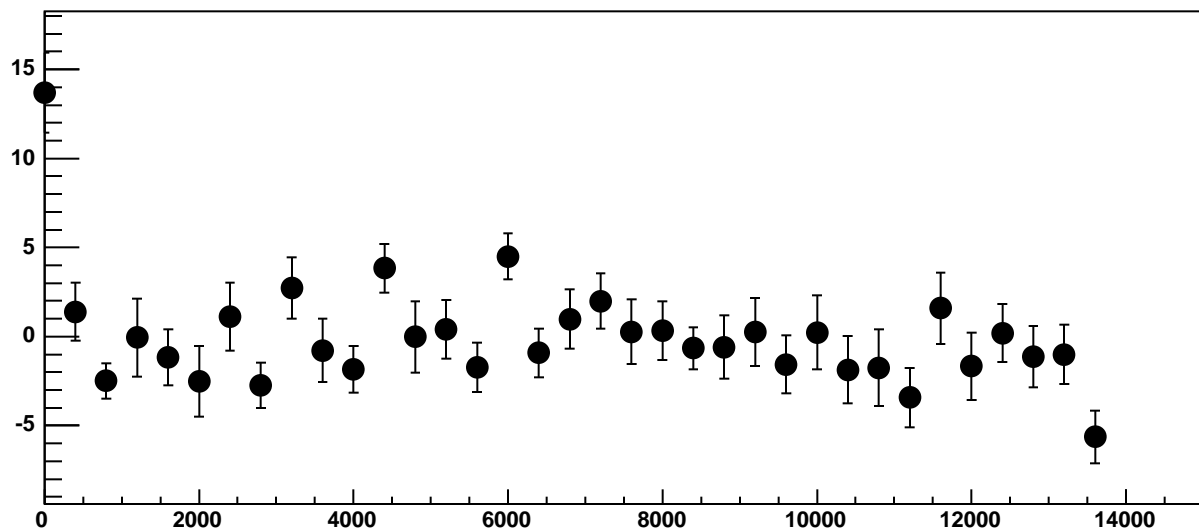


χ^2 / ndf 39.05 / 23
p0 -565.1 ± 0.7887
p1 0.03089 ± 0.0001224

Chip 11, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 11, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

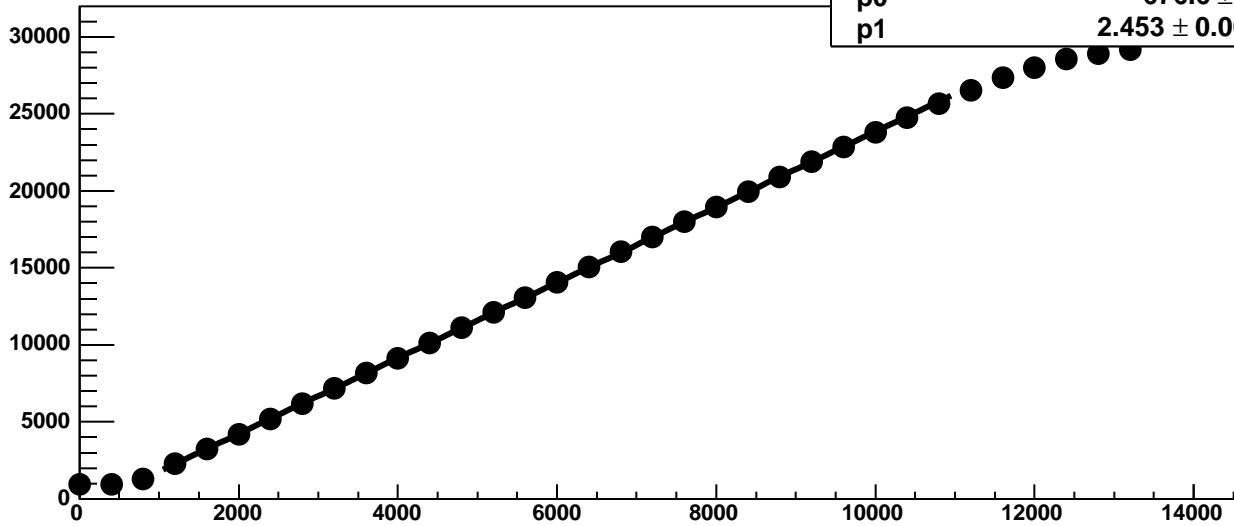
2595 / 23

p0

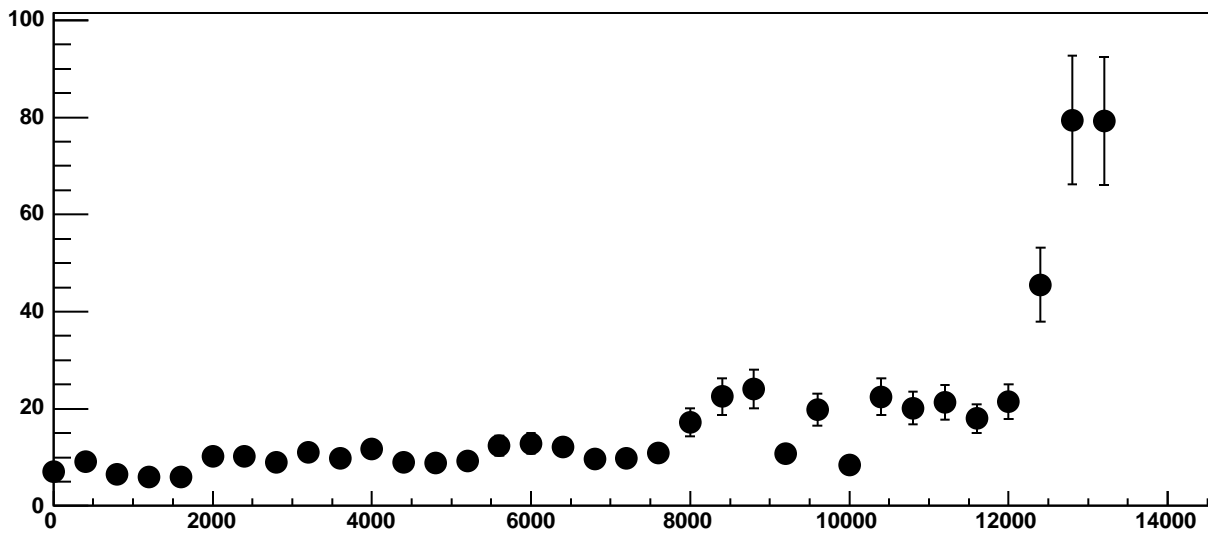
-676.6 ± 0.9075

p1

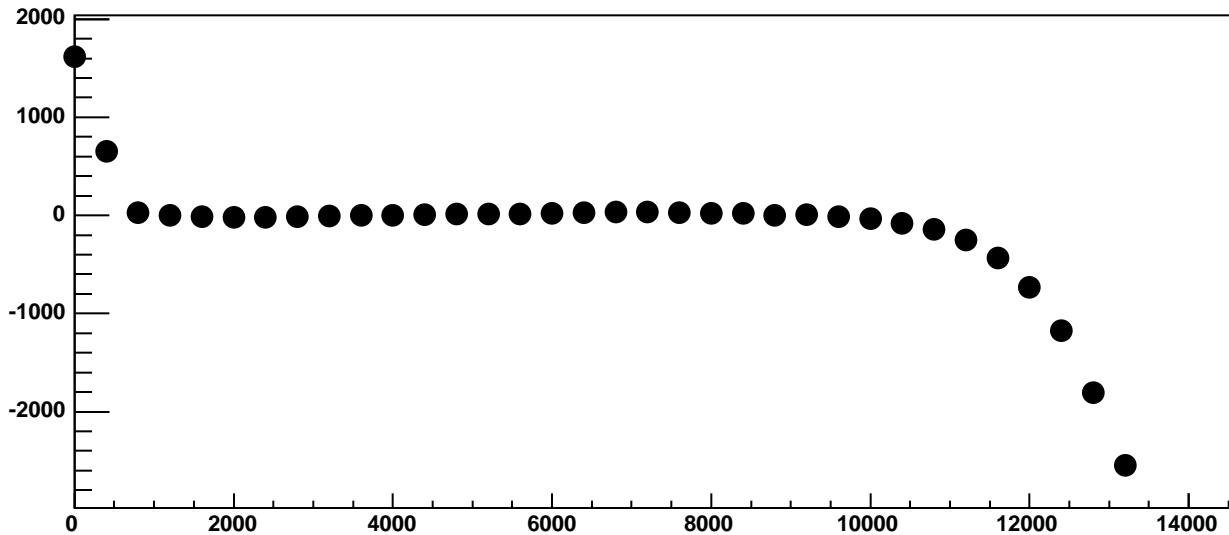
2.453 ± 0.0001657



Chip 11, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC

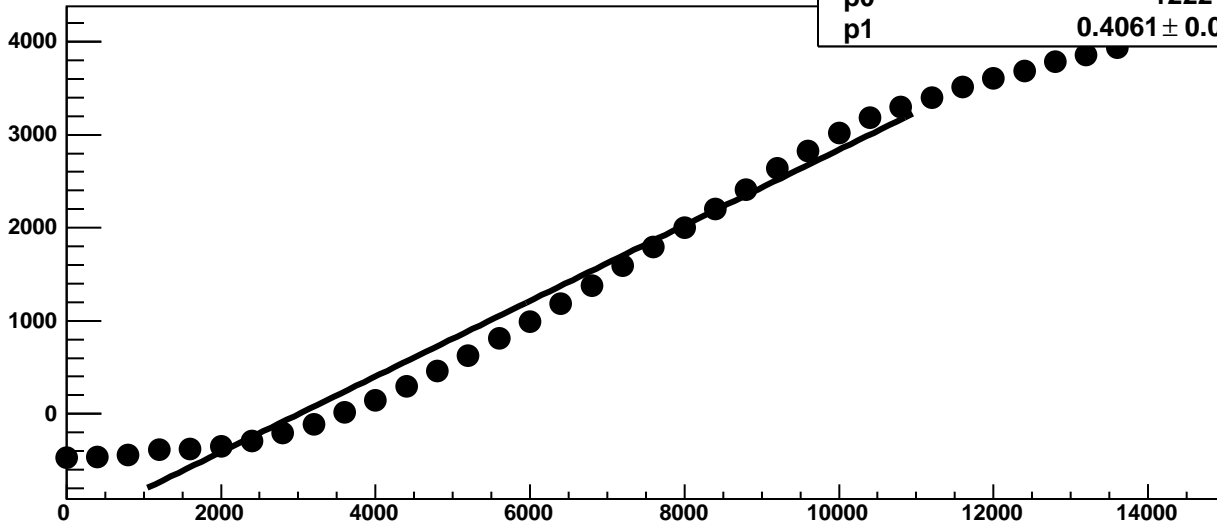


Chip 11, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC

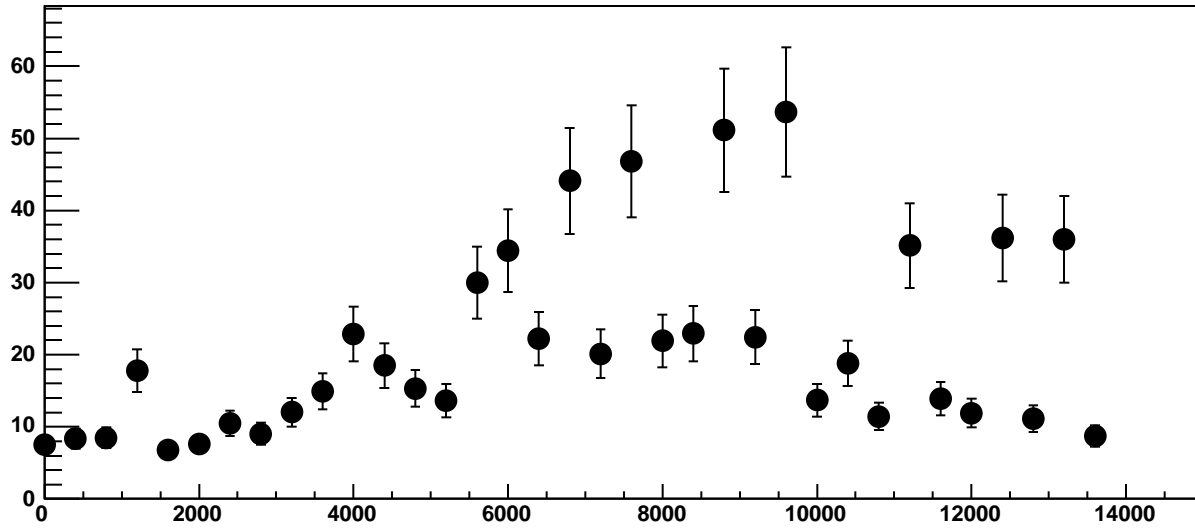


Chip 11, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

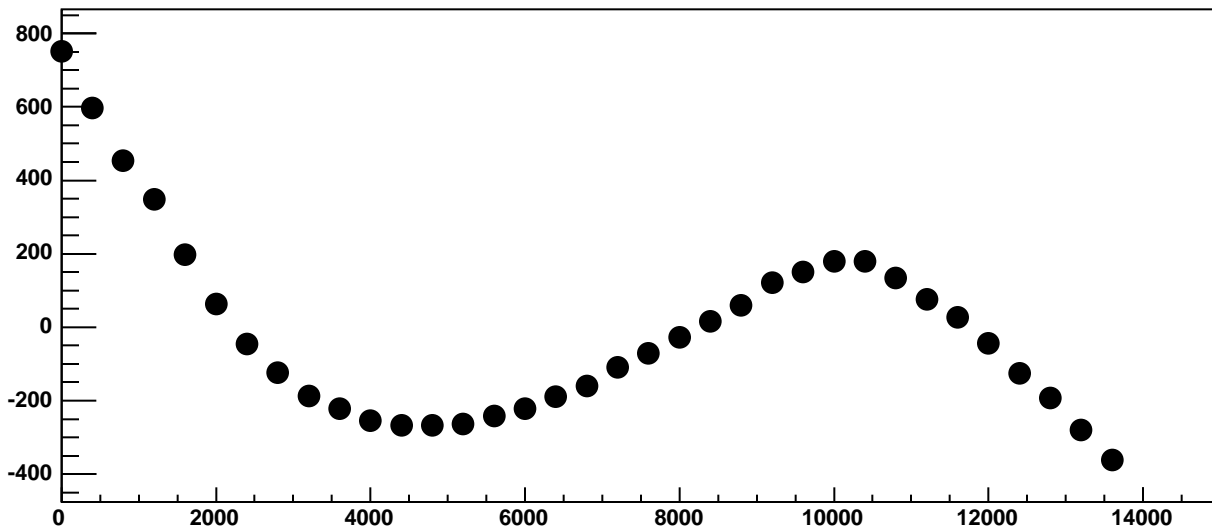
χ^2 / ndf 6.918e+04 / 23
p0 -1222 ± 1.135
p1 0.4061 ± 0.0002161



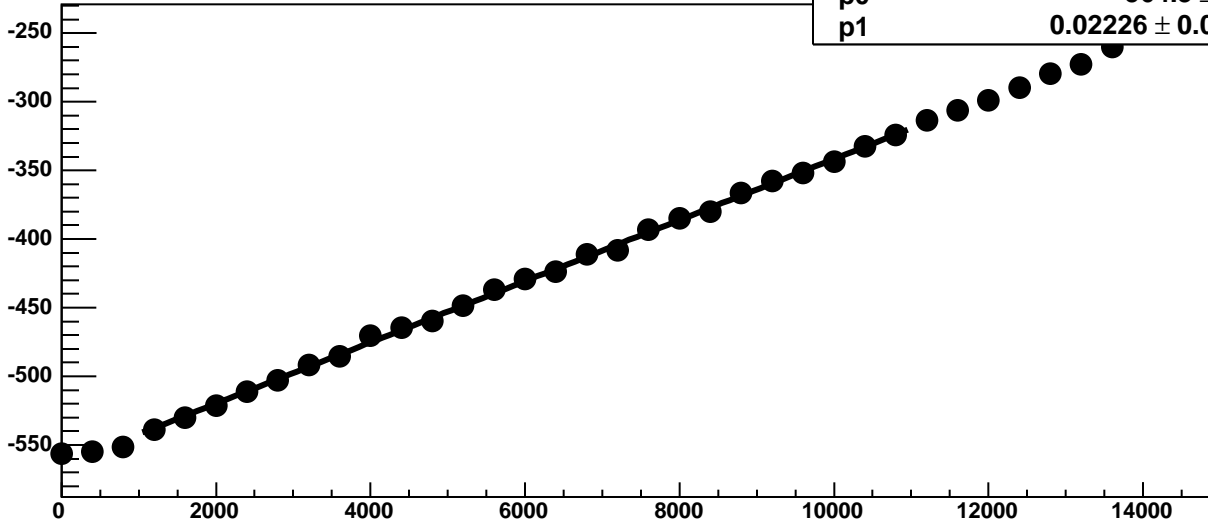
Chip 11, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

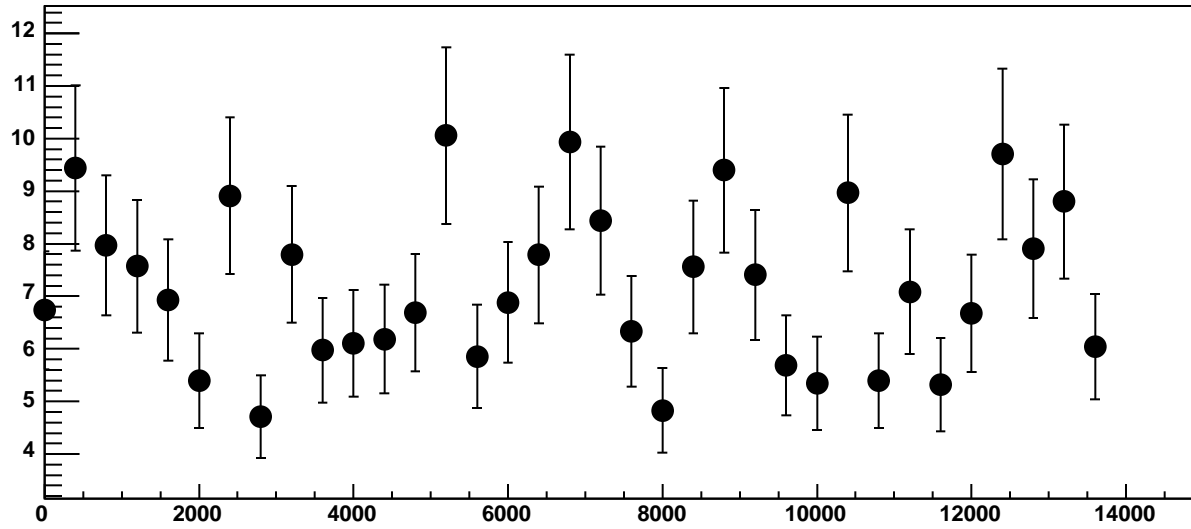


Chip 11, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

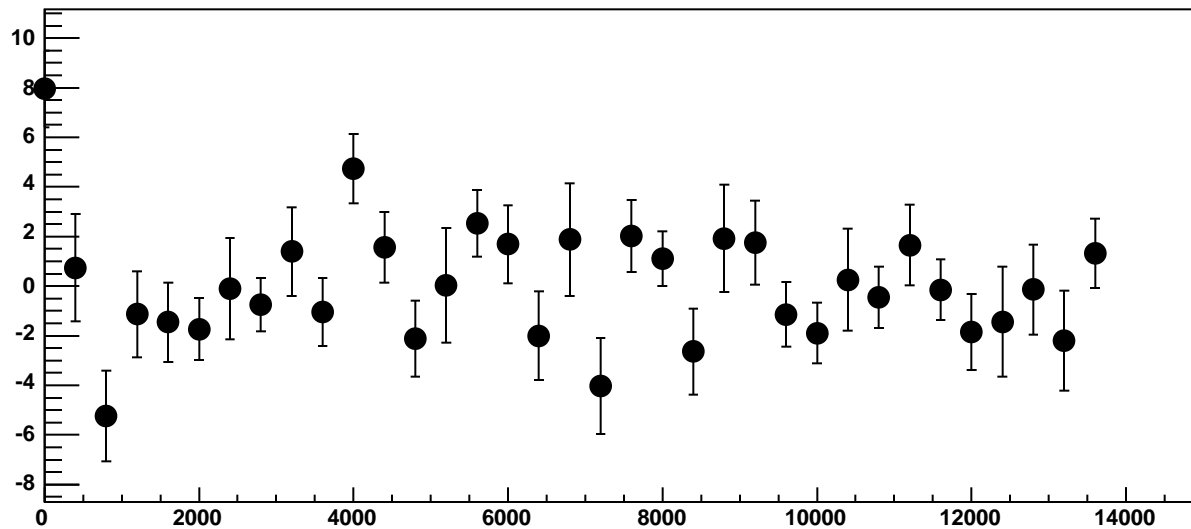


χ^2 / ndf 40.88 / 23
p0 -564.3 ± 0.6795
p1 0.02226 ± 0.0001014

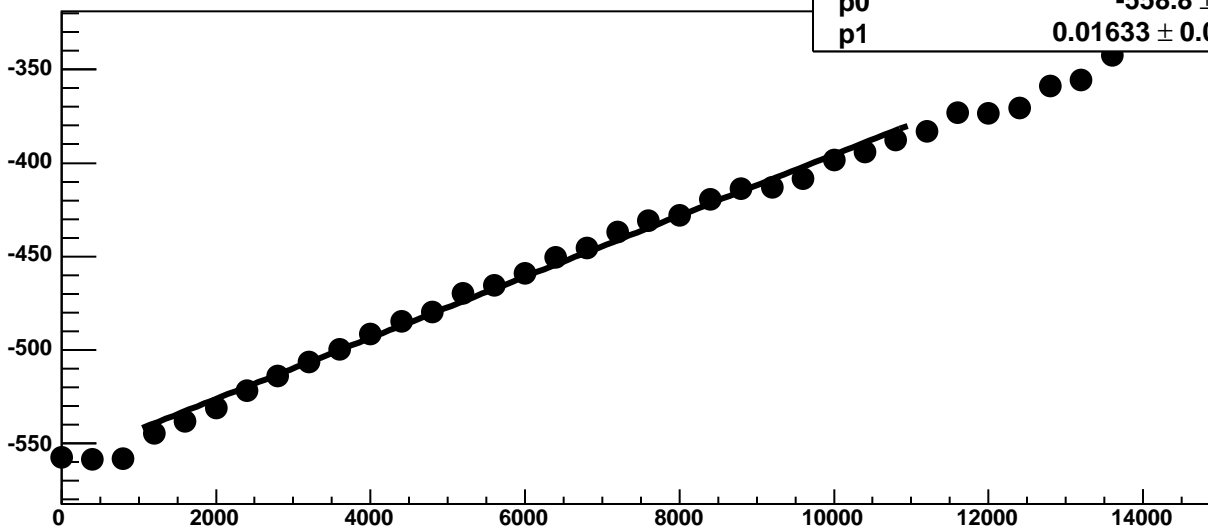
Chip 11, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

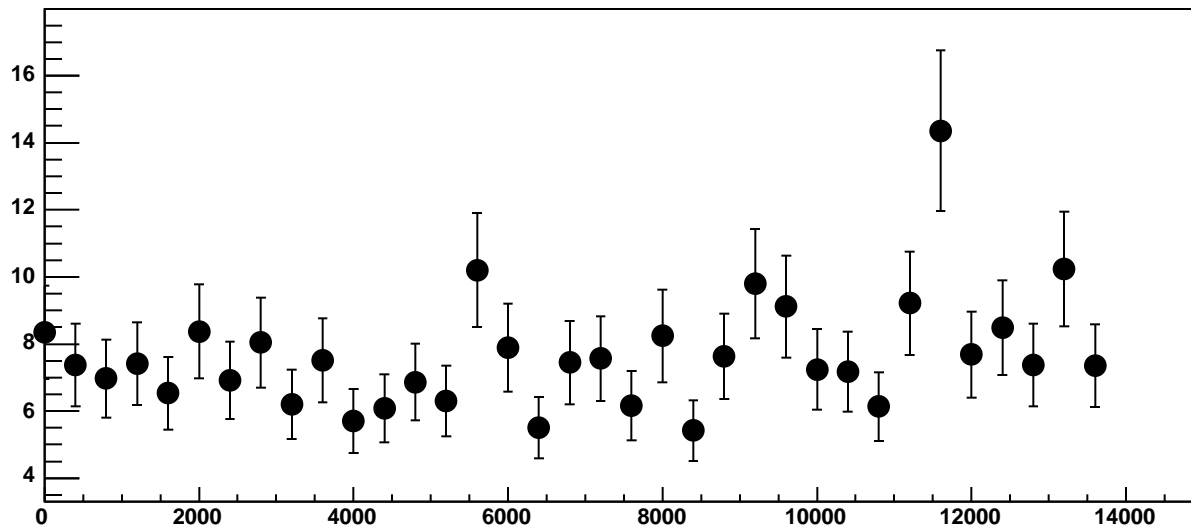


Chip 11, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

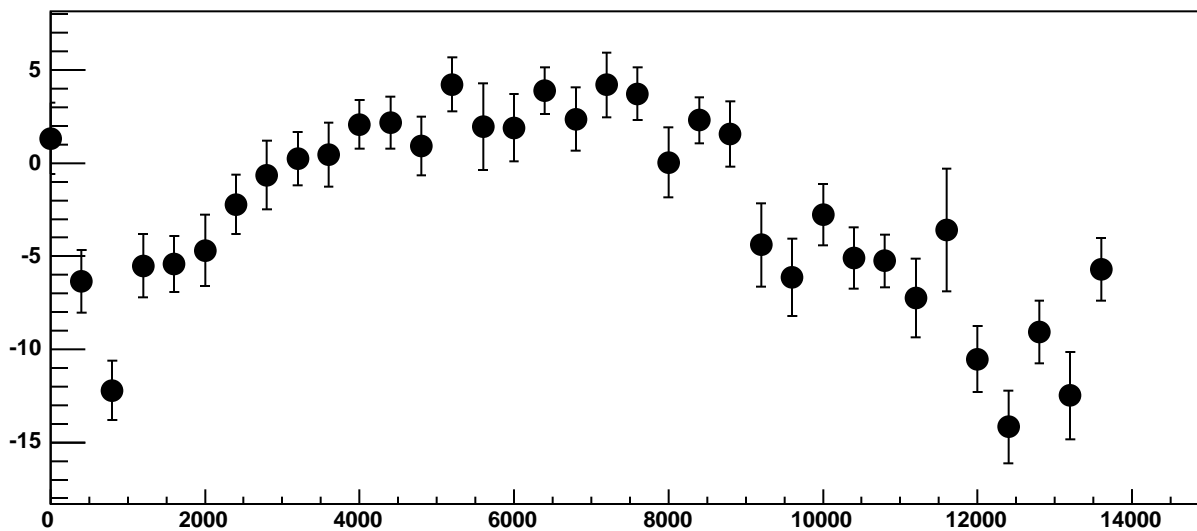


χ^2 / ndf 114.5 / 23
p0 -558.8 ± 0.7442
p1 0.01633 ± 0.0001129

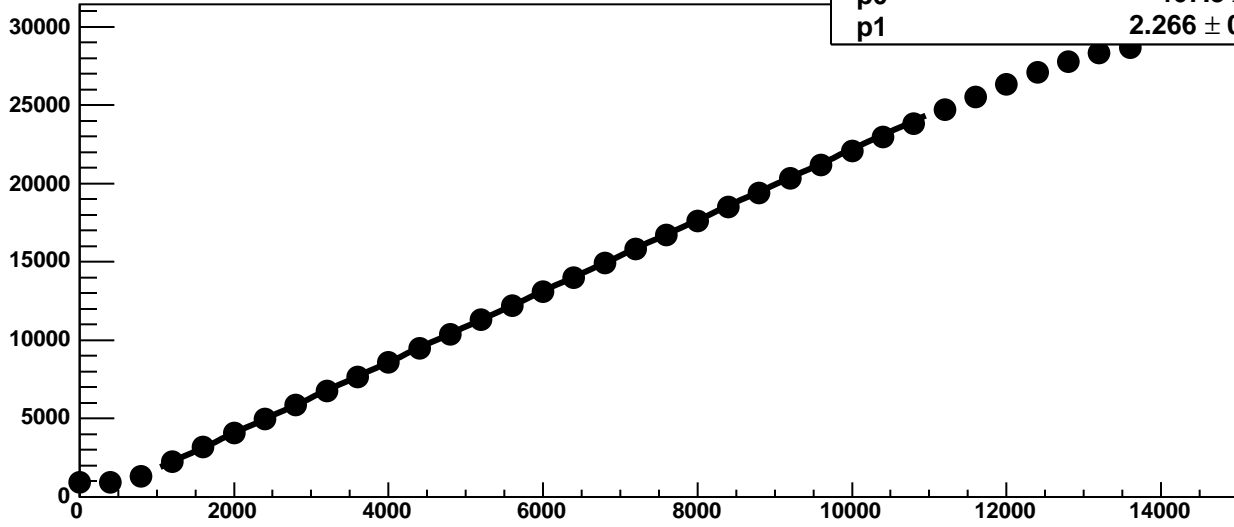
Chip 11, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



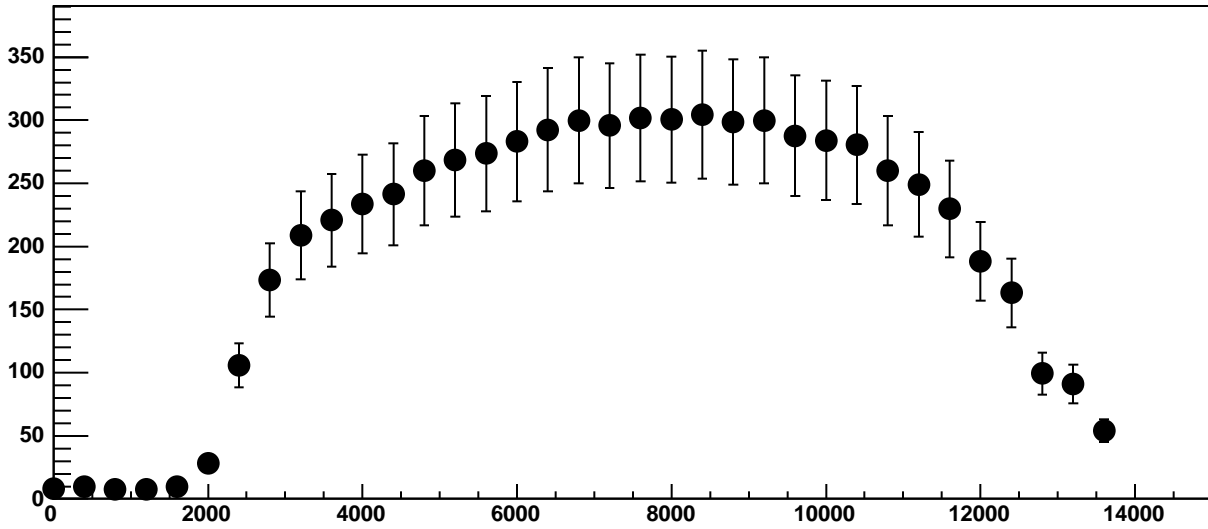
Chip 11, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



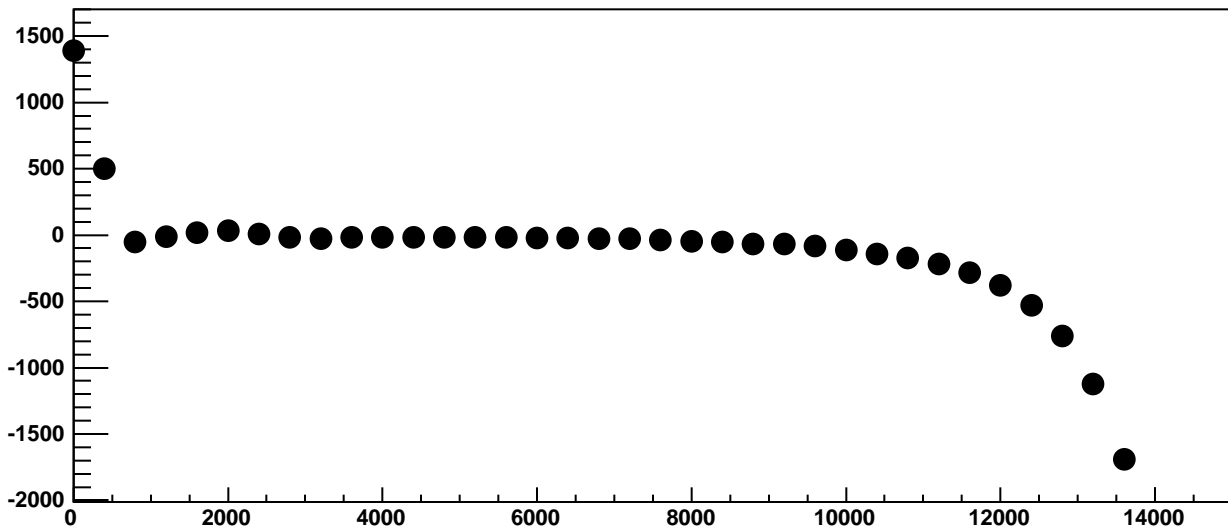
Chip 11, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC



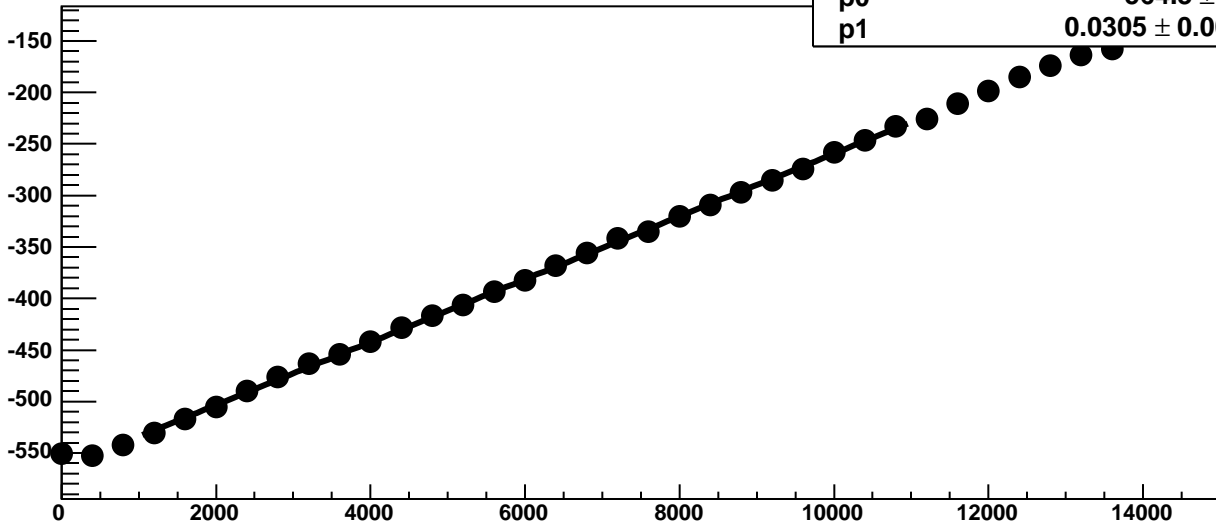
Chip 11, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



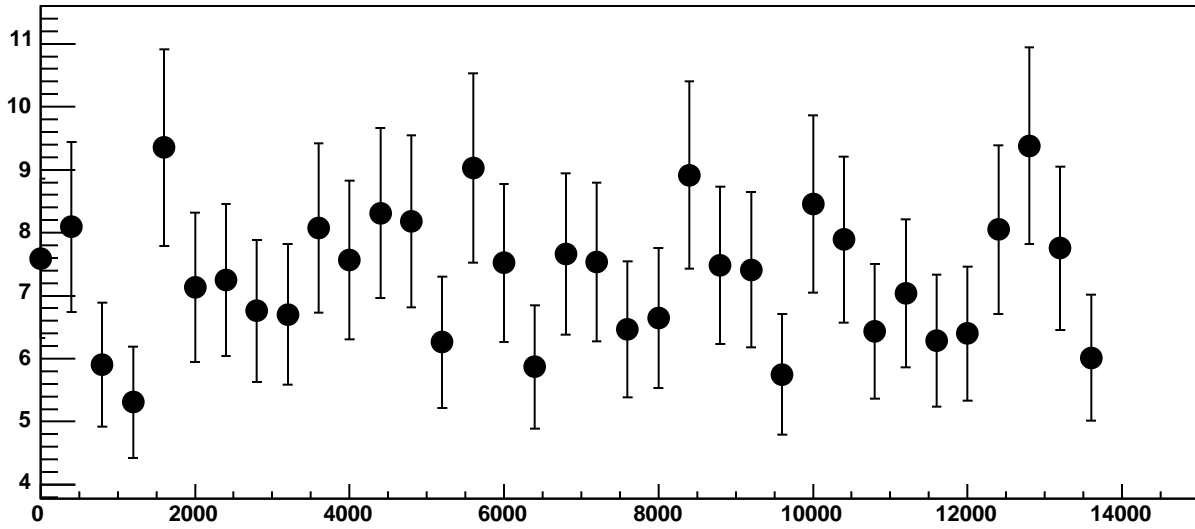
Chip 11, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC



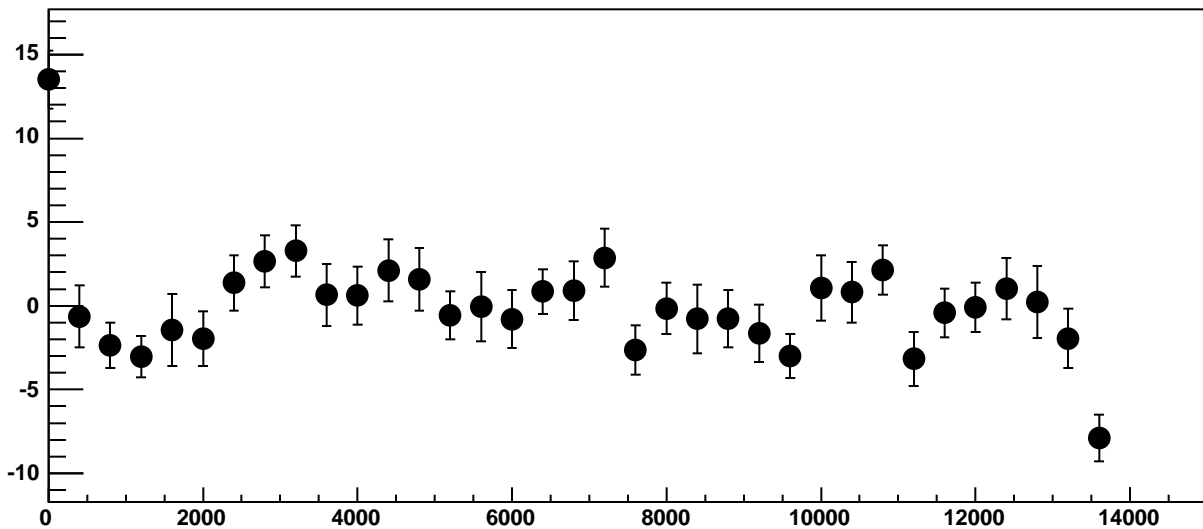
Chip 11, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



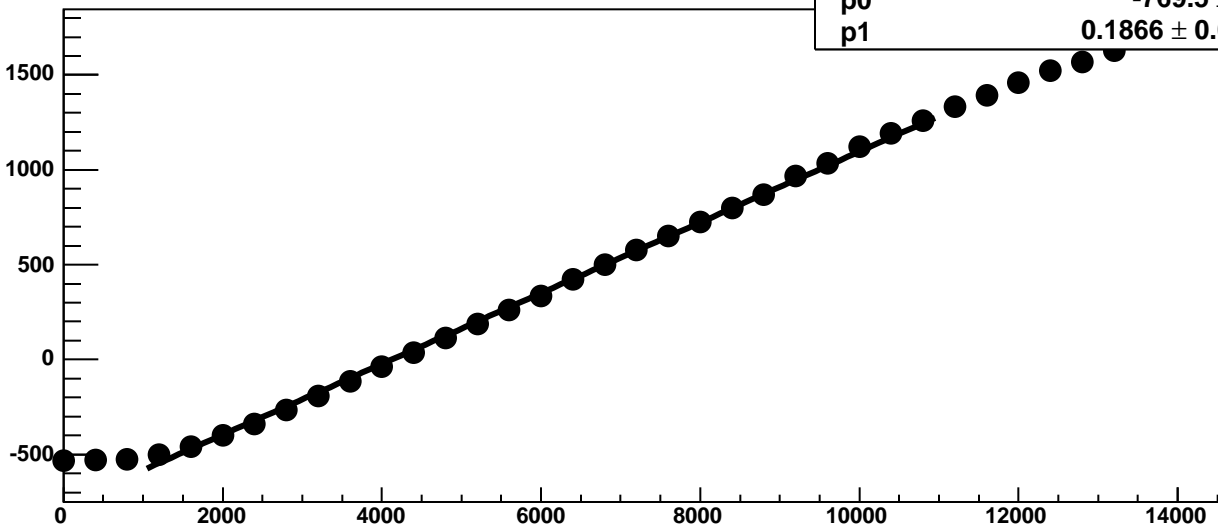
Chip 11, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



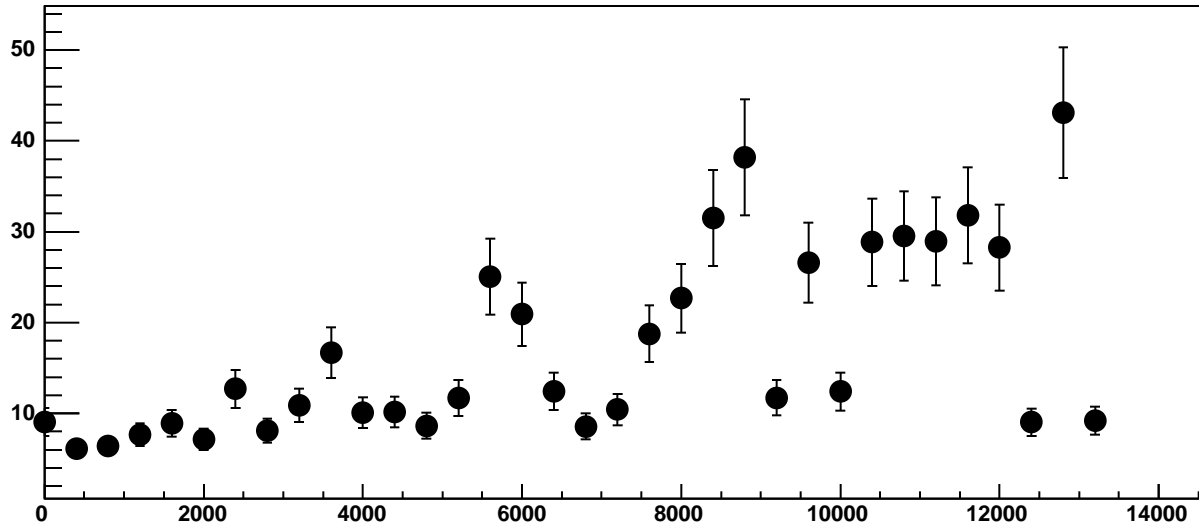
Chip 11, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



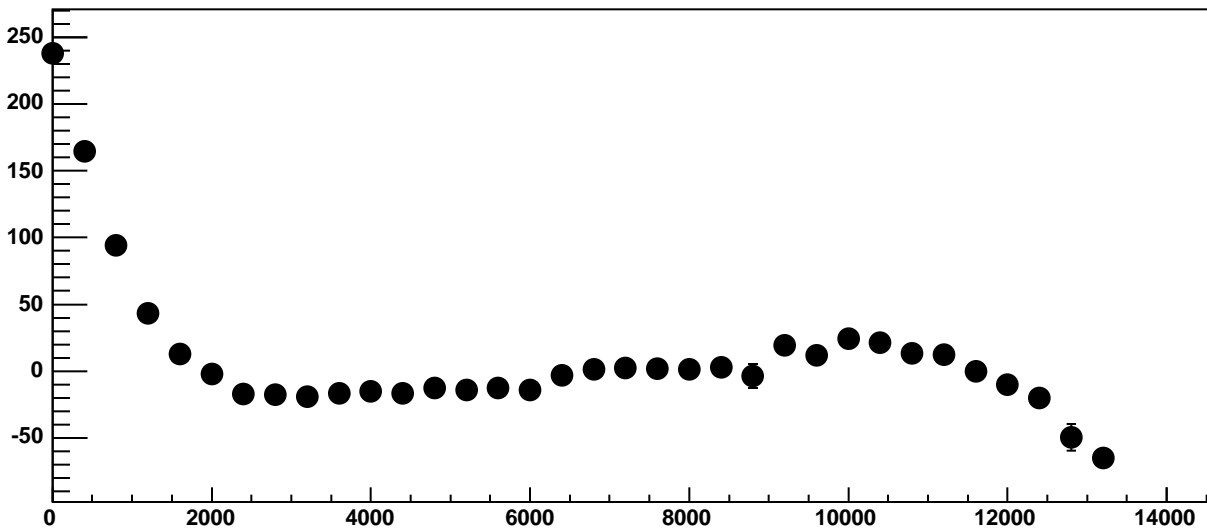
Chip 11, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



Chip 11, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



Chip 11, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

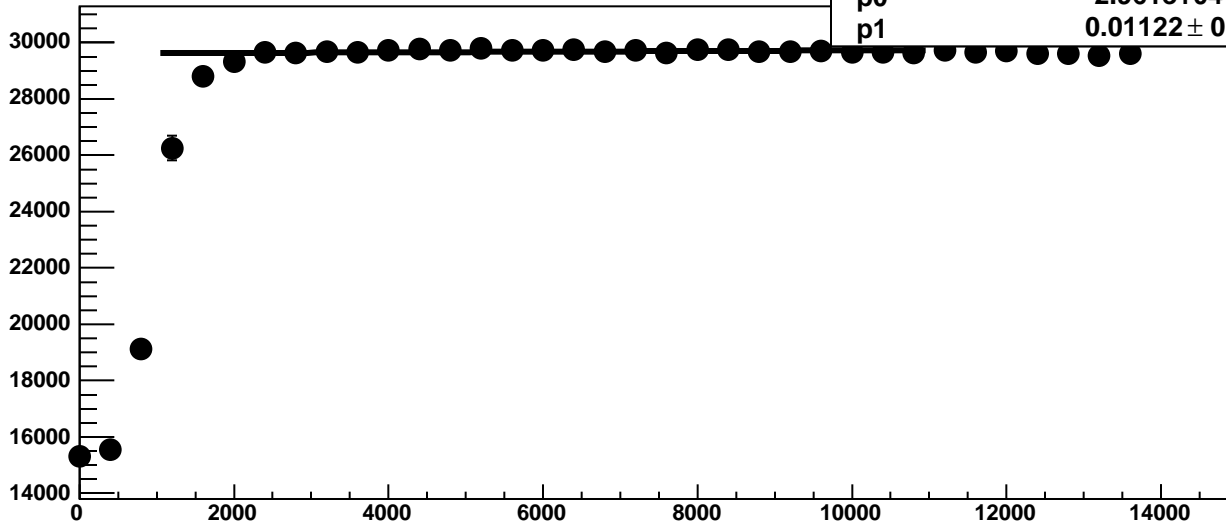
304.4 / 23

p0

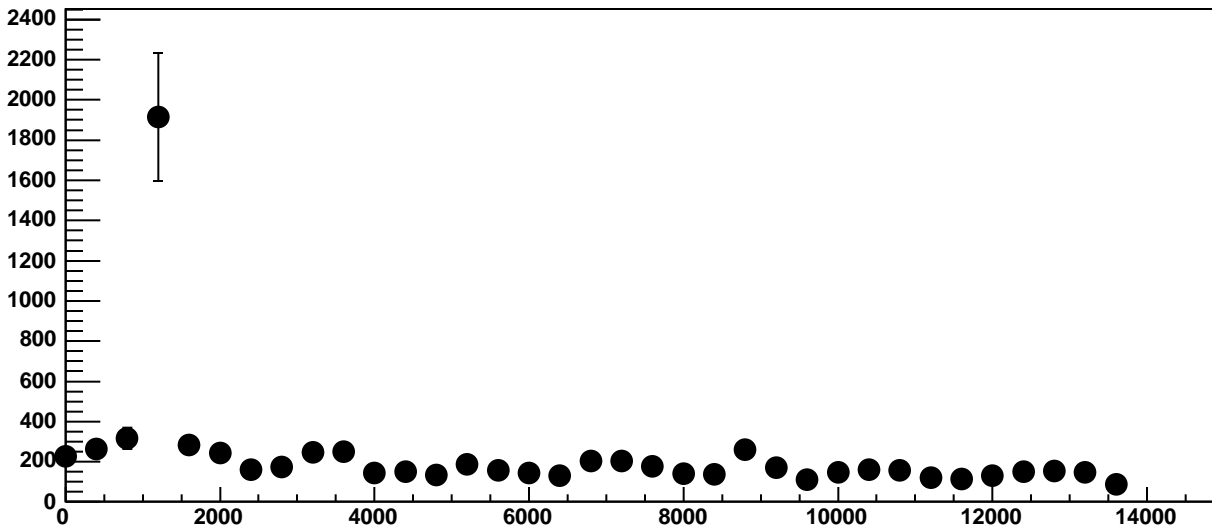
$2.961\text{e}+04 \pm 21.12$

p1

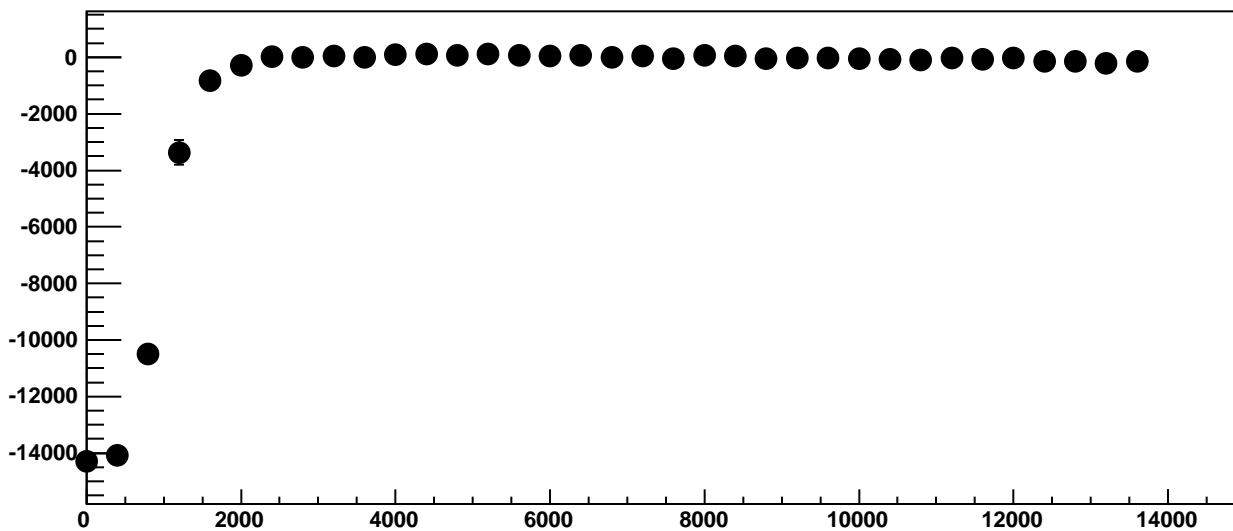
0.01122 ± 0.002936



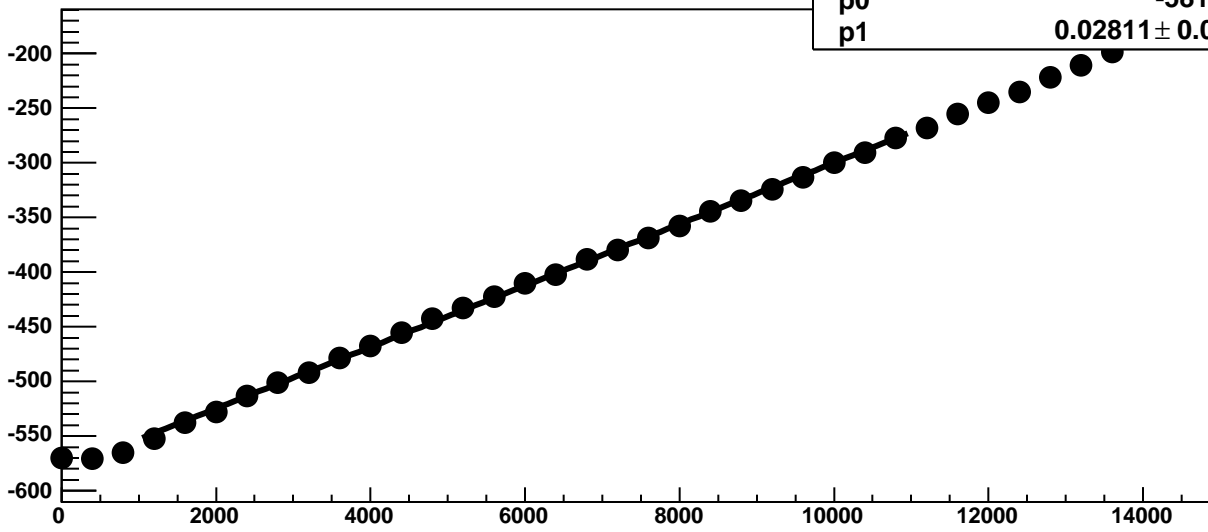
Chip 11, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

27.15 / 23

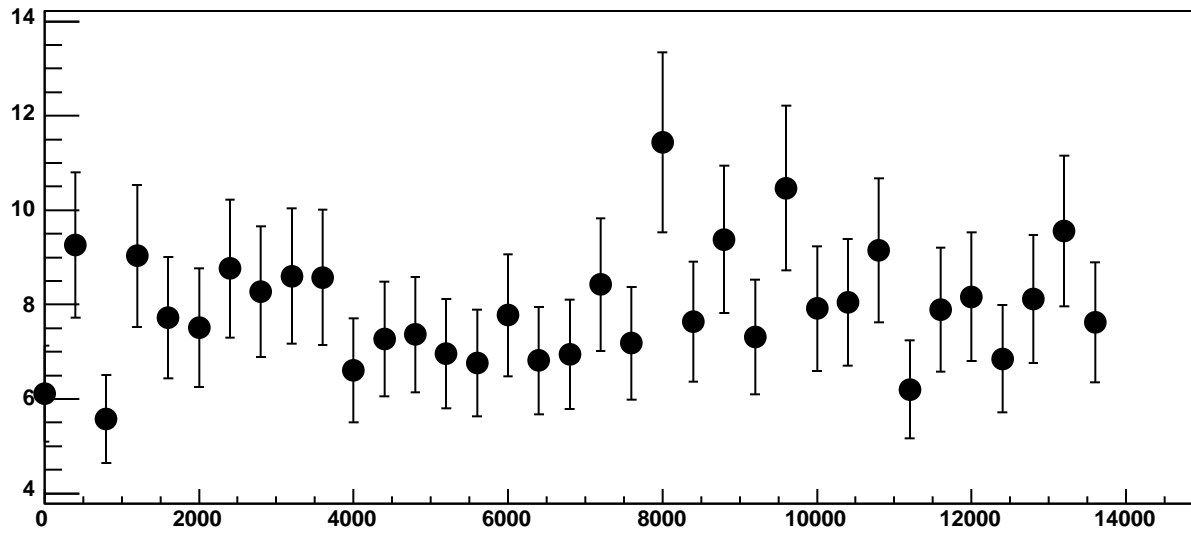
p0

-581 ± 0.857

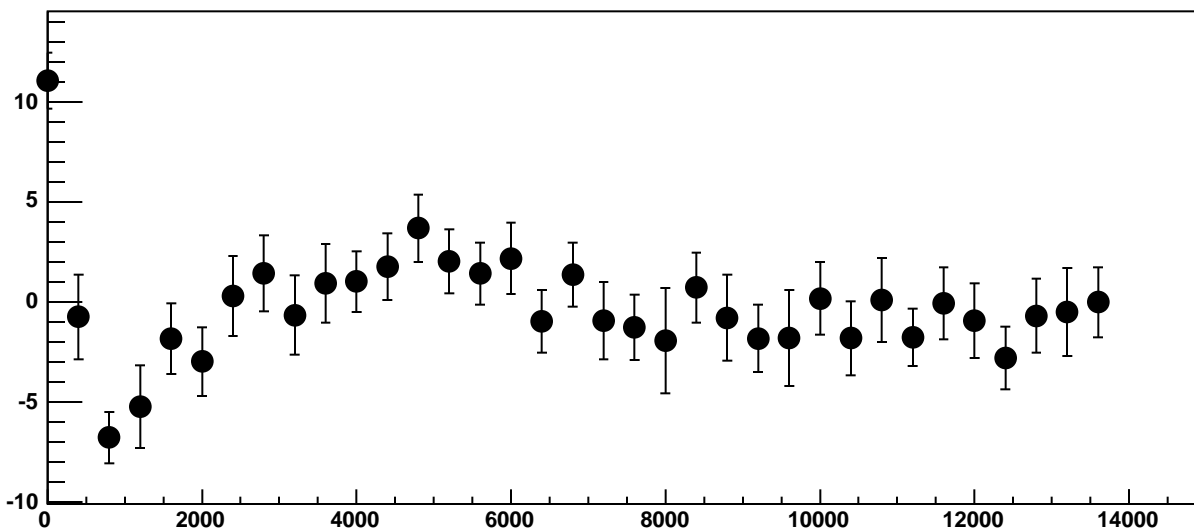
p1

0.02811 ± 0.0001322

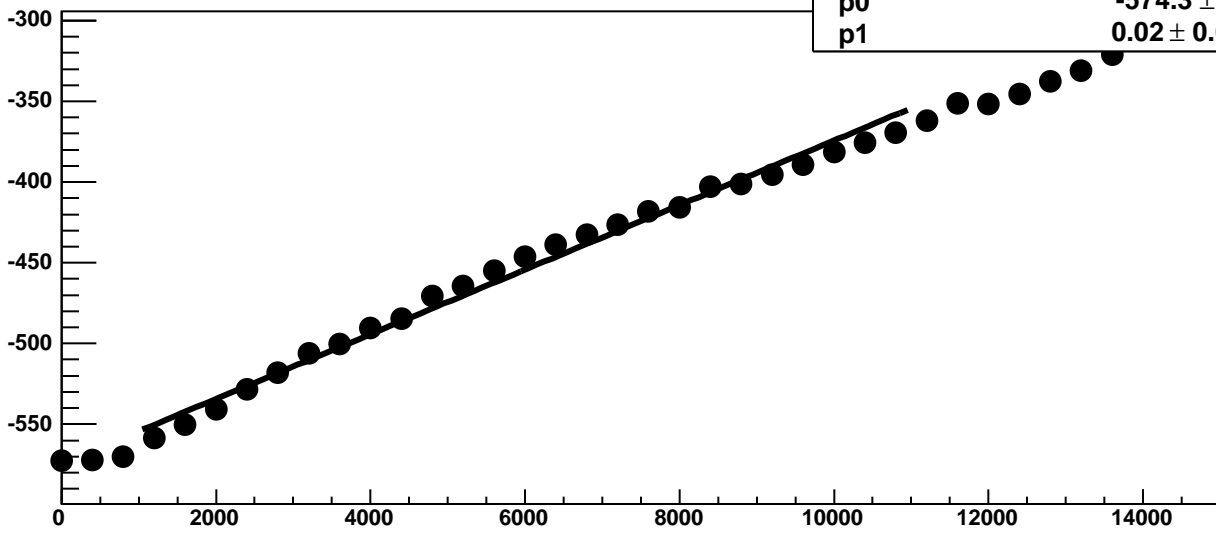
Chip 11, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

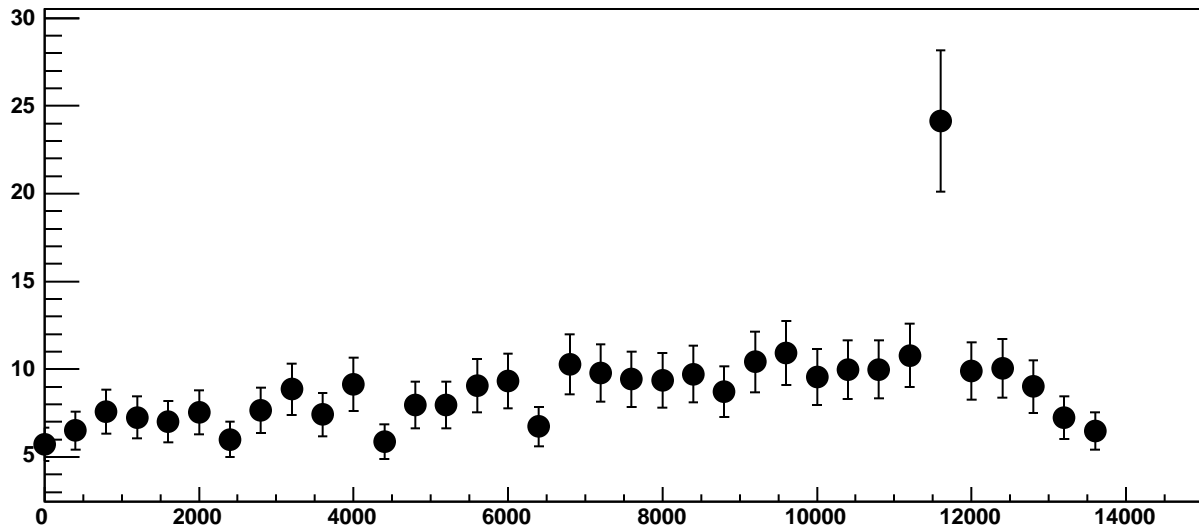


Chip 11, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

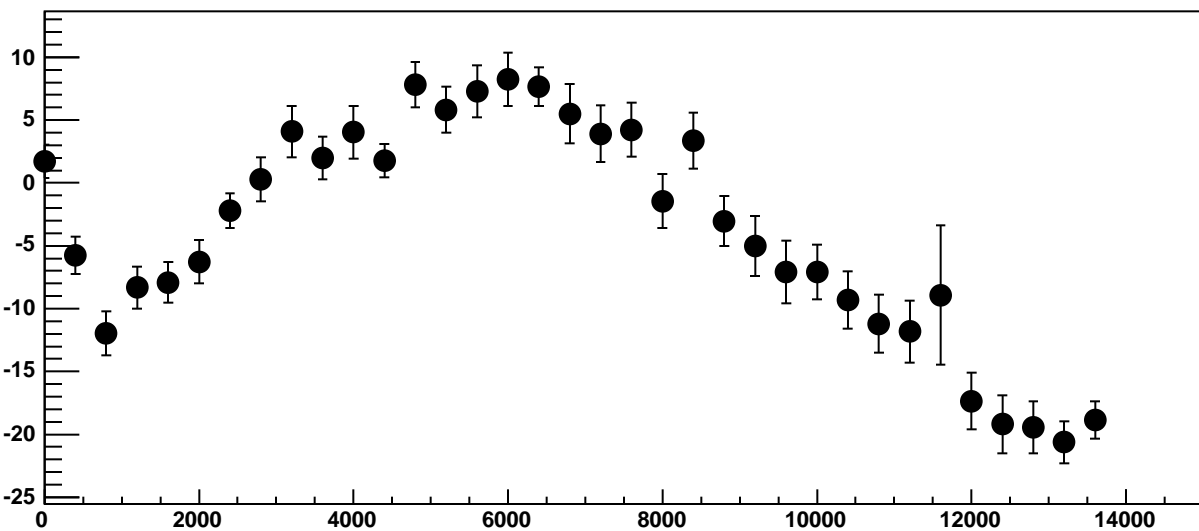


χ^2 / ndf 236.8 / 23
p0 -574.3 ± 0.8073
p1 0.02 ± 0.000135

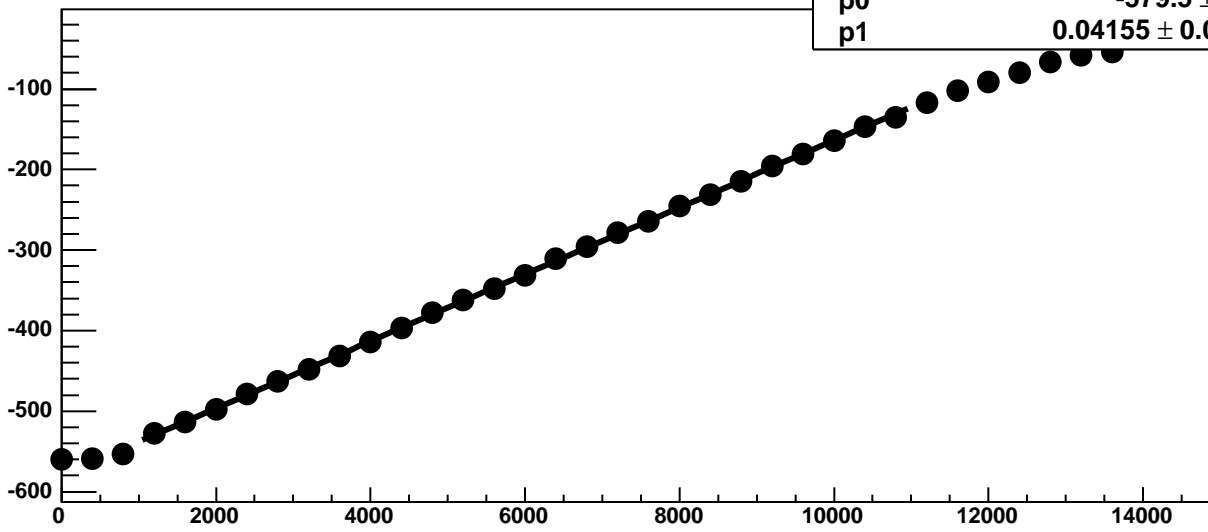
Chip 11, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

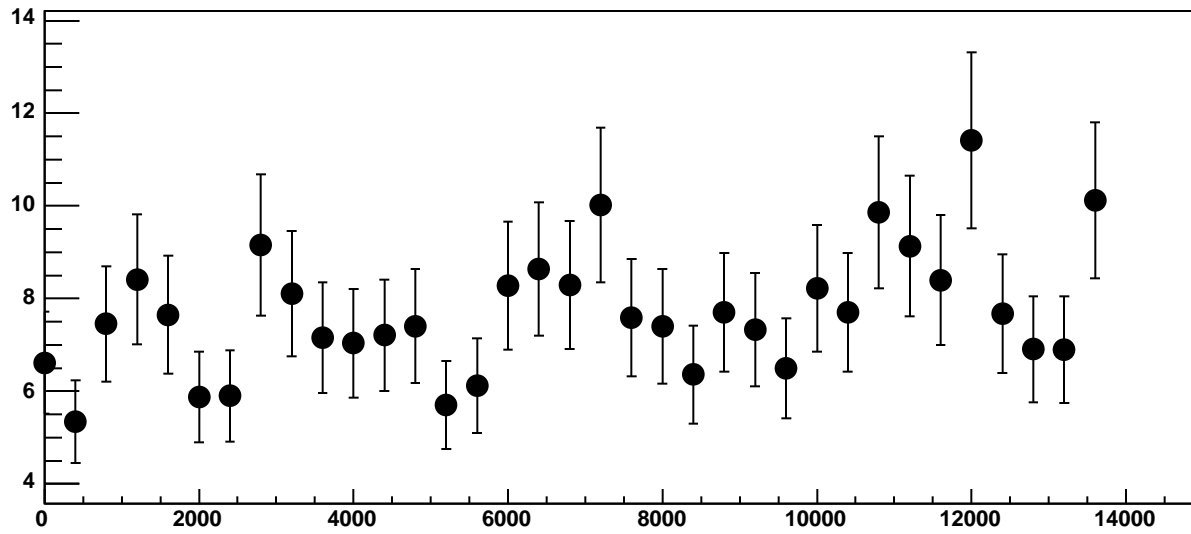


Chip 11, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC

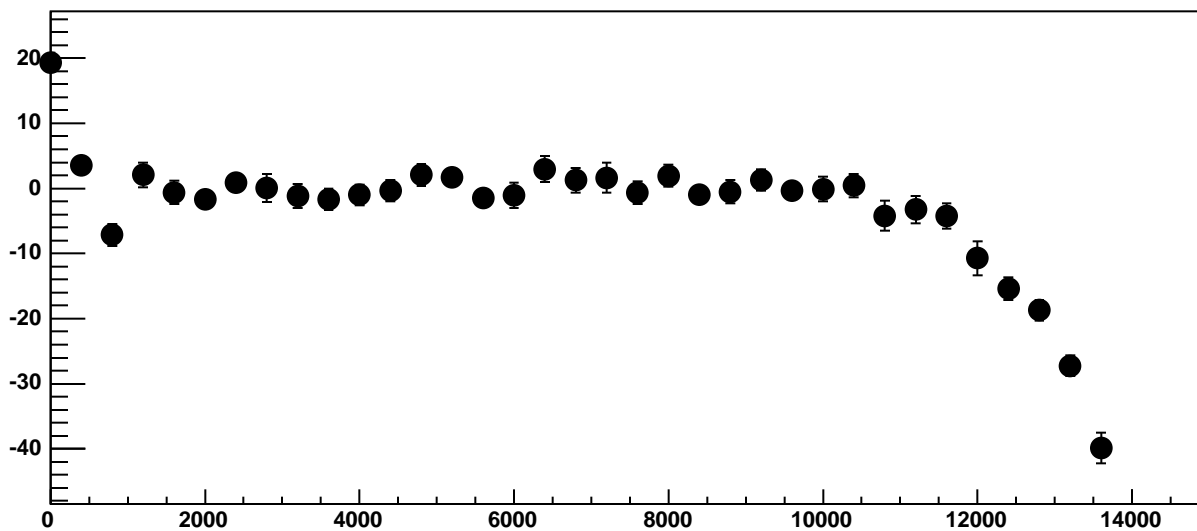


χ^2 / ndf 18.72 / 23
p0 -579.5 ± 0.7676
p1 0.04155 ± 0.0001189

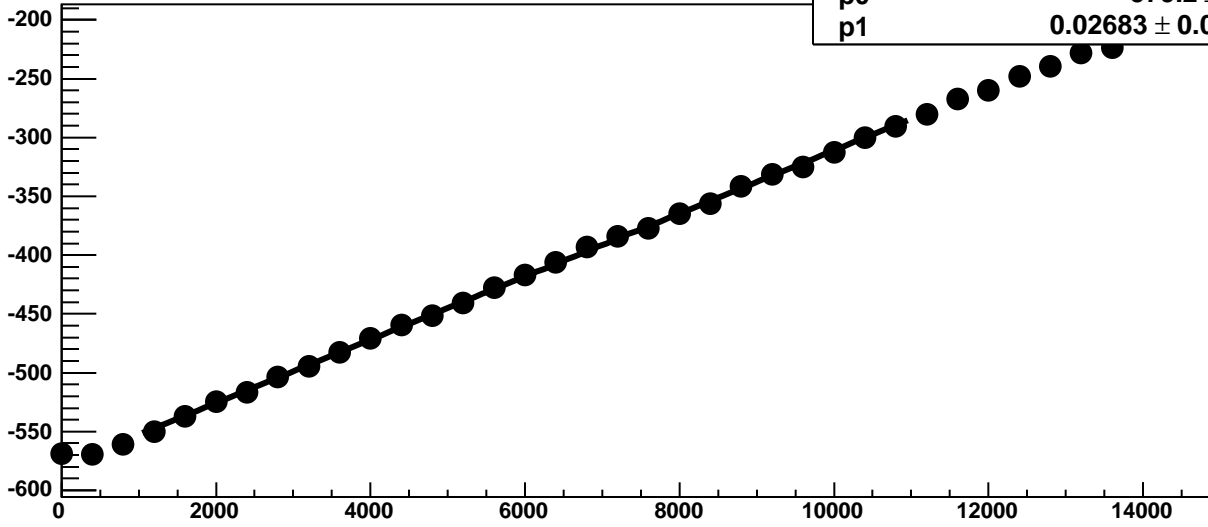
Chip 11, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

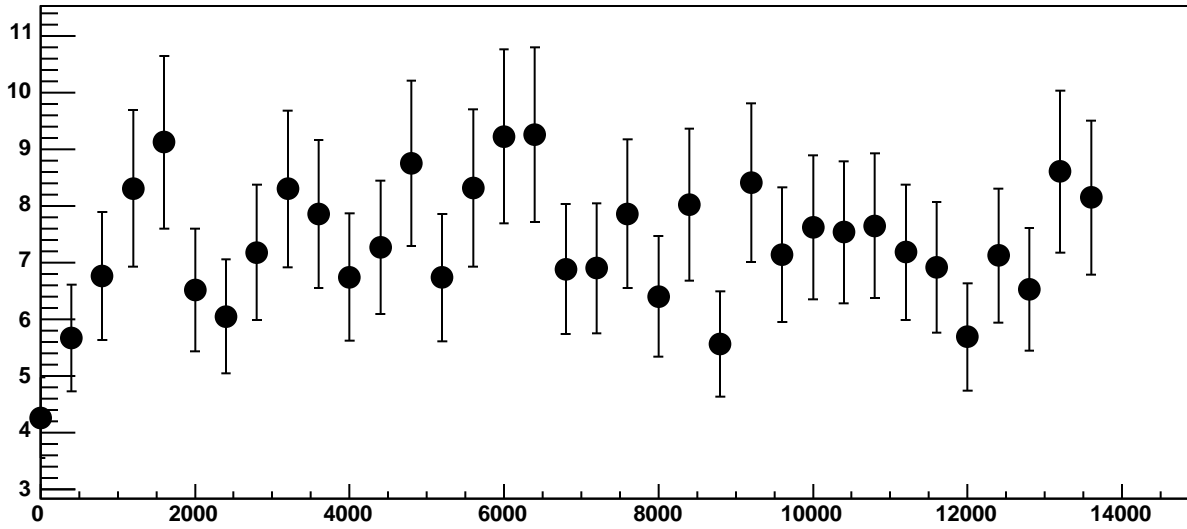


Chip 11, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

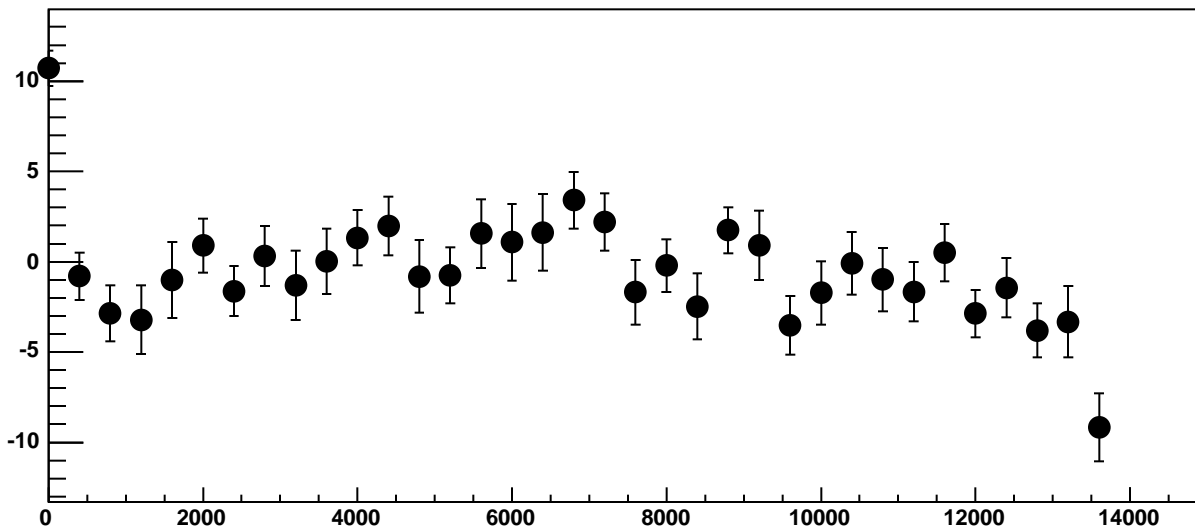


χ^2 / ndf 26.74 / 23
p0 -579.2 ± 0.7871
p1 0.02683 ± 0.0001175

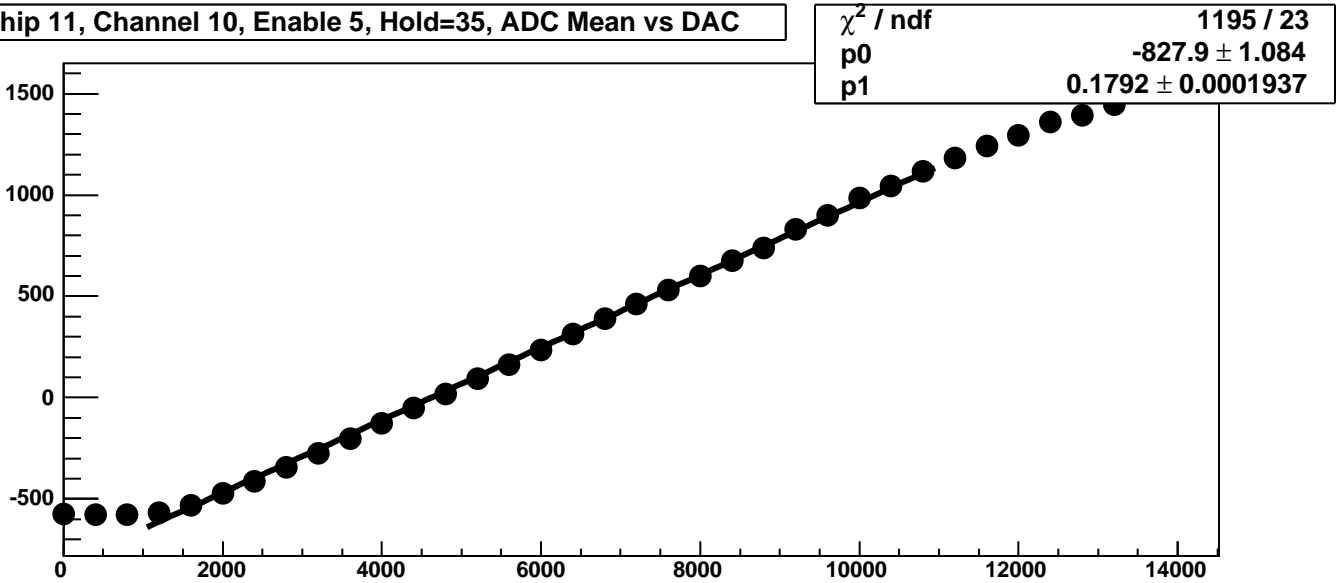
Chip 11, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



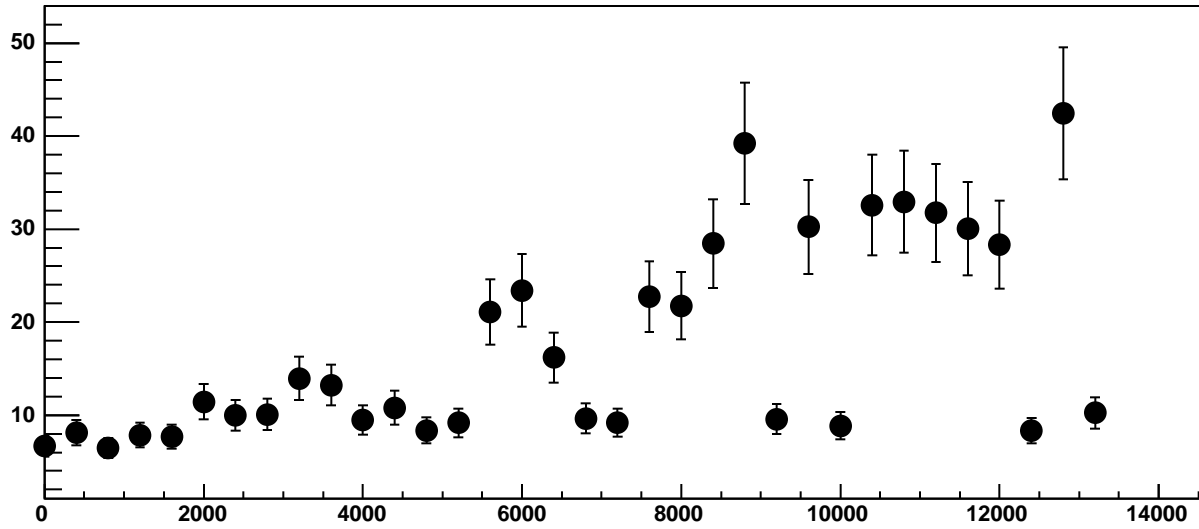
Chip 11, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



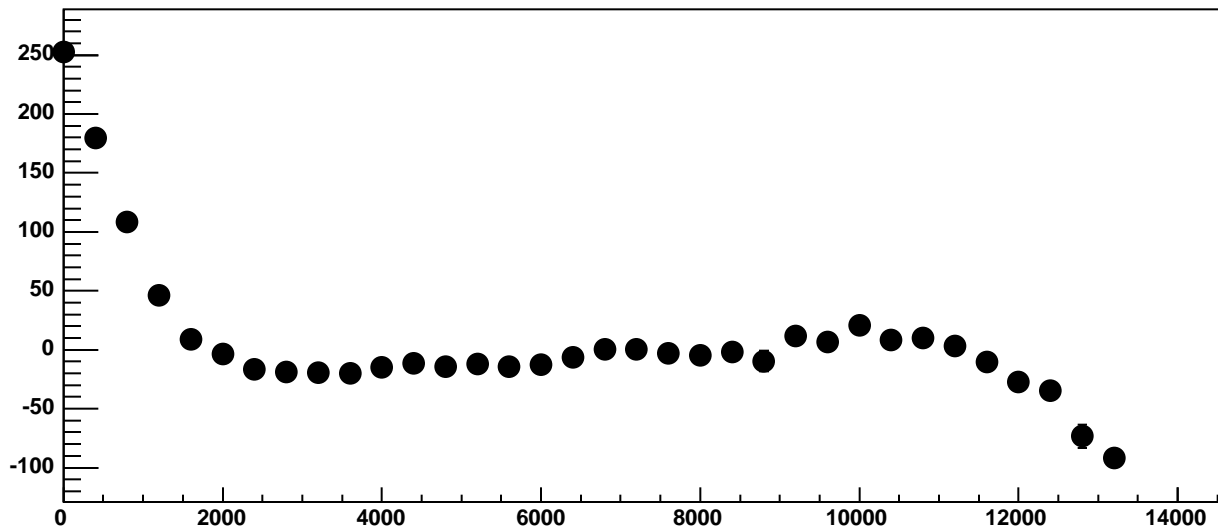
Chip 11, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



Chip 11, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

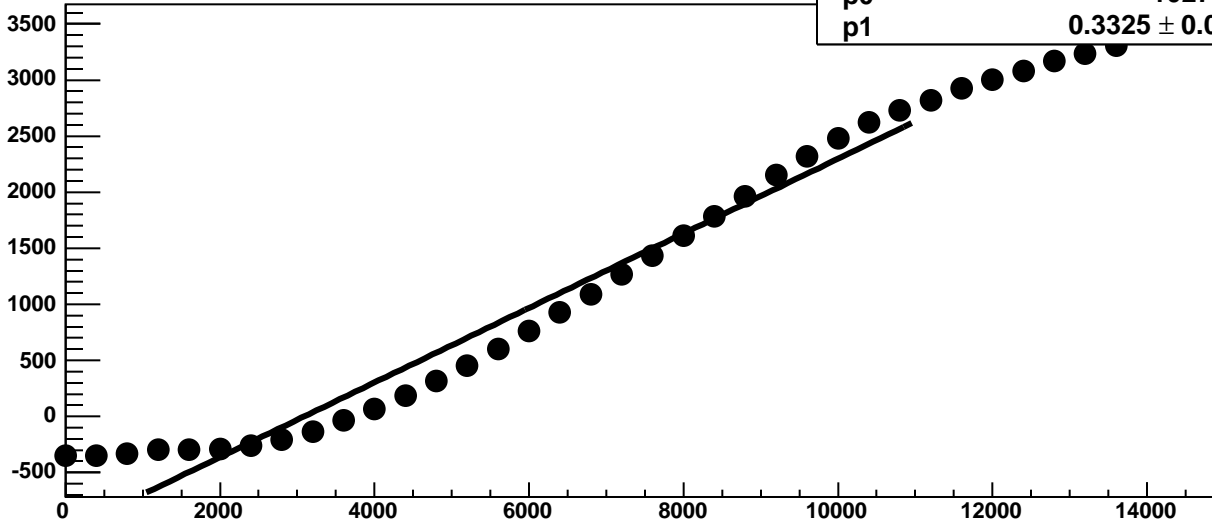


Chip 11, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

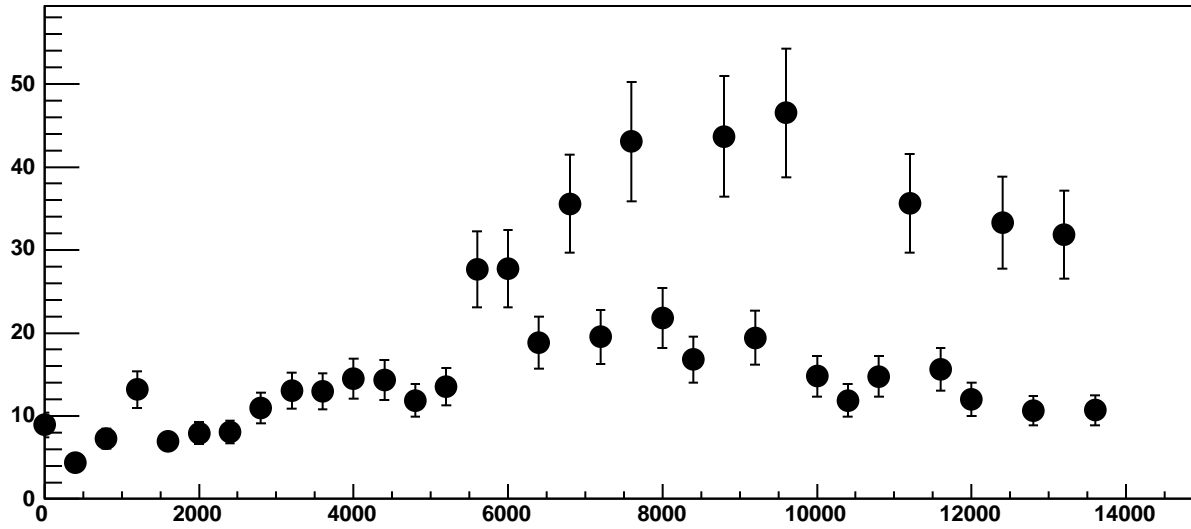


Chip 11, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC

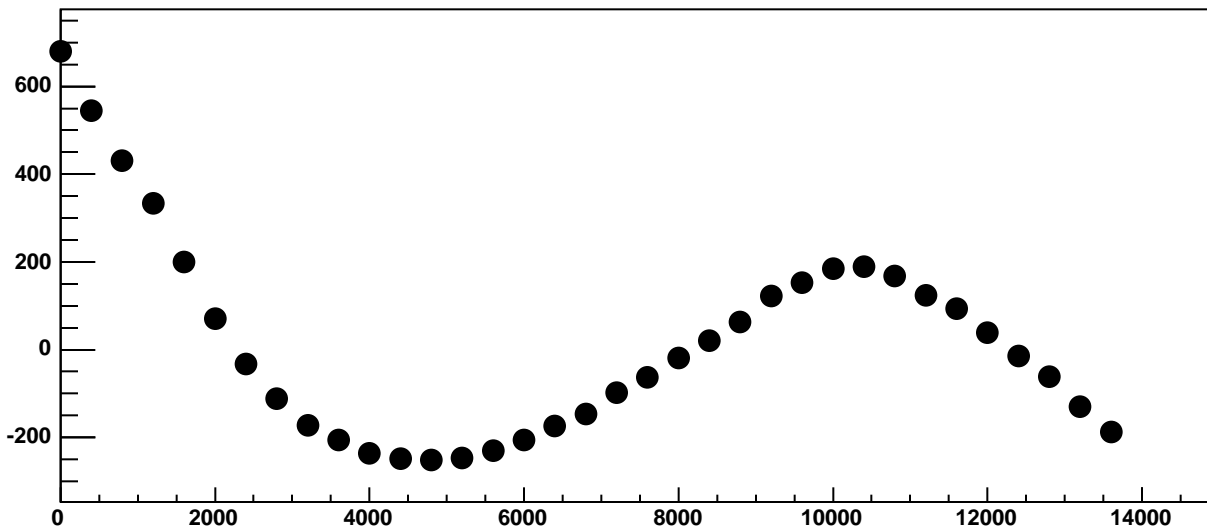
χ^2 / ndf 8.139e+04 / 23
p0 -1027 ± 1.098
p1 0.3325 ± 0.0002082



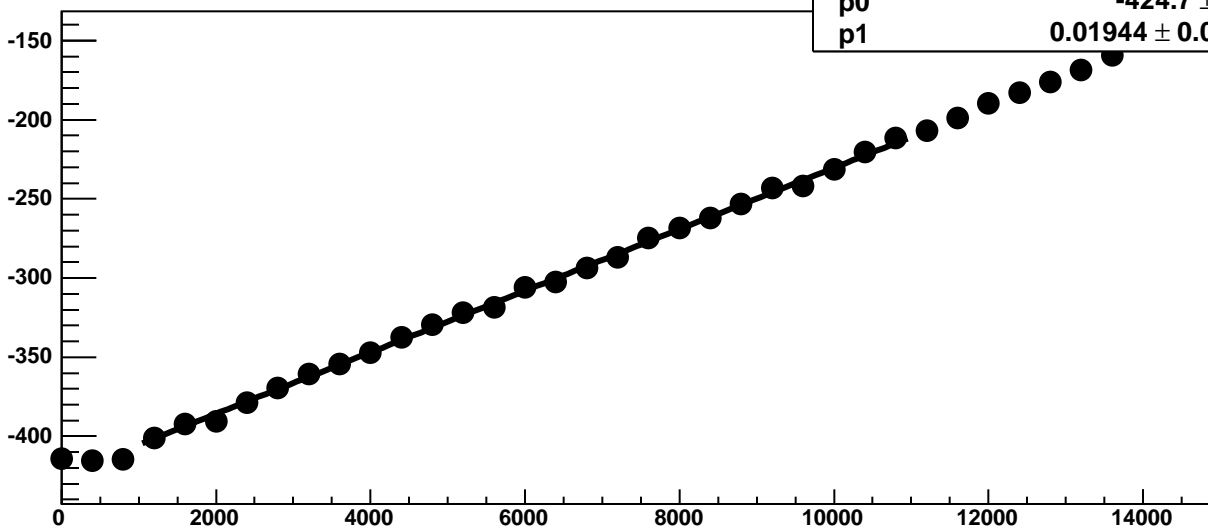
Chip 11, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



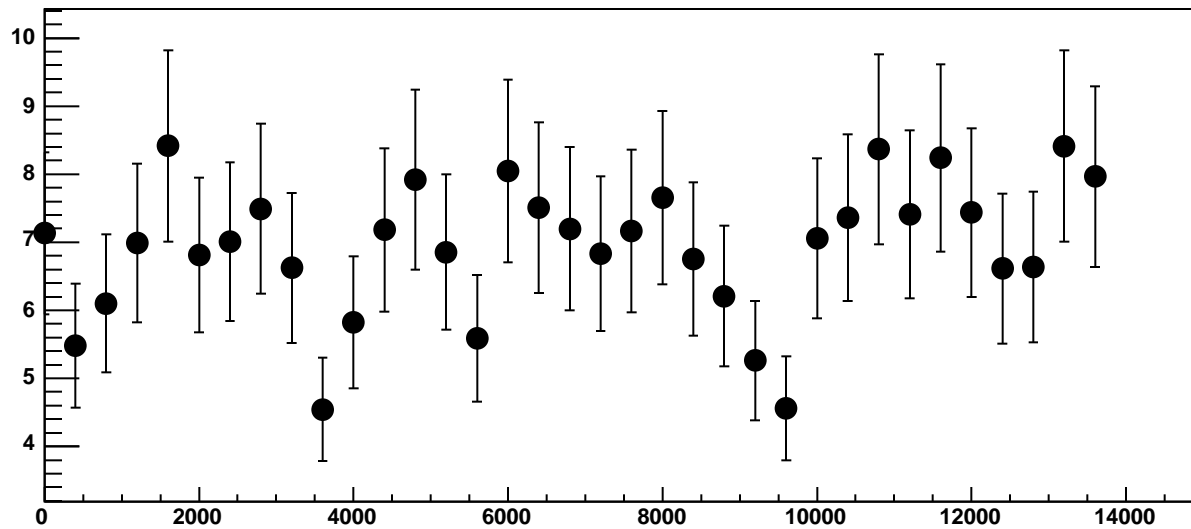
Chip 11, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC



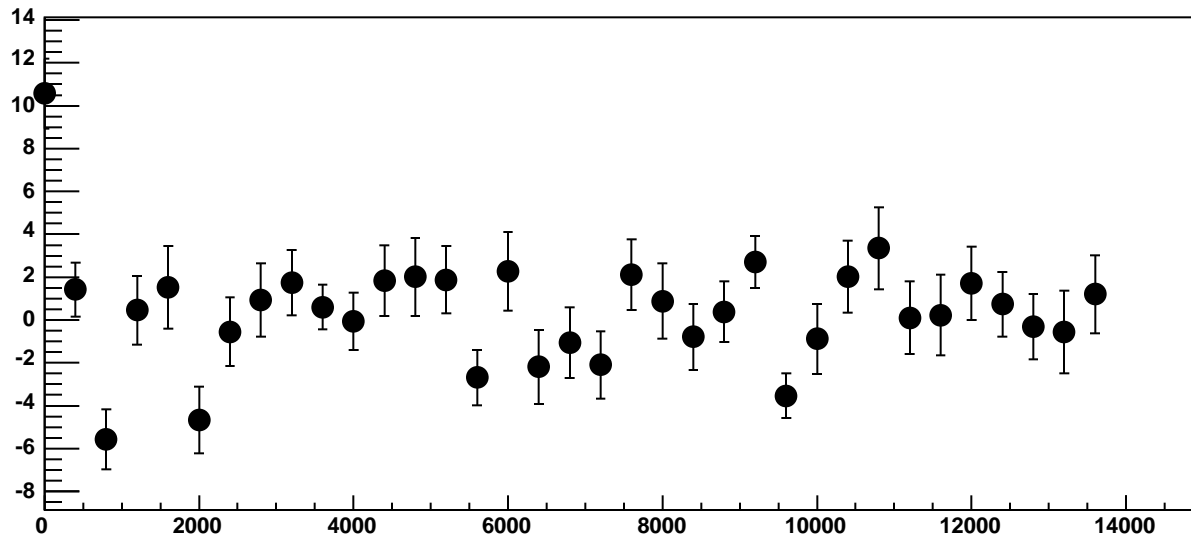
Chip 11, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



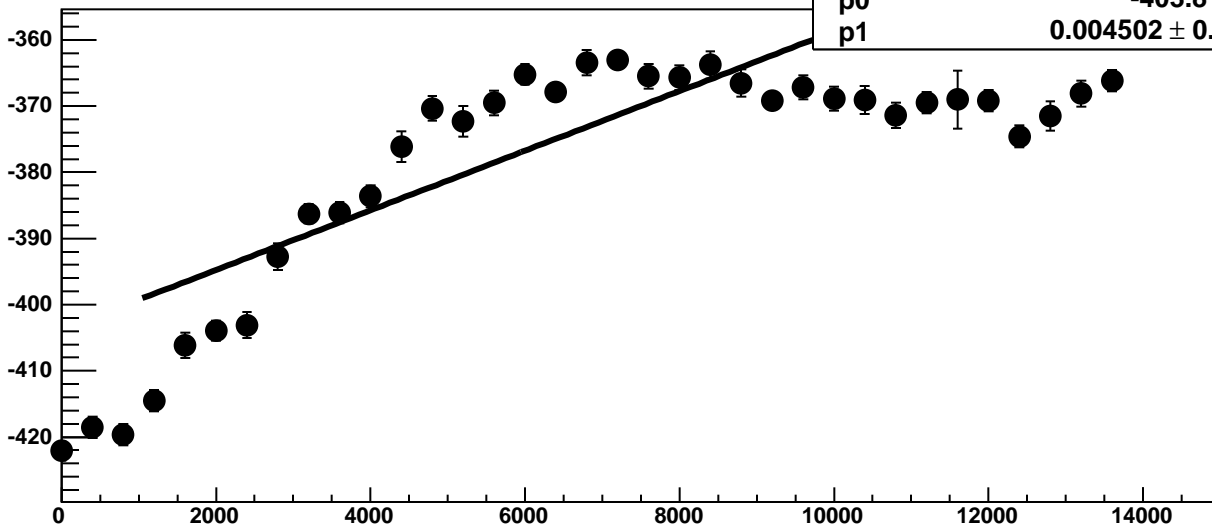
Chip 11, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



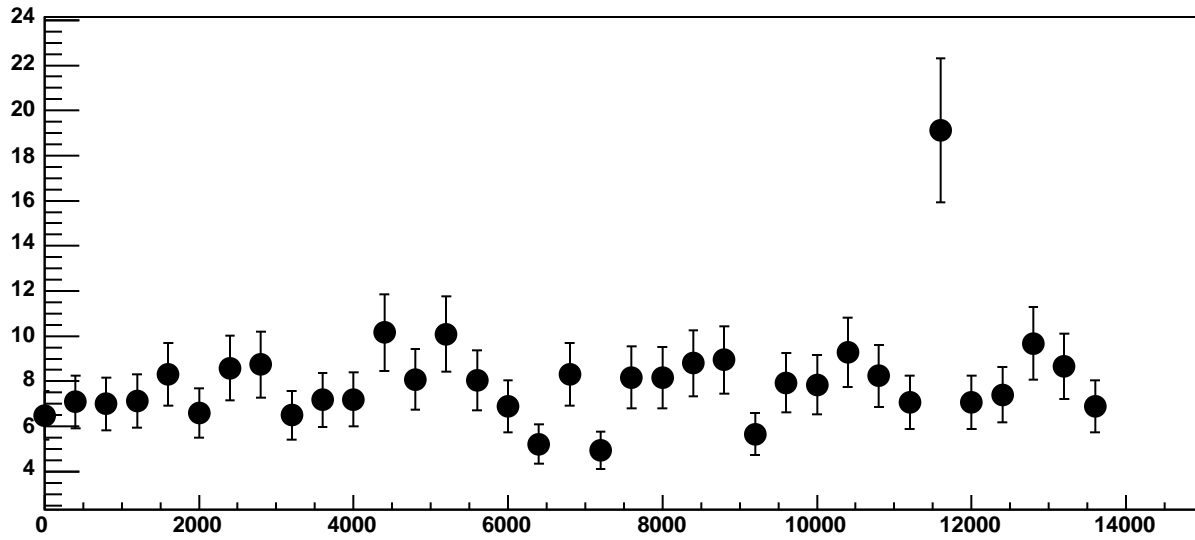
Chip 11, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



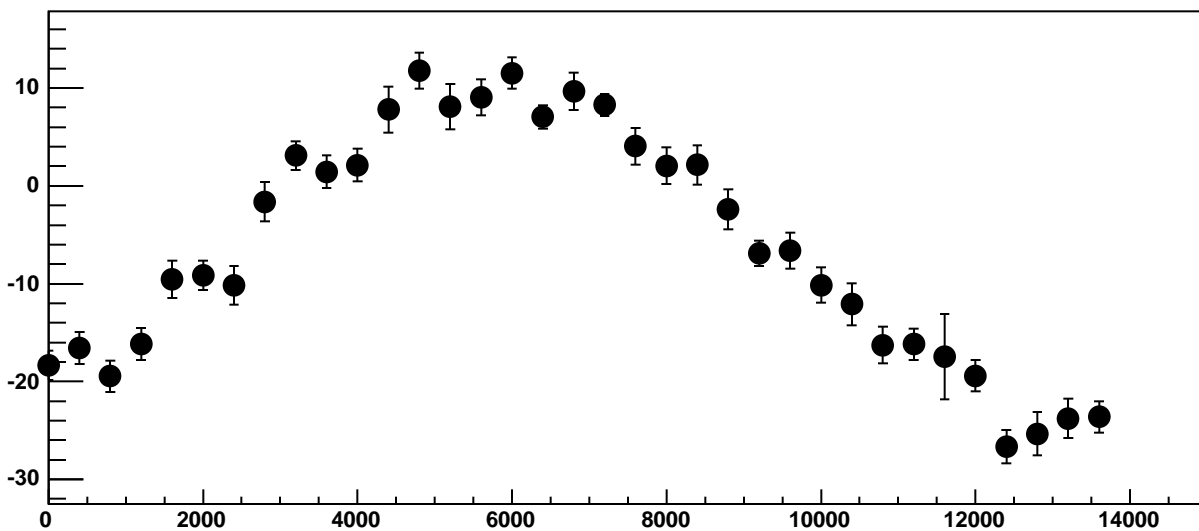
Chip 11, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



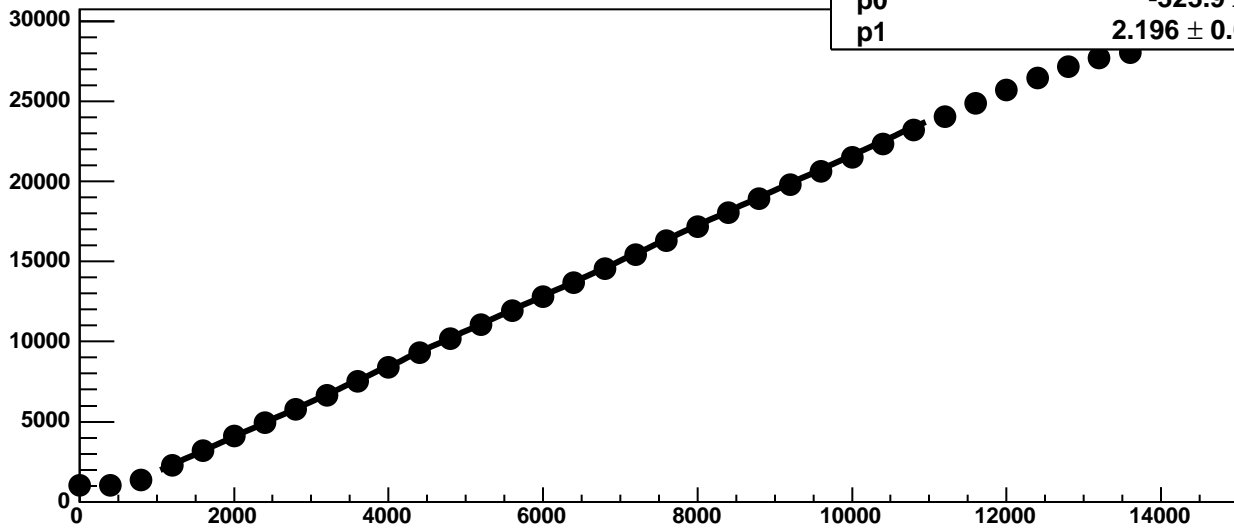
Chip 11, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

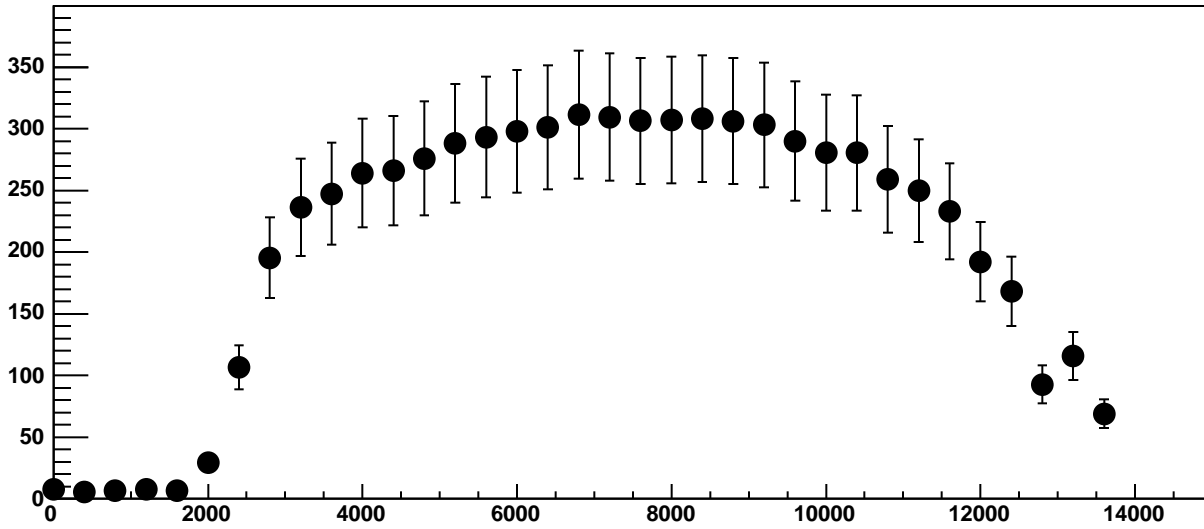


Chip 11, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC

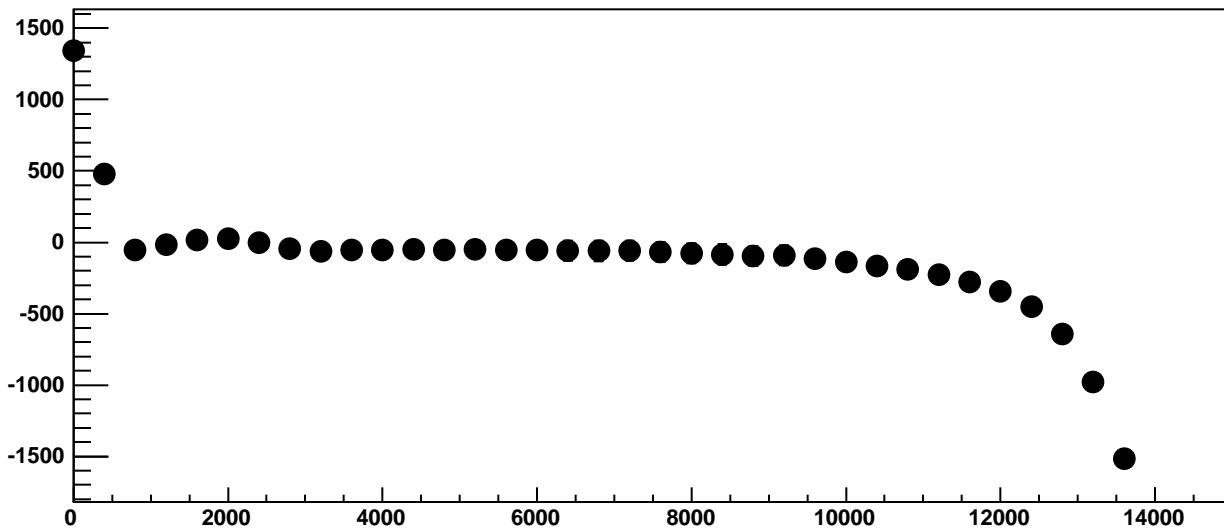


χ^2 / ndf 234.4 / 23
p0 -323.9 ± 3.517
p1 2.196 ± 0.002236

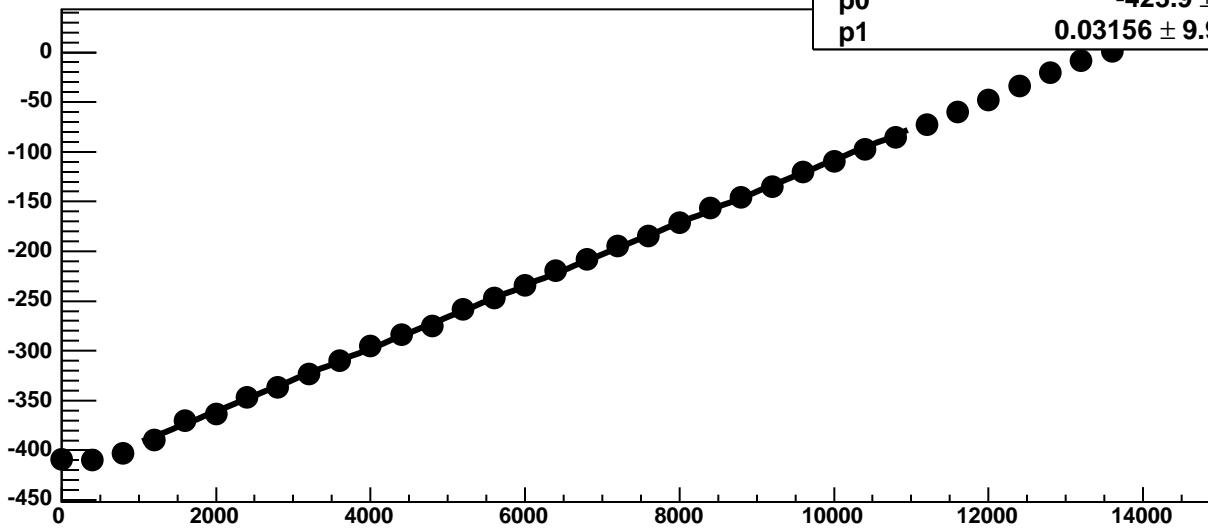
Chip 11, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC

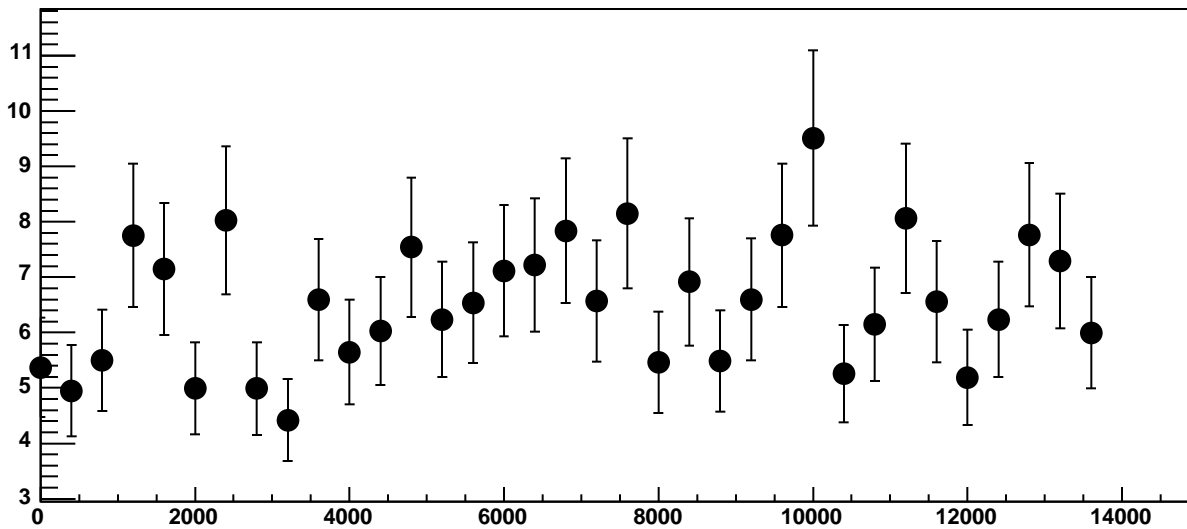


Chip 11, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

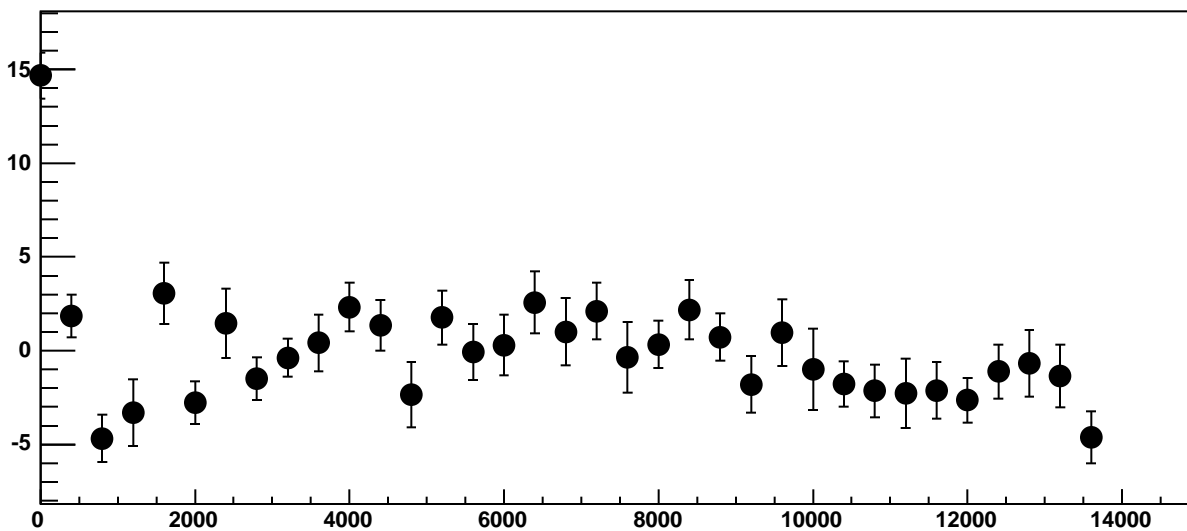


χ^2 / ndf 36.35 / 23
p0 -423.9 ± 0.6465
p1 $0.03156 \pm 9.959e-05$

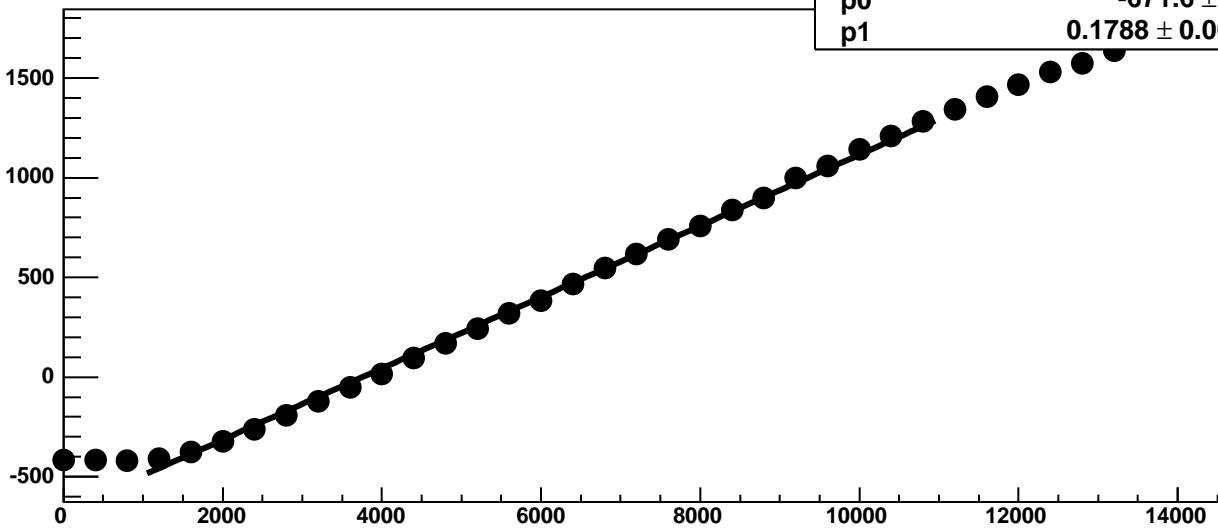
Chip 11, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



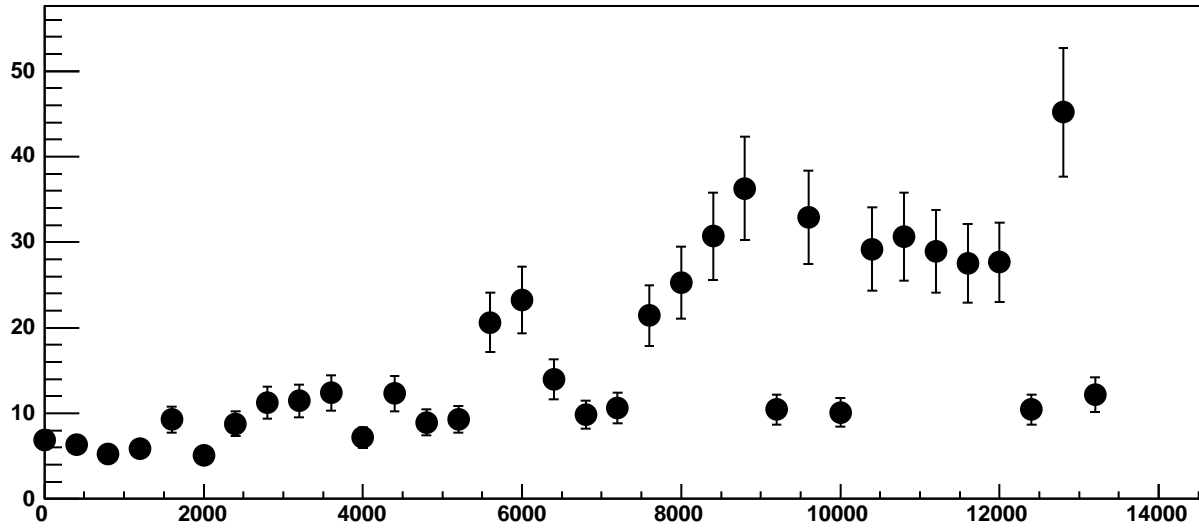
Chip 11, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



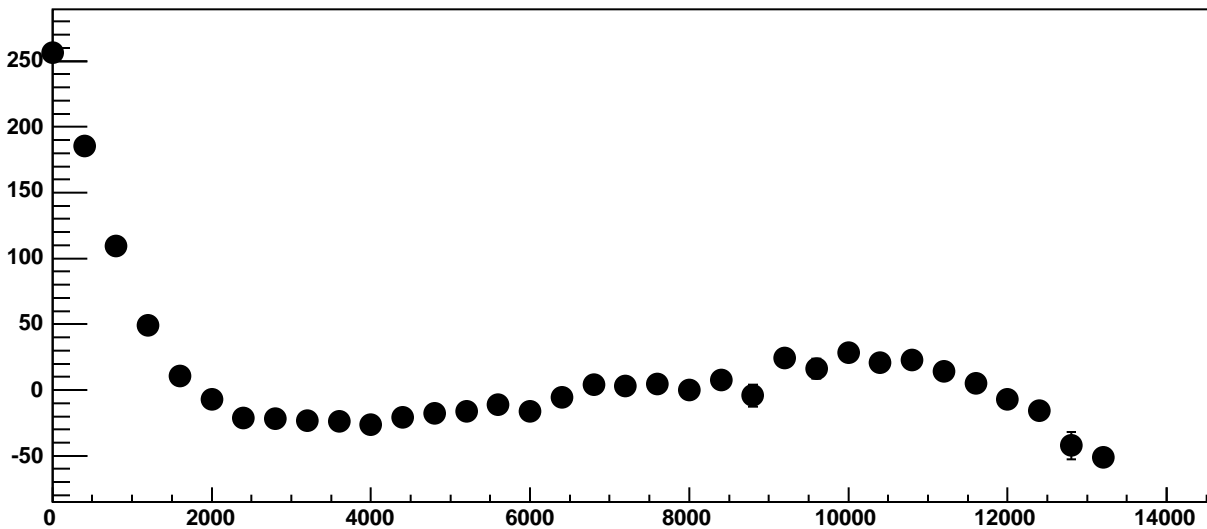
Chip 11, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



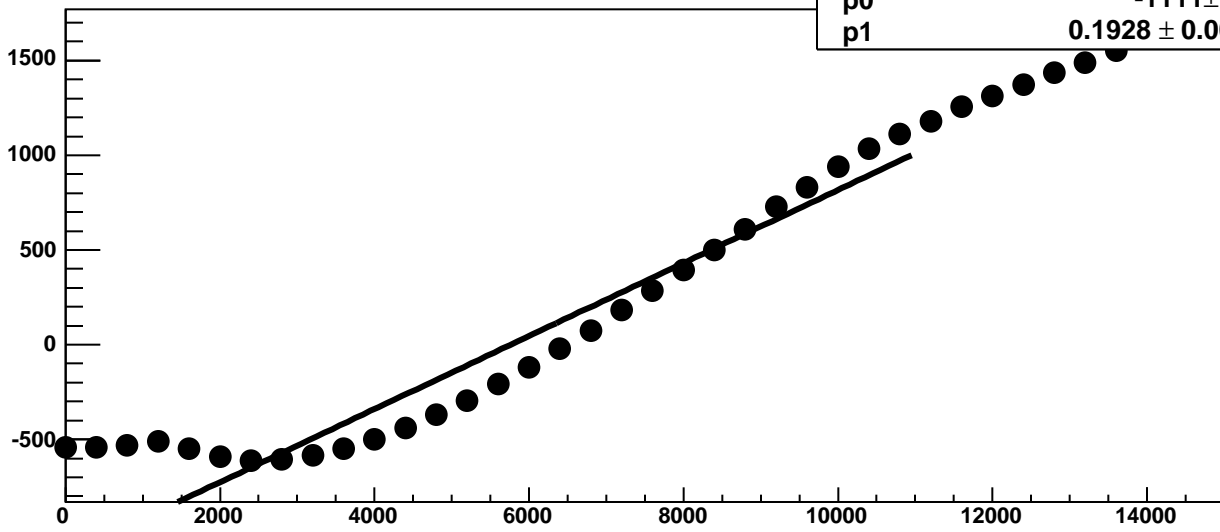
Chip 11, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



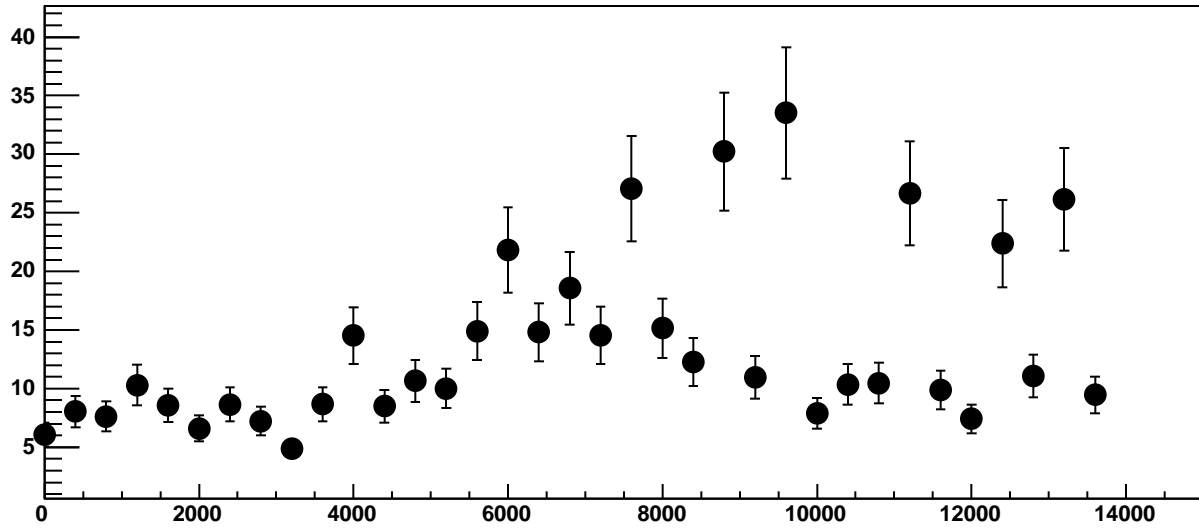
Chip 11, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



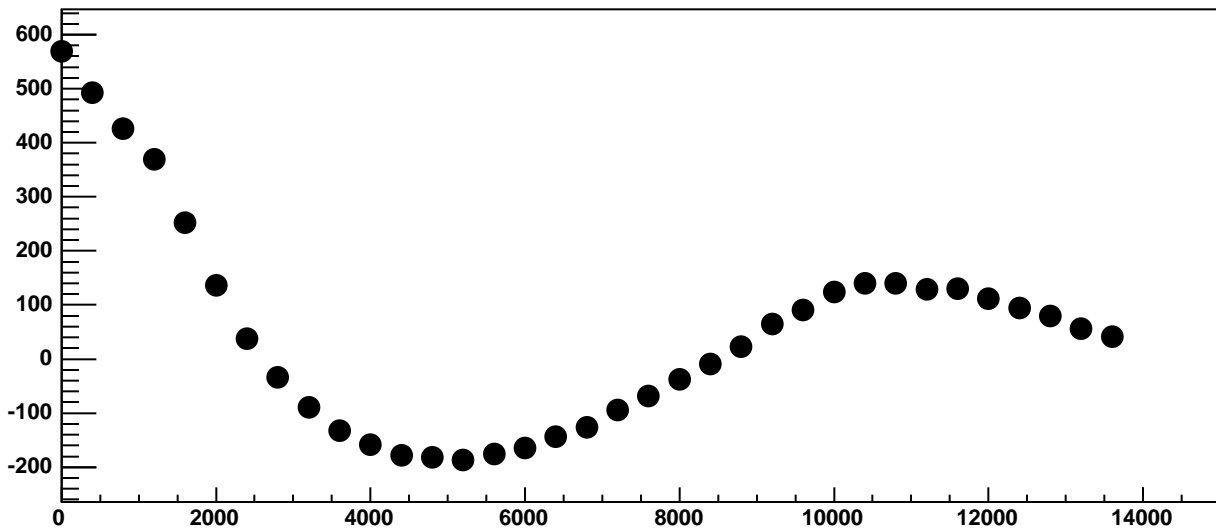
Chip 11, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



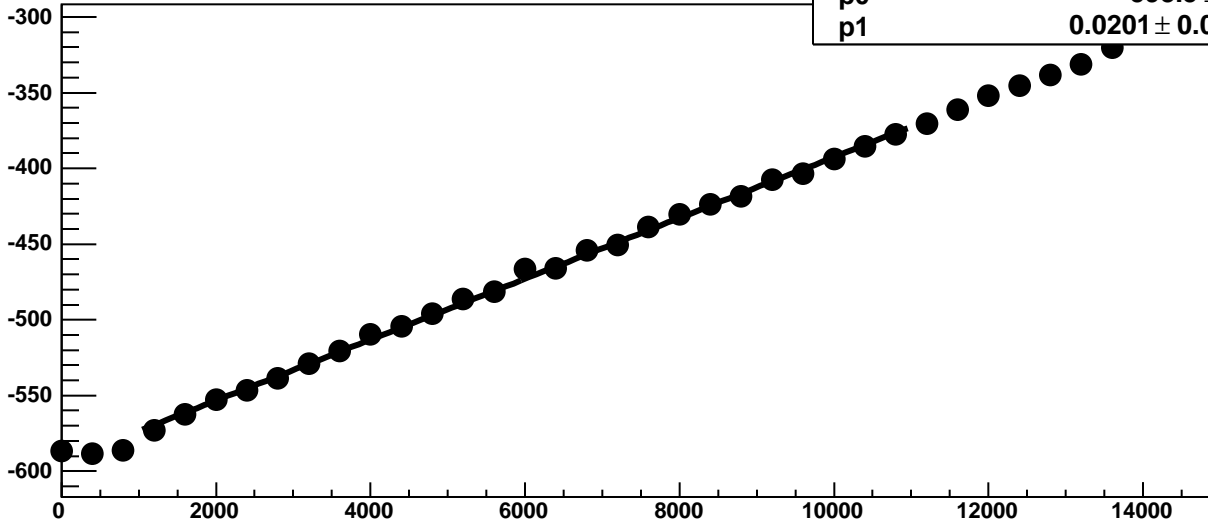
Chip 11, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

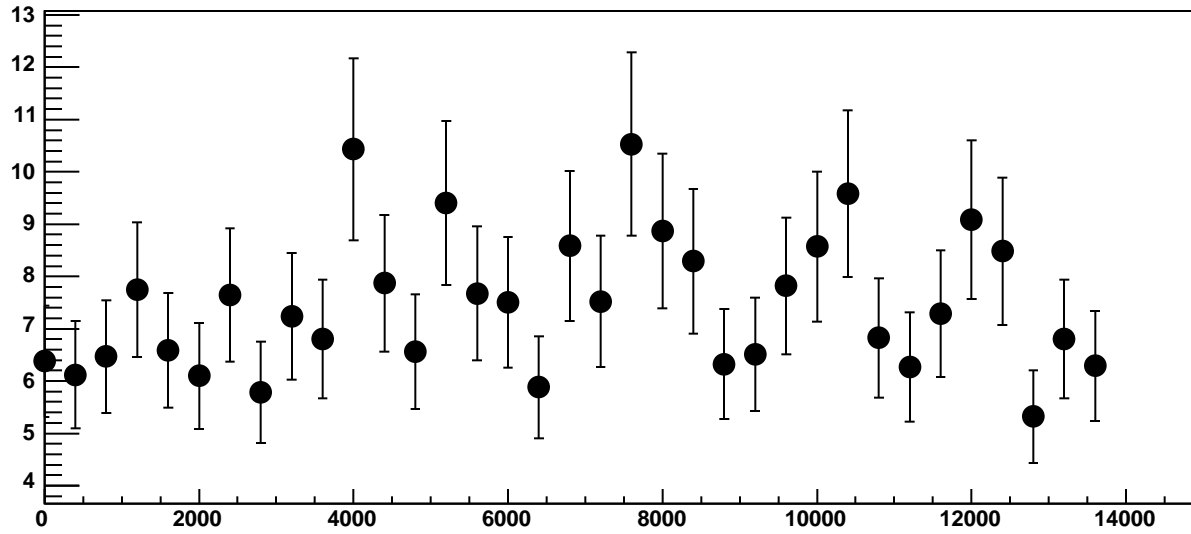


Chip 11, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

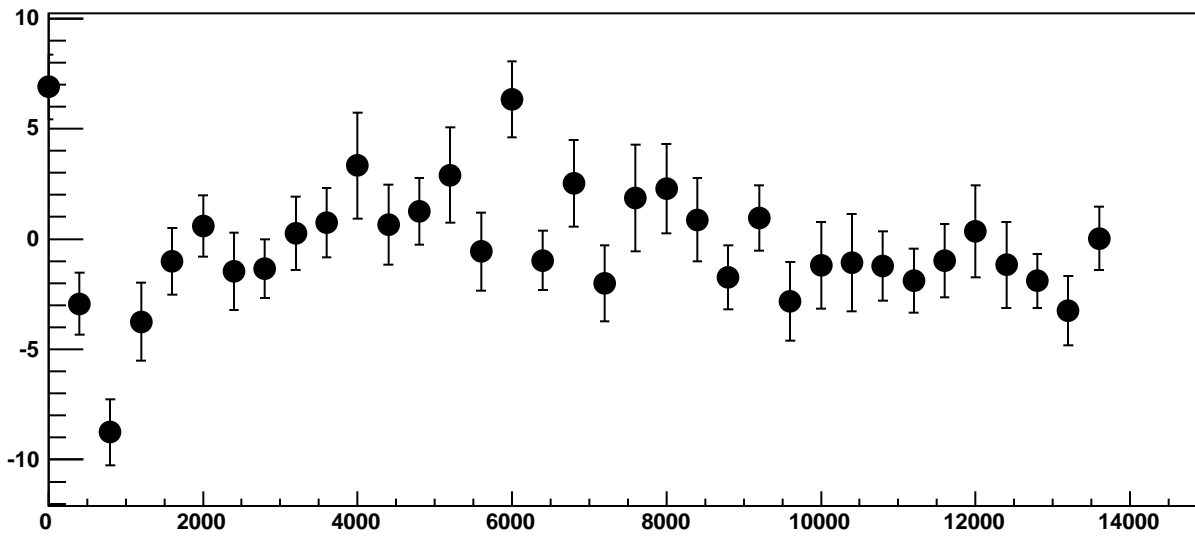


χ^2 / ndf 36.3 / 23
p0 -593.5 ± 0.7481
p1 0.0201 ± 0.0001158

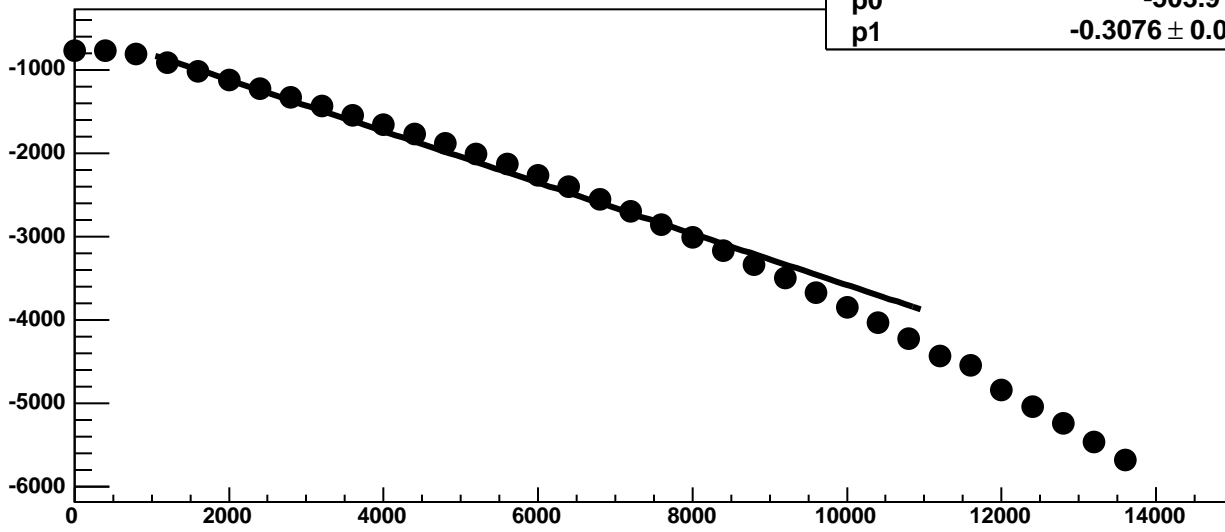
Chip 11, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

4004 / 23

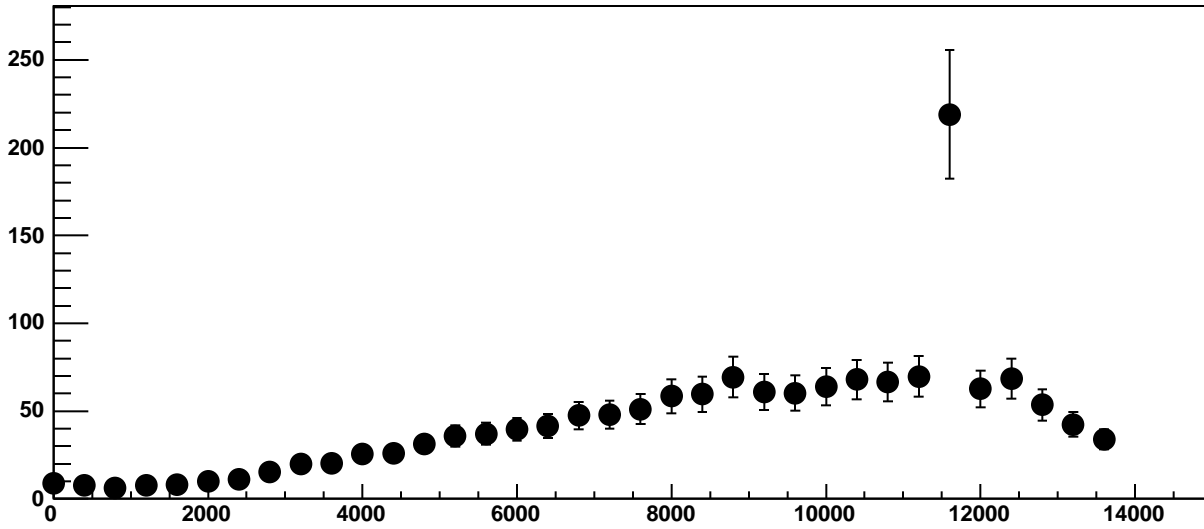
p0

-503.9 ± 1.525

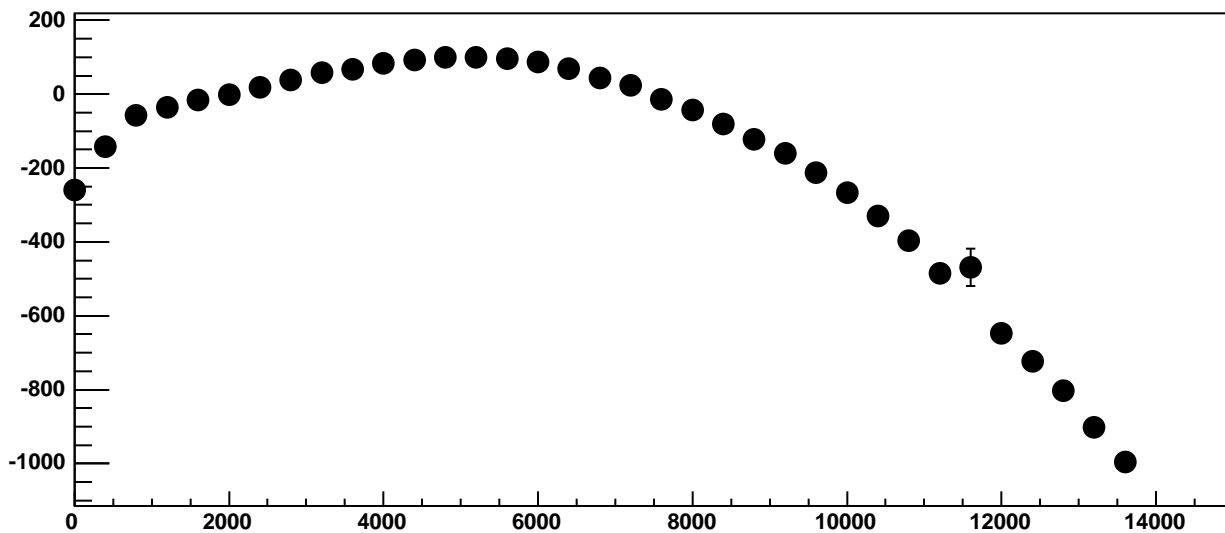
p1

-0.3076 ± 0.0004962

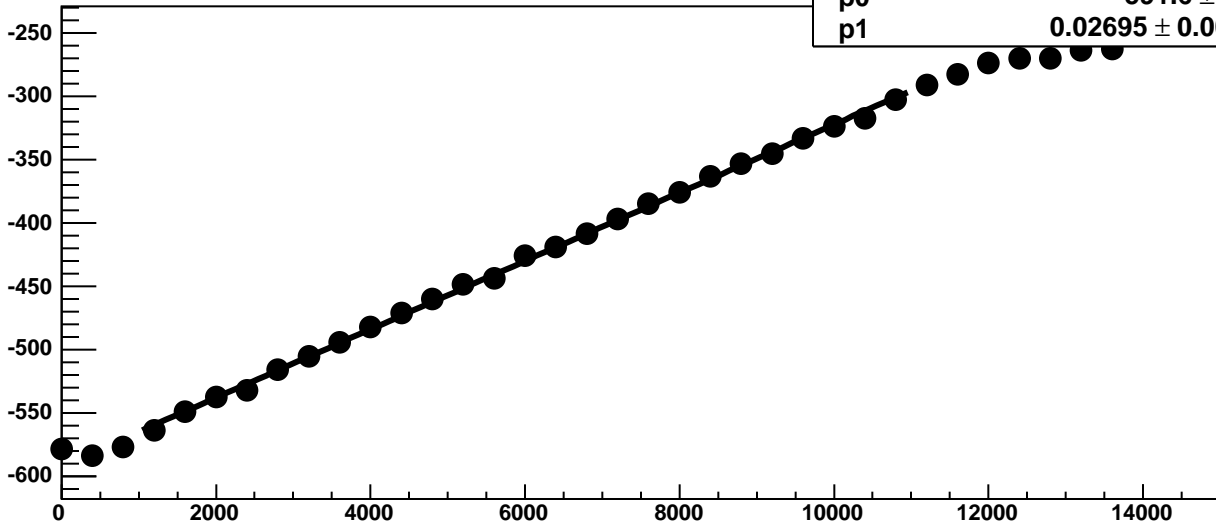
Chip 11, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



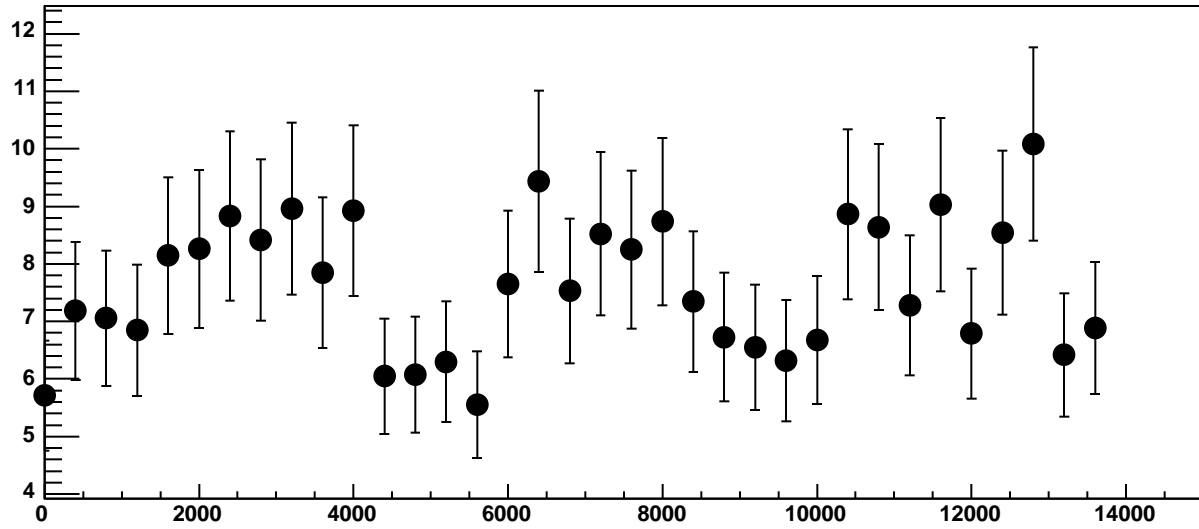
Chip 11, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



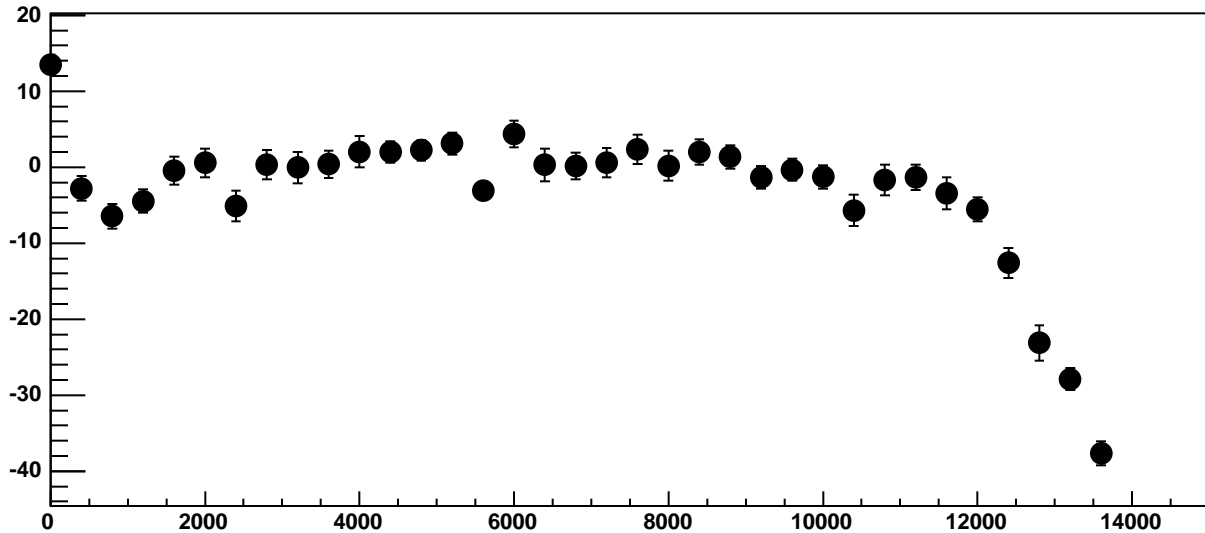
Chip 11, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



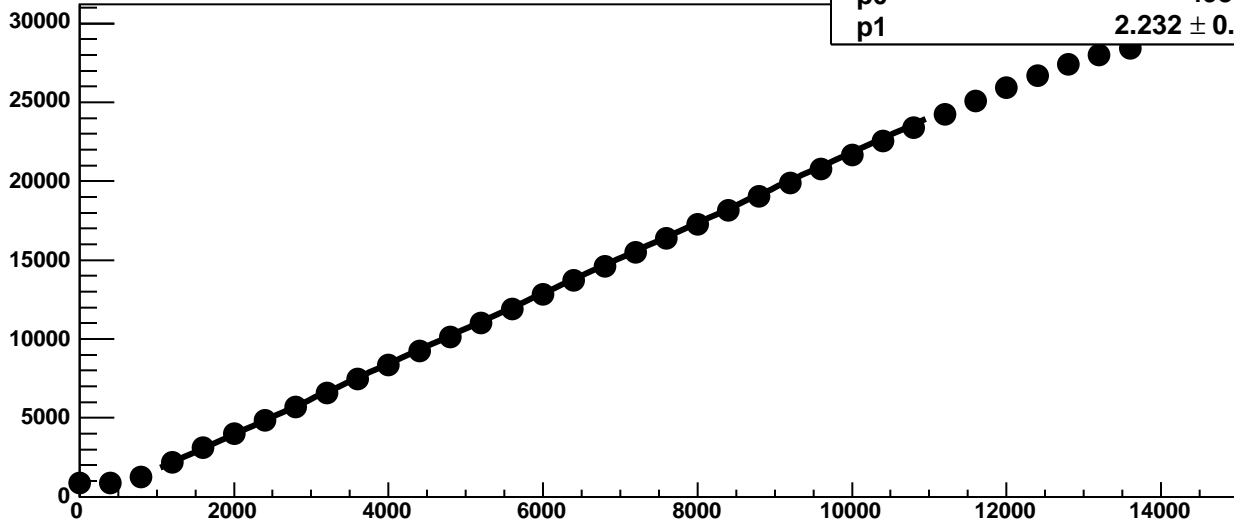
Chip 11, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



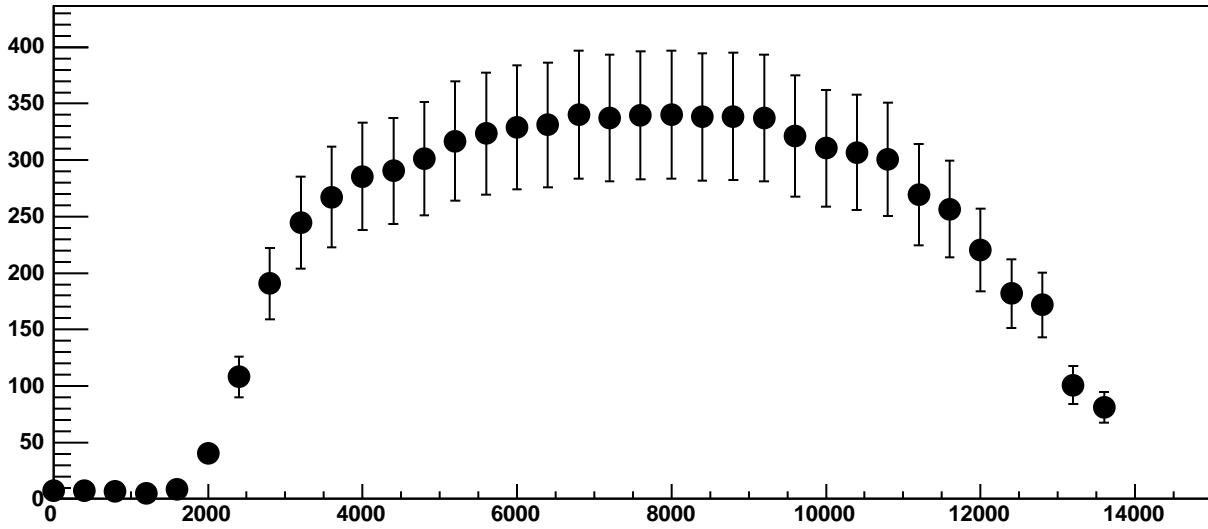
Chip 11, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



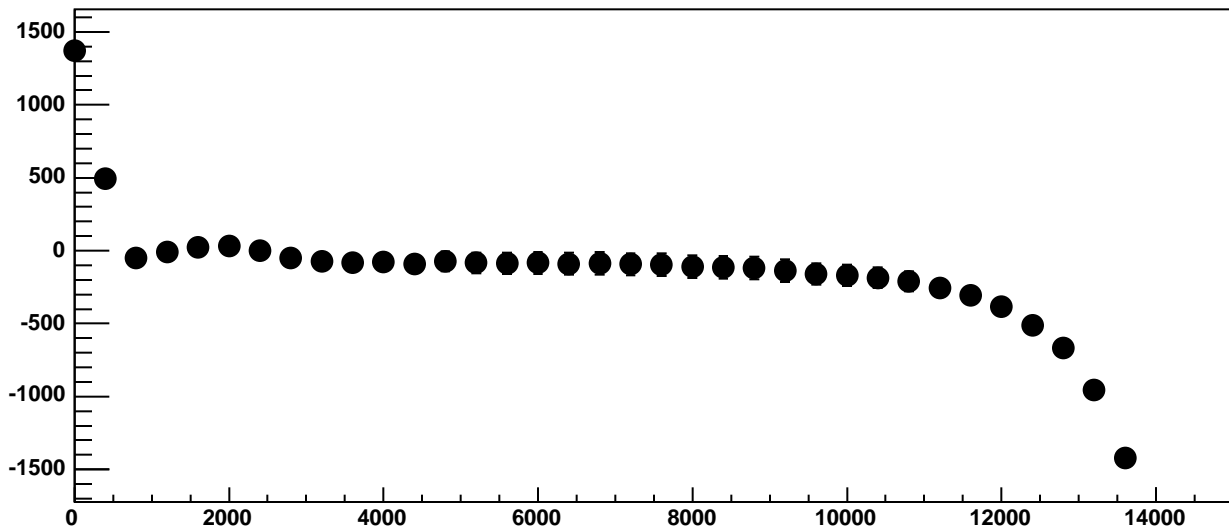
Chip 11, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC



Chip 11, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC

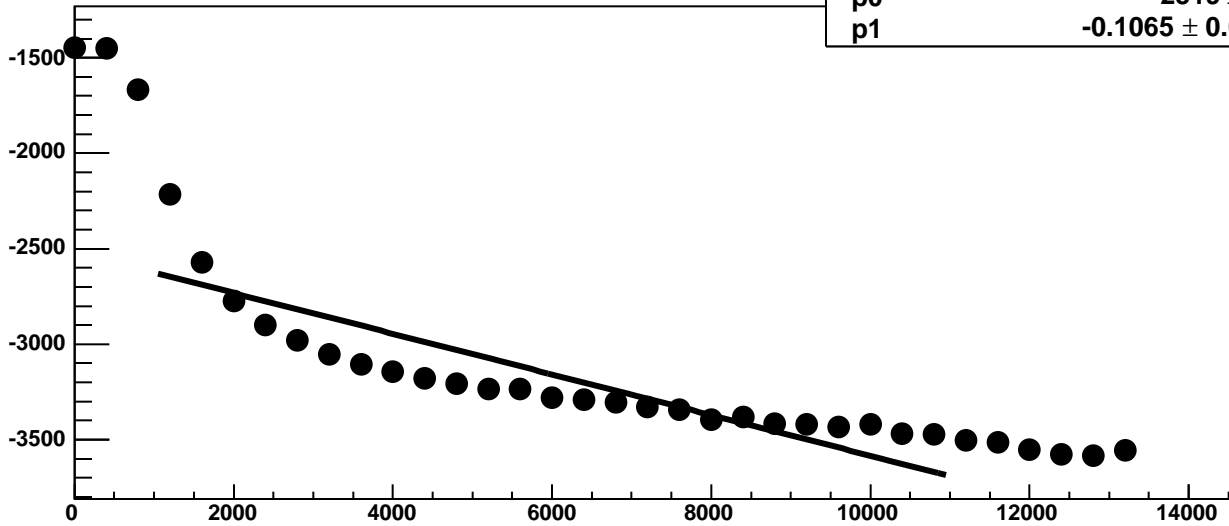


Chip 11, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC

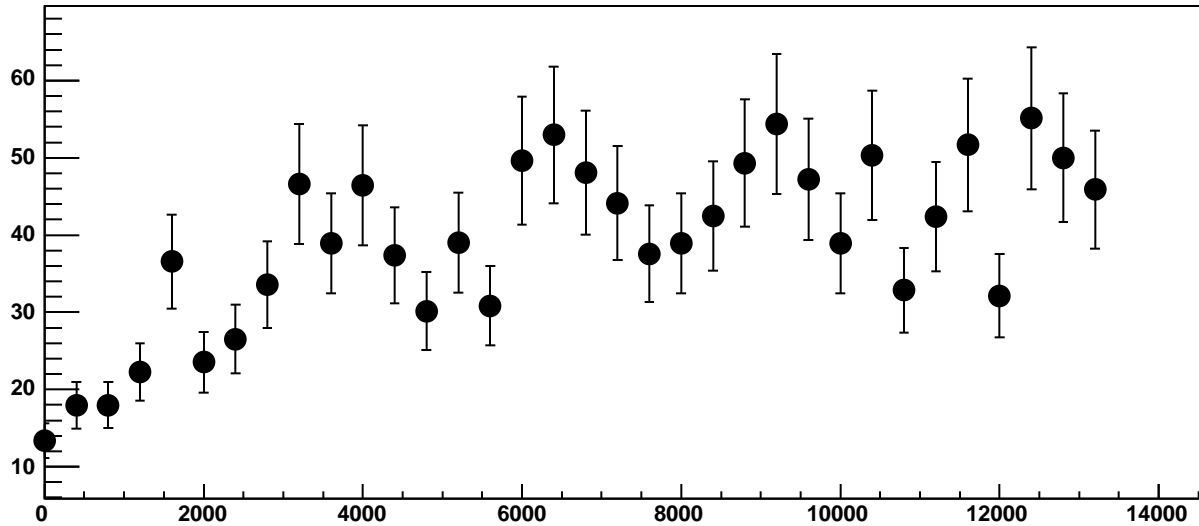


Chip 11, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

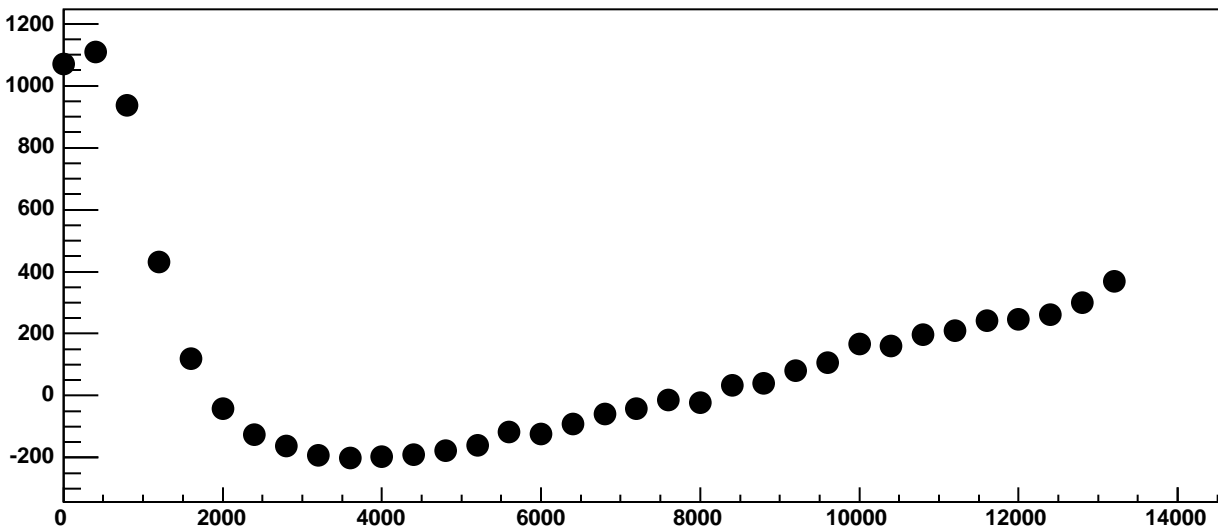
χ^2 / ndf 1.284e+04 / 23
p0 -2519 ± 3.239
p1 -0.1065 ± 0.000549



Chip 11, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

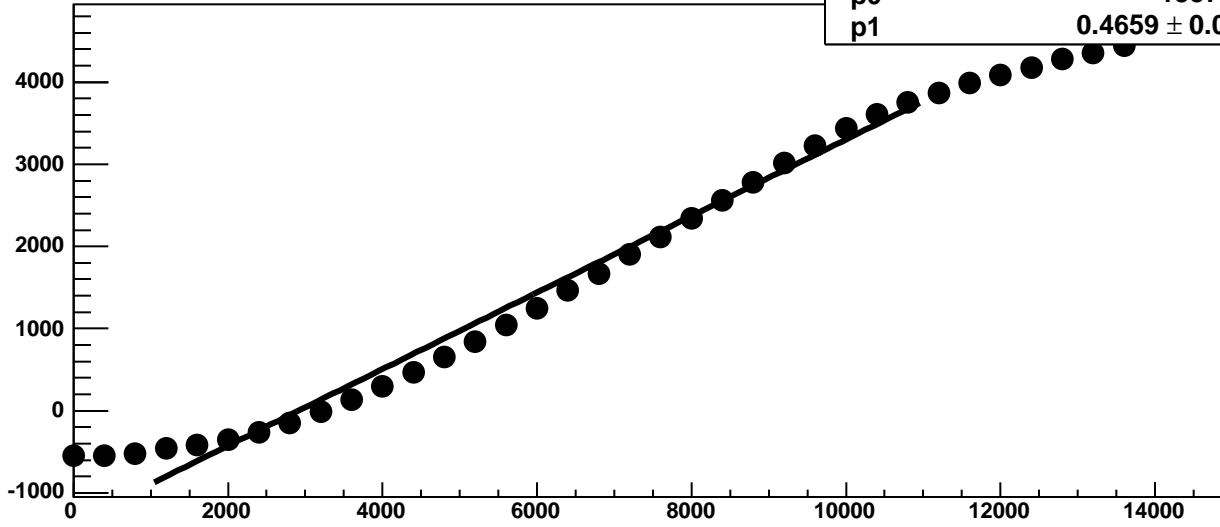


Chip 11, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

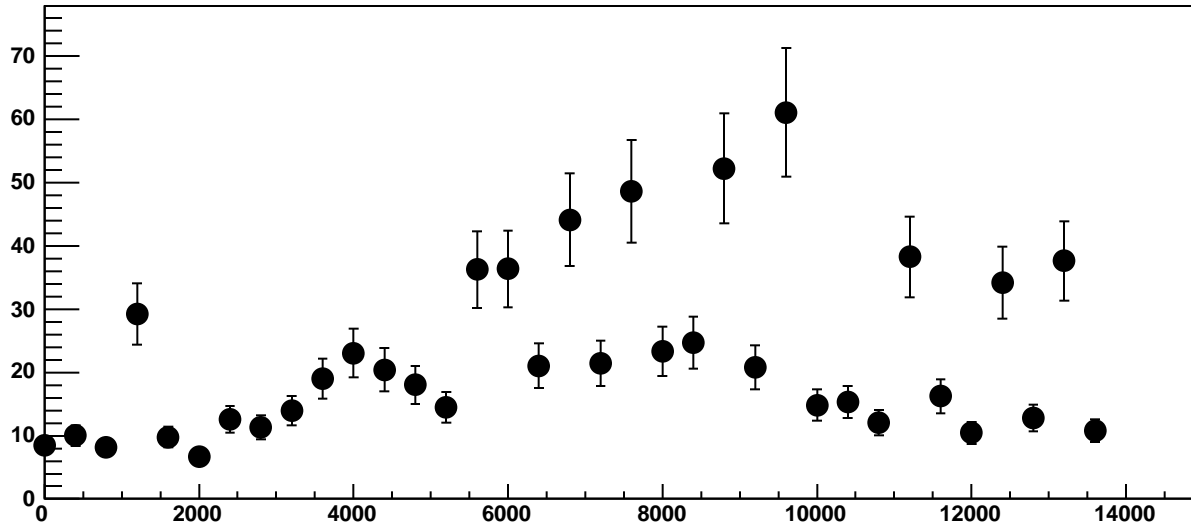


Chip 11, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

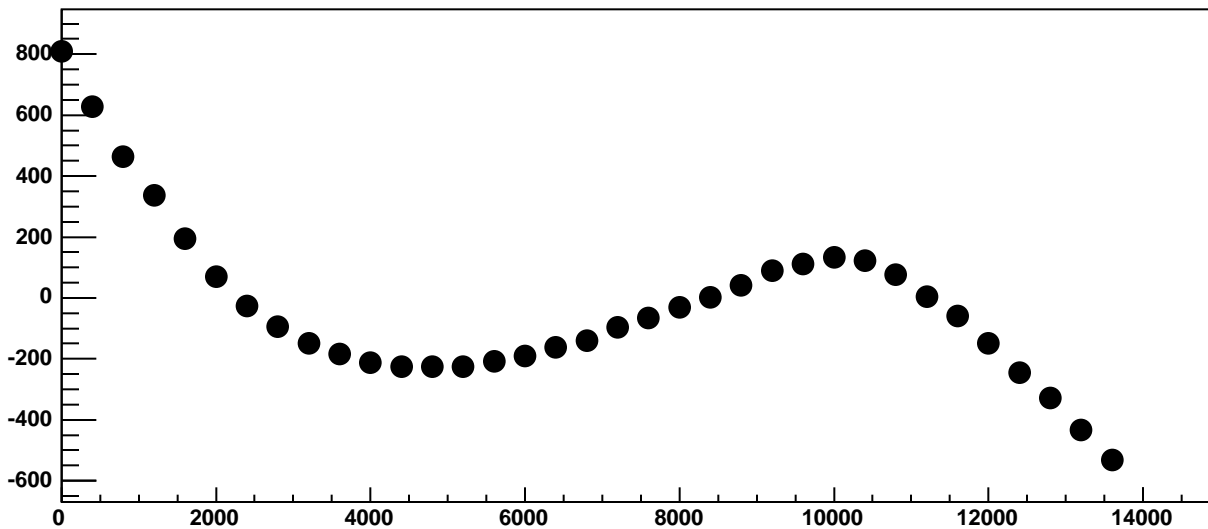
χ^2 / ndf 3.572e+04 / 23
p0 -1357 ± 1.288
p1 0.4659 ± 0.0002284



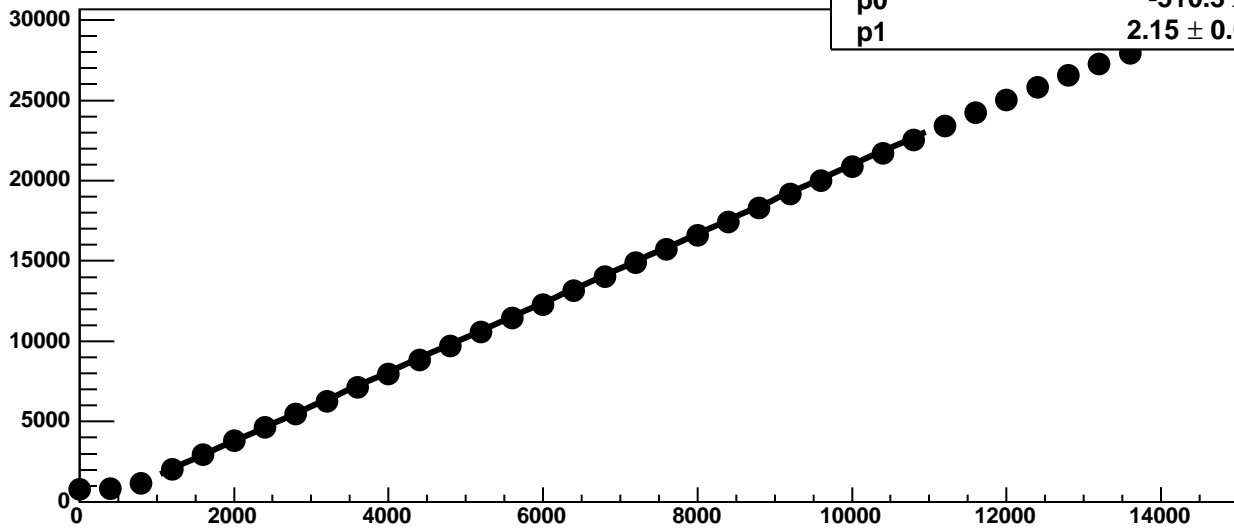
Chip 11, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



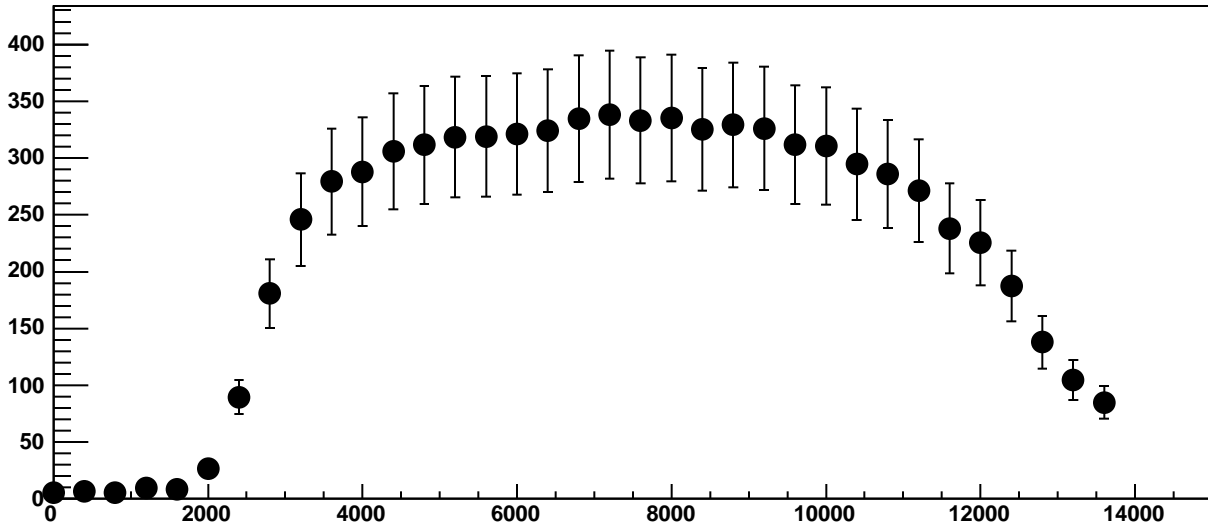
Chip 11, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



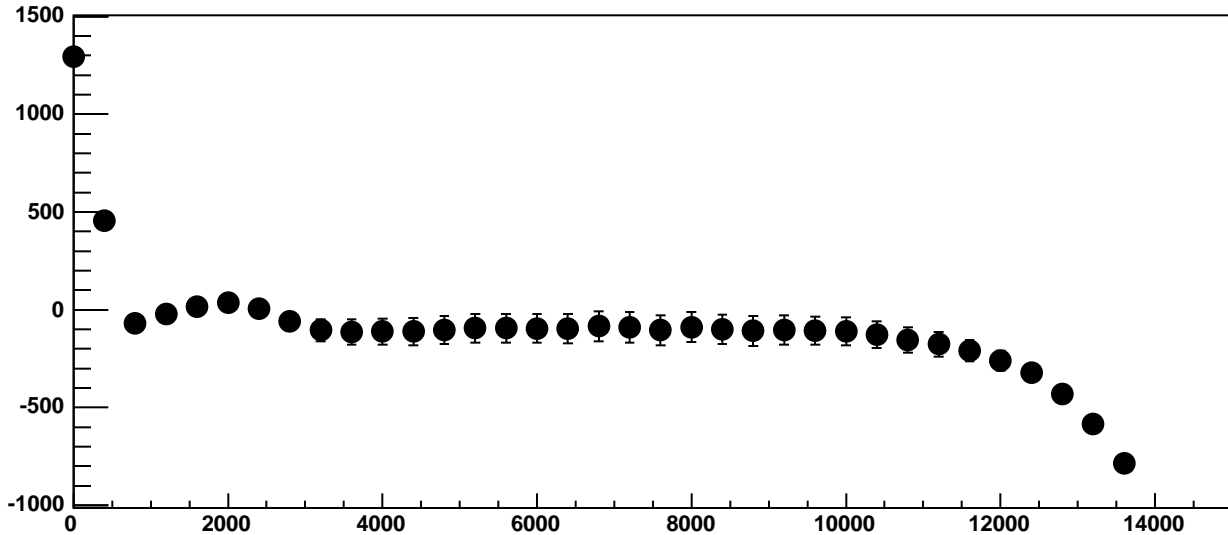
Chip 11, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC



Chip 11, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

χ^2 / ndf

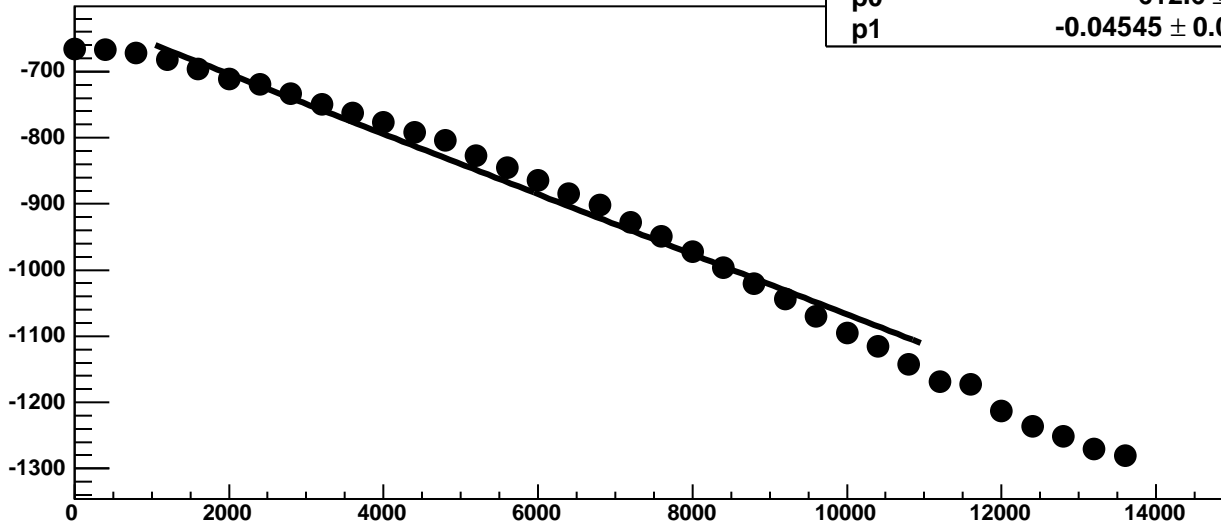
753.2 / 23

p0

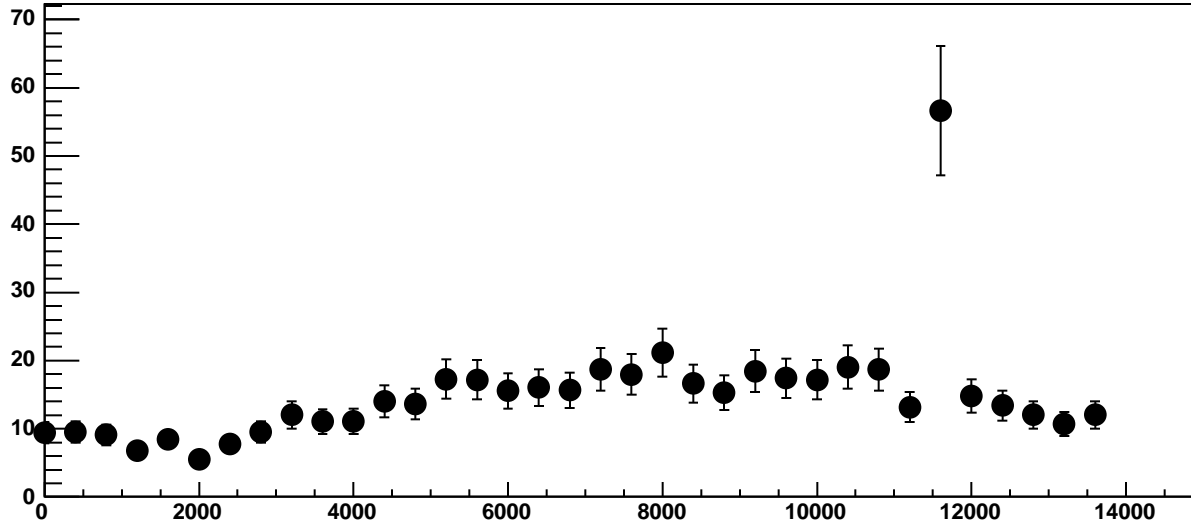
-612.6 ± 0.9513

p1

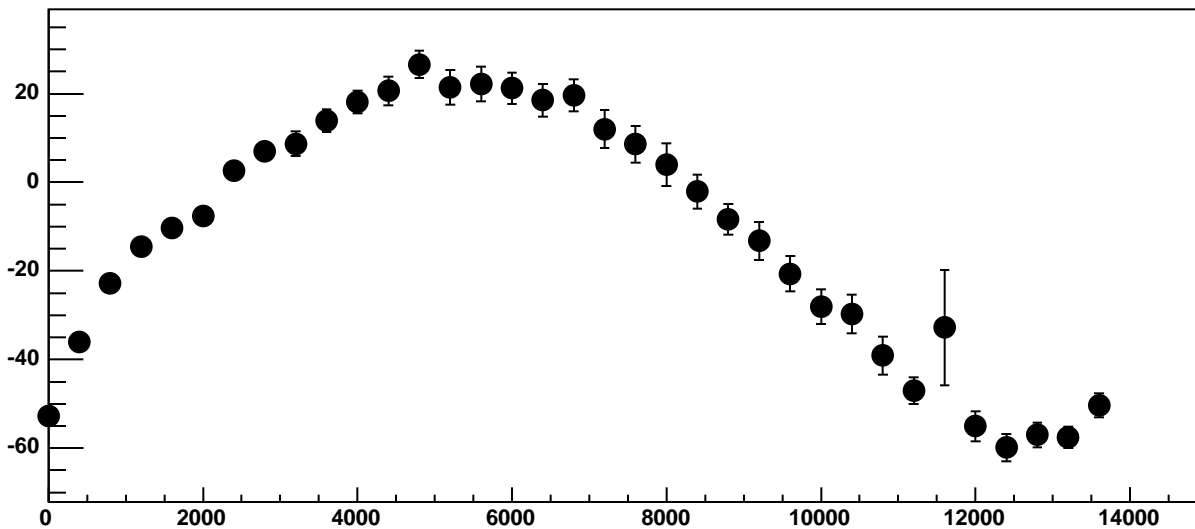
-0.04545 ± 0.0001971



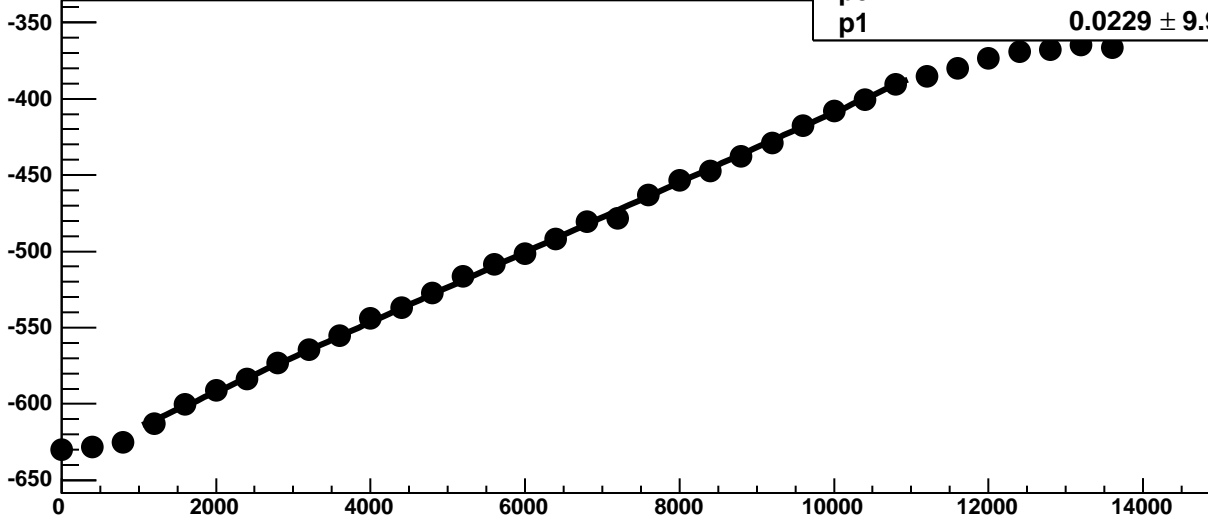
Chip 11, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



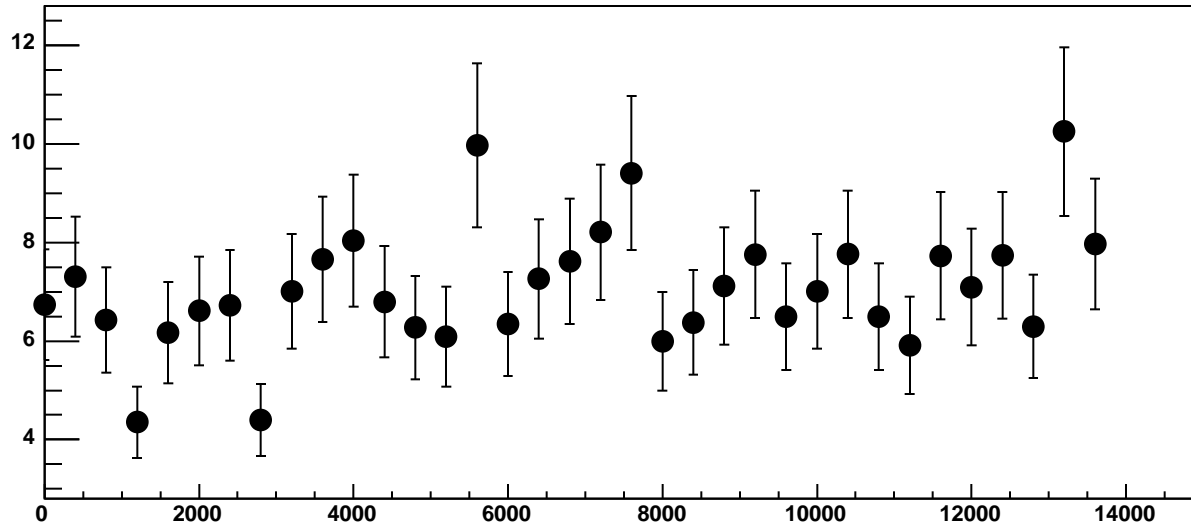
Chip 11, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



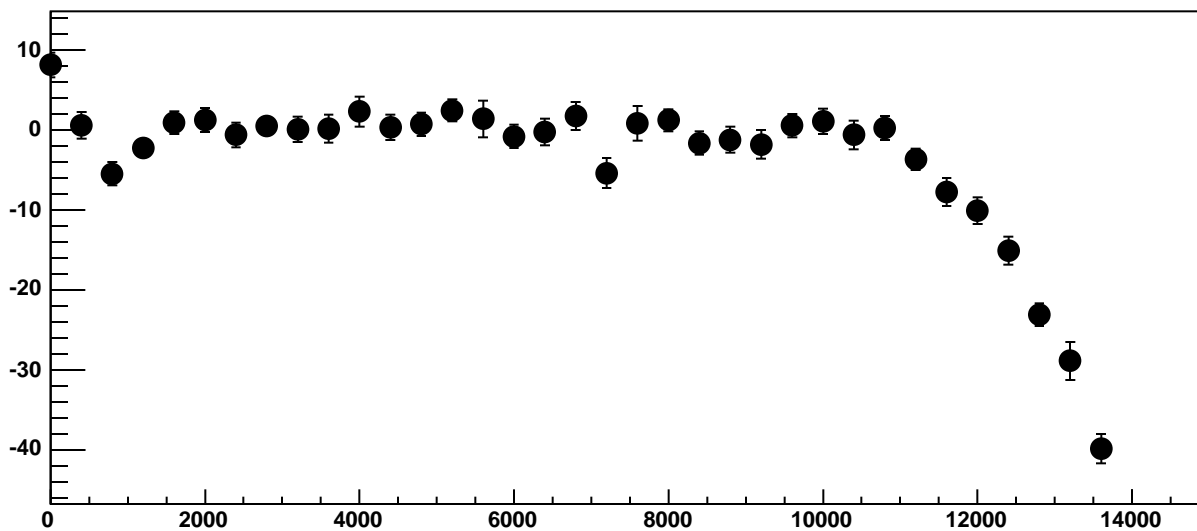
Chip 11, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC



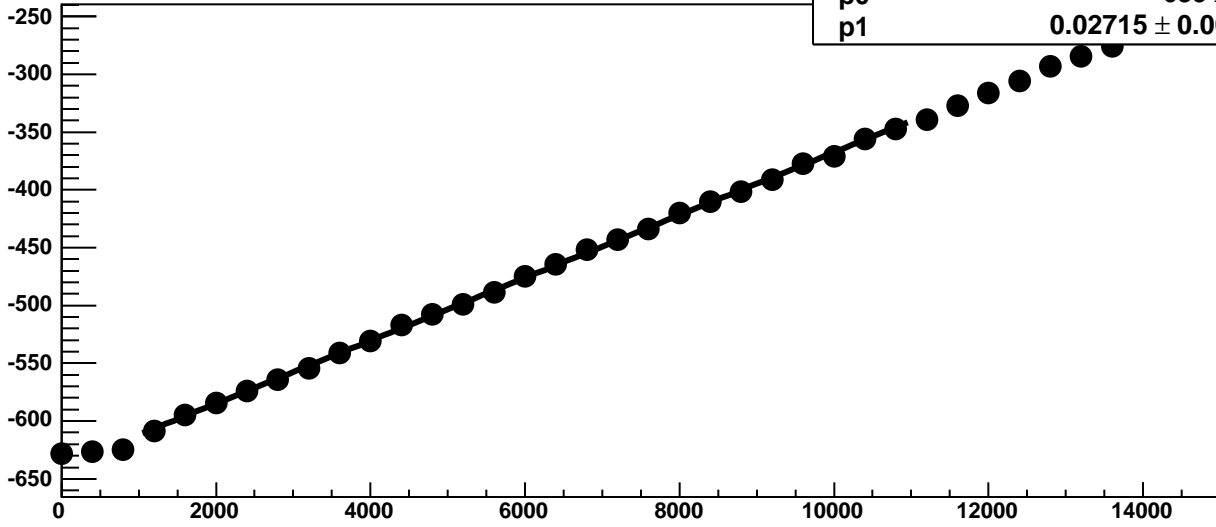
Chip 11, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

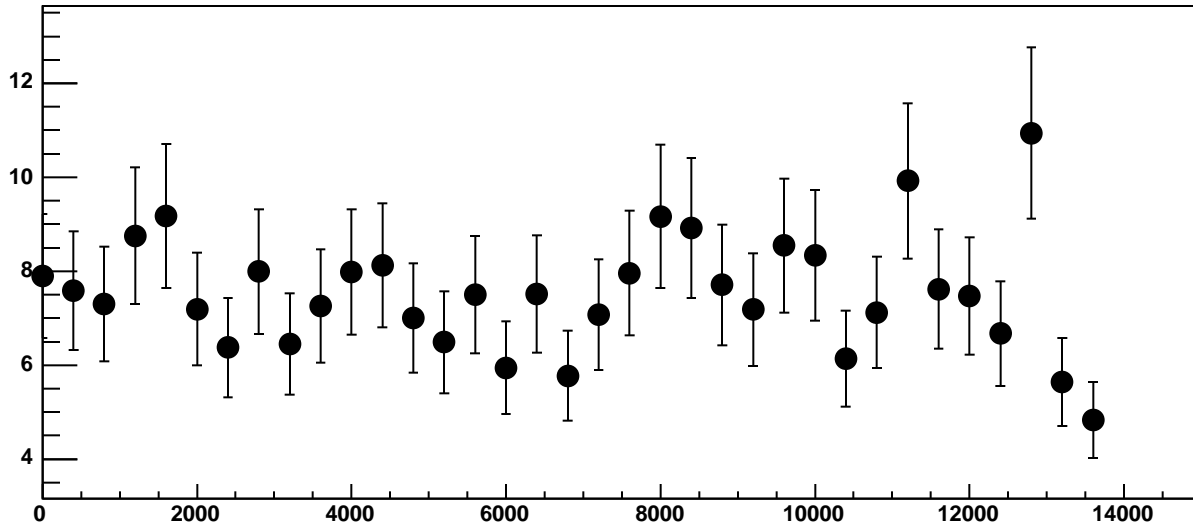


Chip 11, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC

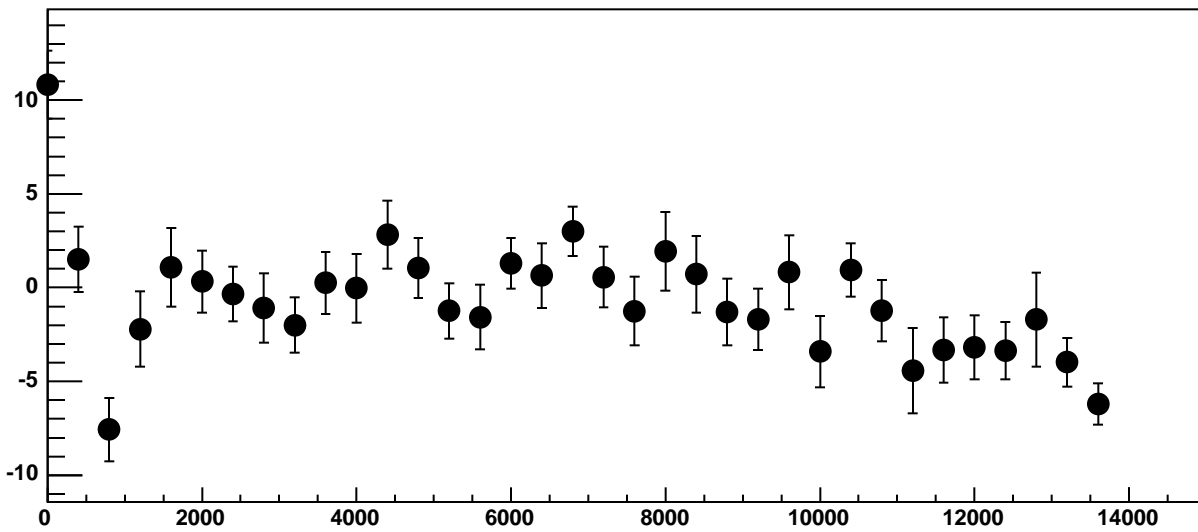


χ^2 / ndf 21.84 / 23
p0 -639 ± 0.794
p1 0.02715 ± 0.0001195

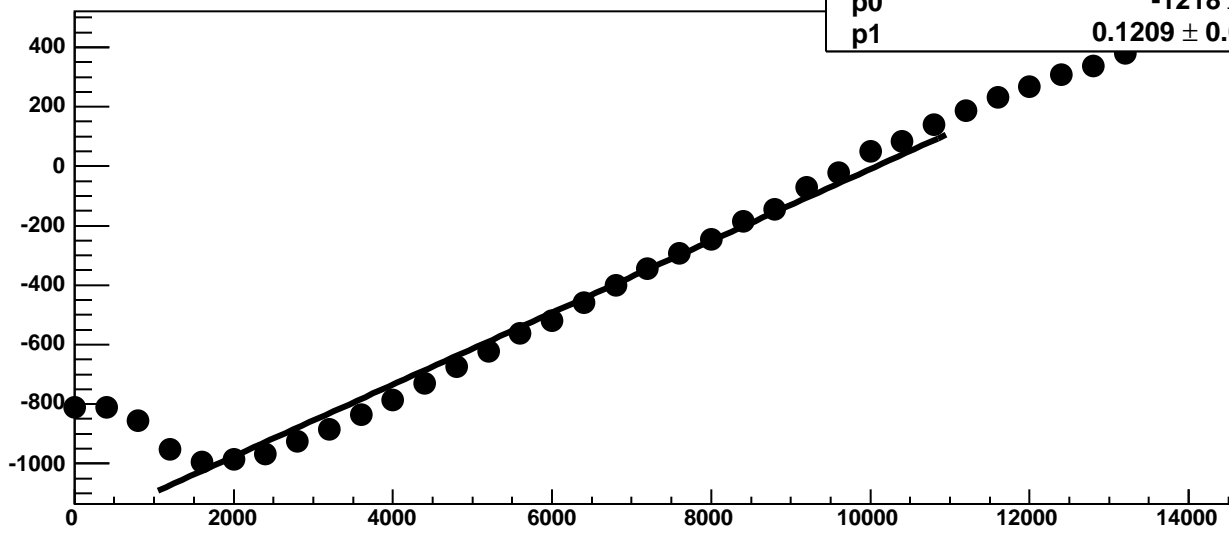
Chip 11, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



Chip 11, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

5719 / 23

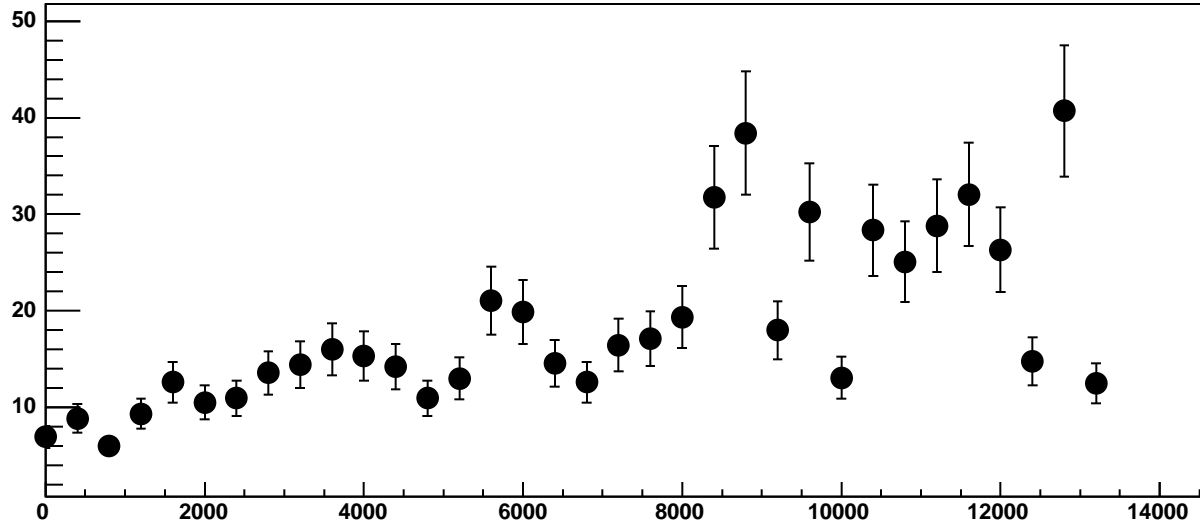
p0

-1218 ± 1.355

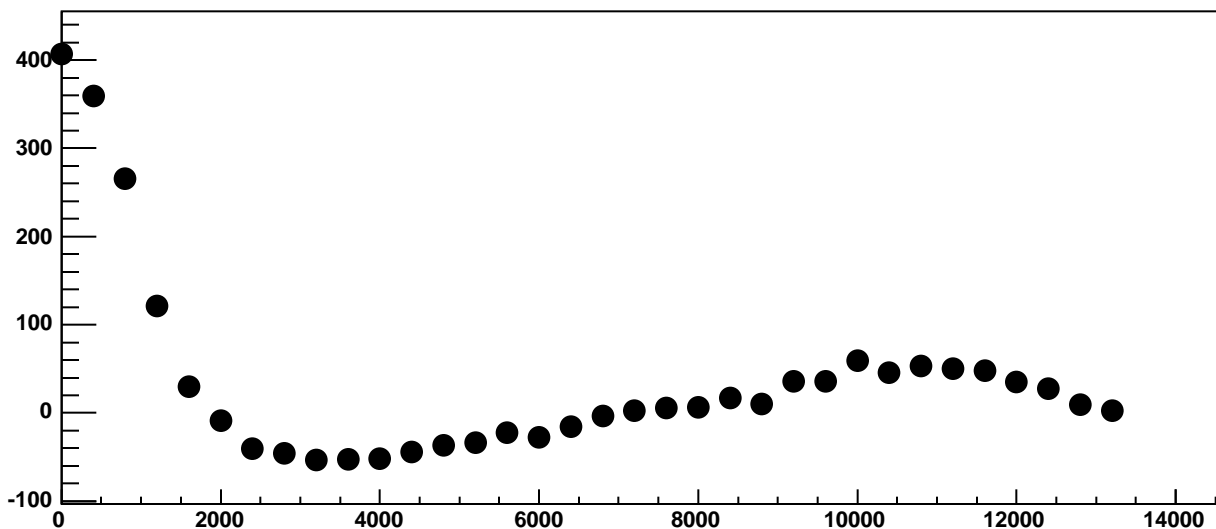
p1

0.1209 ± 0.000248

Chip 11, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

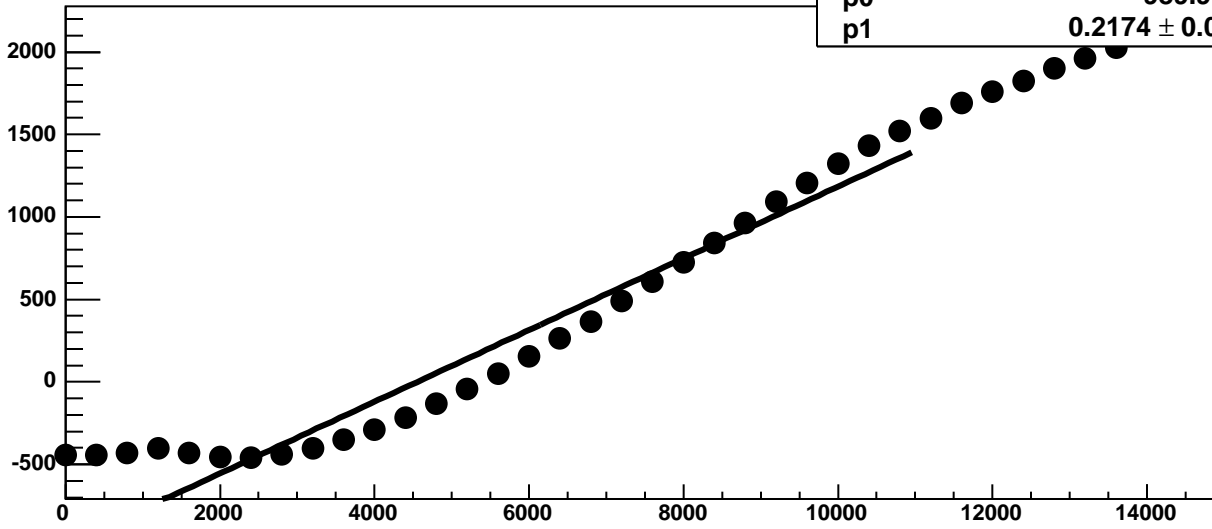


Chip 11, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

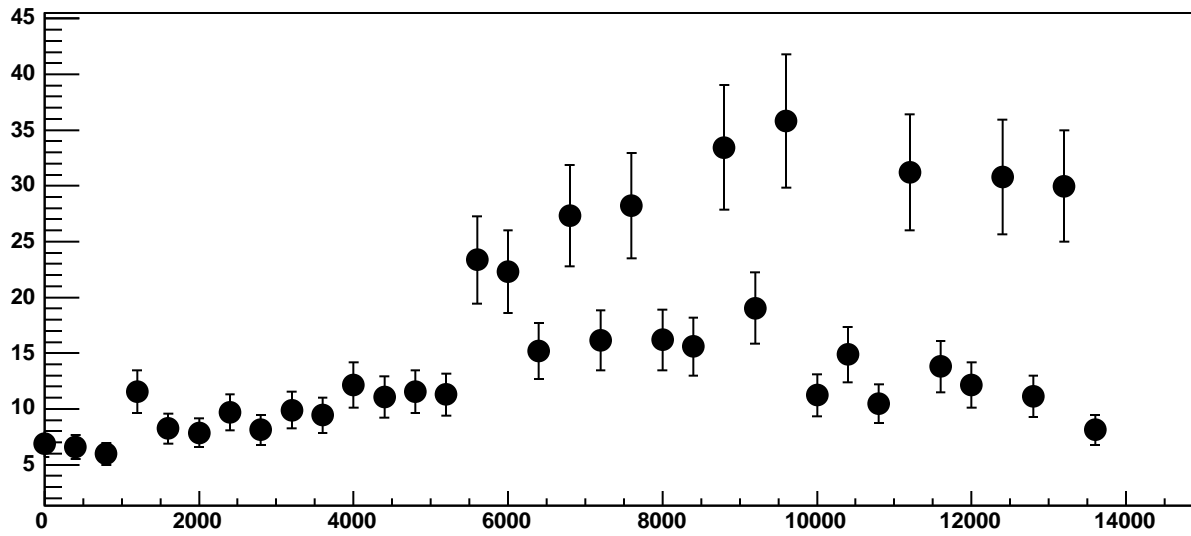


Chip 11, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

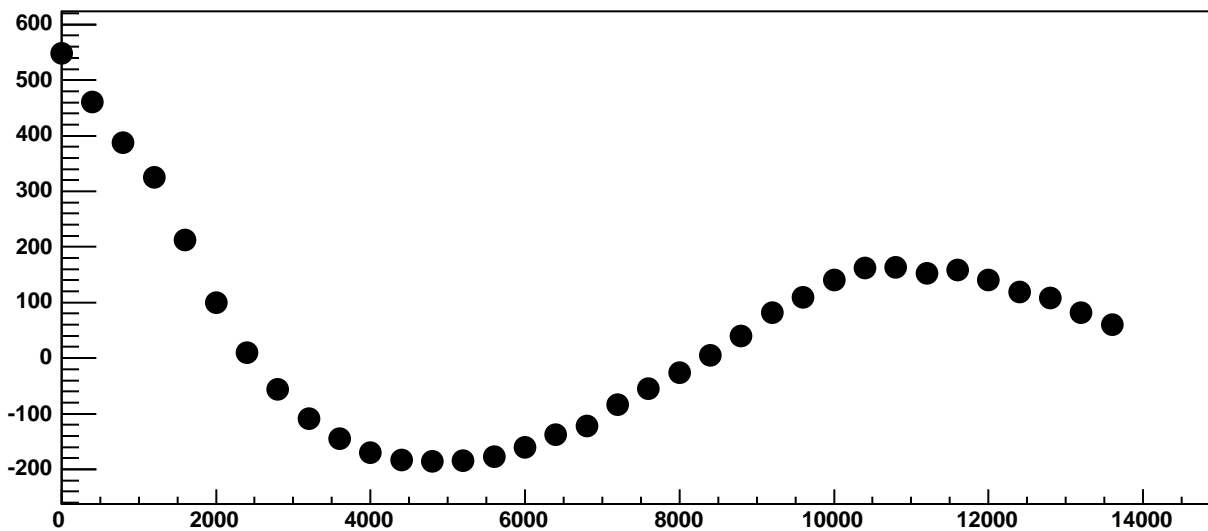
χ^2 / ndf 7.241e+04 / 23
p0 -989.9 ± 1.056
p1 0.2174 ± 0.0001899



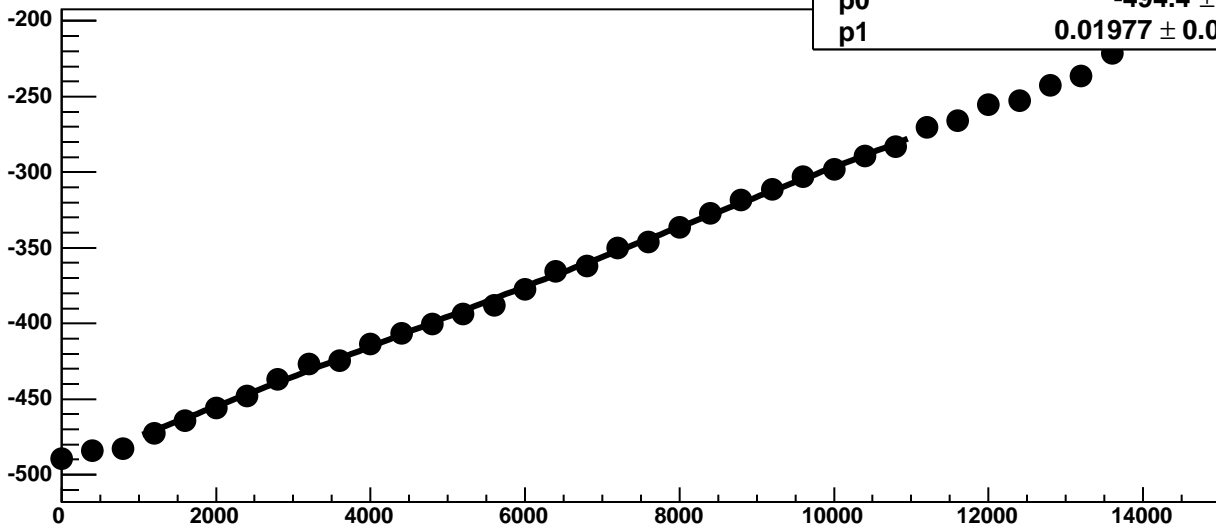
Chip 11, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

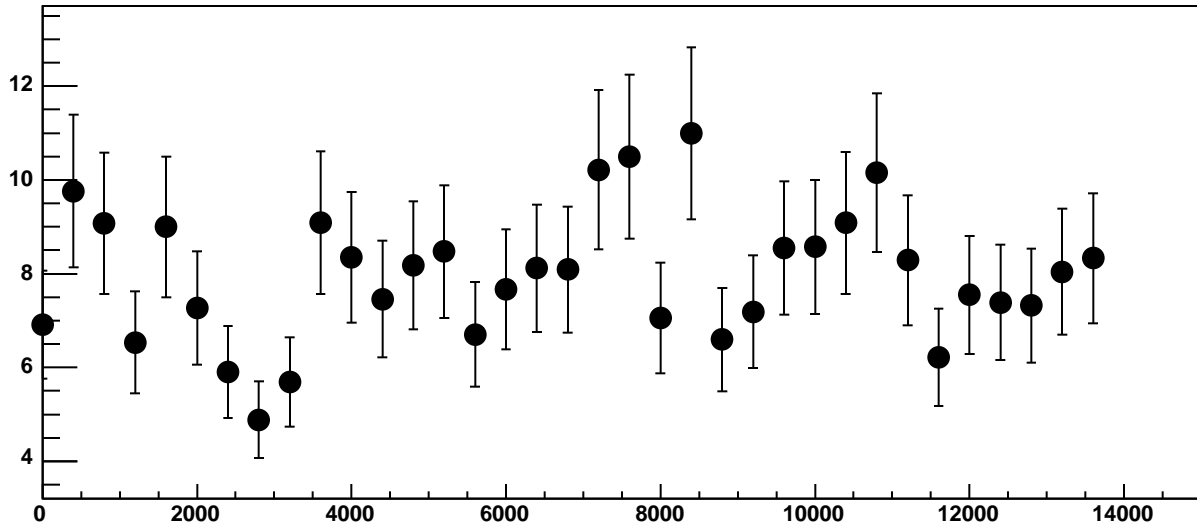


Chip 11, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC

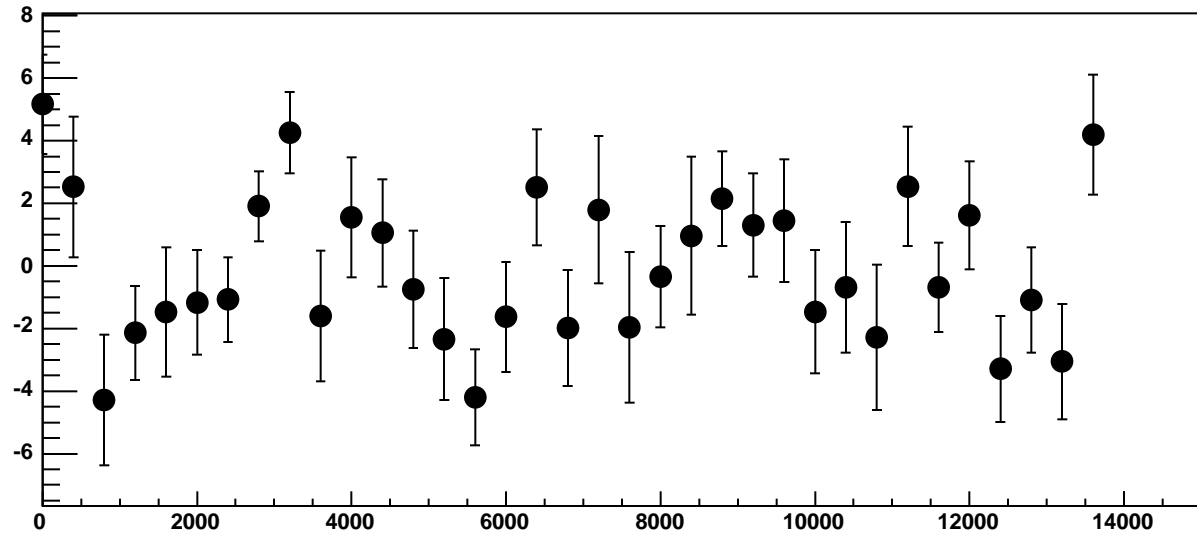


χ^2 / ndf 37.97 / 23
p0 -494.4 ± 0.7417
p1 0.01977 ± 0.0001211

Chip 11, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

χ^2 / ndf

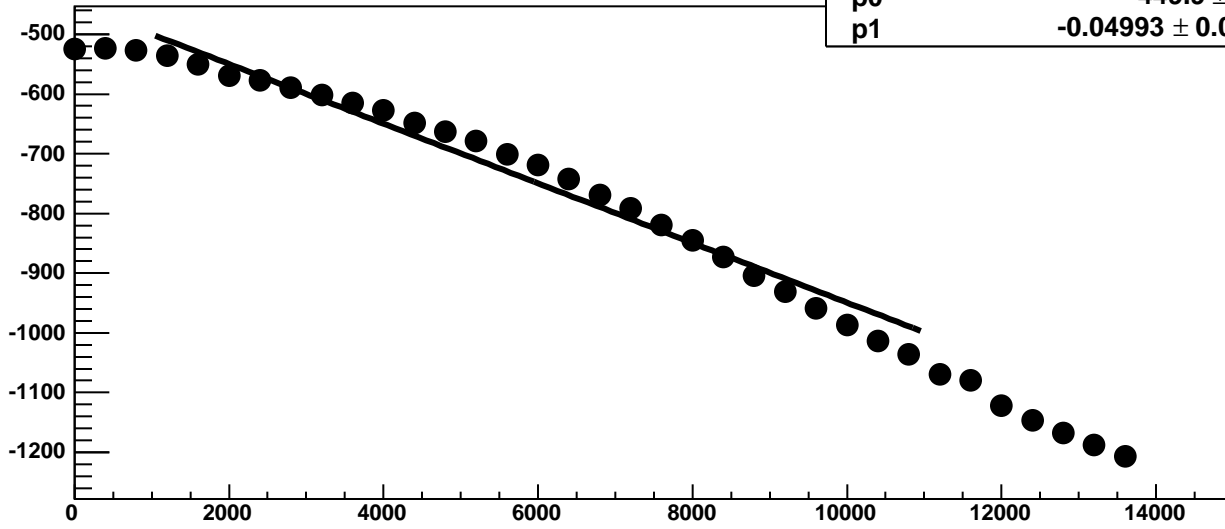
2291 / 23

p0

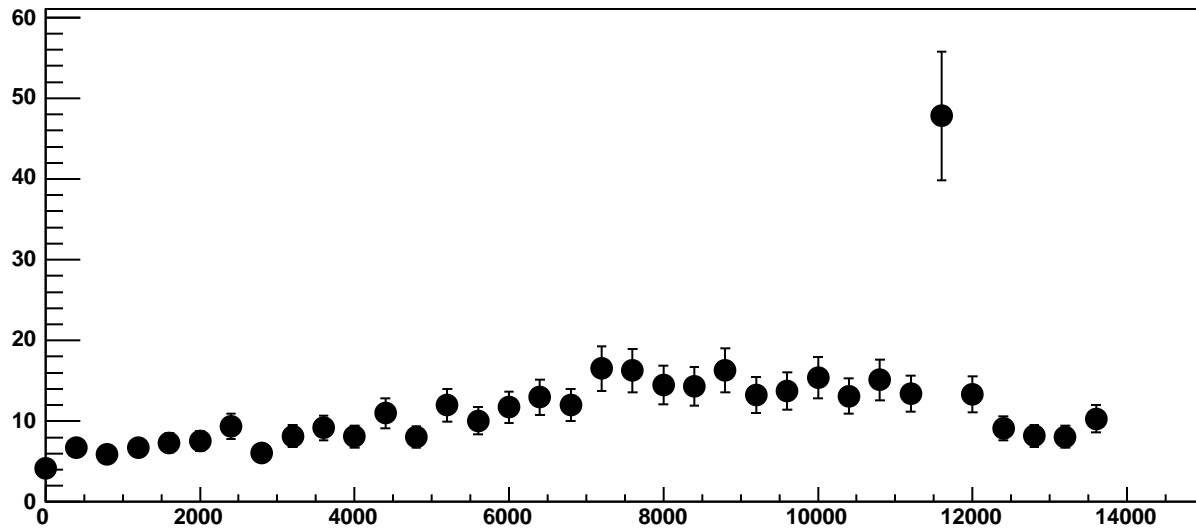
-449.9 ± 0.8937

p1

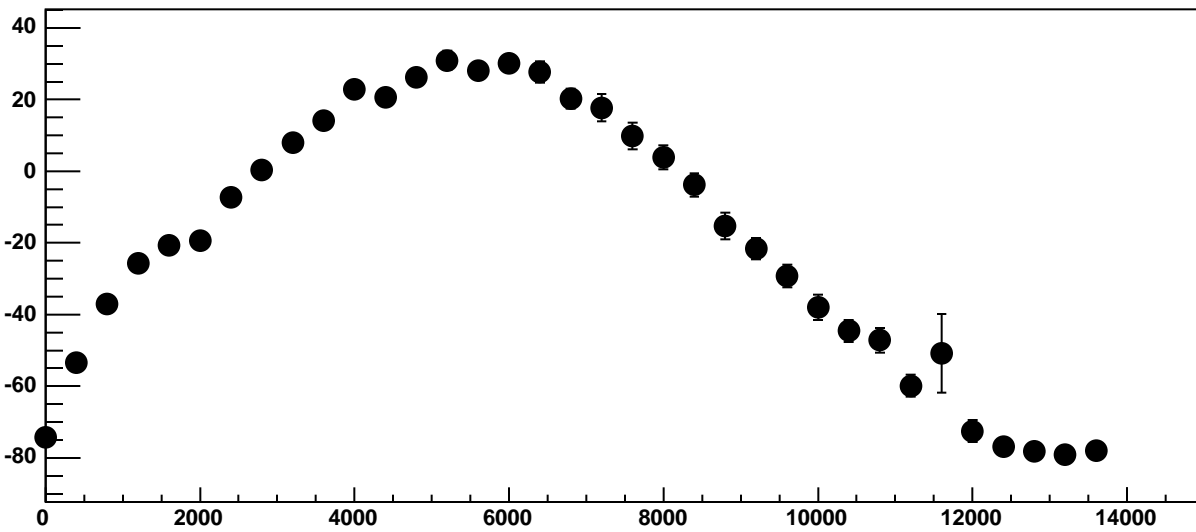
-0.04993 ± 0.0001711



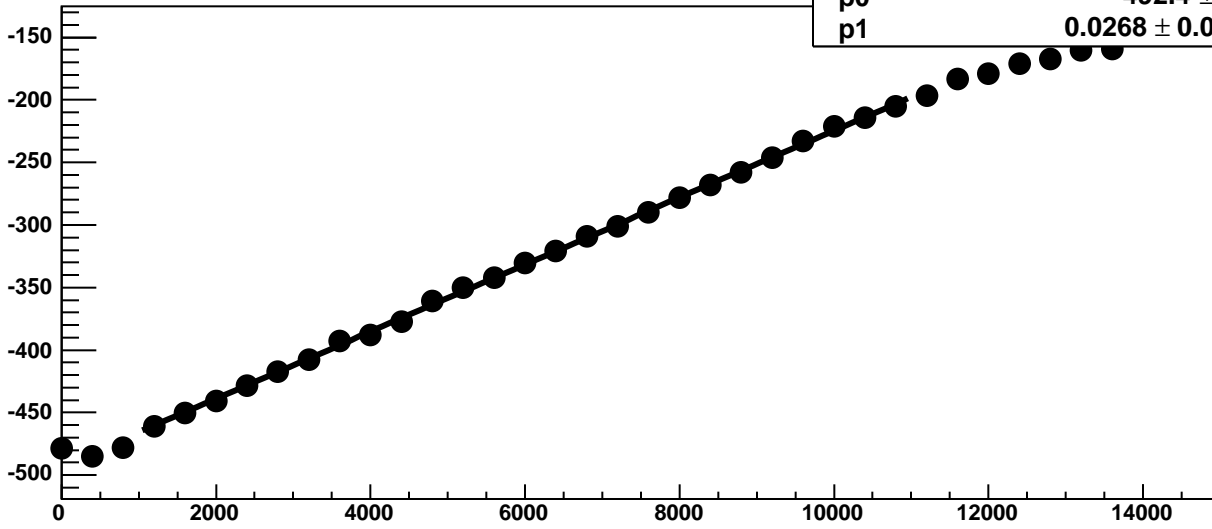
Chip 11, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

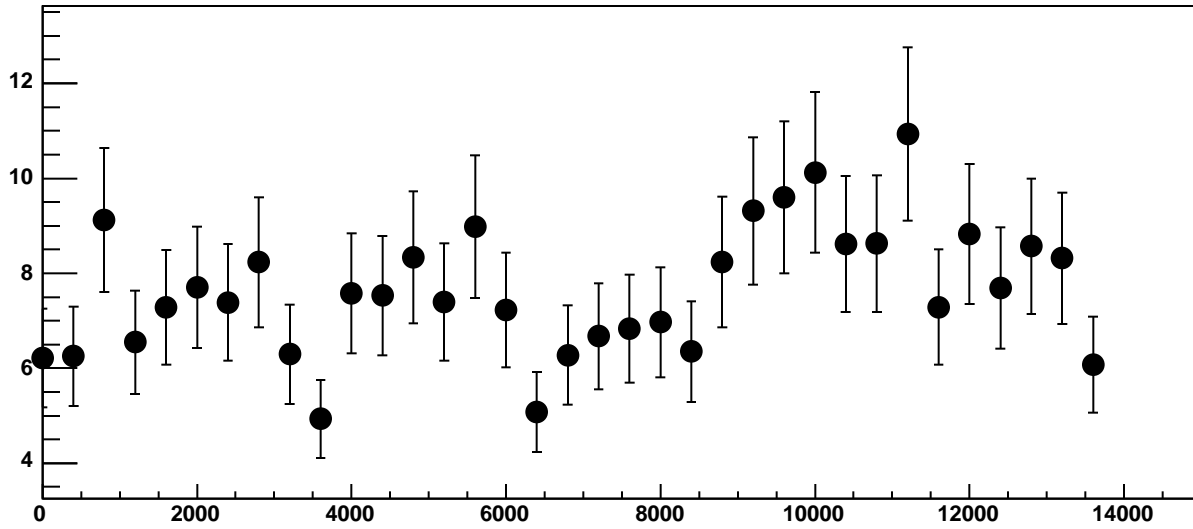


Chip 11, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

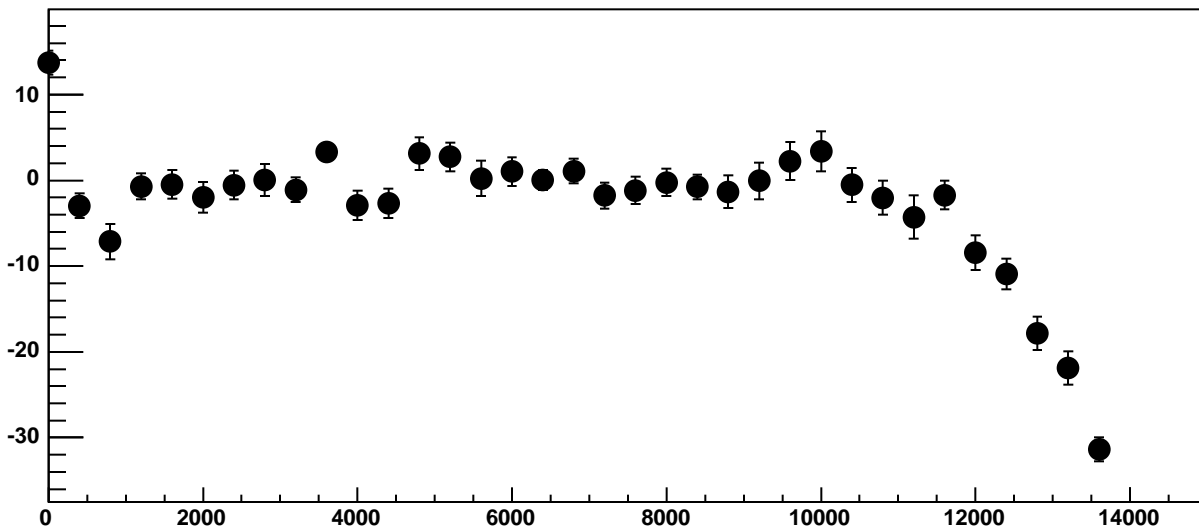


χ^2 / ndf 29.01 / 23
p0 -492.4 ± 0.7659
p1 0.0268 ± 0.0001222

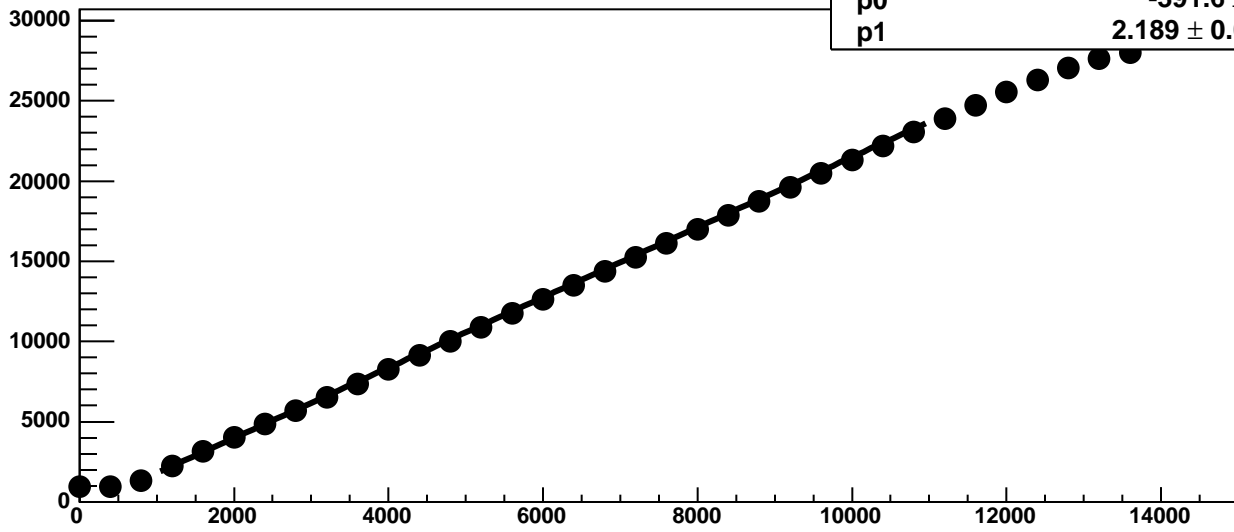
Chip 11, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



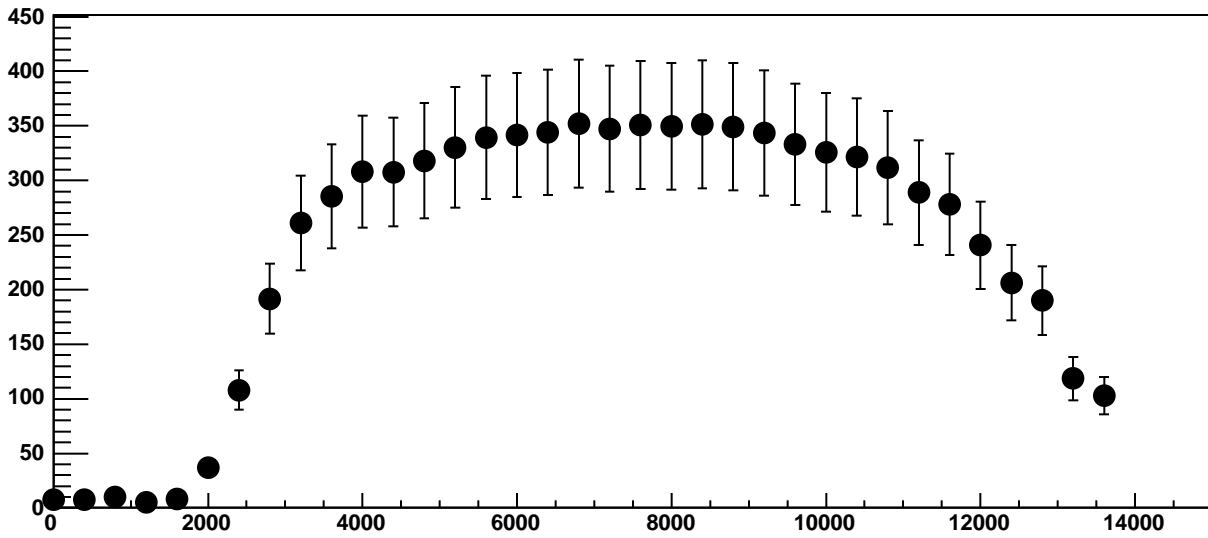
Chip 11, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



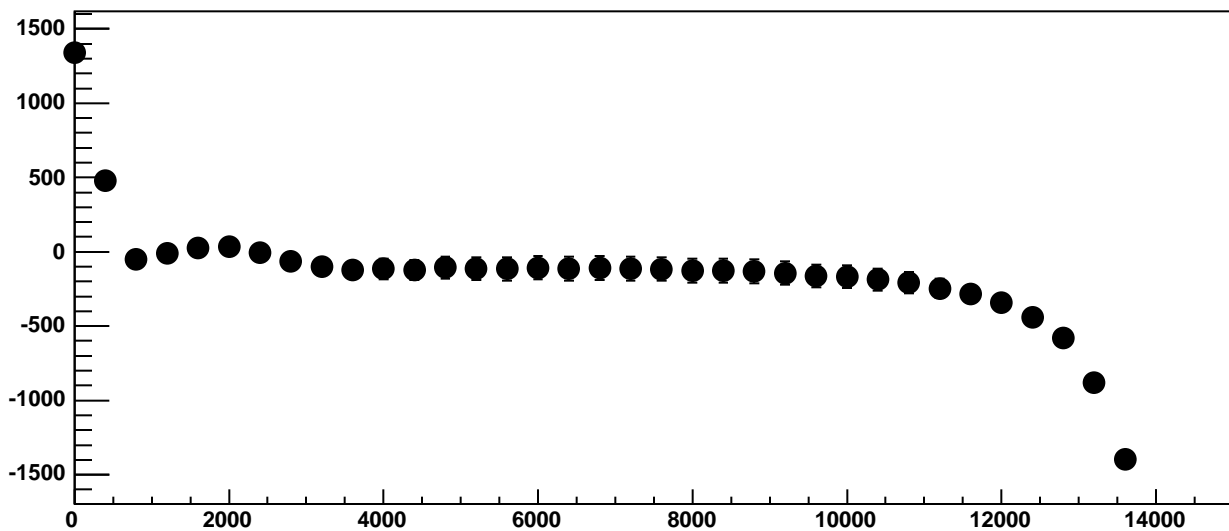
Chip 11, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC



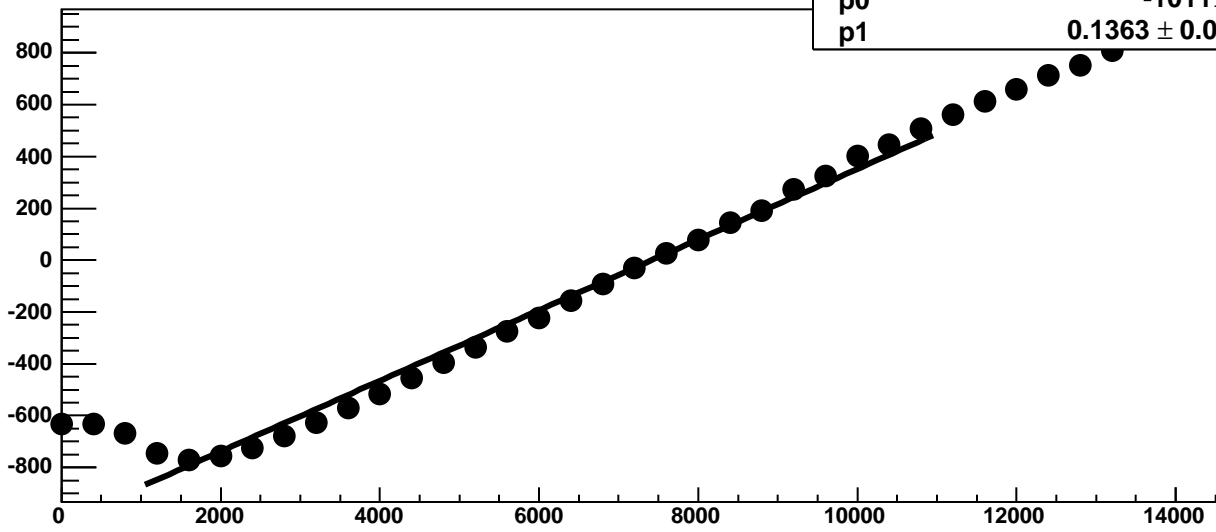
Chip 11, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



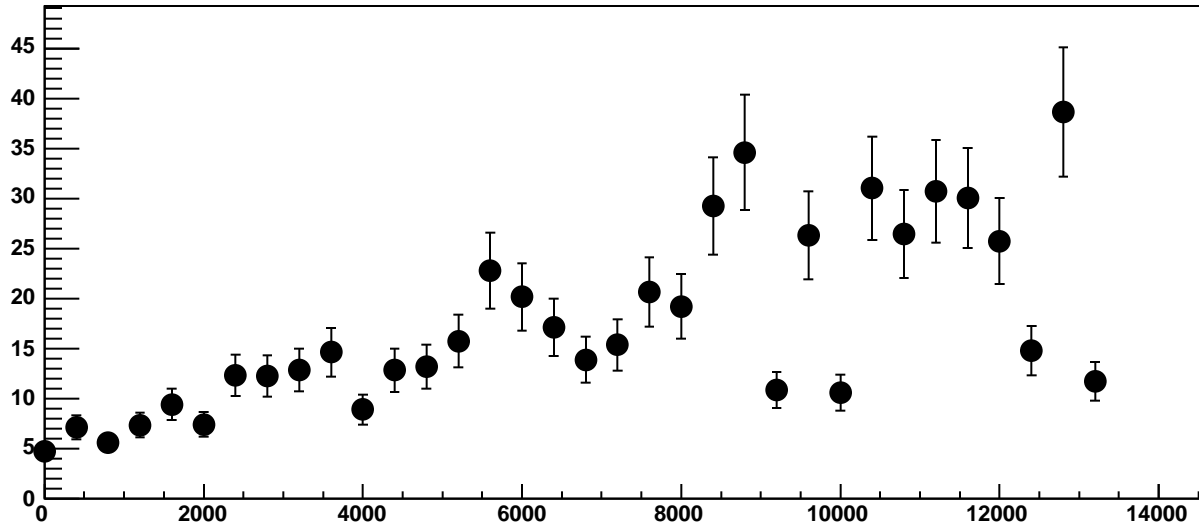
Chip 11, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC



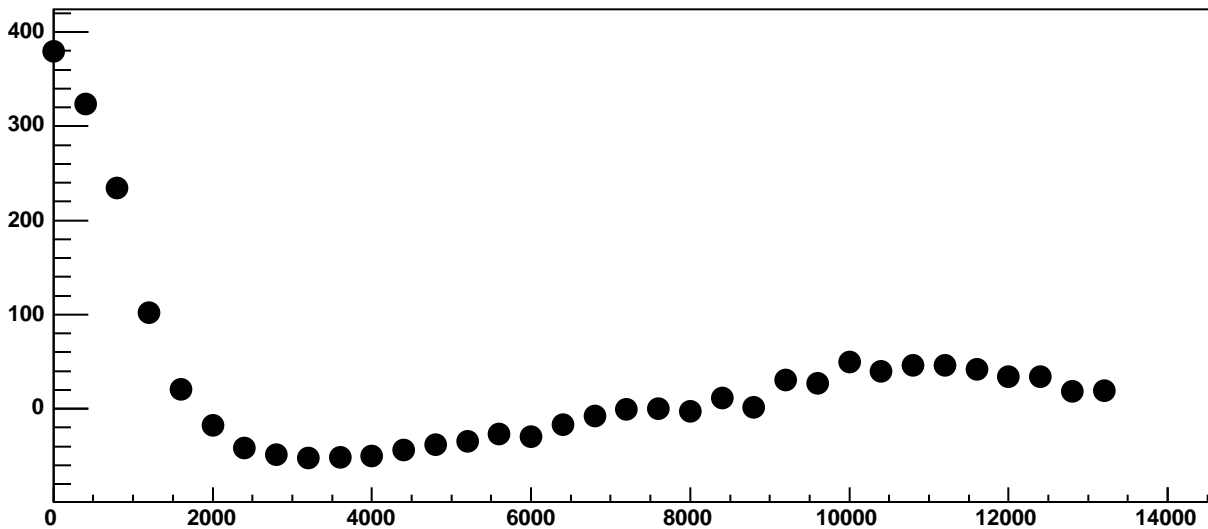
Chip 11, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



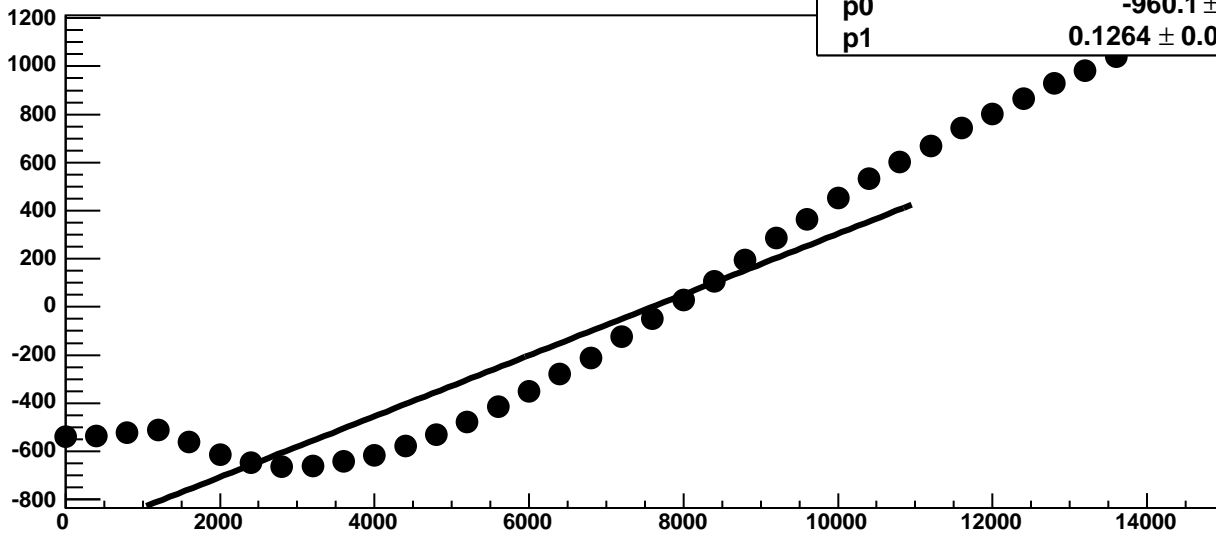
Chip 11, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



Chip 11, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

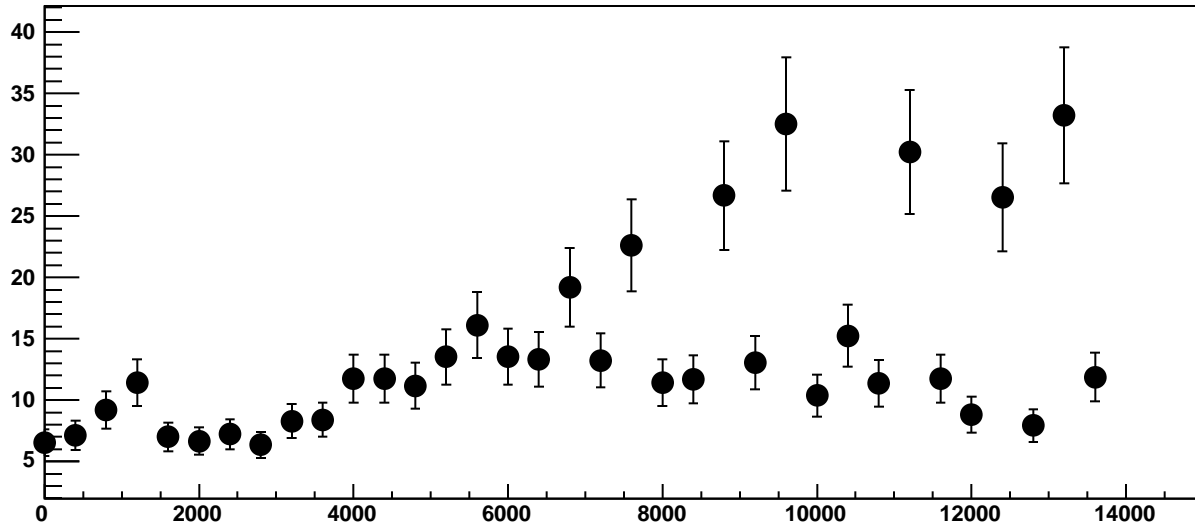


Chip 11, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

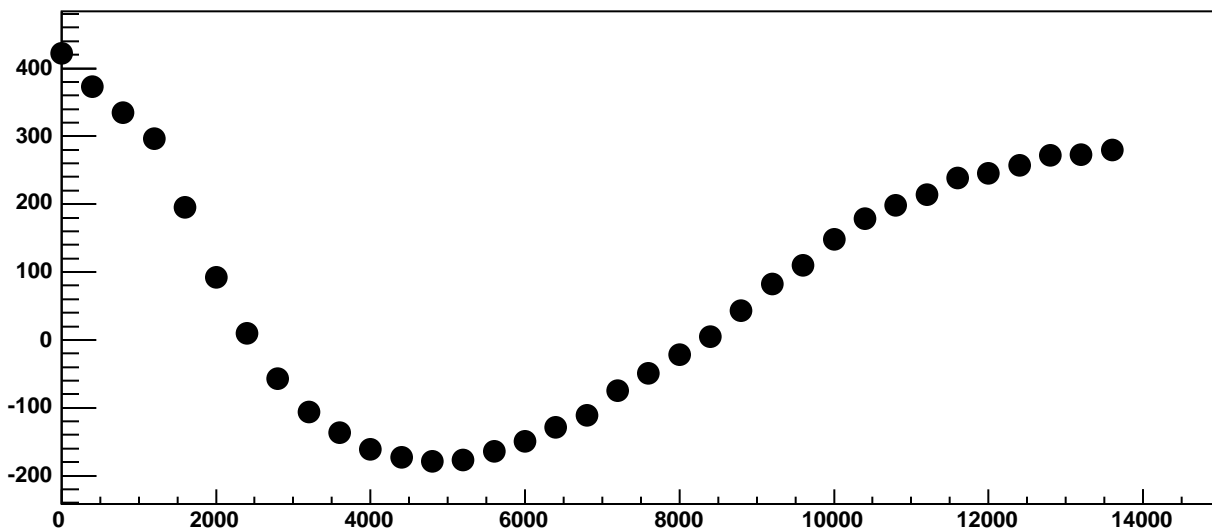


χ^2 / ndf 7.741e+04 / 23
p0 -960.1 ± 0.9169
p1 0.1264 ± 0.0001697

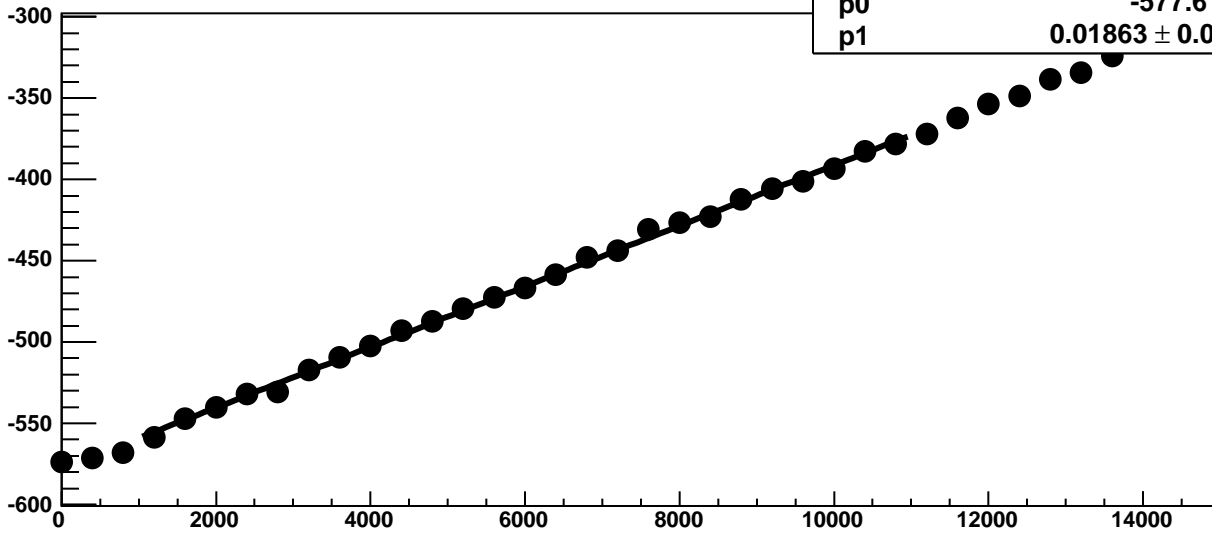
Chip 11, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

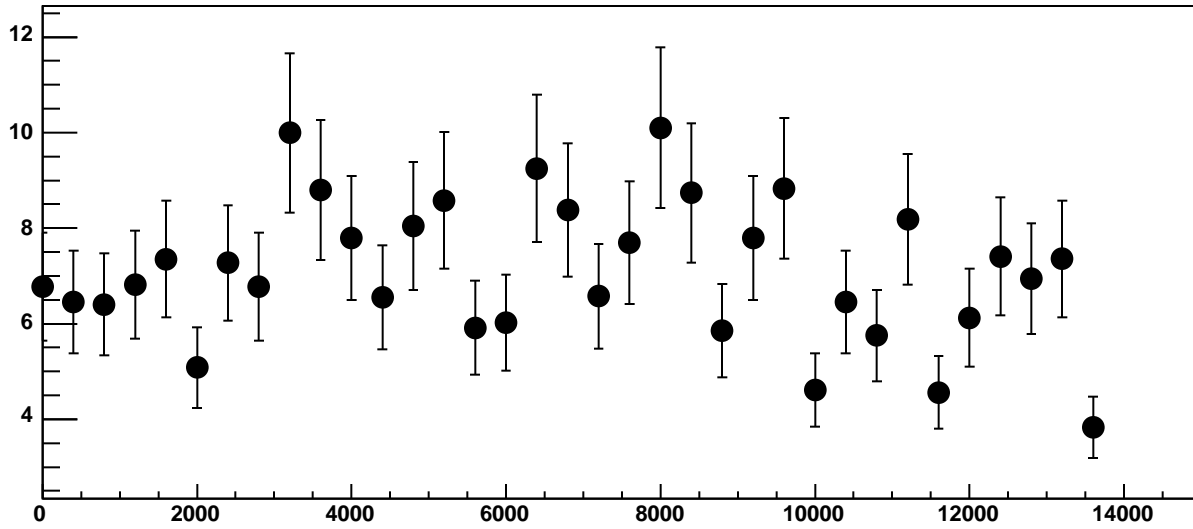


Chip 11, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

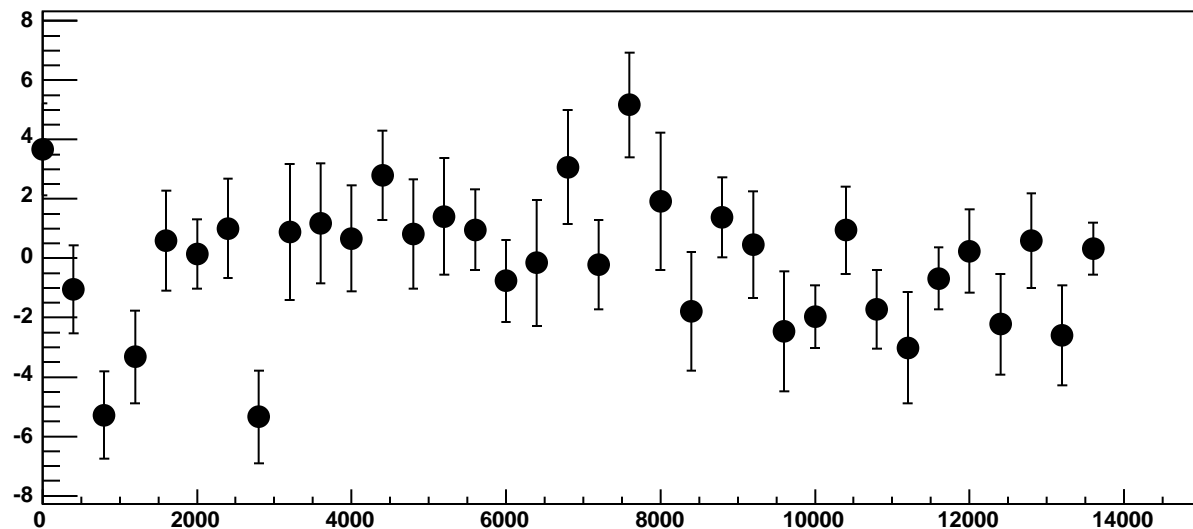


χ^2 / ndf 43.22 / 23
p0 -577.6 ± 0.715
p1 0.01863 ± 0.0001035

Chip 11, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

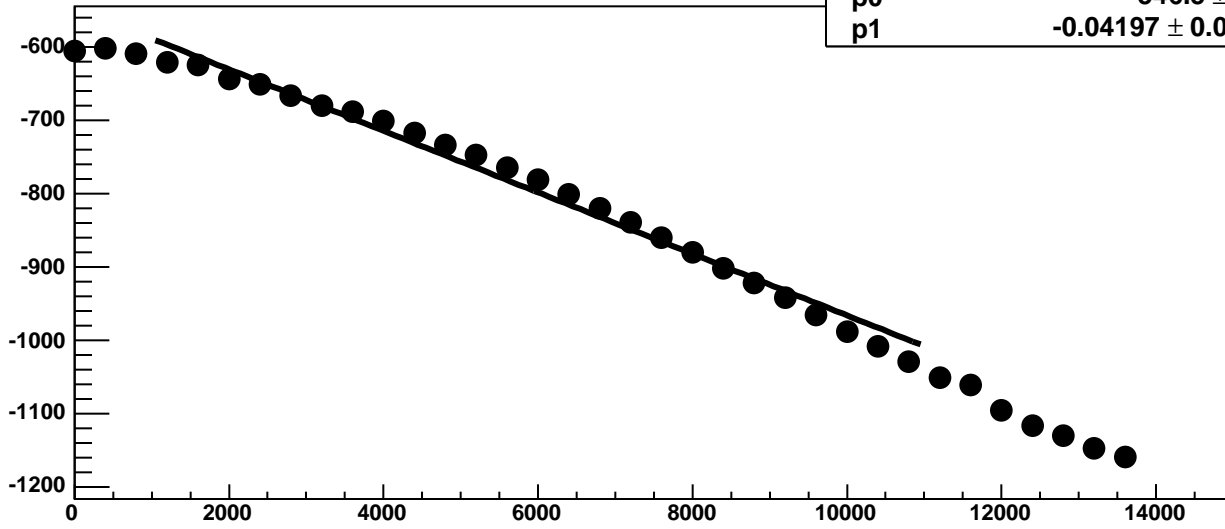


Chip 11, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

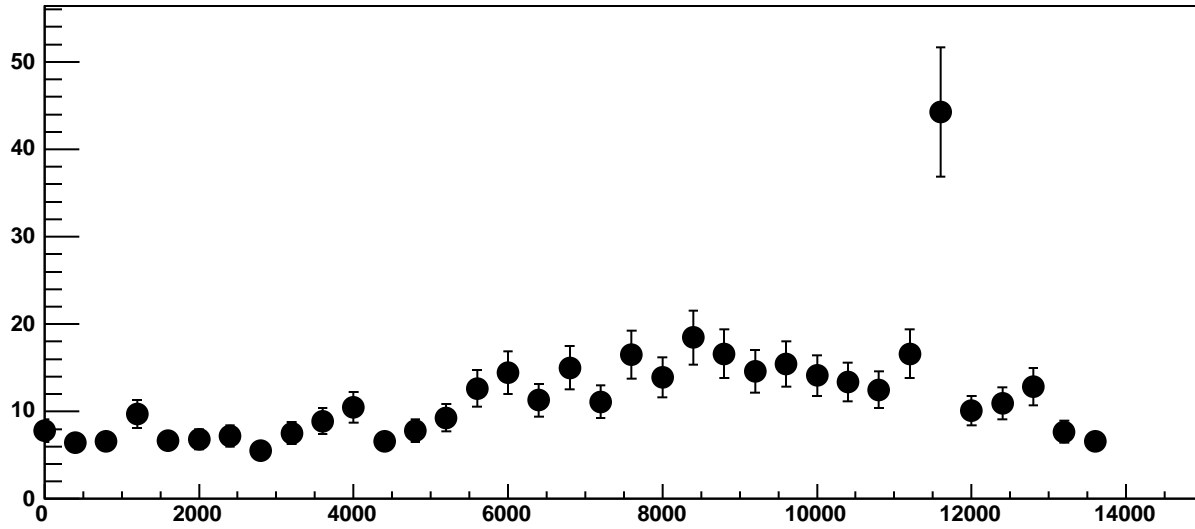


Chip 11, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

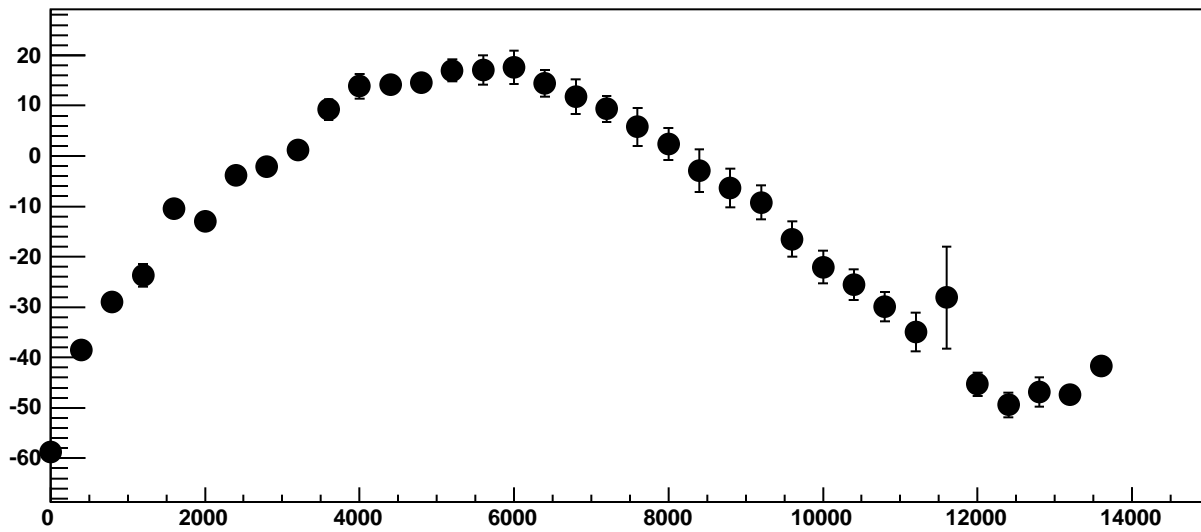
χ^2 / ndf 886.4 / 23
p0 -546.5 ± 0.8762
p1 -0.04197 ± 0.0001703



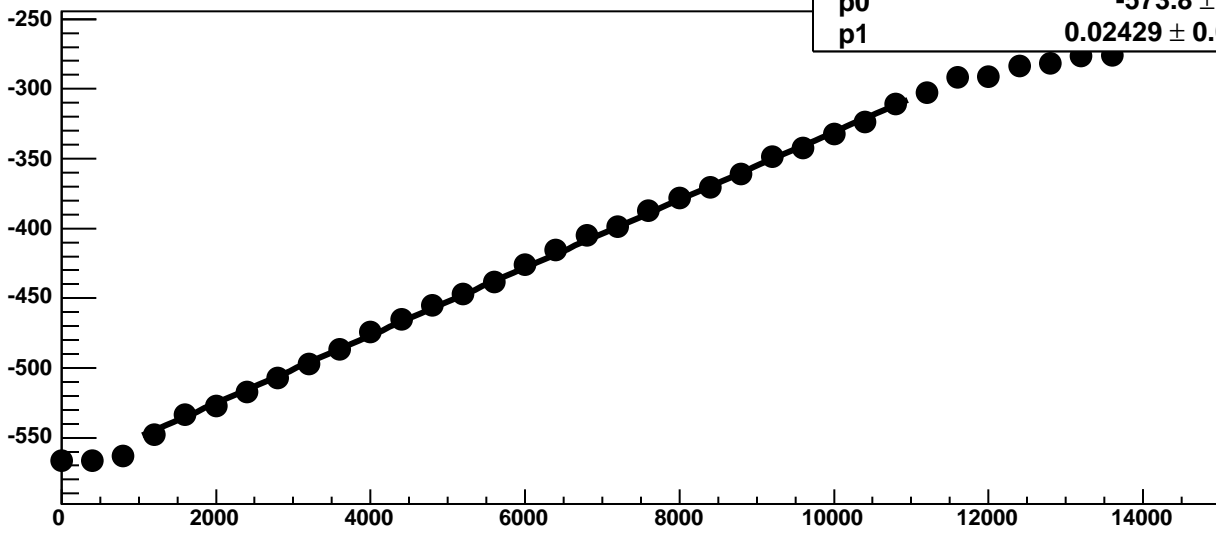
Chip 11, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC

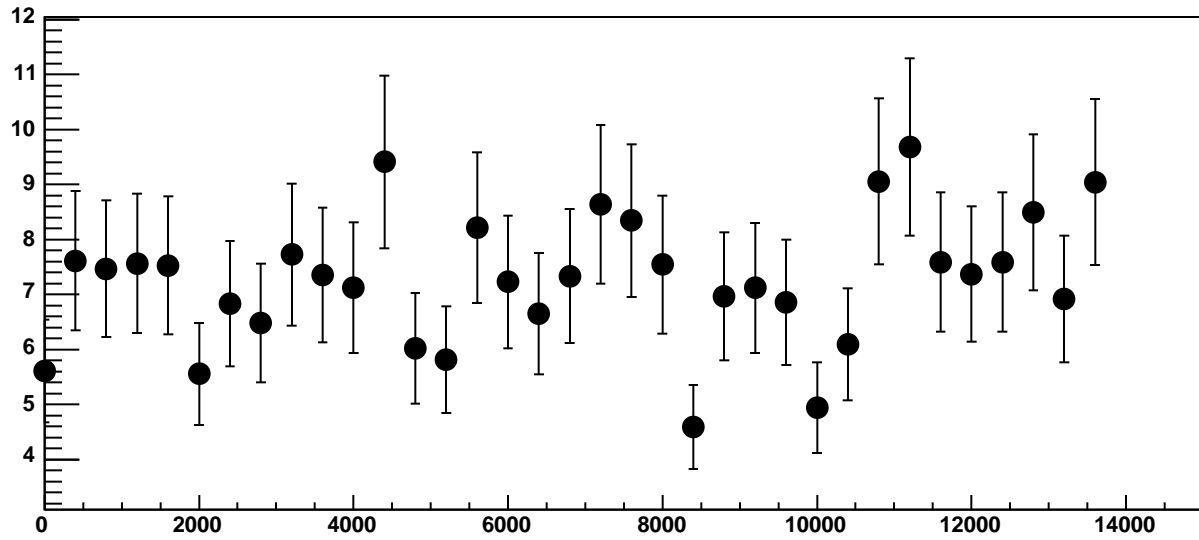


Chip 11, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

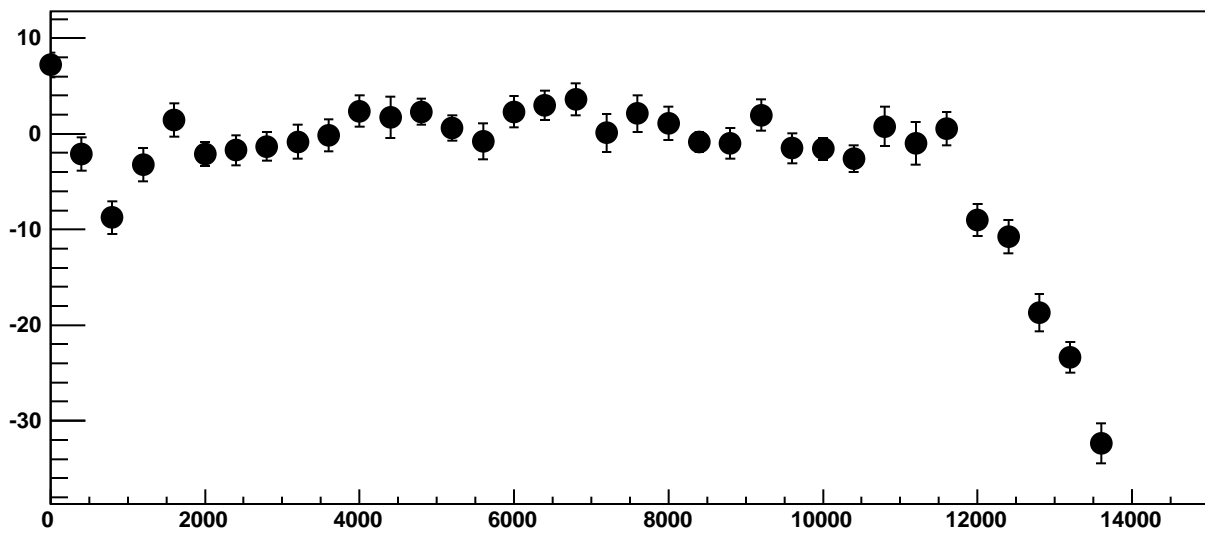


χ^2 / ndf 36.03 / 23
p0 -573.8 ± 0.7244
p1 0.02429 ± 0.000106

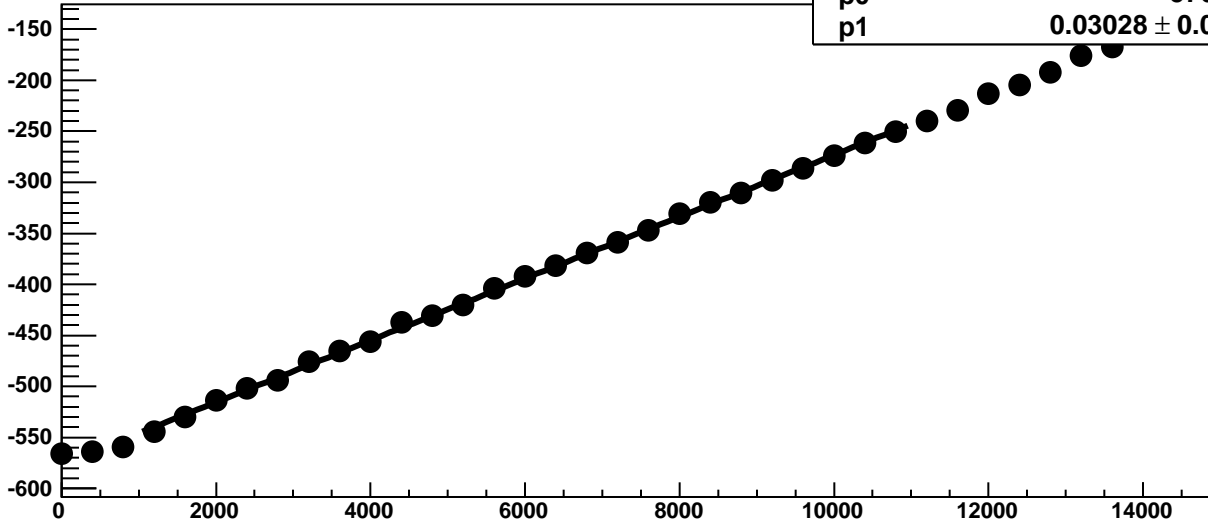
Chip 11, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

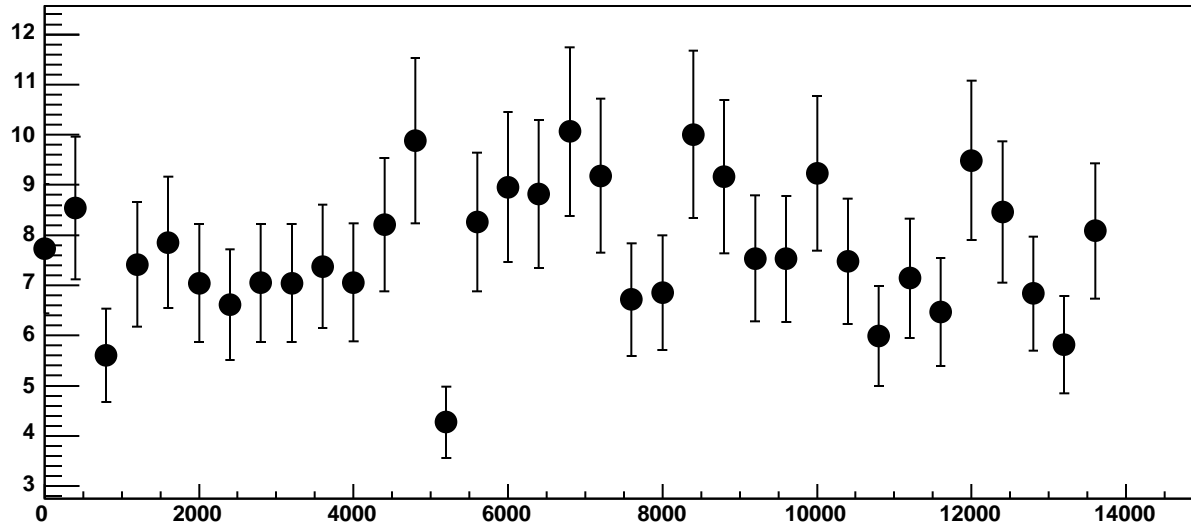


Chip 11, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

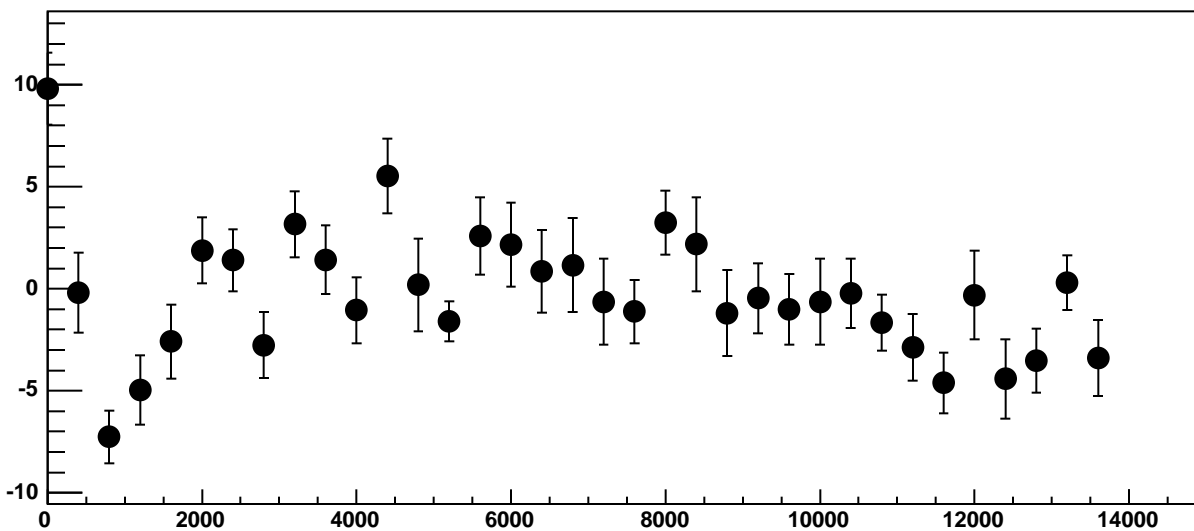


χ^2 / ndf 43.88 / 23
p0 -576 ± 0.76
p1 0.03028 ± 0.0001164

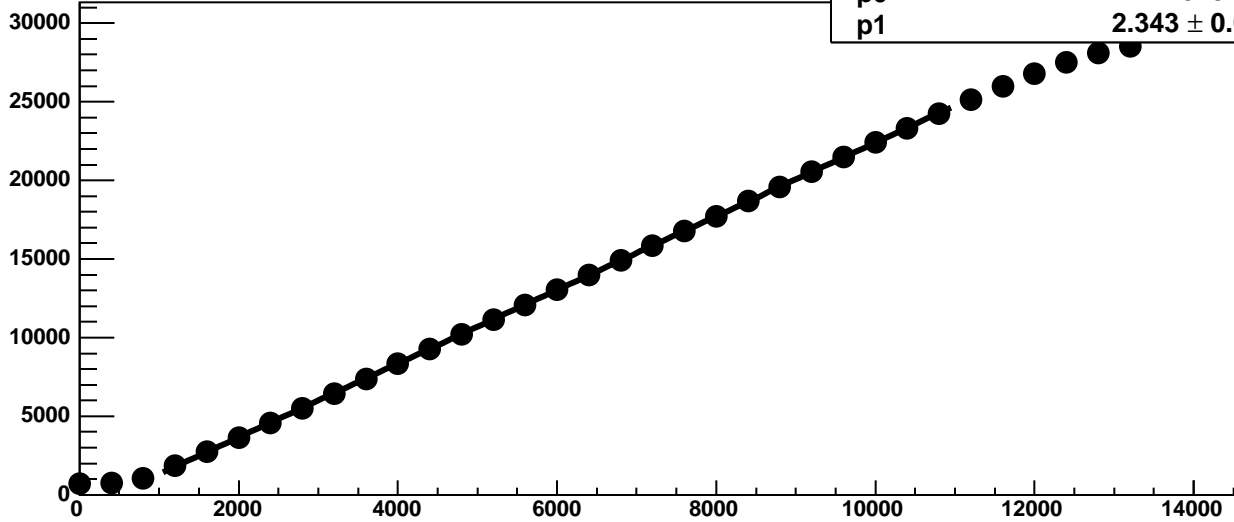
Chip 11, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



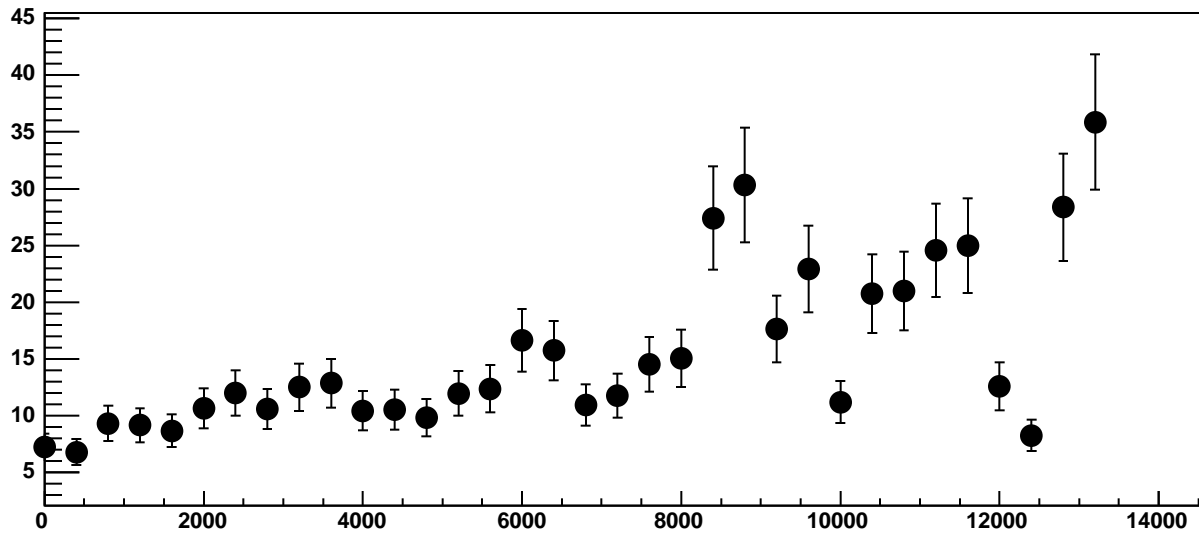
Chip 11, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



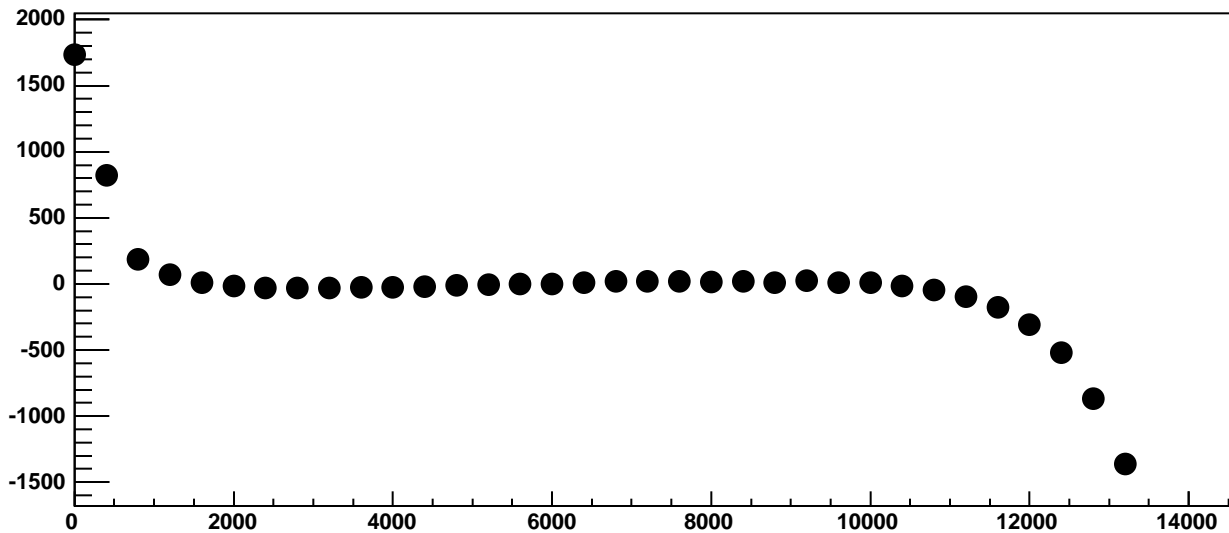
Chip 11, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC



Chip 11, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC

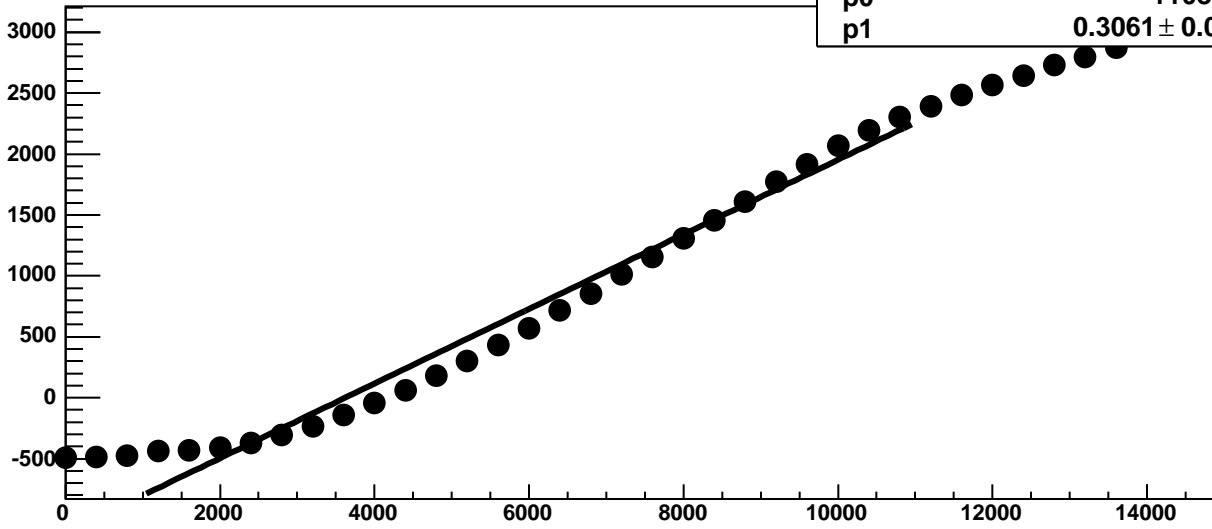


Chip 11, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC

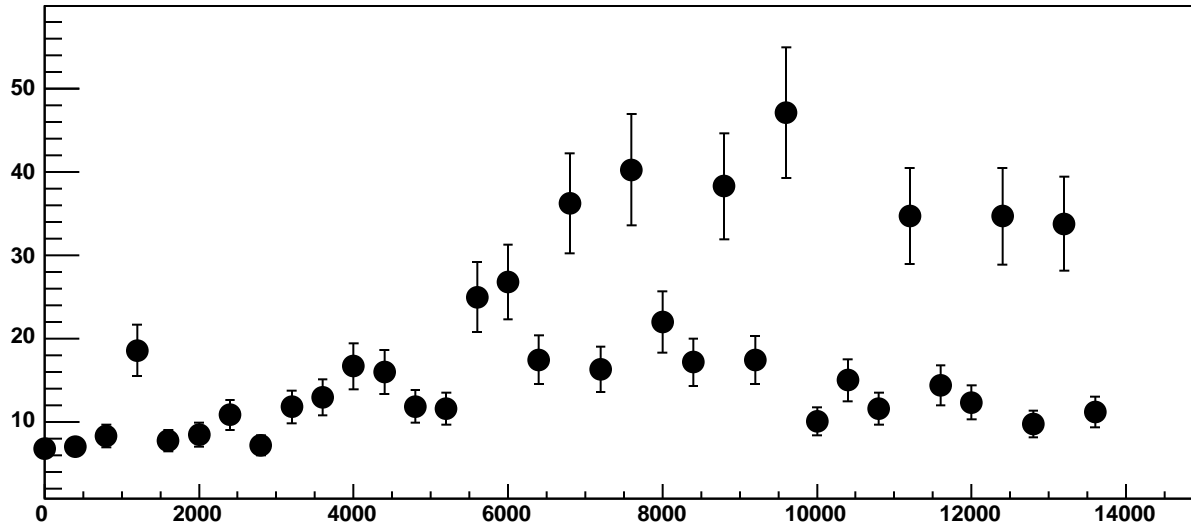


Chip 11, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

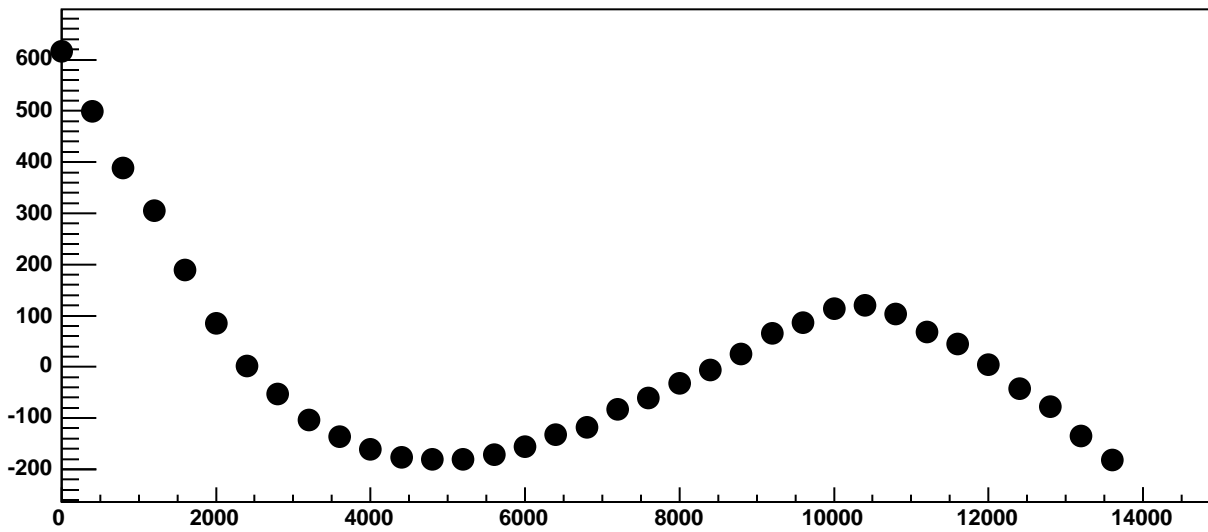
χ^2 / ndf 4.505e+04 / 23
p0 -1108 ± 1.127
p1 0.3061 ± 0.0001978



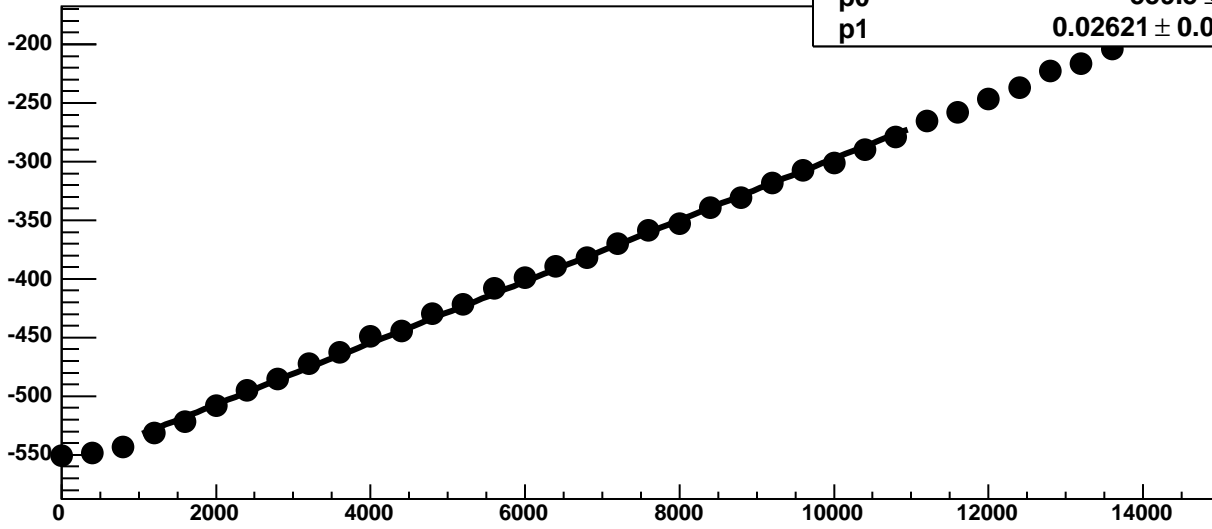
Chip 11, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

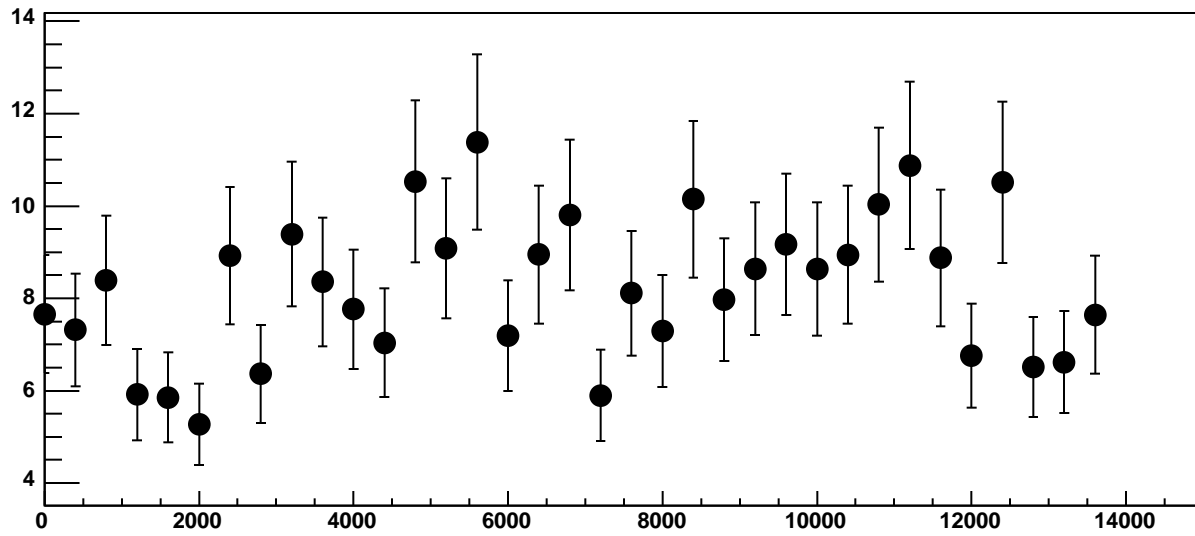


Chip 11, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

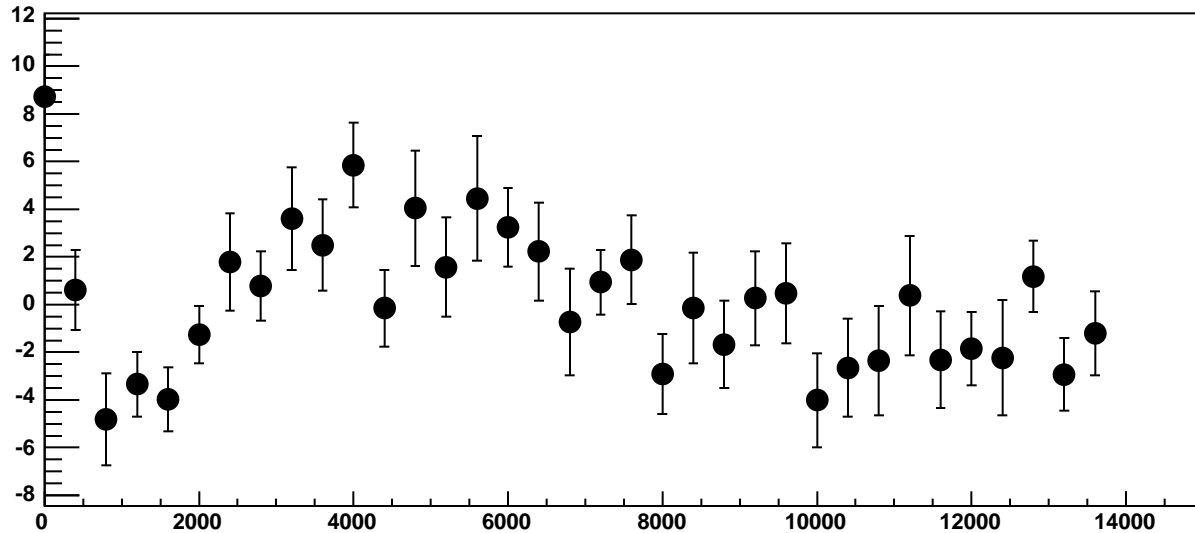


χ^2 / ndf 55.98 / 23
p0 -559.5 ± 0.7331
p1 0.02621 ± 0.0001198

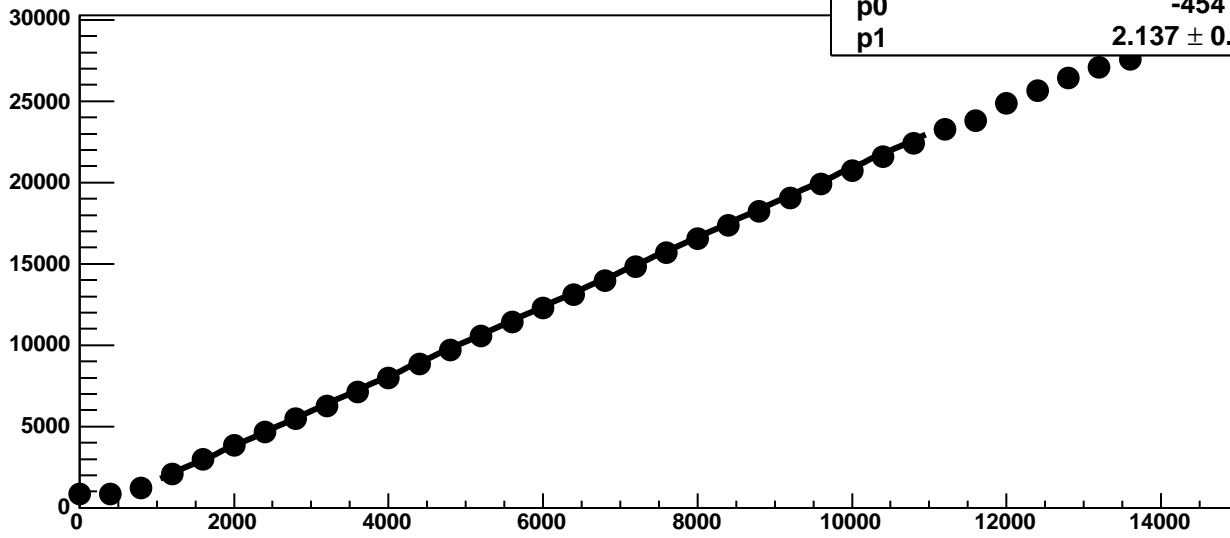
Chip 11, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

299.2 / 23

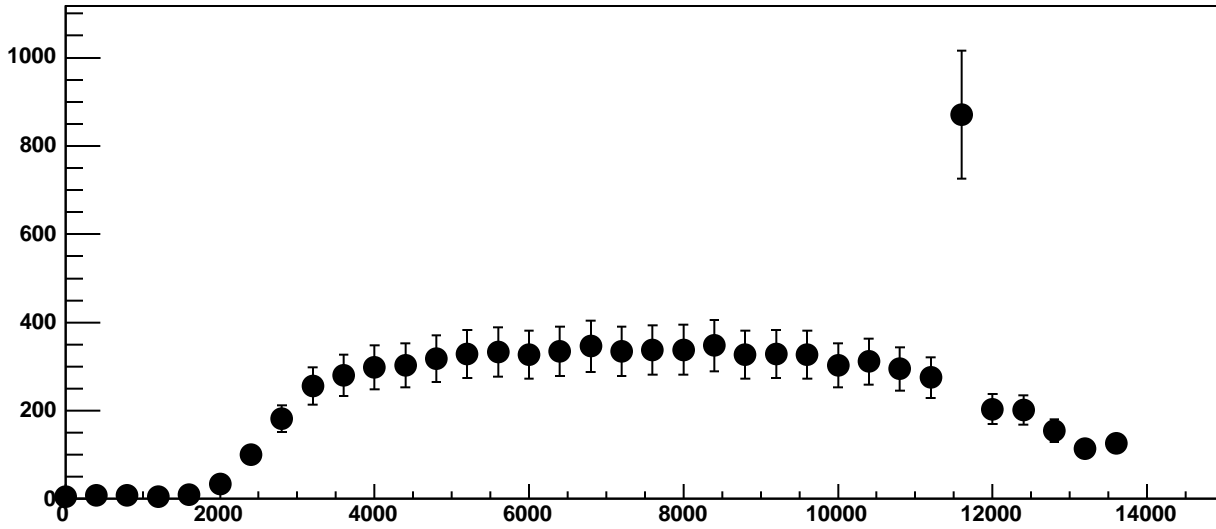
p0

-454 ± 3.392

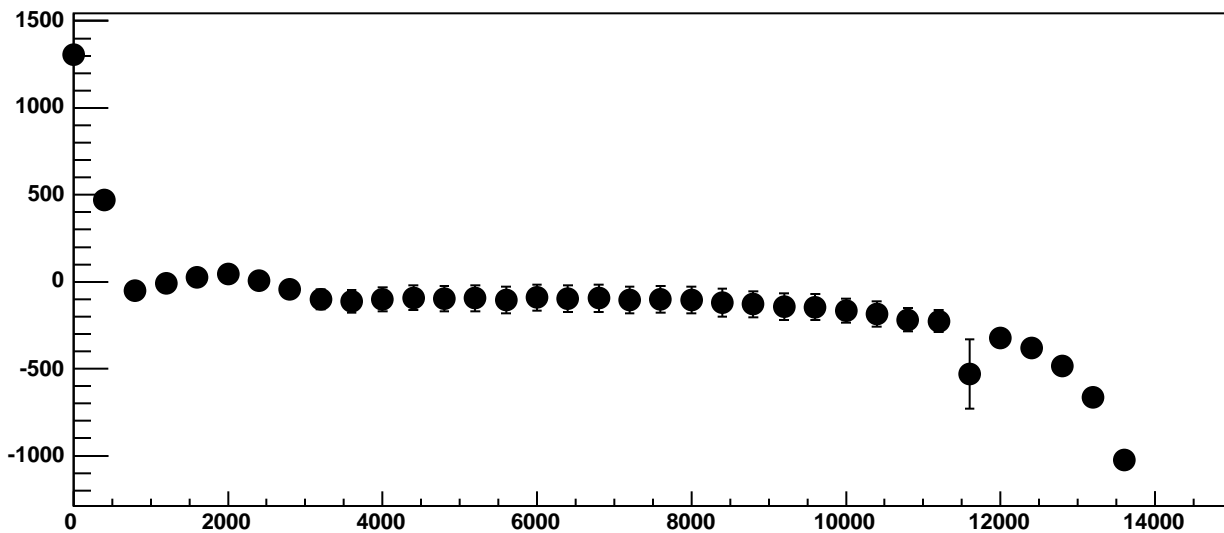
p1

2.137 ± 0.002384

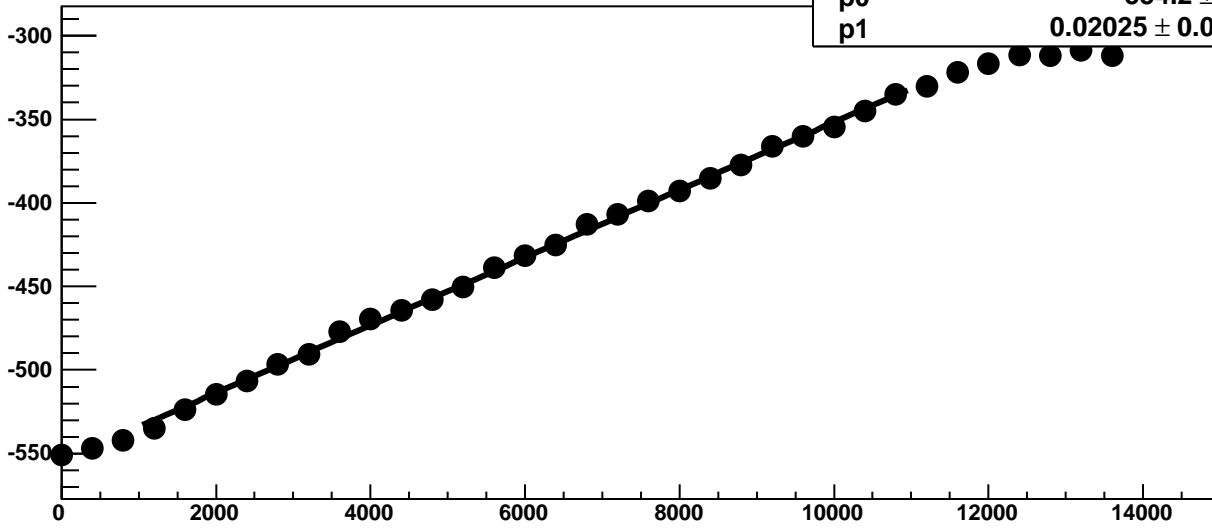
Chip 11, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC

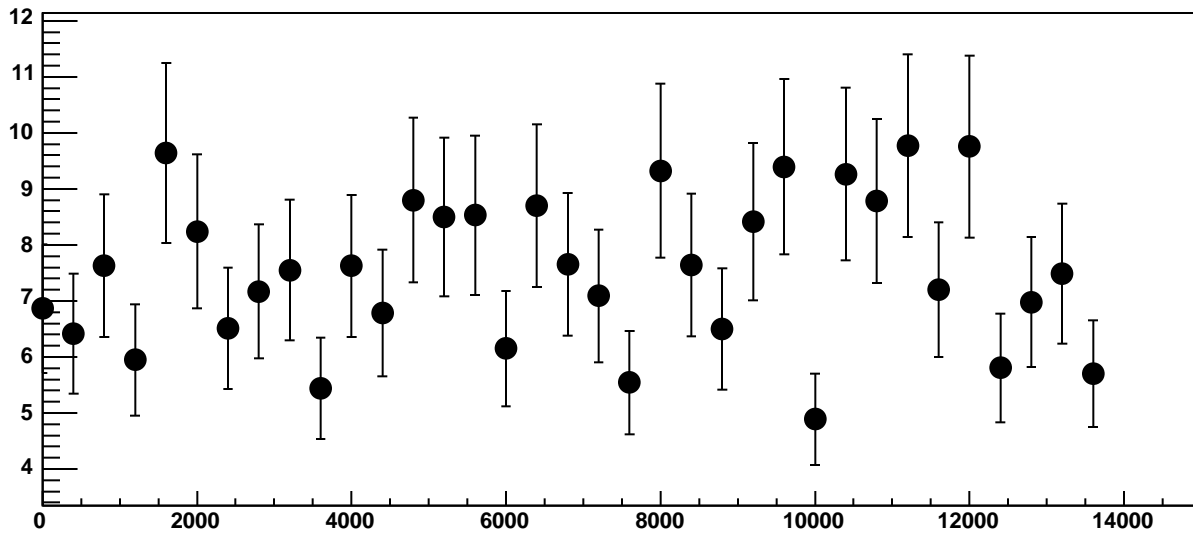


Chip 11, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

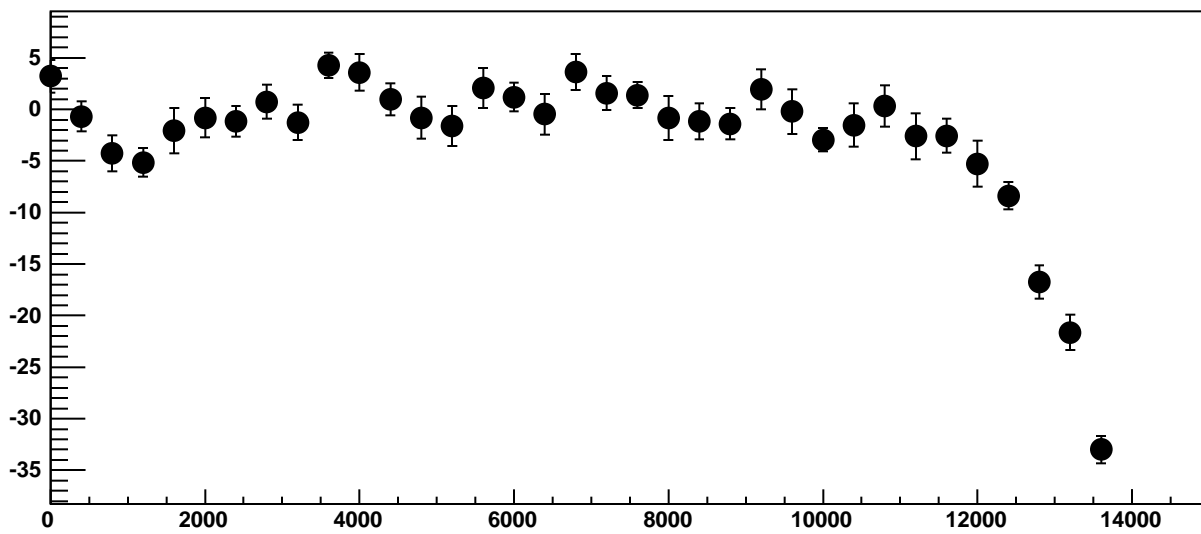


χ^2 / ndf 51.98 / 23
p0 -554.2 ± 0.7557
p1 0.02025 ± 0.0001144

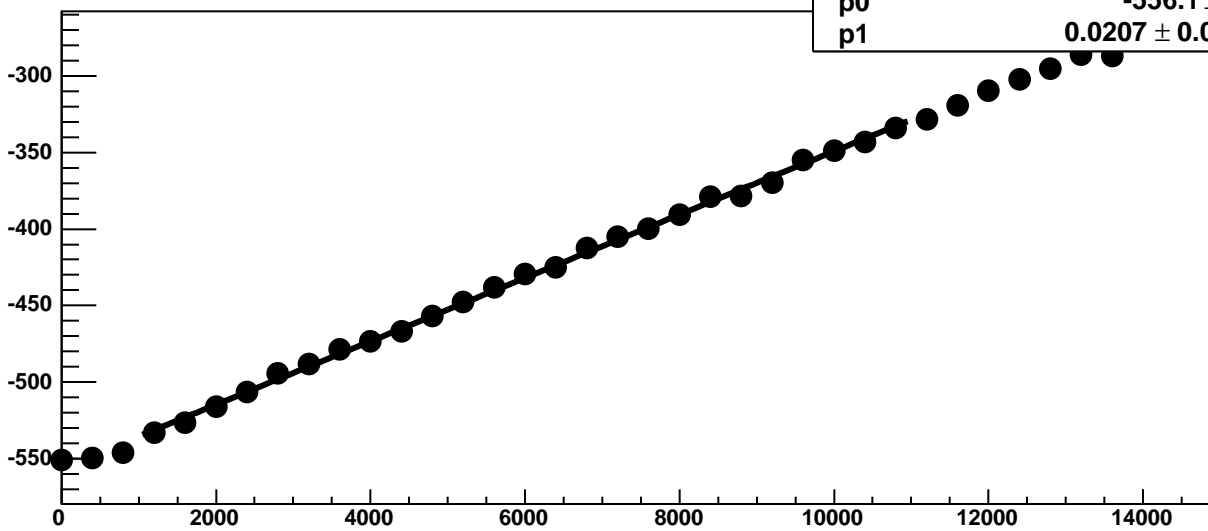
Chip 11, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

56.74 / 23

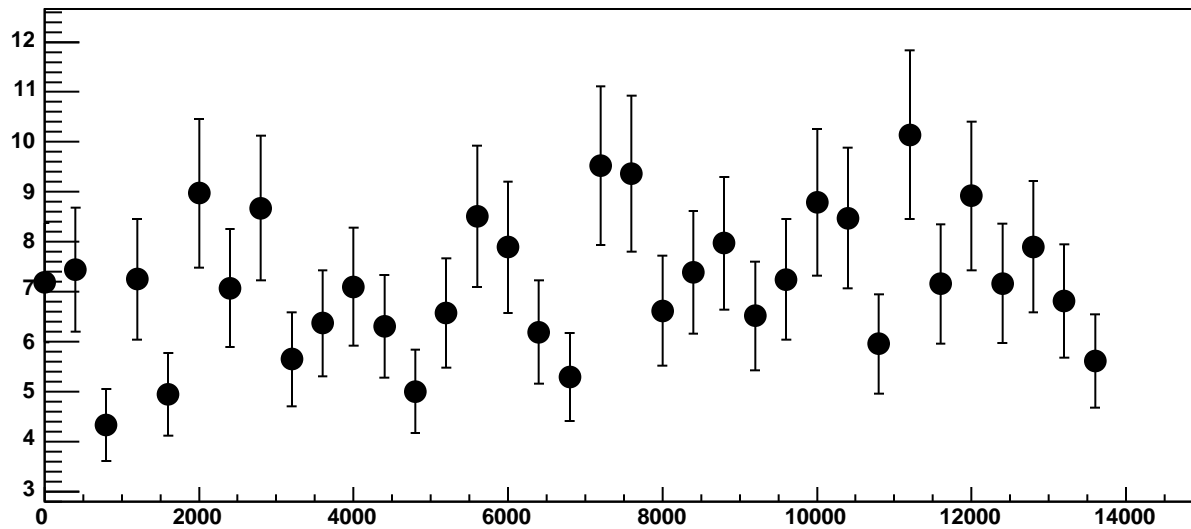
p0

-556.1 ± 0.7011

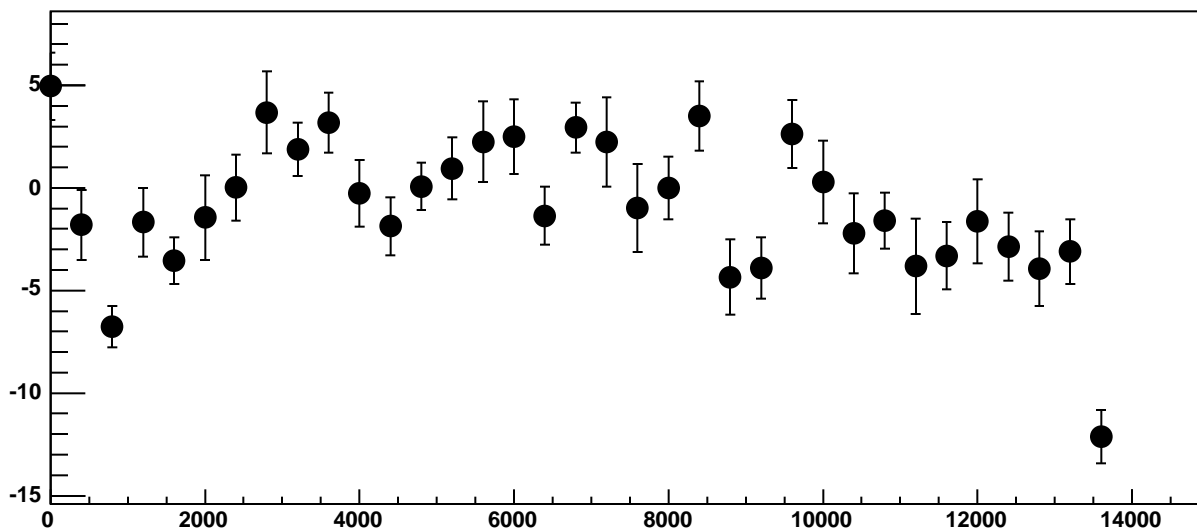
p1

0.0207 ± 0.0001093

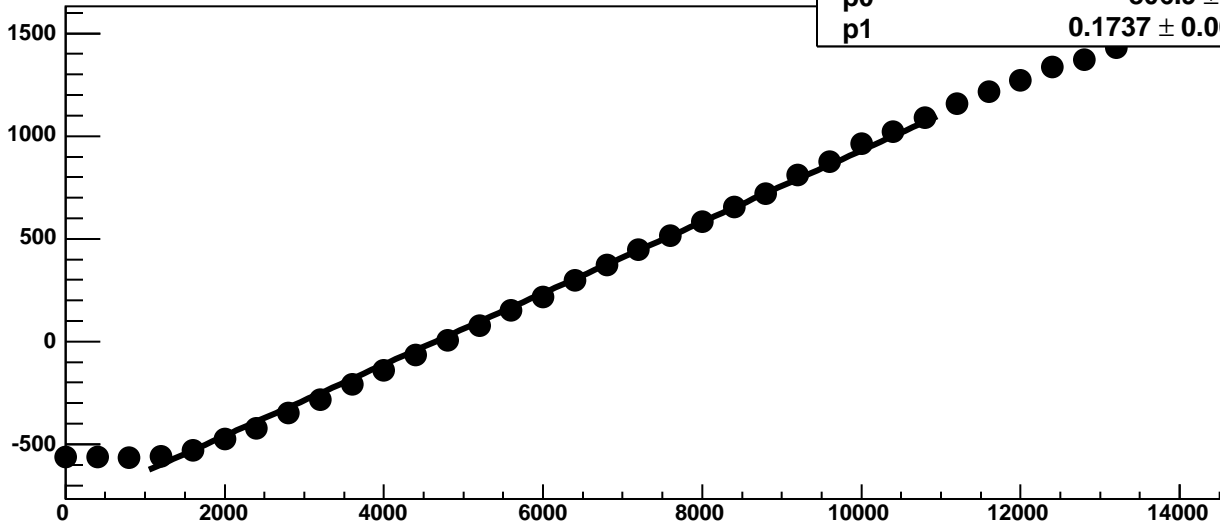
Chip 11, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



Chip 11, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

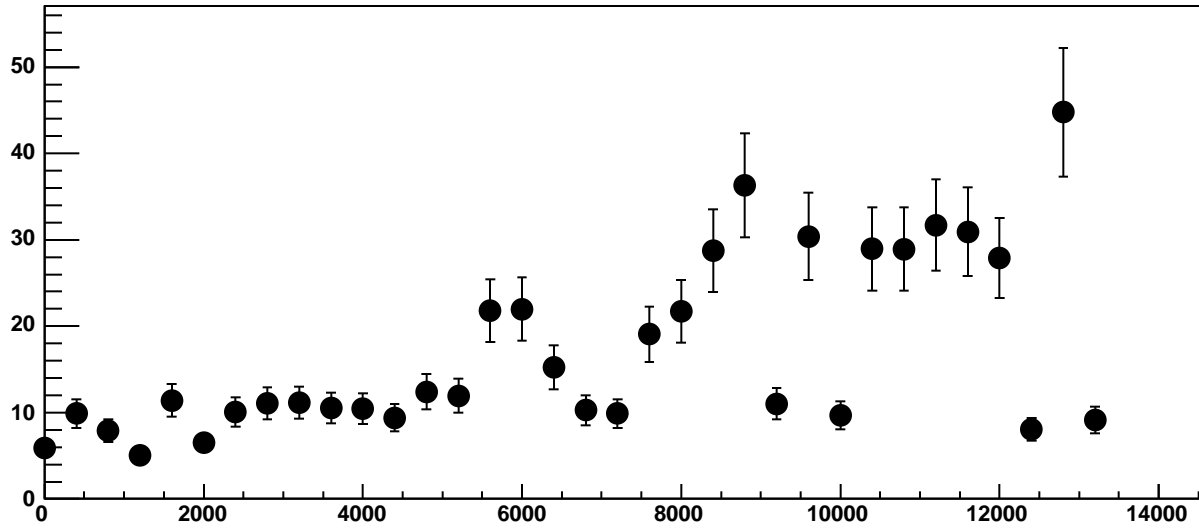


Chip 11, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC

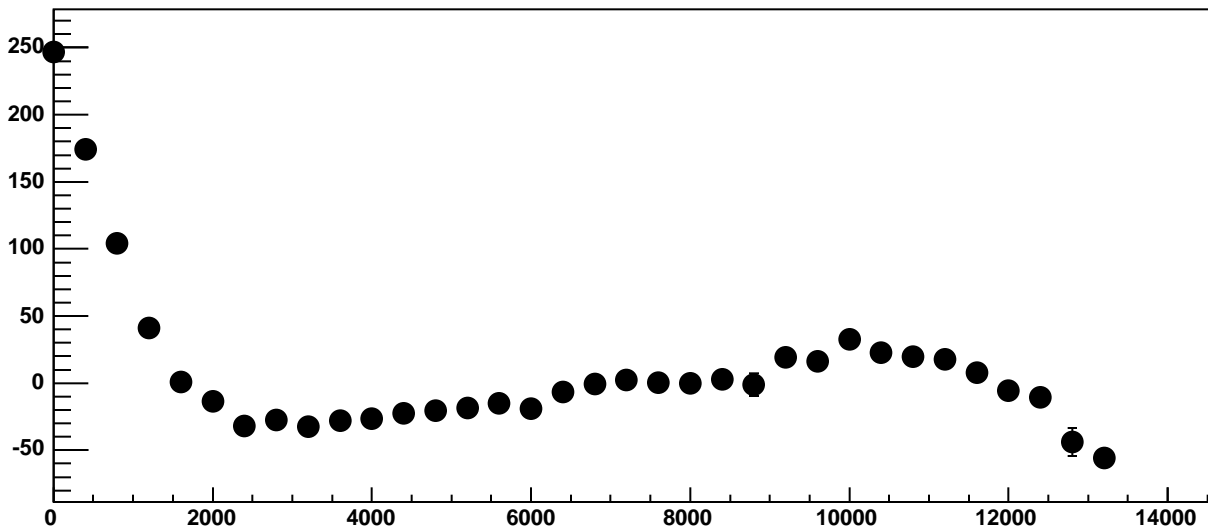


χ^2 / ndf 2621 / 23
p0 -806.9 ± 0.9252
p1 0.1737 ± 0.0001829

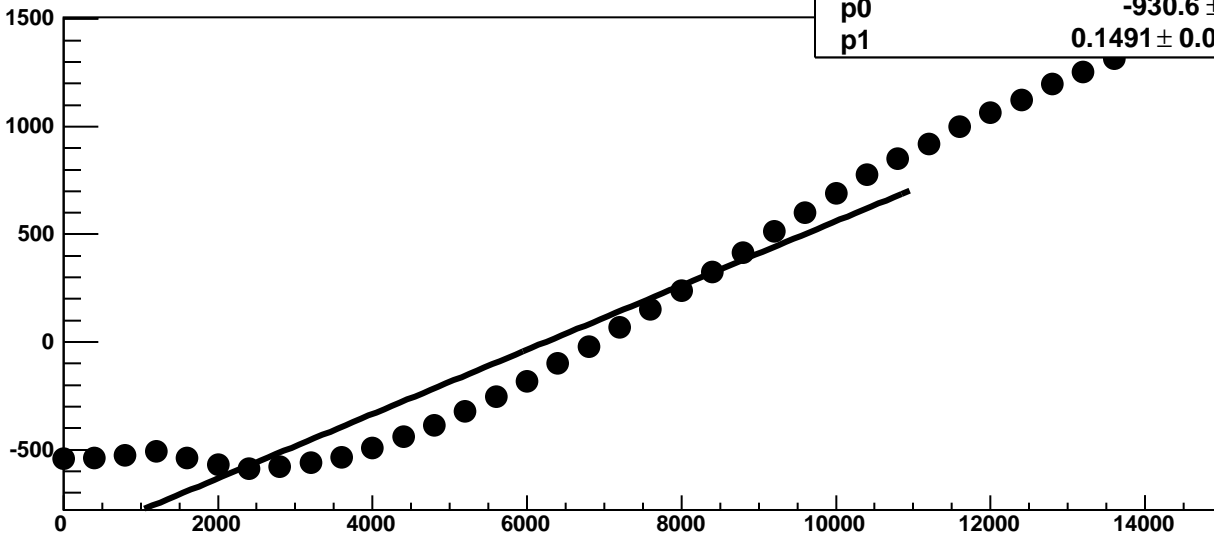
Chip 11, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



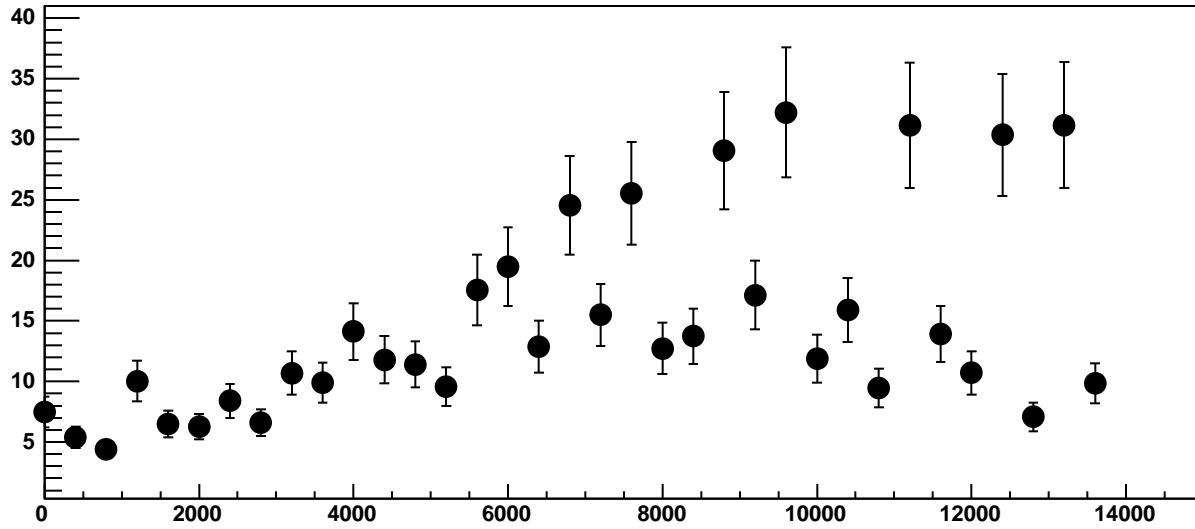
Chip 11, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC



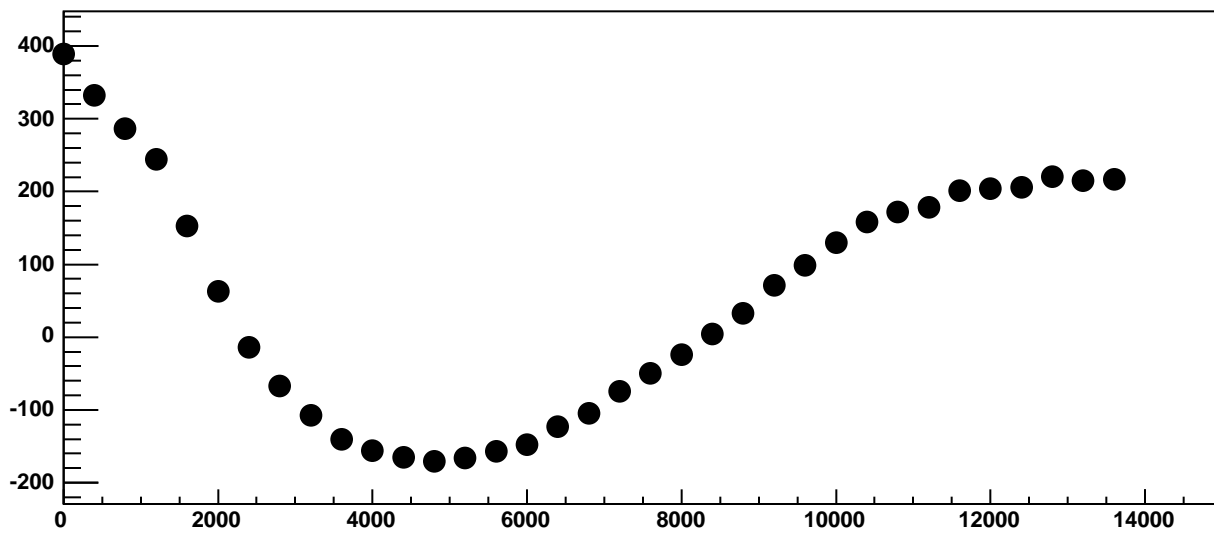
Chip 11, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



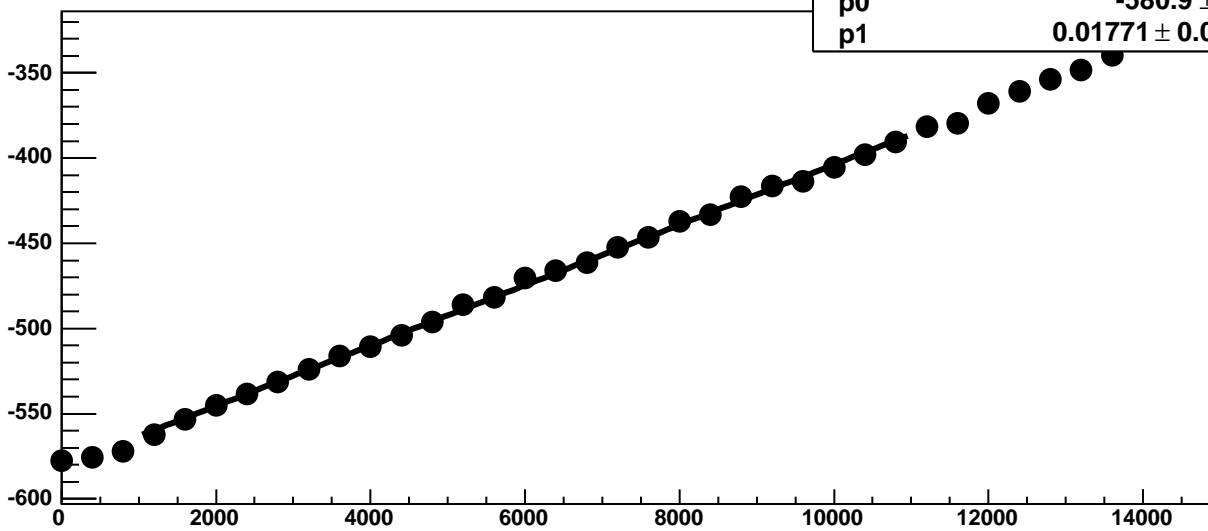
Chip 11, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

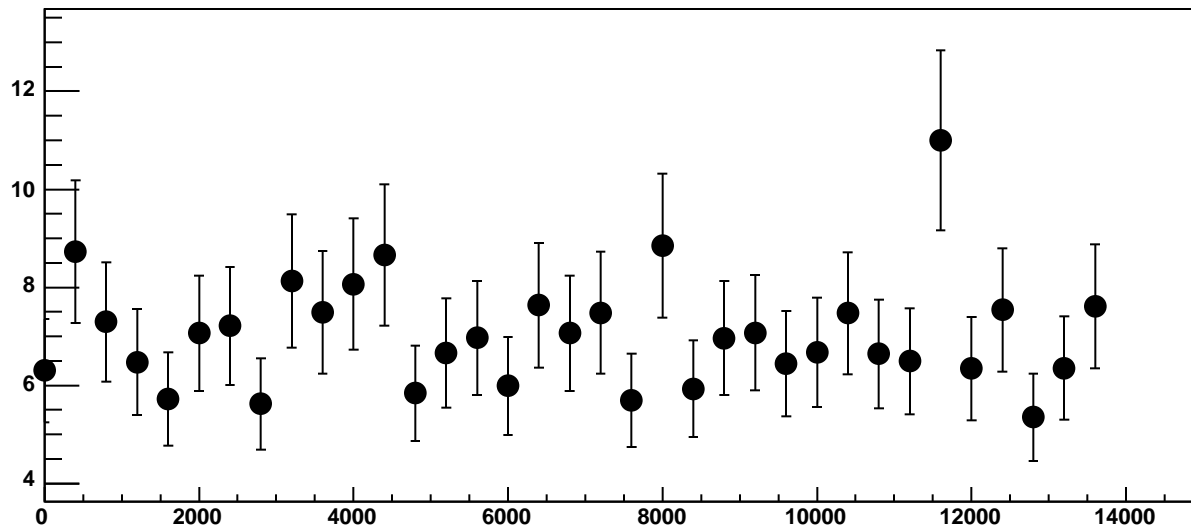


Chip 11, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

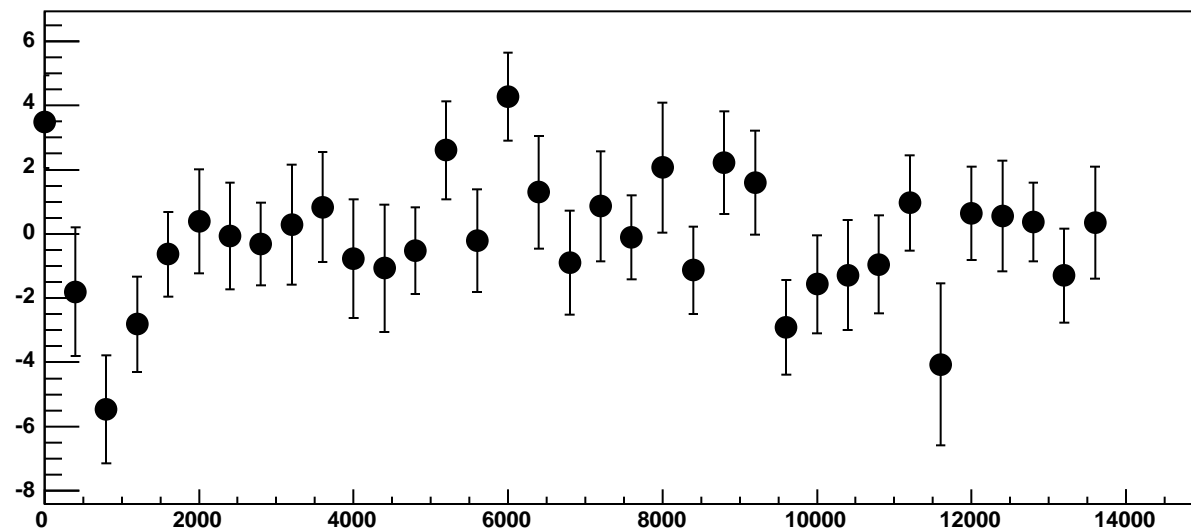


χ^2 / ndf 29 / 23
p0 -580.9 ± 0.7058
p1 0.01771 ± 0.0001064

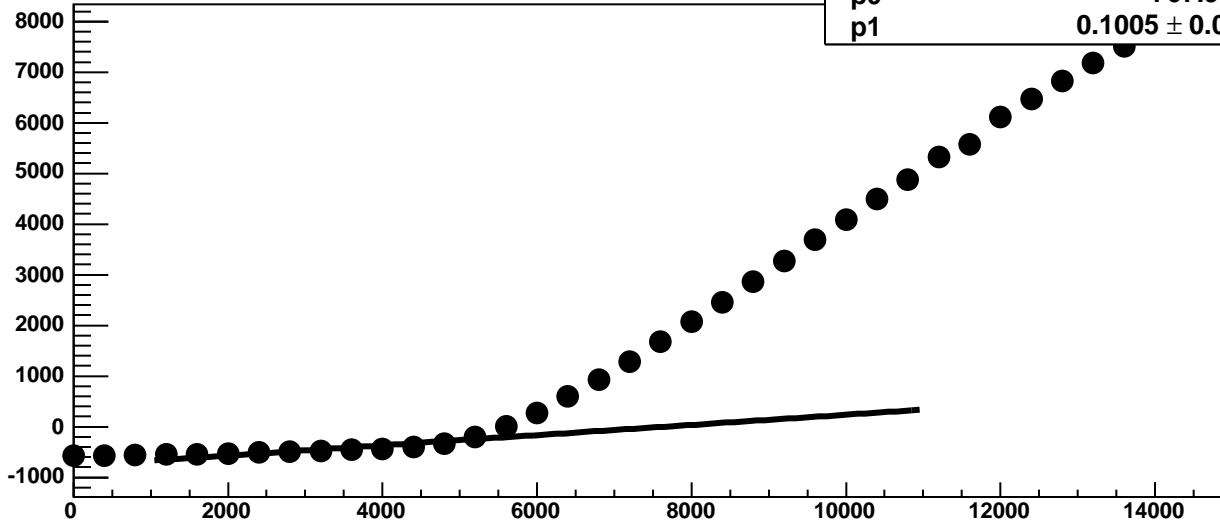
Chip 11, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1.129e+05 / 23

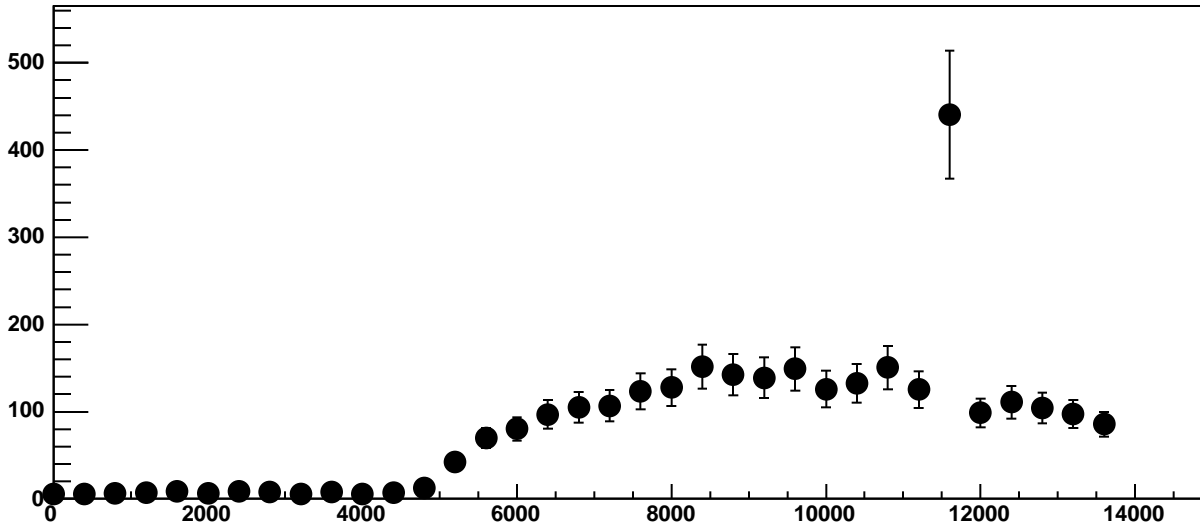
p0

-767.9 ± 1.516

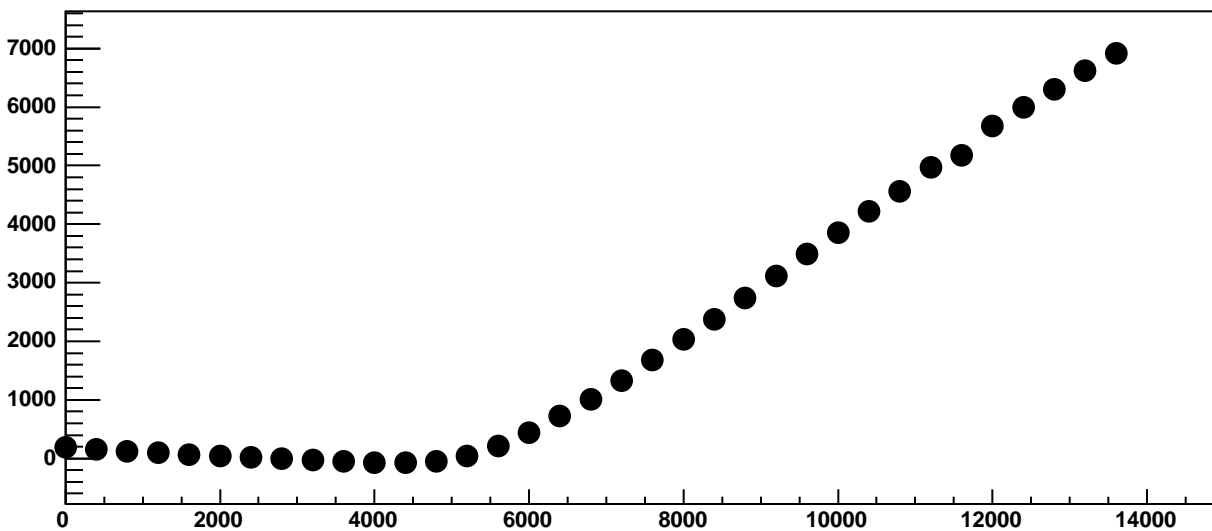
p1

0.1005 ± 0.0004678

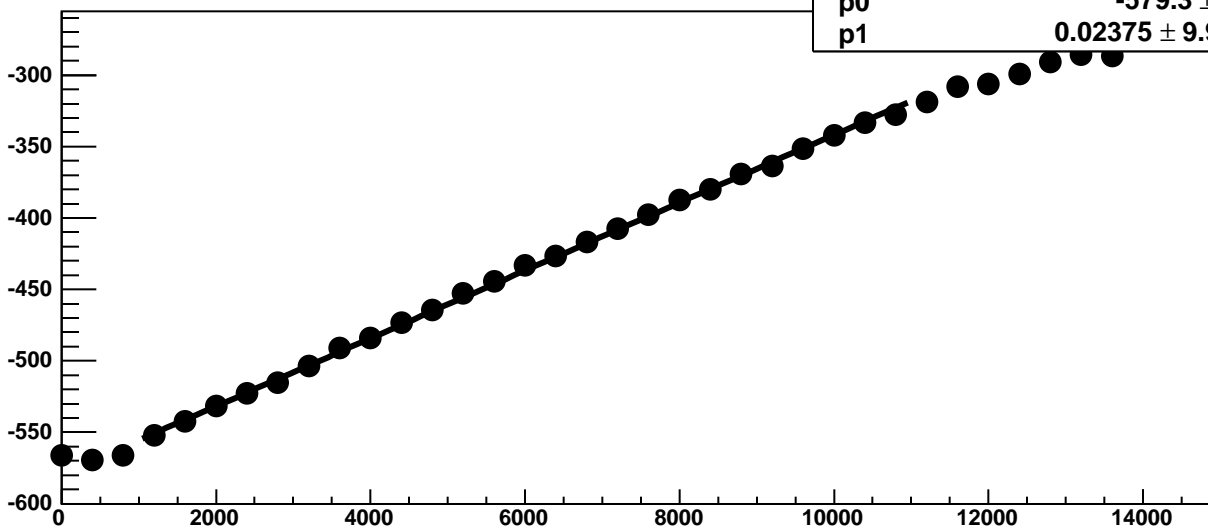
Chip 11, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC

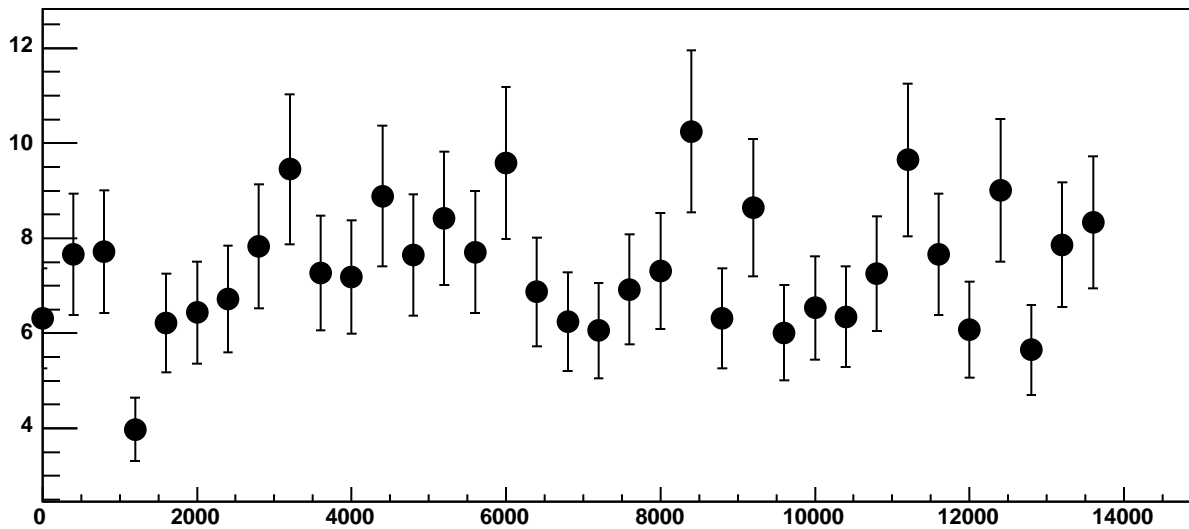


Chip 11, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

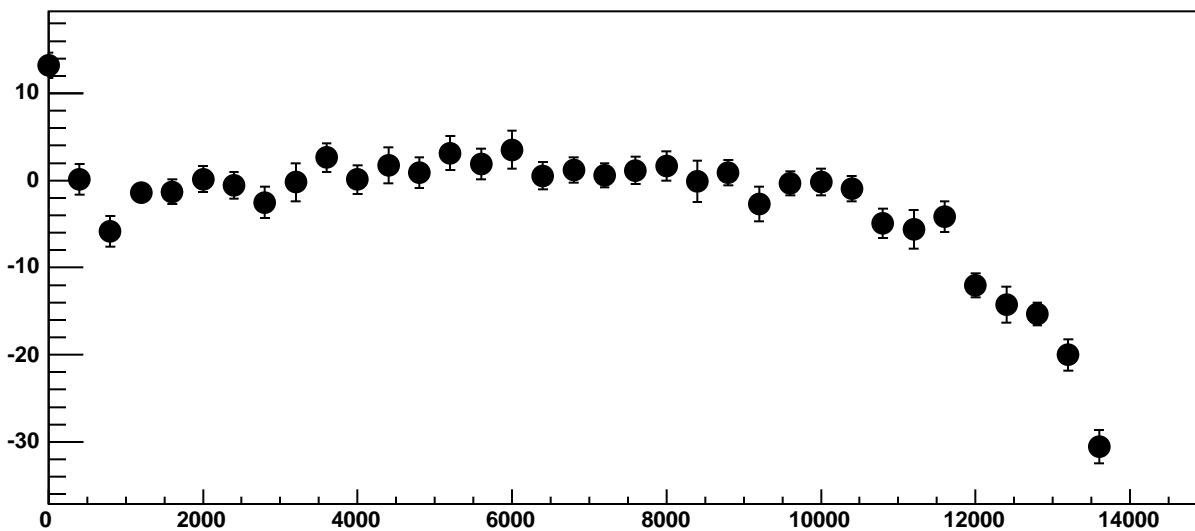


χ^2 / ndf 28.96 / 23
p0 -579.3 ± 0.6488
p1 $0.02375 \pm 9.945e-05$

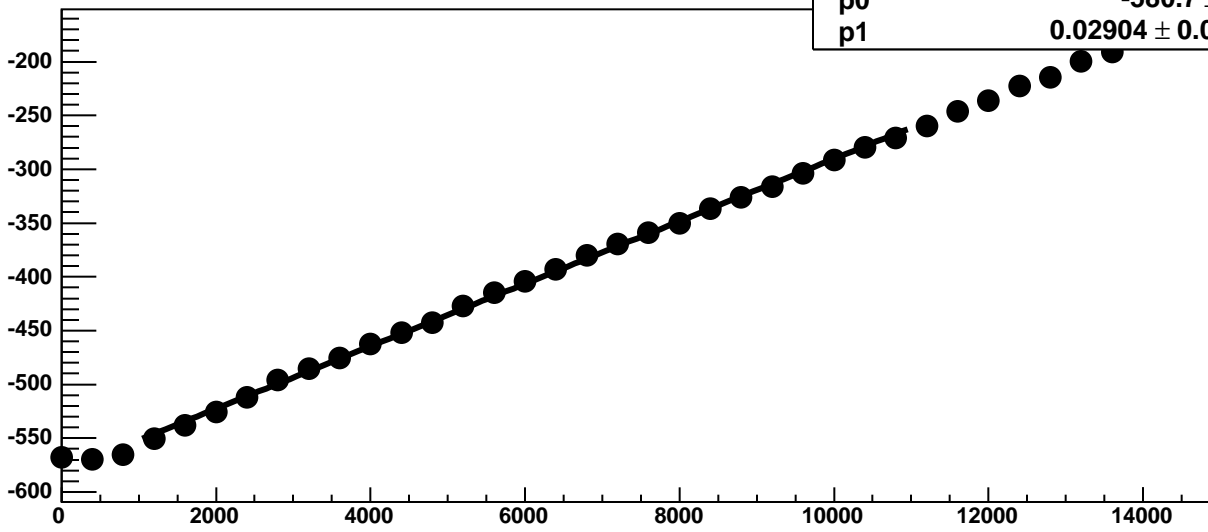
Chip 11, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



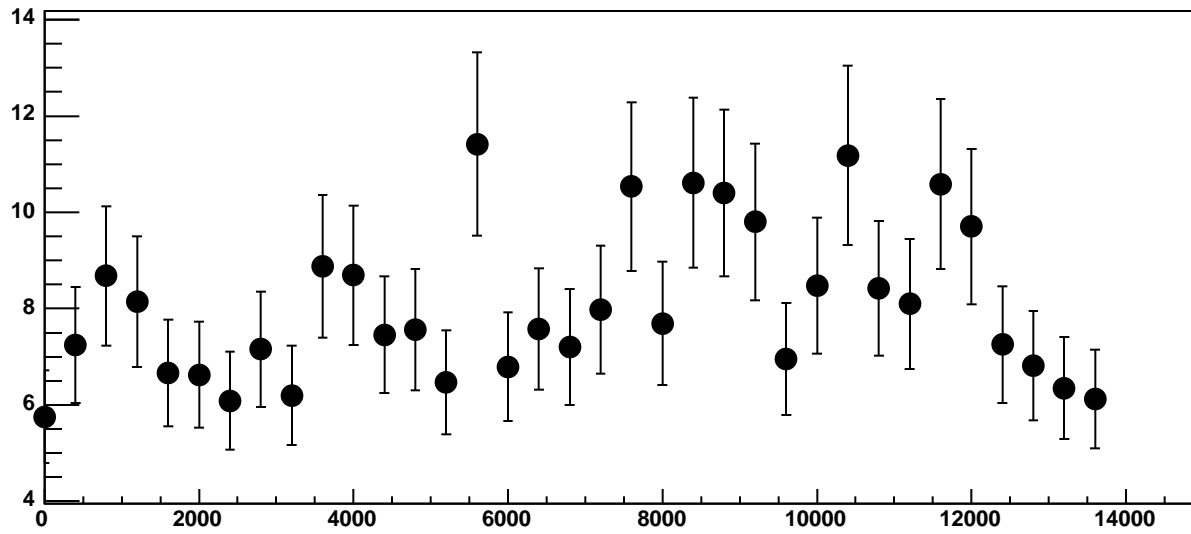
Chip 11, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



Chip 11, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



Chip 11, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

