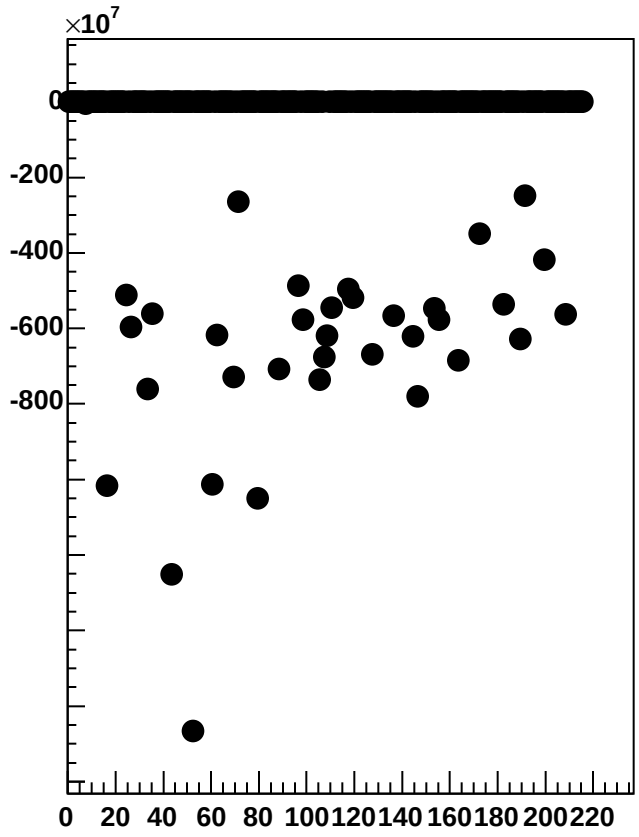
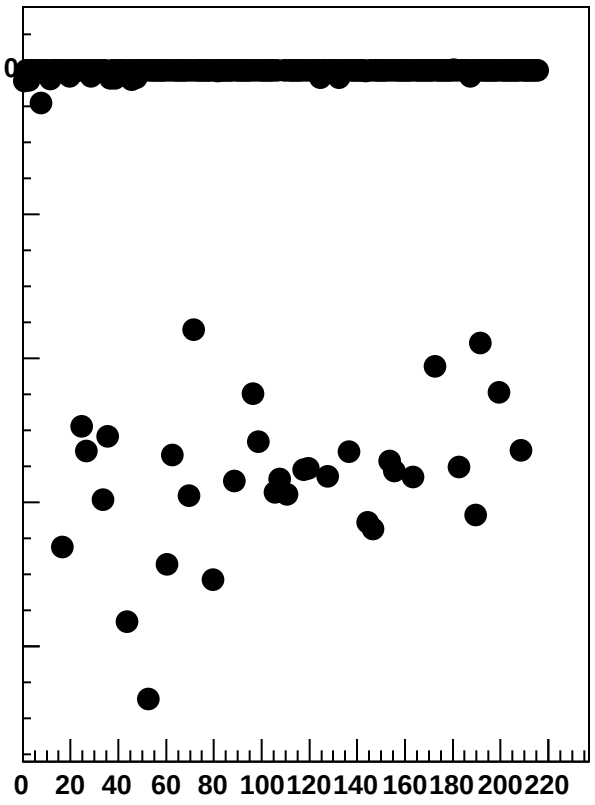
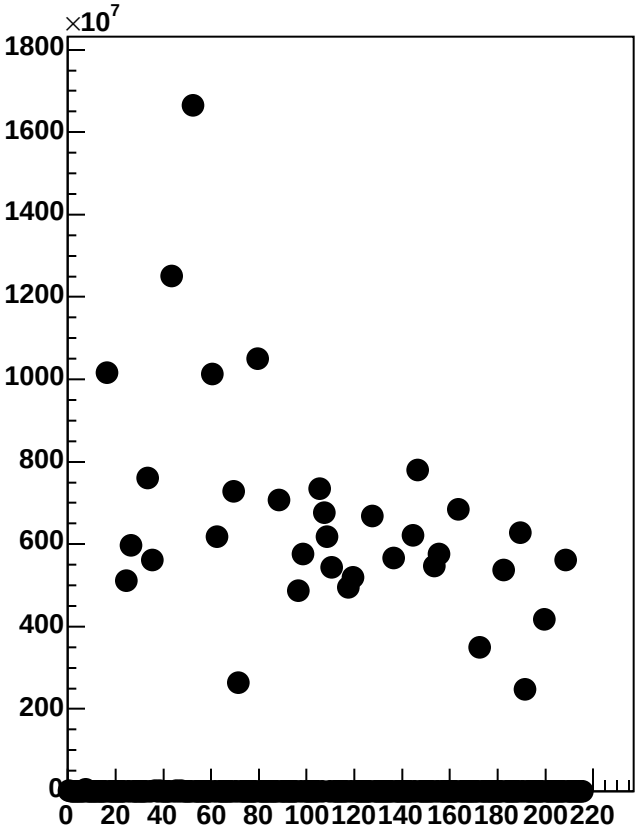
Channel Enabled, DAC=1600, Fit Baseline vs 18*Chip+Chan

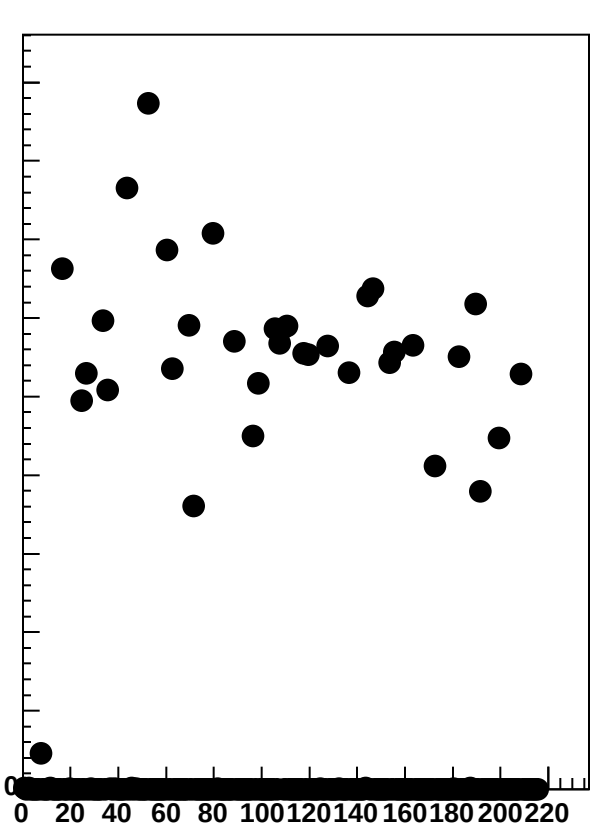


Channel Enabled, DAC=1600, Fit Start Time vs 18*Chip+Chan

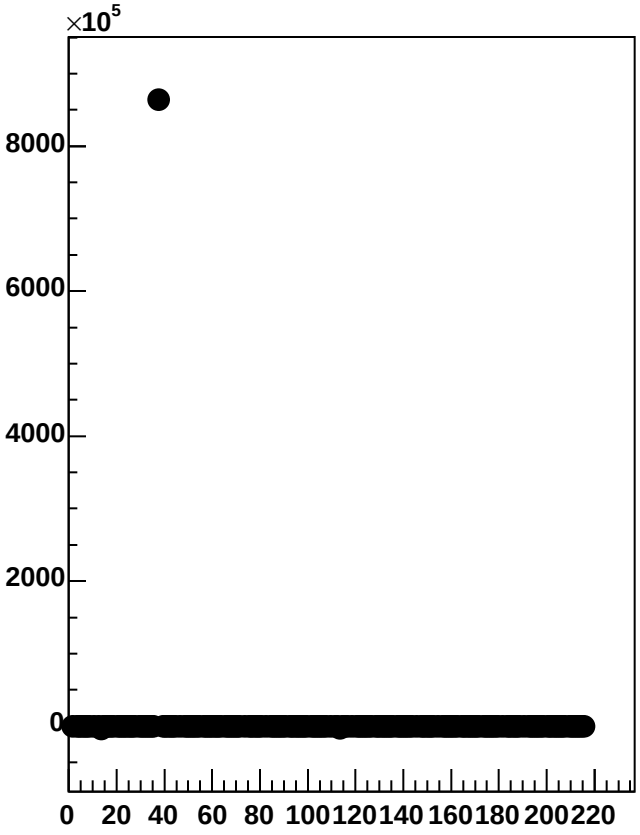


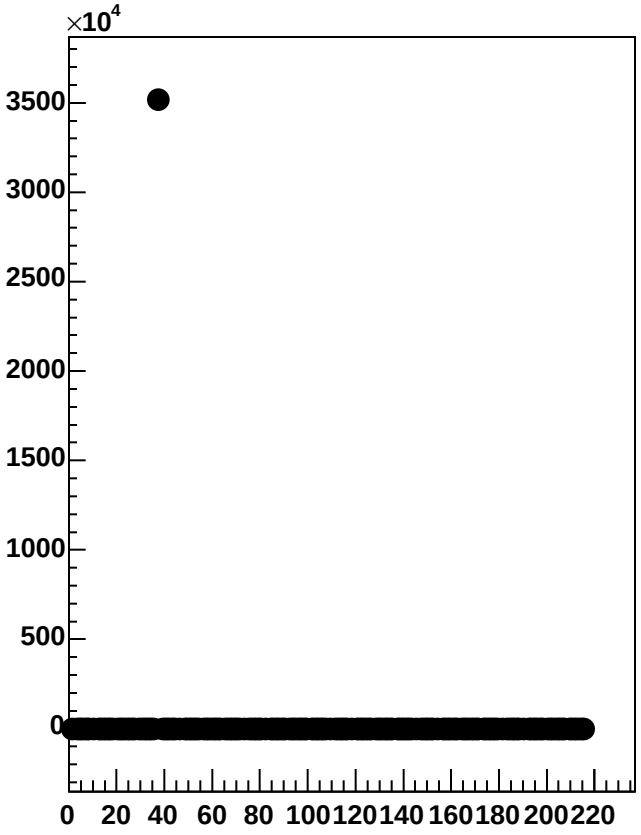
Channel Enabled, DAC=1600, Fit Normalisation vs 18*Chip+Chan



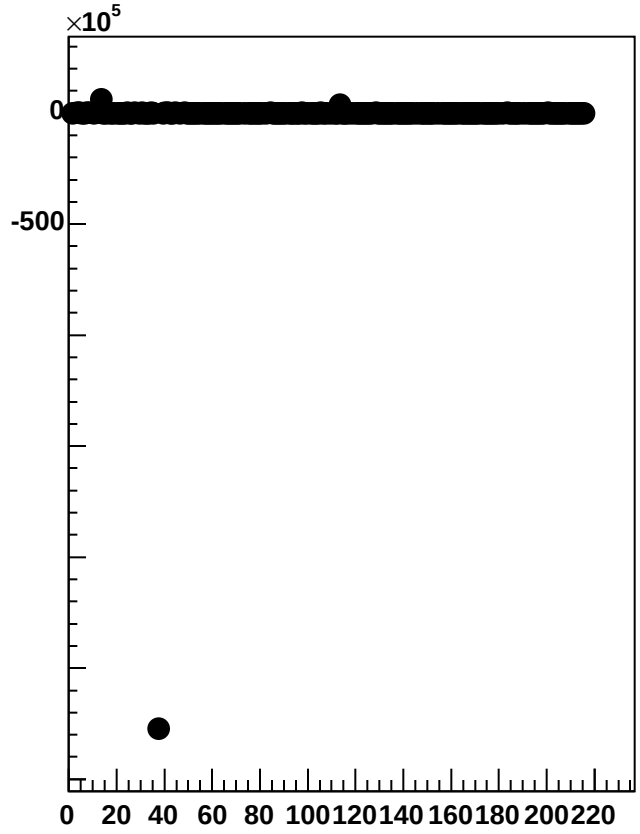


Disabled 0, DAC=1600, Fit Baseline vs 18*Chip+Chan

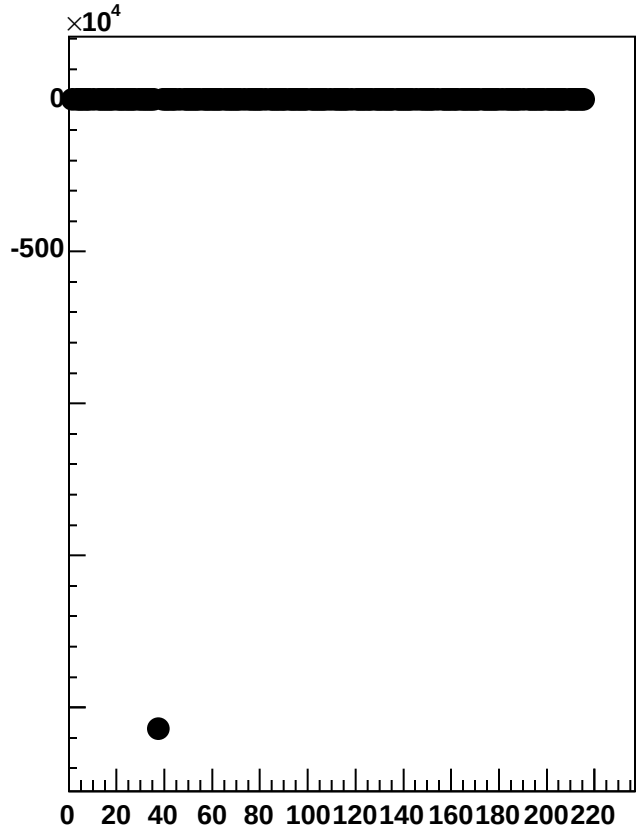




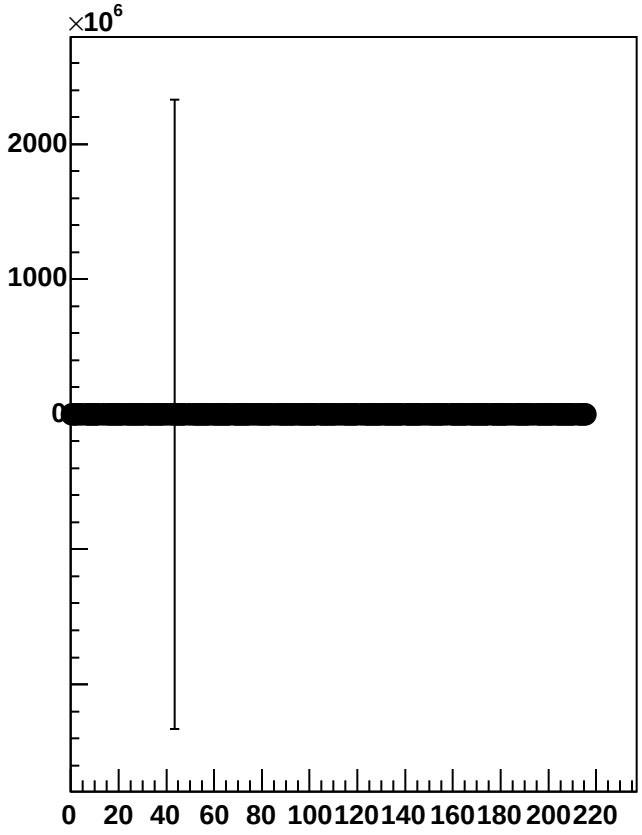
Disabled 0, DAC=1600, Fit Normalisation vs 18*Chip+Chan

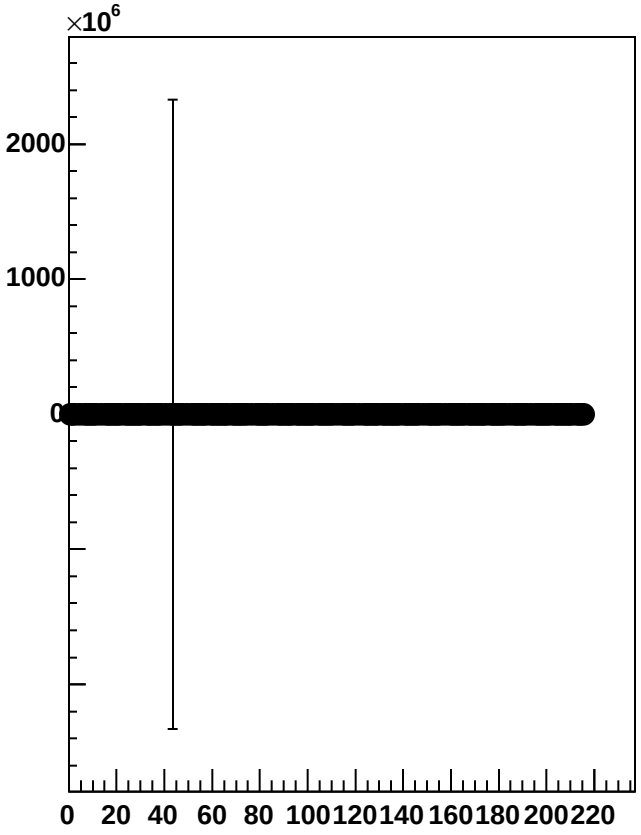


Disabled 0, DAC=1600, Fit Shaping Time vs 18*Chip+Chan

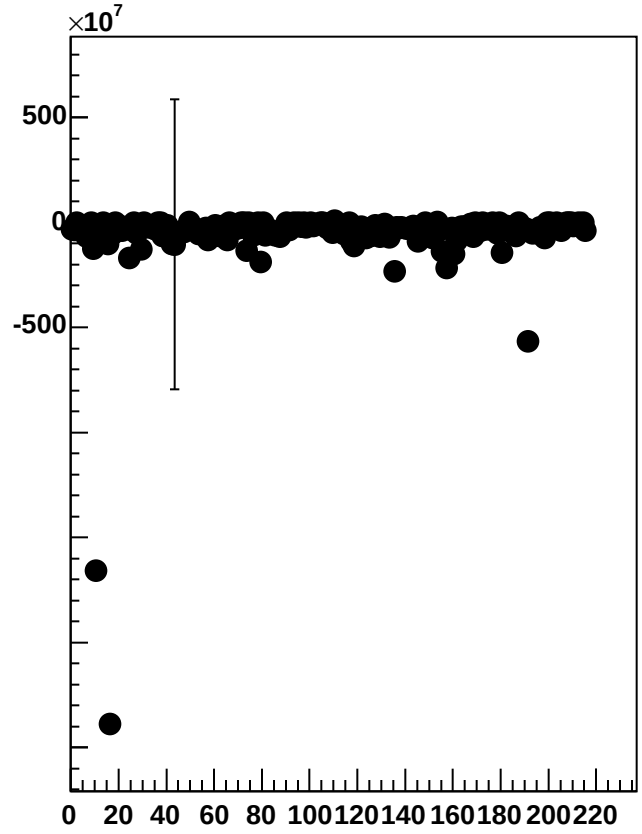


Disabled 1, DAC=1600, Fit Baseline vs 18*Chip+Chan

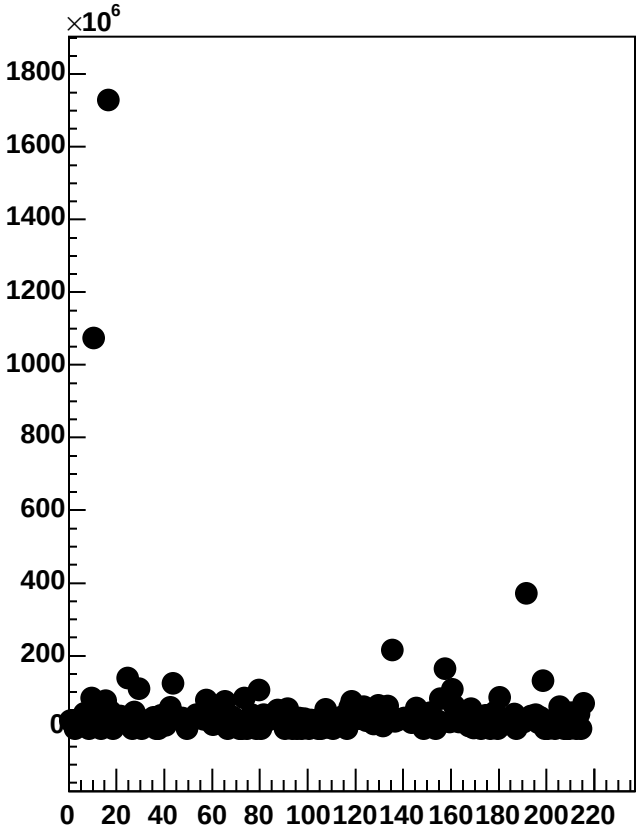


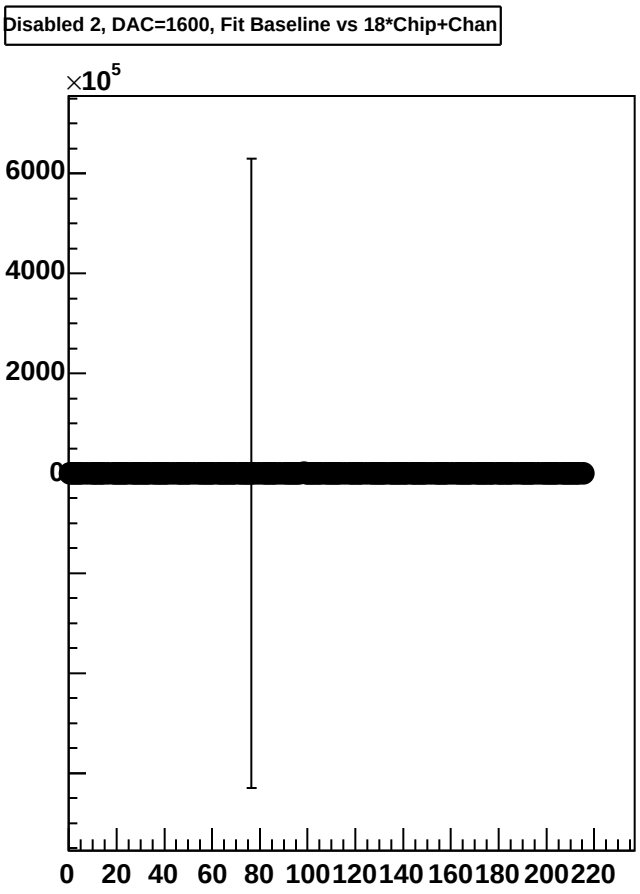


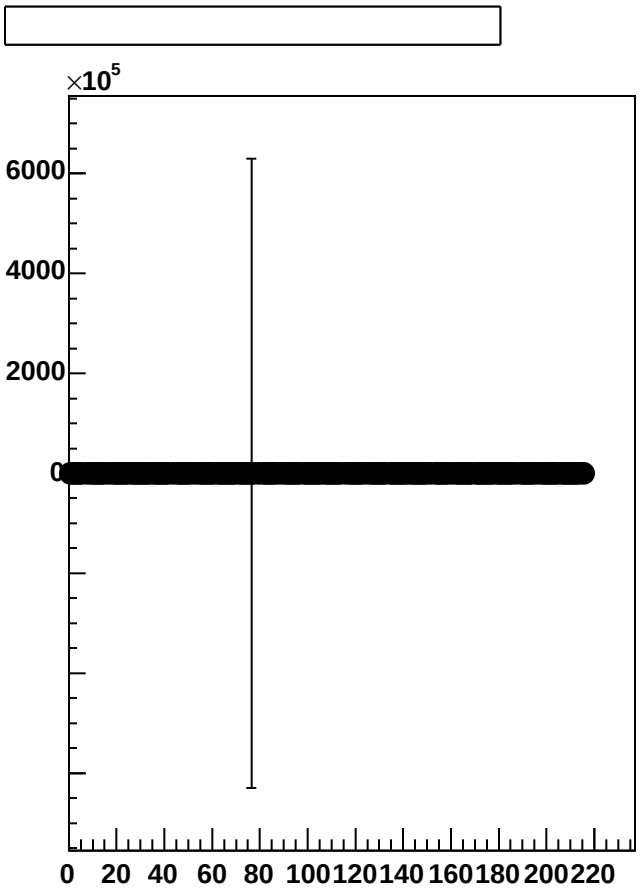
Disabled 1, DAC=1600, Fit Normalisation vs 18*Chip+Chan

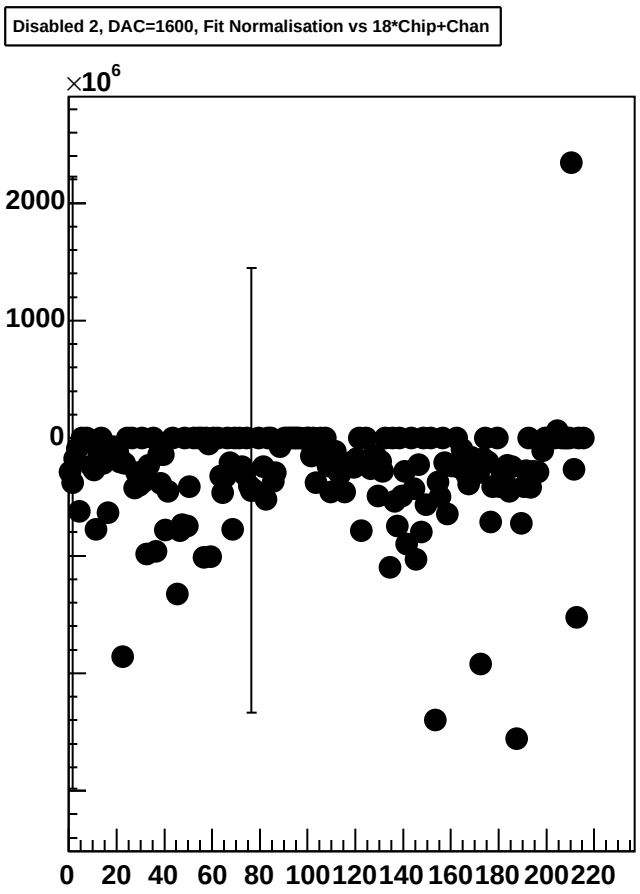


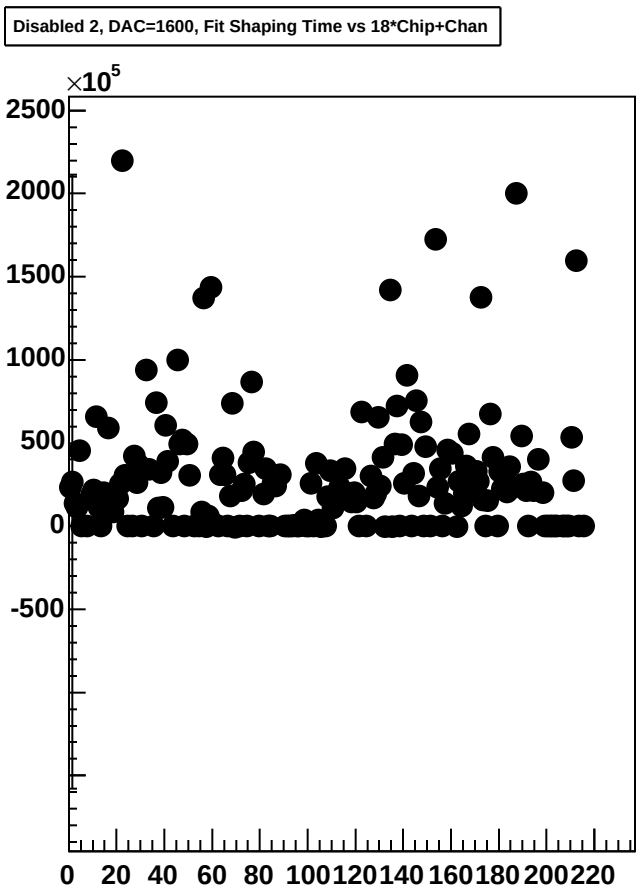
Disabled 1, DAC=1600, Fit Shaping Time vs 18*Chip+Chan

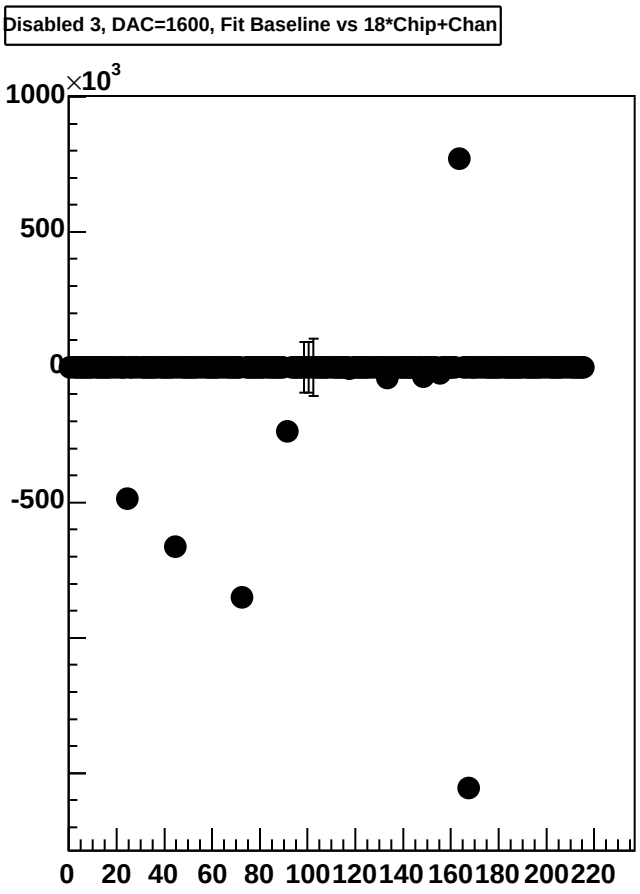


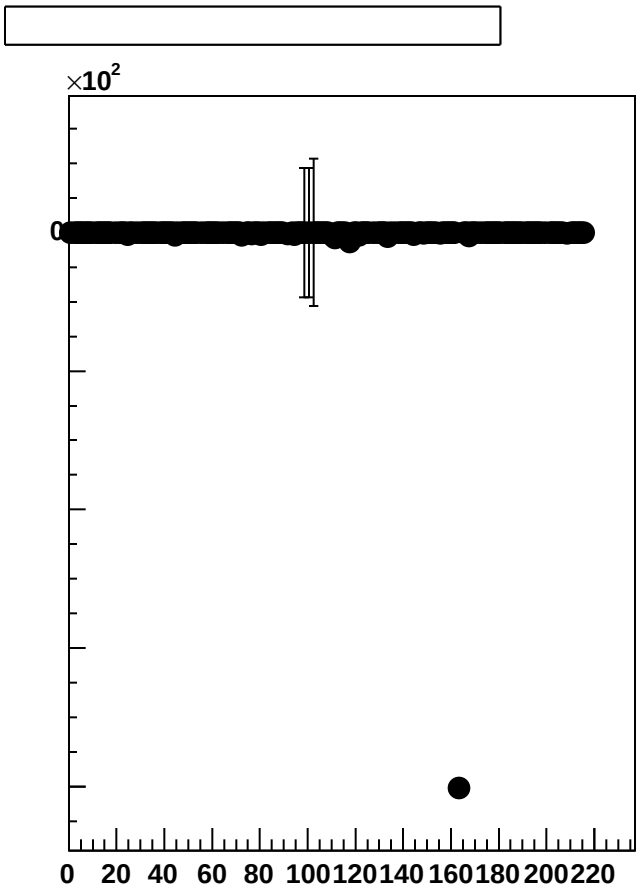

Disabled 2, DAC=1600, Fit Baseline vs 18*Chip+Chan
×10^5
6000
4000
2000
0
0 20 40 60 80 100 120 140 160 180 200 220

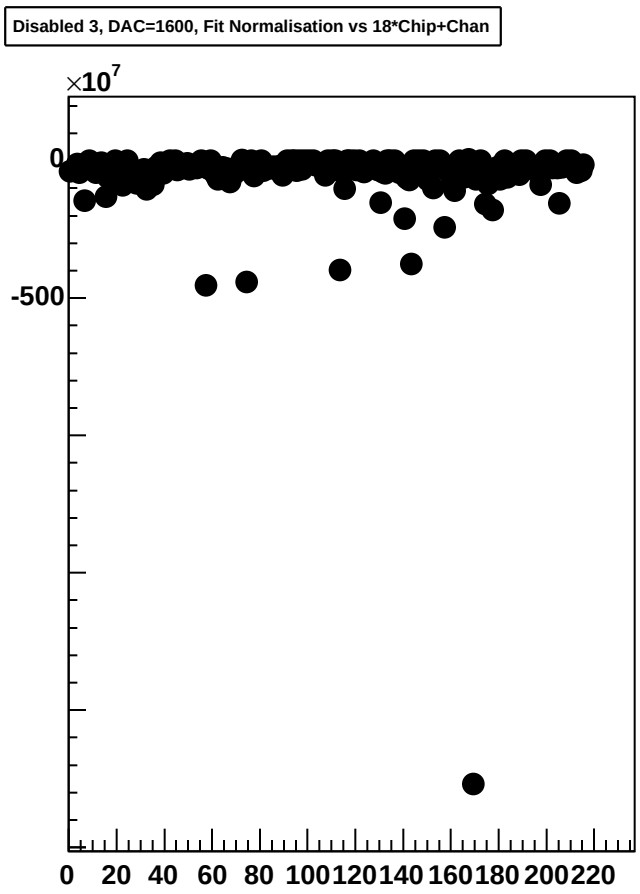

×10^5
6000
4000
2000
0
0 20 40 60 80 100 120 140 160 180 200 220

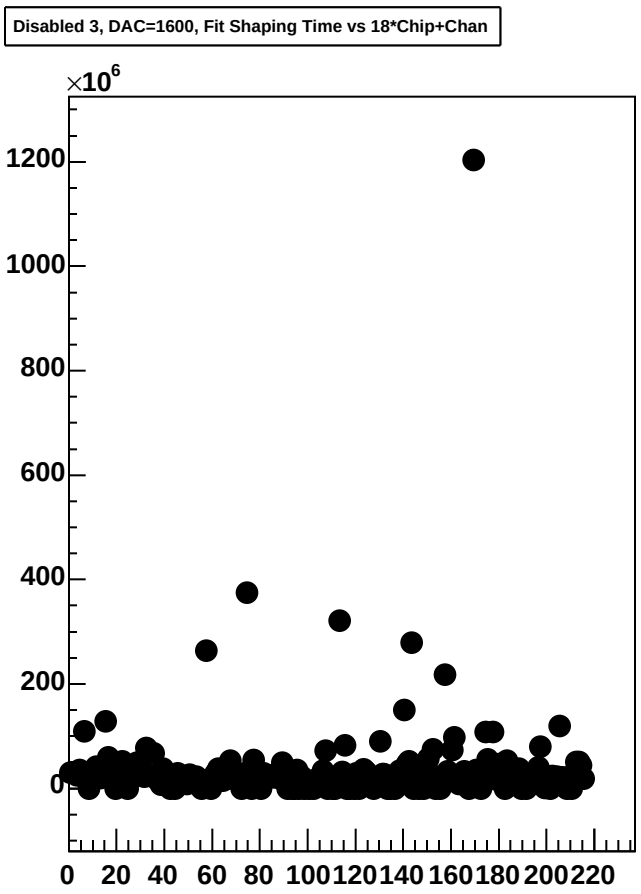

Disabled 2, DAC=1600, Fit Normalisation vs 18*Chip+Chan
×10^6
2000
1000
0
0 20 40 60 80 100 120 140 160 180 200 220

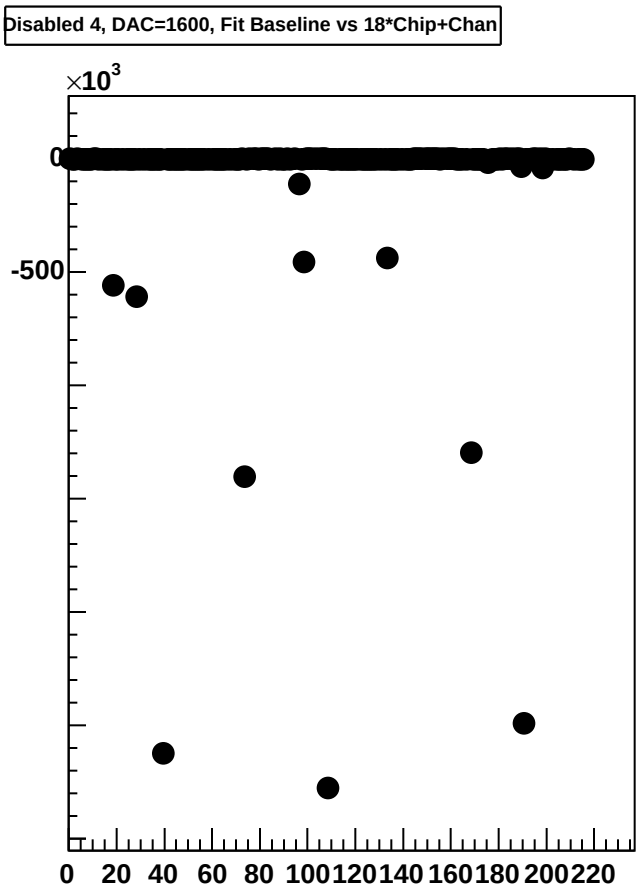

Disabled 2, DAC=1600, Fit Shaping Time vs 18*Chip+Chan
×10^5
2500
2000
1500
1000
500
0
-500
0 20 40 60 80 100 120 140 160 180 200 220

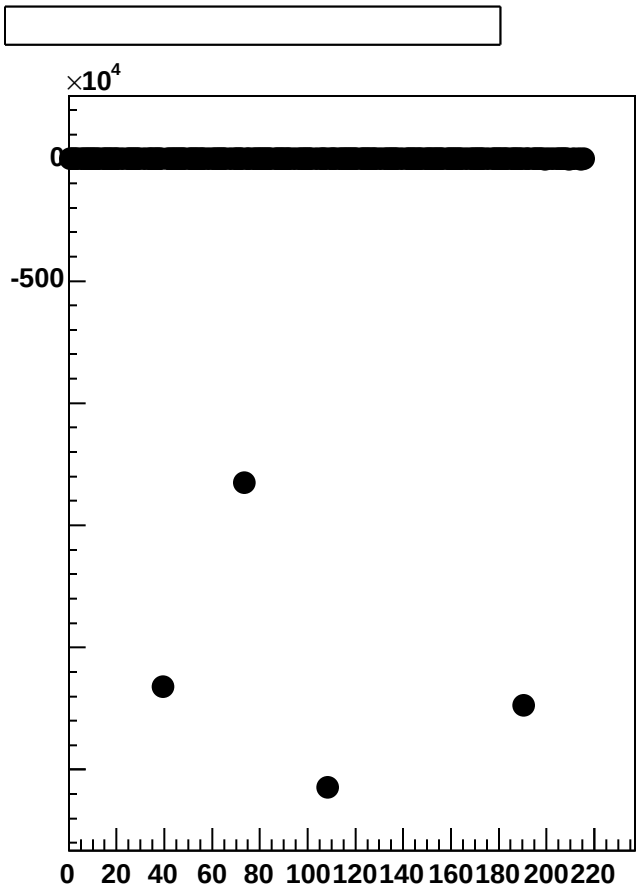

Disabled 3, DAC=1600, Fit Baseline vs 18*Chip+Chan
×10^3
1000
500
0
-500
0 20 40 60 80 100 120 140 160 180 200 220

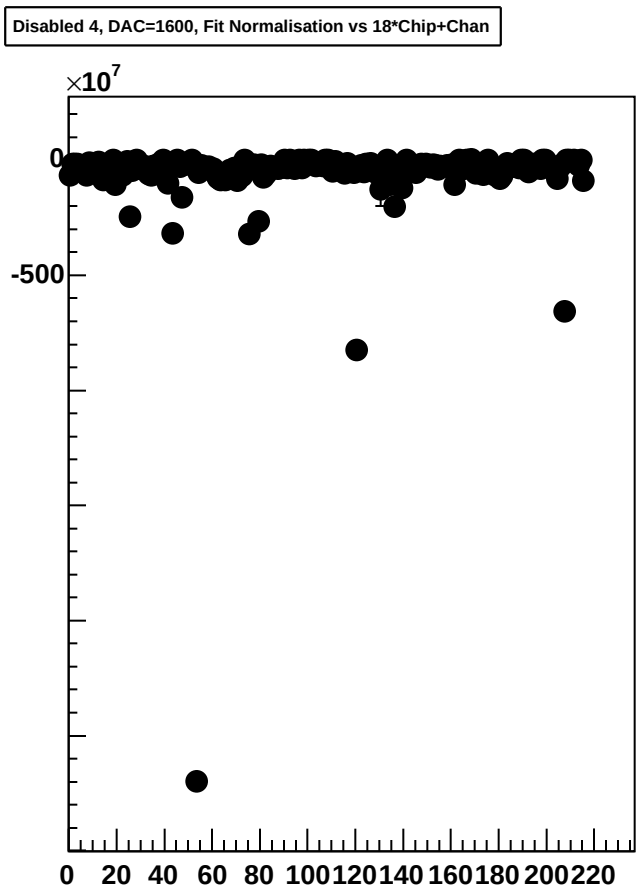

×10^2
0
0 20 40 60 80 100 120 140 160 180 200 220

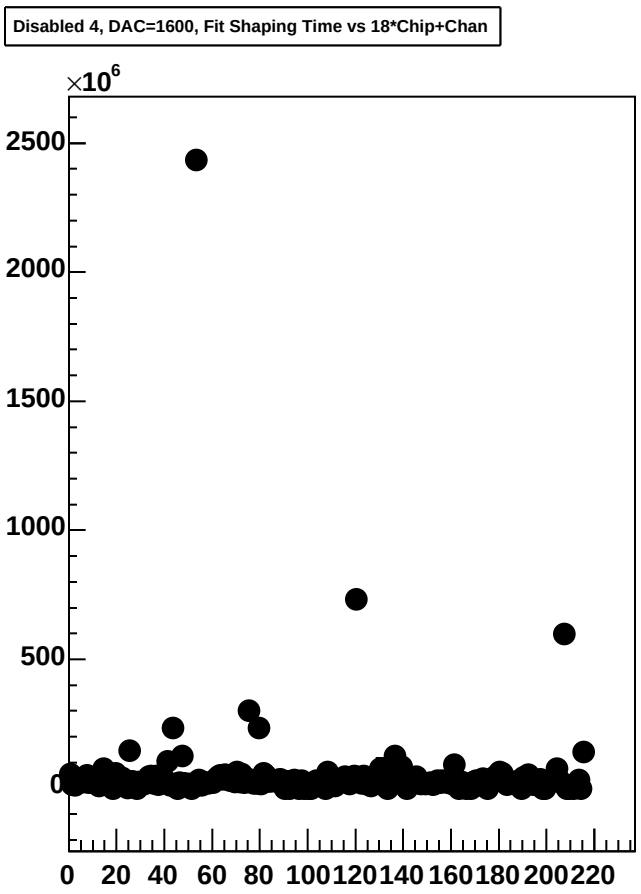

Disabled 3, DAC=1600, Fit Normalisation vs 18*Chip+Chan
×10^7
0
-500
0 20 40 60 80 100 120 140 160 180 200 220


Disabled 3, DAC=1600, Fit Shaping Time vs 18*Chip+Chan
×10^6
1200
1000
800
600
400
200
0
0 20 40 60 80 100 120 140 160 180 200 220

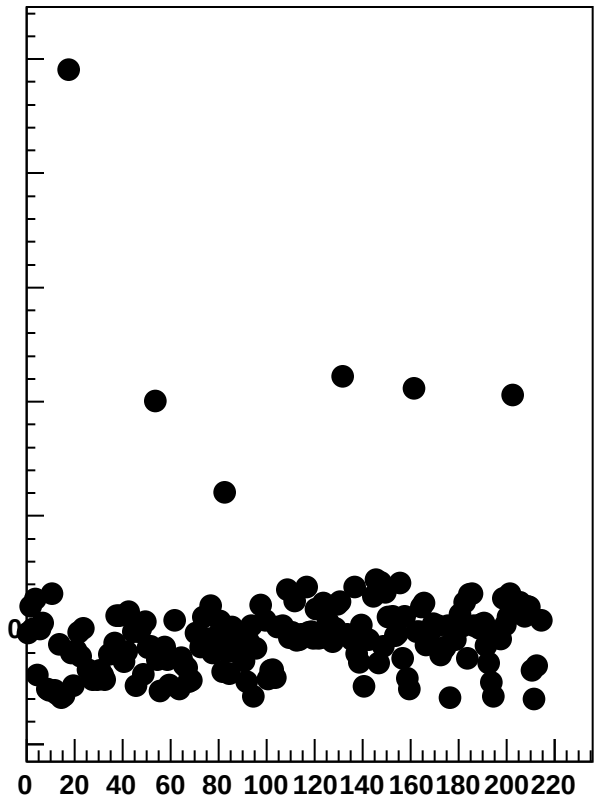

Disabled 4, DAC=1600, Fit Baseline vs 18*Chip+Chan
×10^3
0
-500
0 20 40 60 80 100 120 140 160 180 200 220

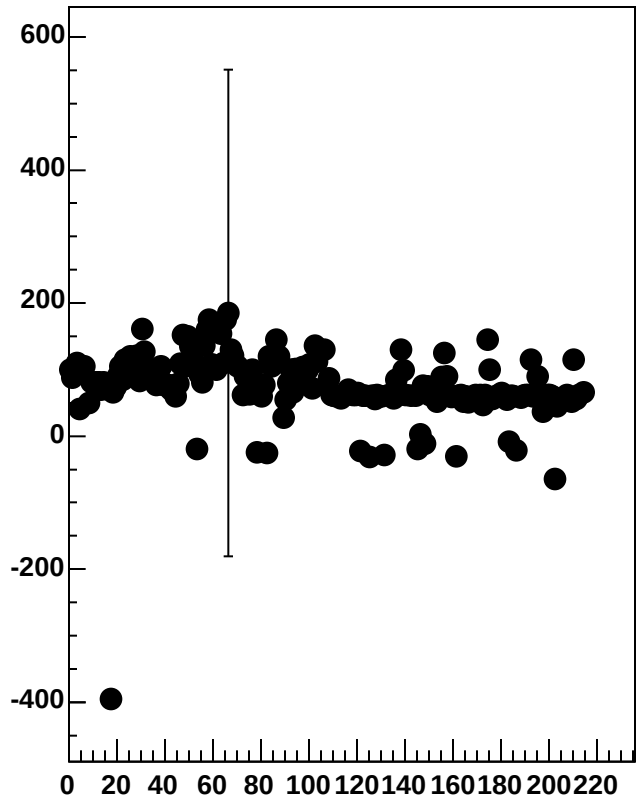

×10^4
0
-500
0 20 40 60 80 100 120 140 160 180 200 220


Disabled 4, DAC=1600, Fit Normalisation vs 18*Chip+Chan
×10^7
0
-500
0 20 40 60 80 100 120 140 160 180 200 220

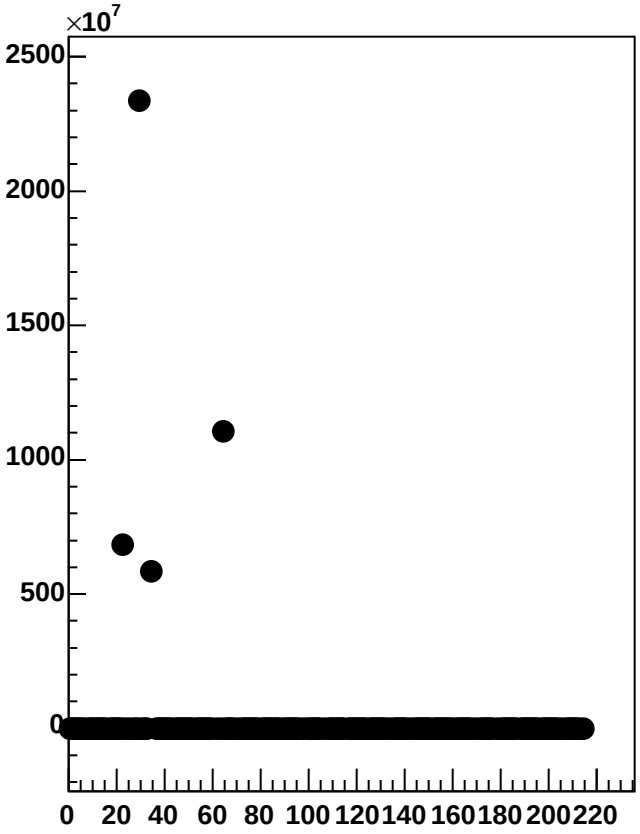

Disabled 4, DAC=1600, Fit Shaping Time vs 18*Chip+Chan
×10^6
2500
2000
1500
1000
500
0
0 20 40 60 80 100 120 140 160 180 200 220

Disabled 5, DAC=1600, Fit Baseline vs 18*Chip+Chan

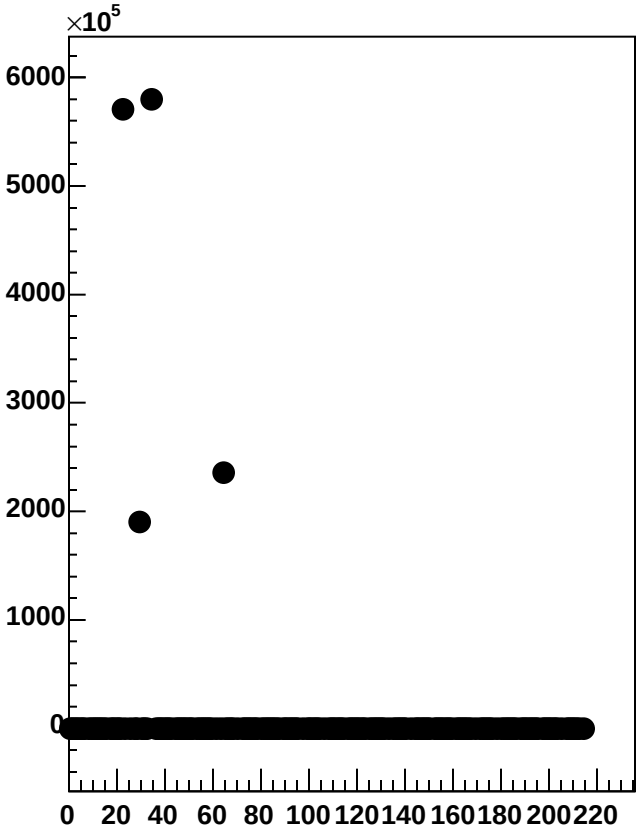




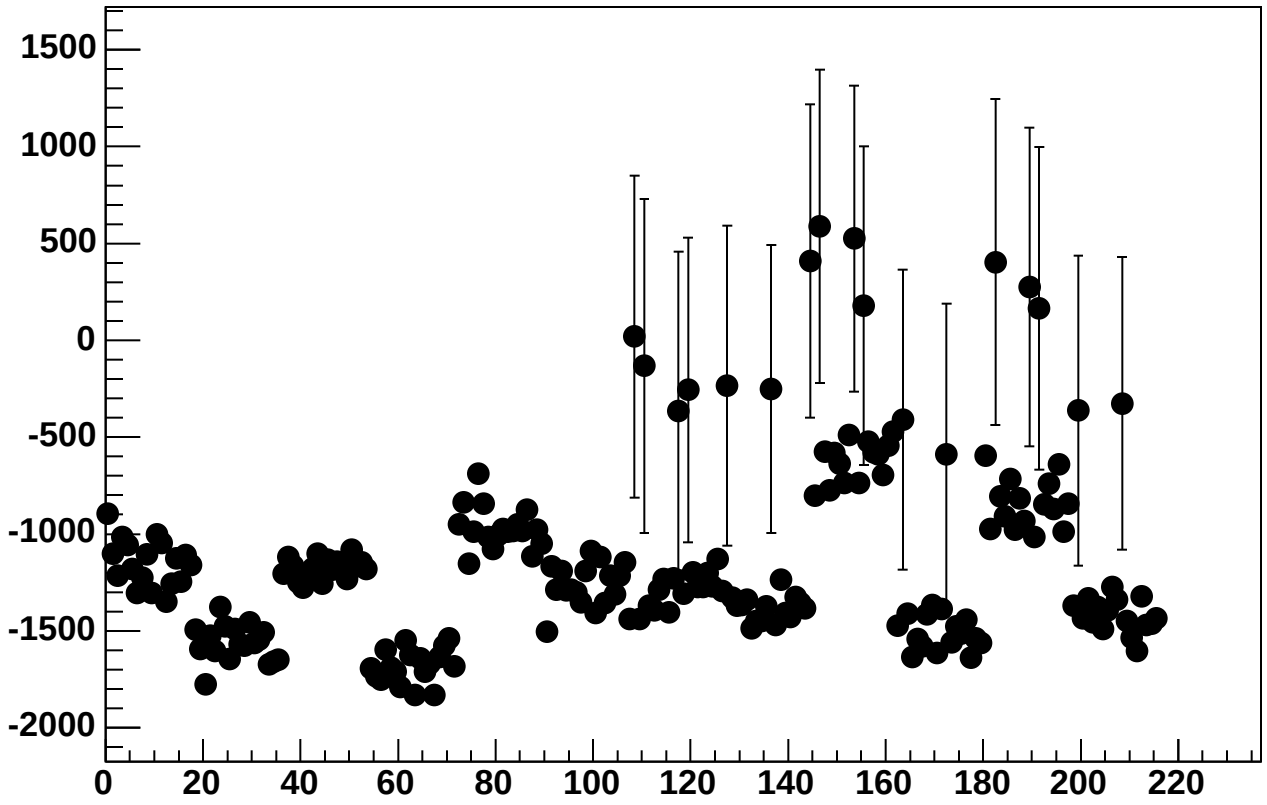
Disabled 5, DAC=1600, Fit Normalisation vs 18*Chip+Chan



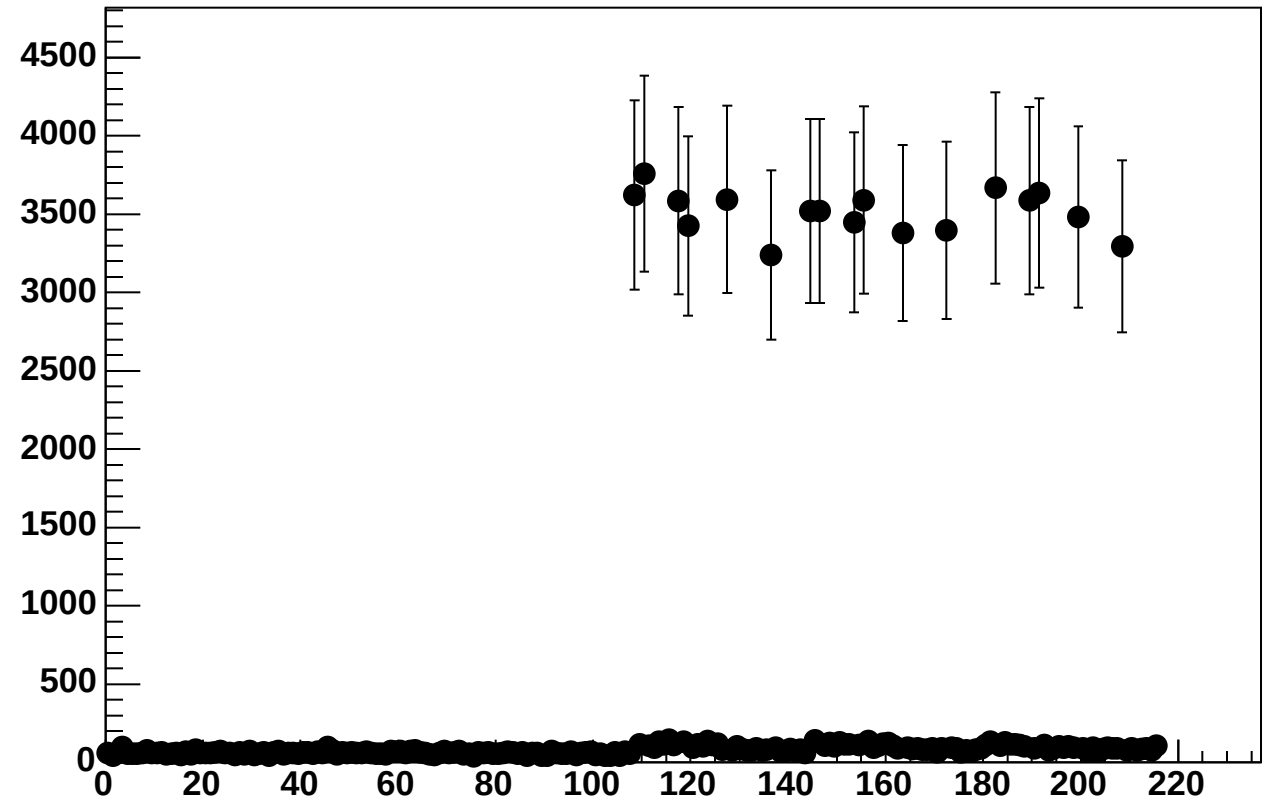
Disabled 5, DAC=1600, Fit Shaping Time vs 18*Chip+Chan



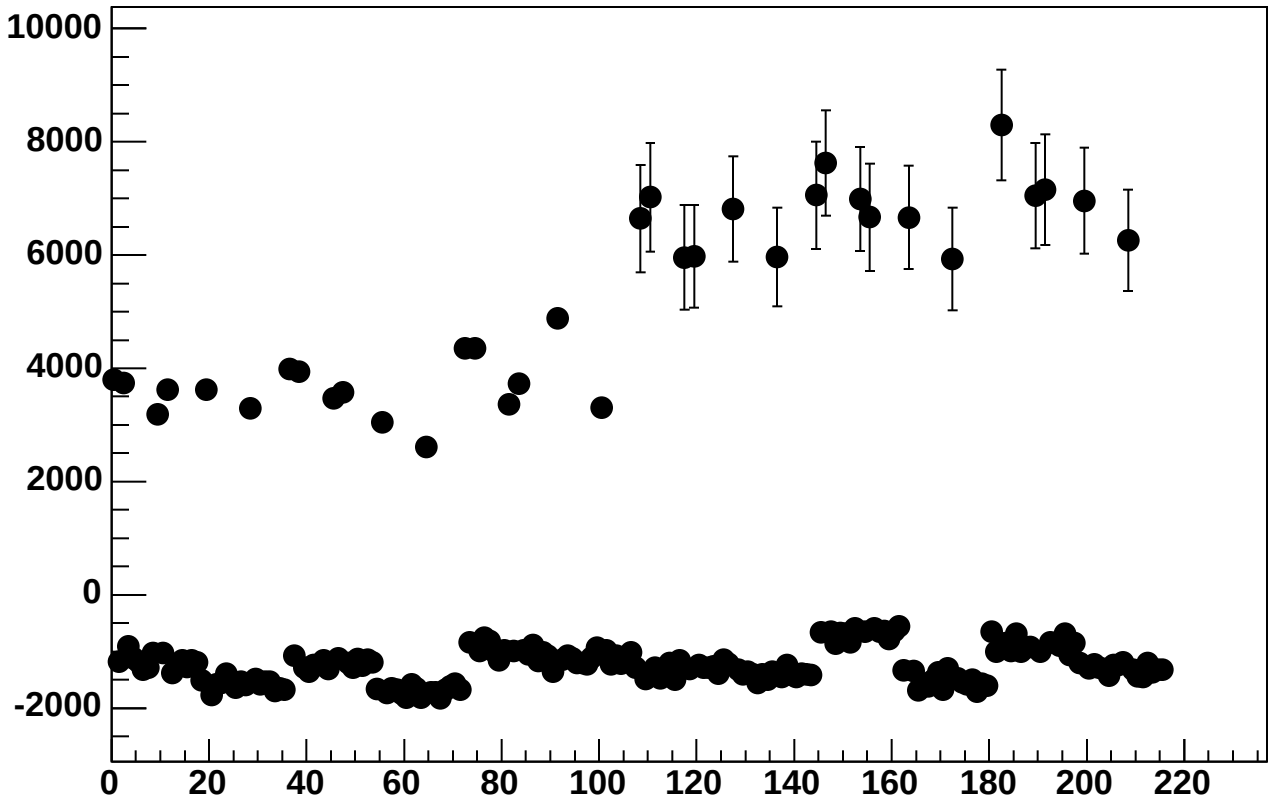
Enable 0, DAC=1600, Hold=0, ADC Mean vs 18*Chip+Chan



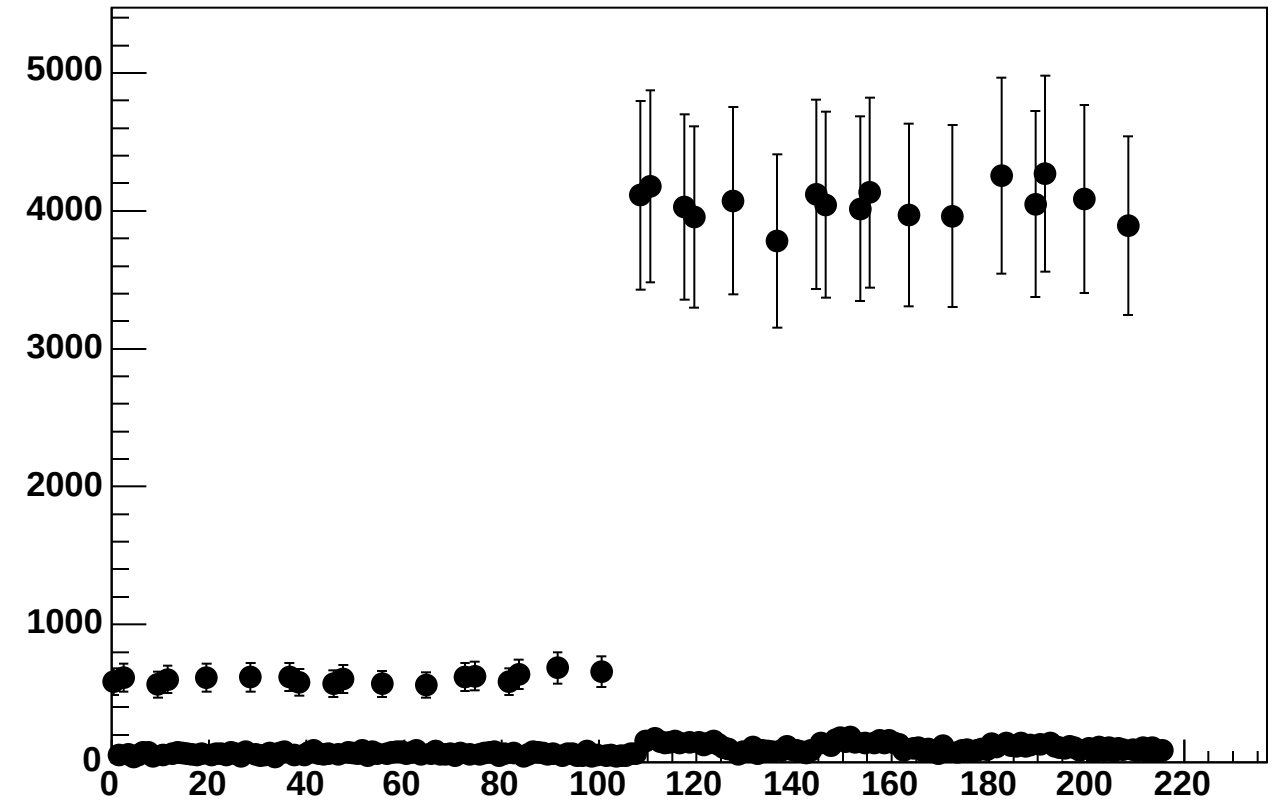
Enable 0, DAC=1600, Hold=0, ADC Noise vs 18*Chip+Chan



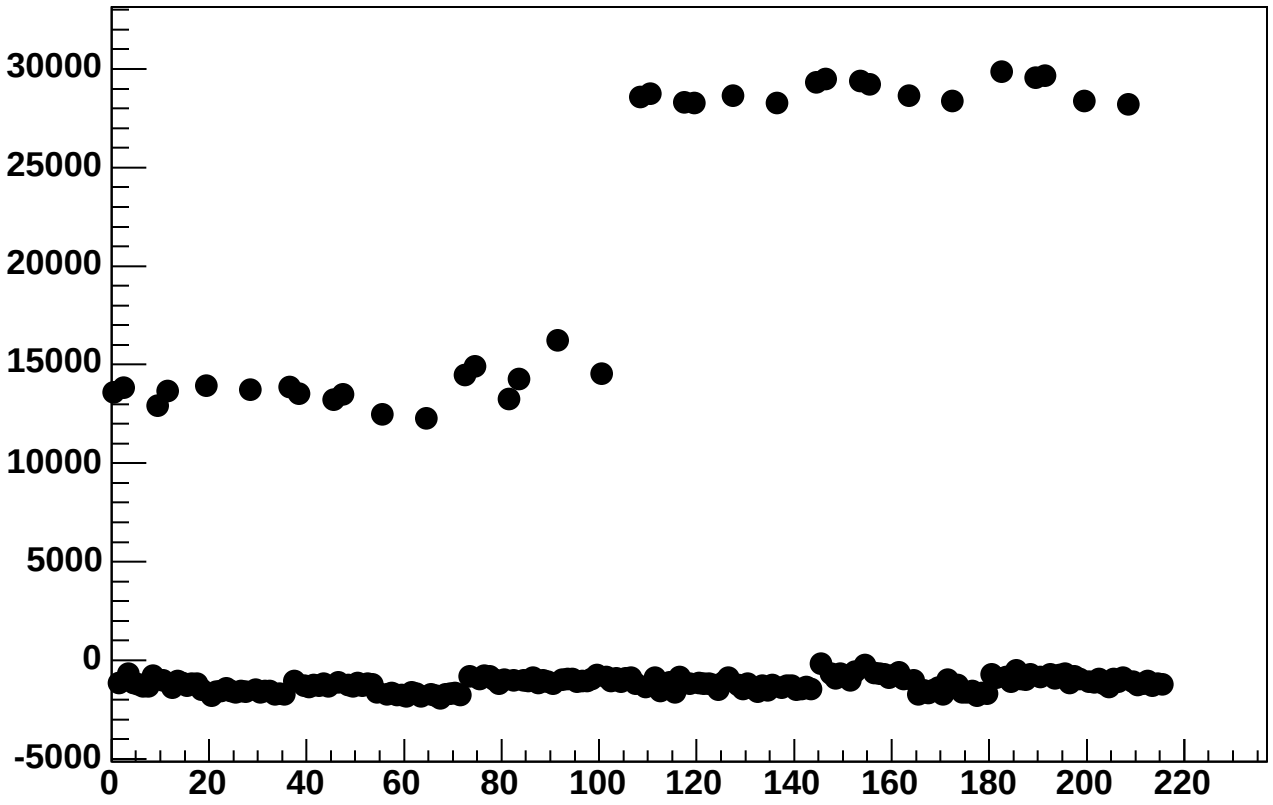
Enable 0, DAC=1600, Hold=5, ADC Mean vs 18*Chip+Chan



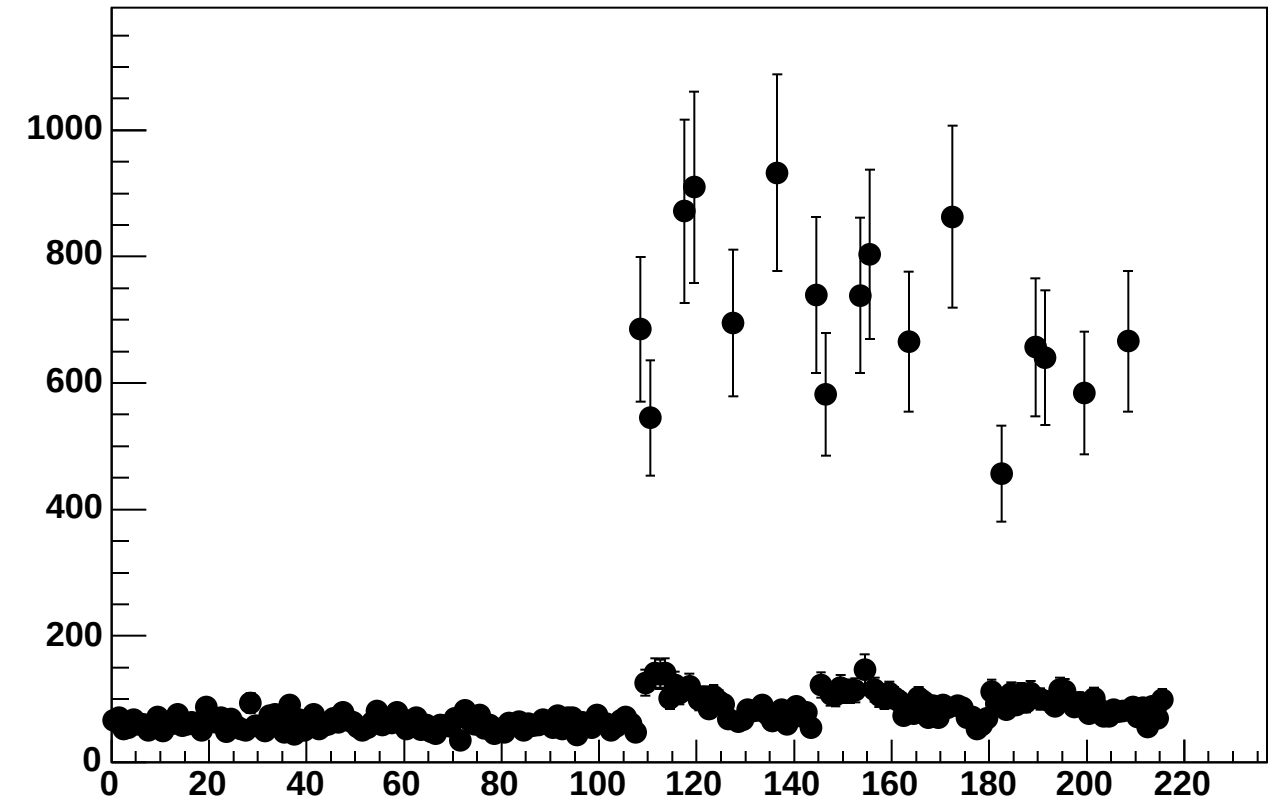
Enable 0, DAC=1600, Hold=5, ADC Noise vs 18*Chip+Chan



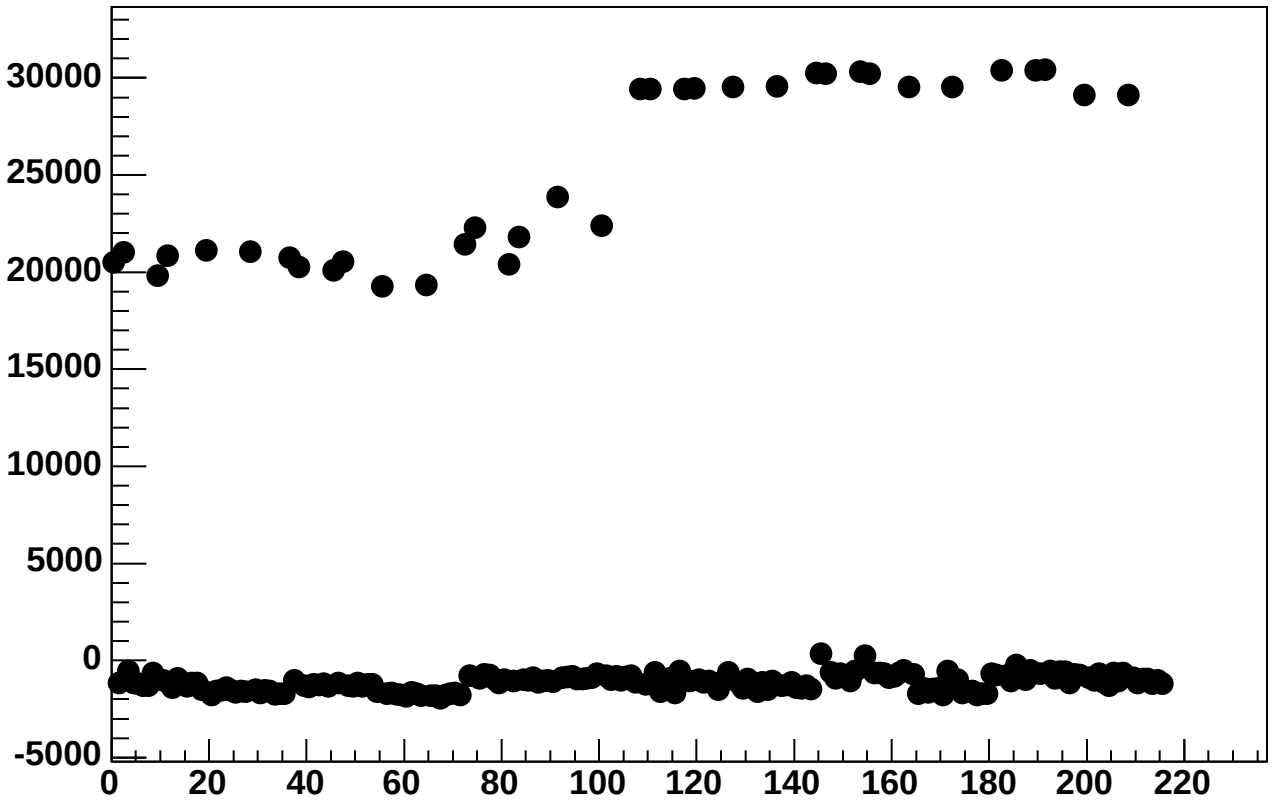
Enable 0, DAC=1600, Hold=10, ADC Mean vs 18*Chip+Chan



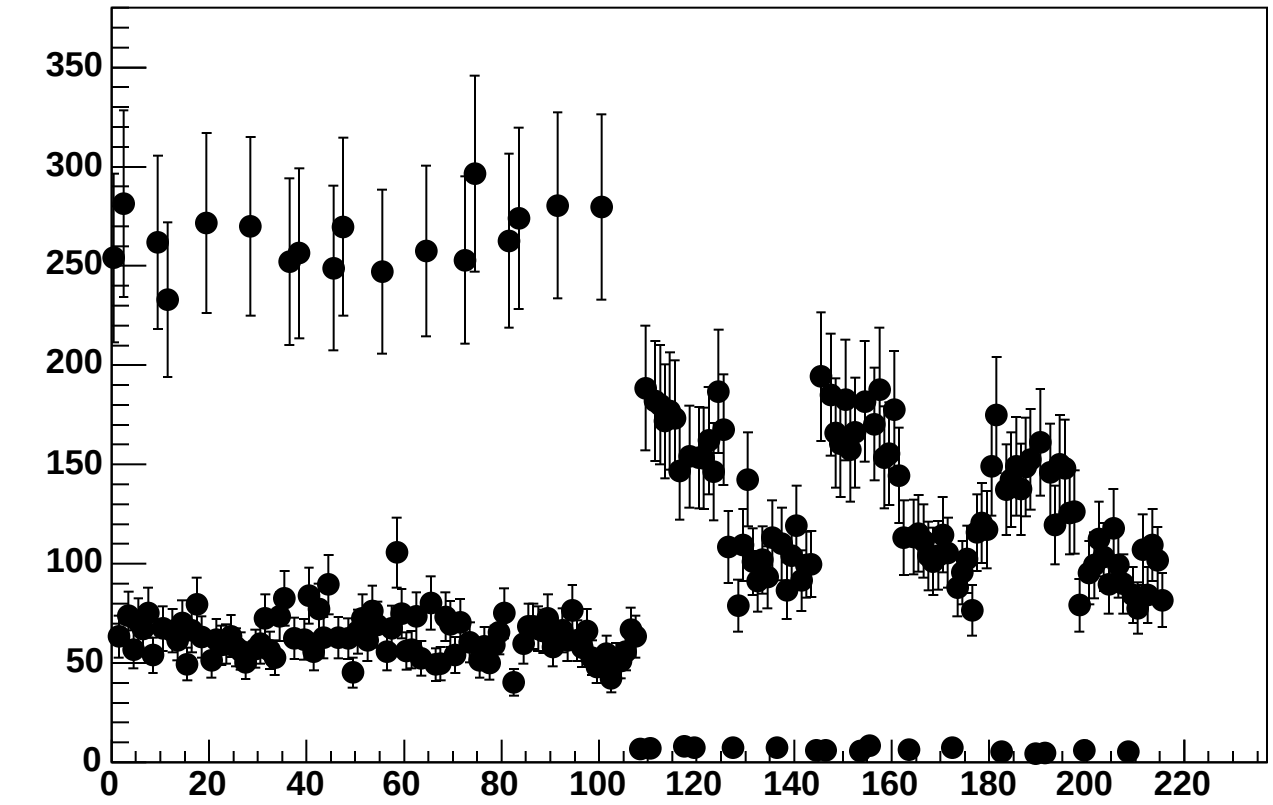
Enable 0, DAC=1600, Hold=10, ADC Noise vs 18*Chip+Chan



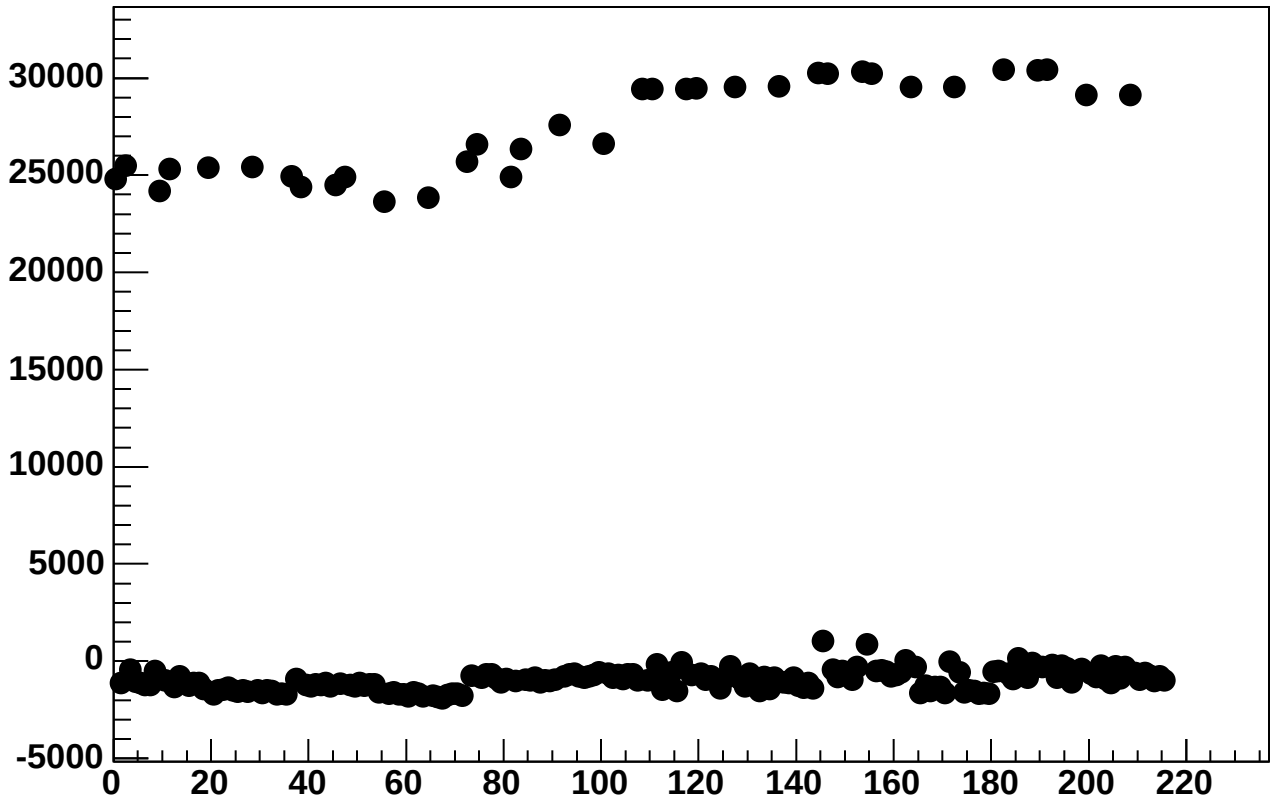
Enable 0, DAC=1600, Hold=15, ADC Mean vs 18*Chip+Chan



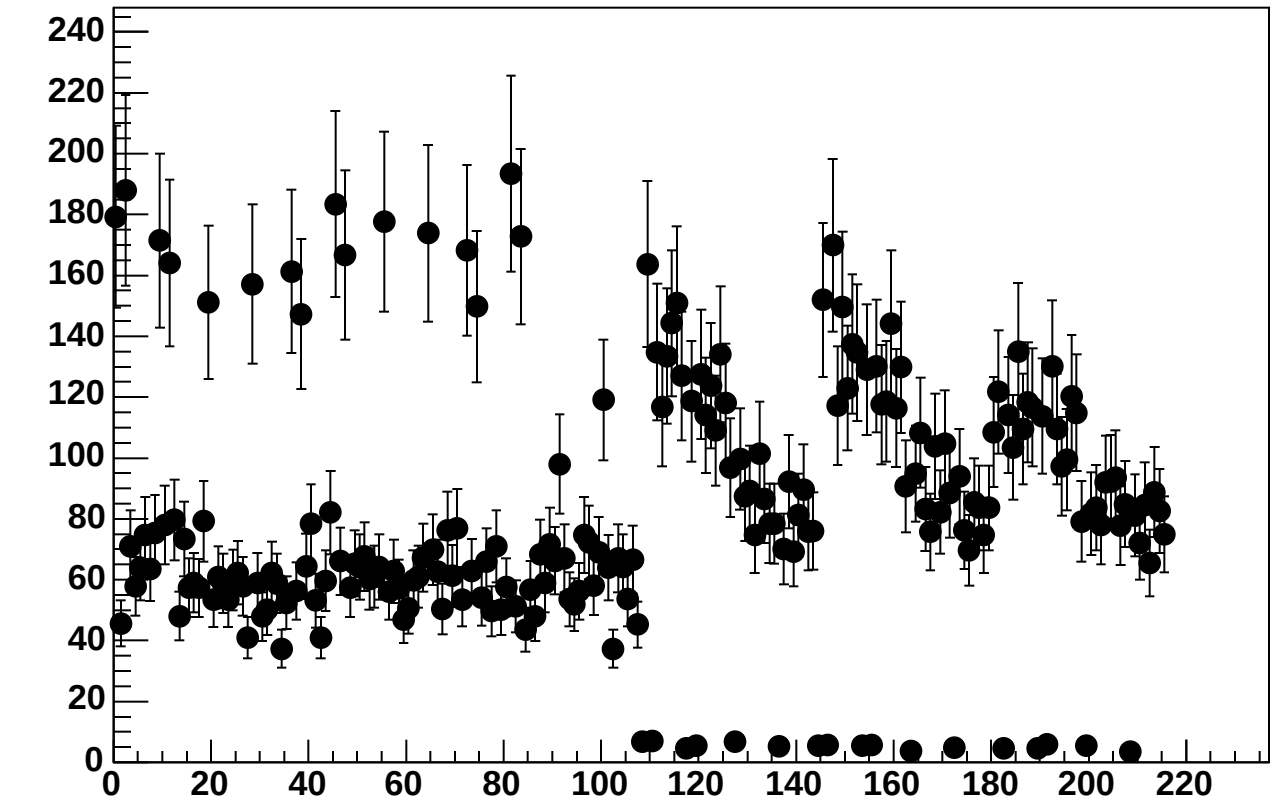
Enable 0, DAC=1600, Hold=15, ADC Noise vs 18*Chip+Chan



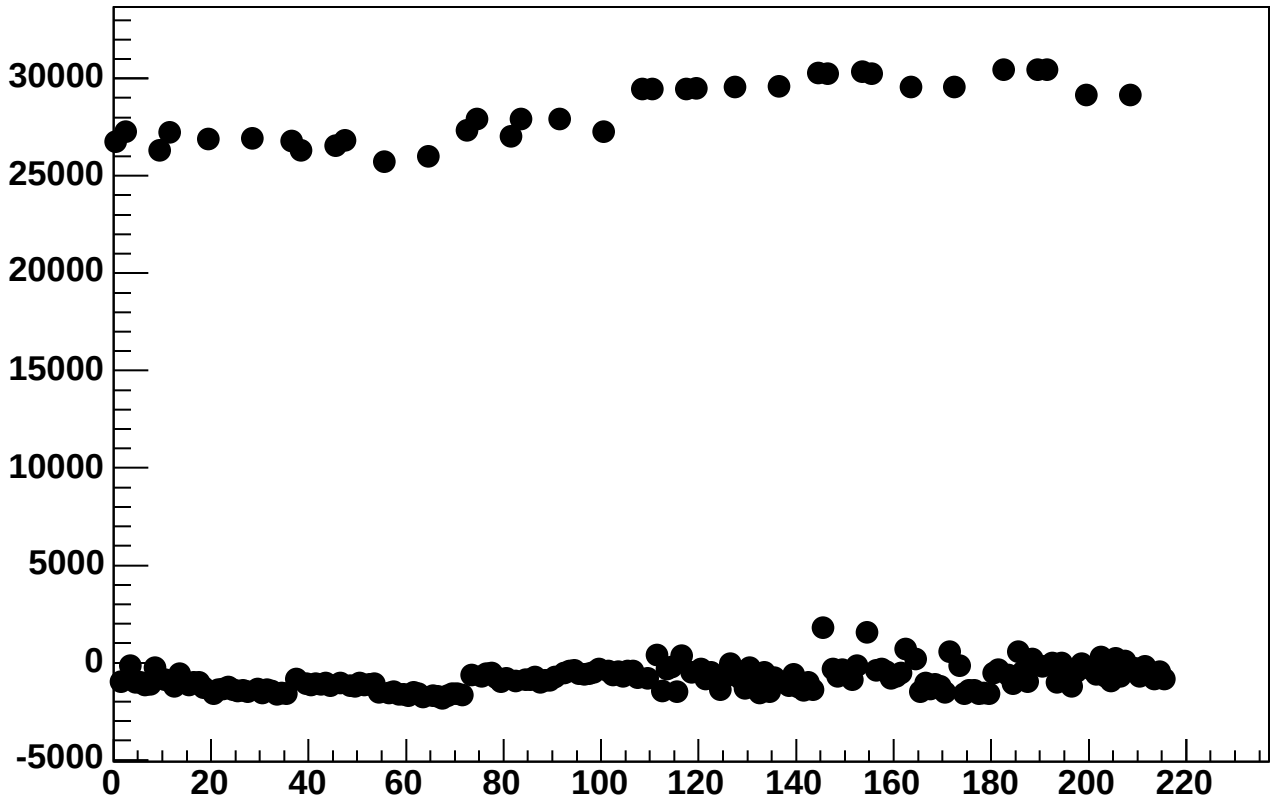
Enable 0, DAC=1600, Hold=20, ADC Mean vs 18*Chip+Chan



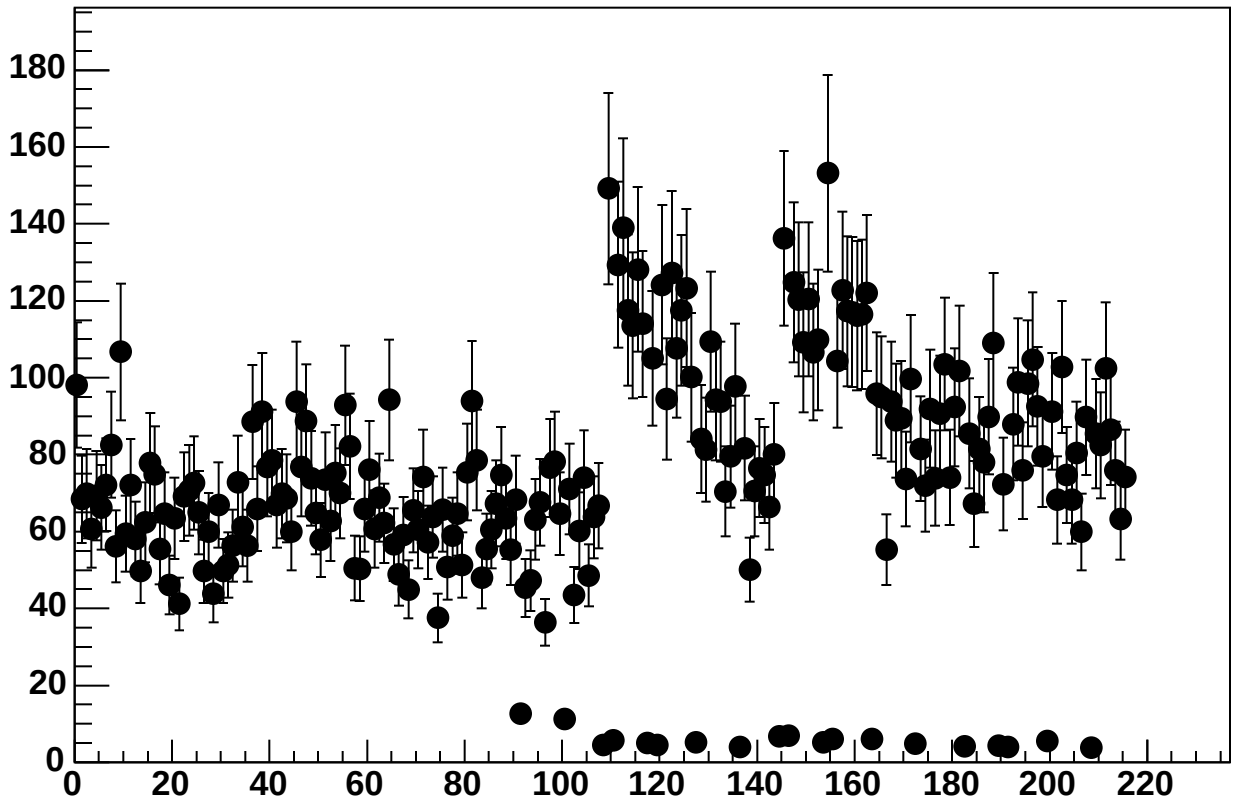
Enable 0, DAC=1600, Hold=20, ADC Noise vs 18*Chip+Chan



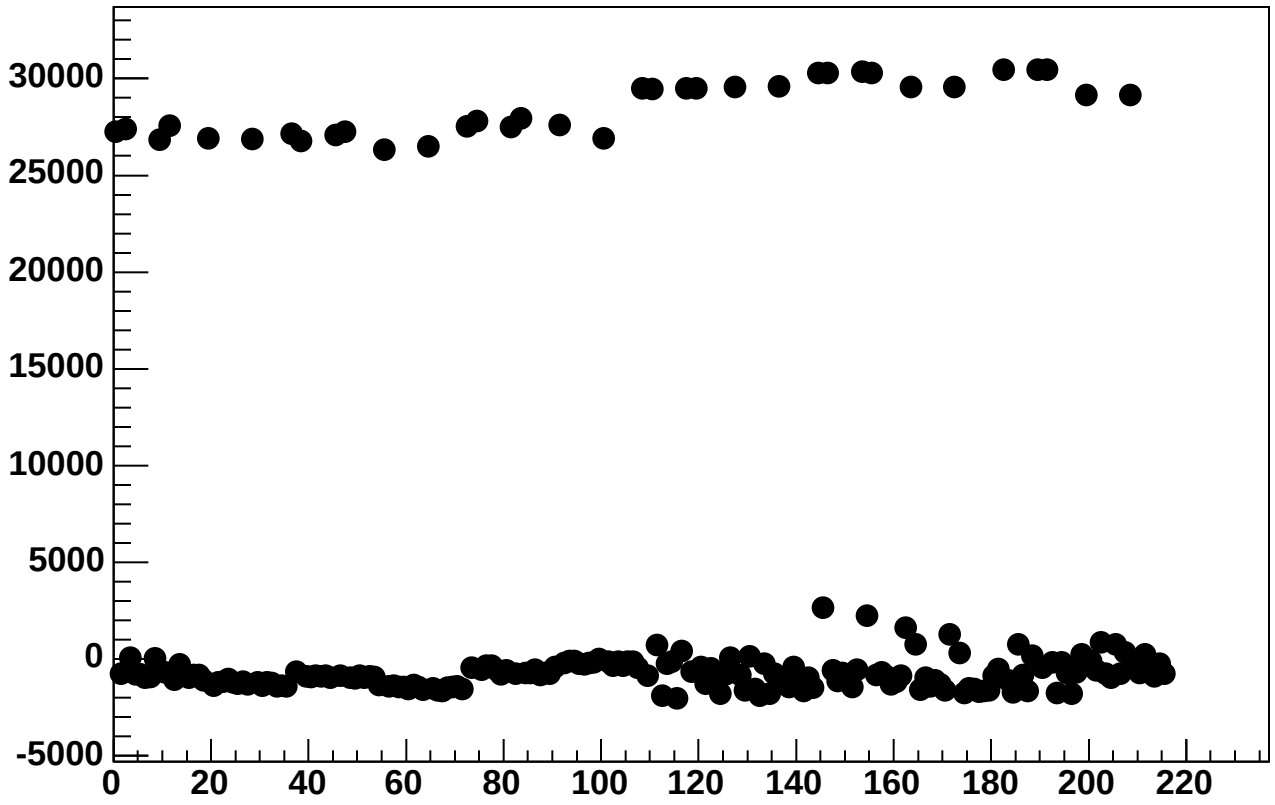
Enable 0, DAC=1600, Hold=25, ADC Mean vs 18*Chip+Chan



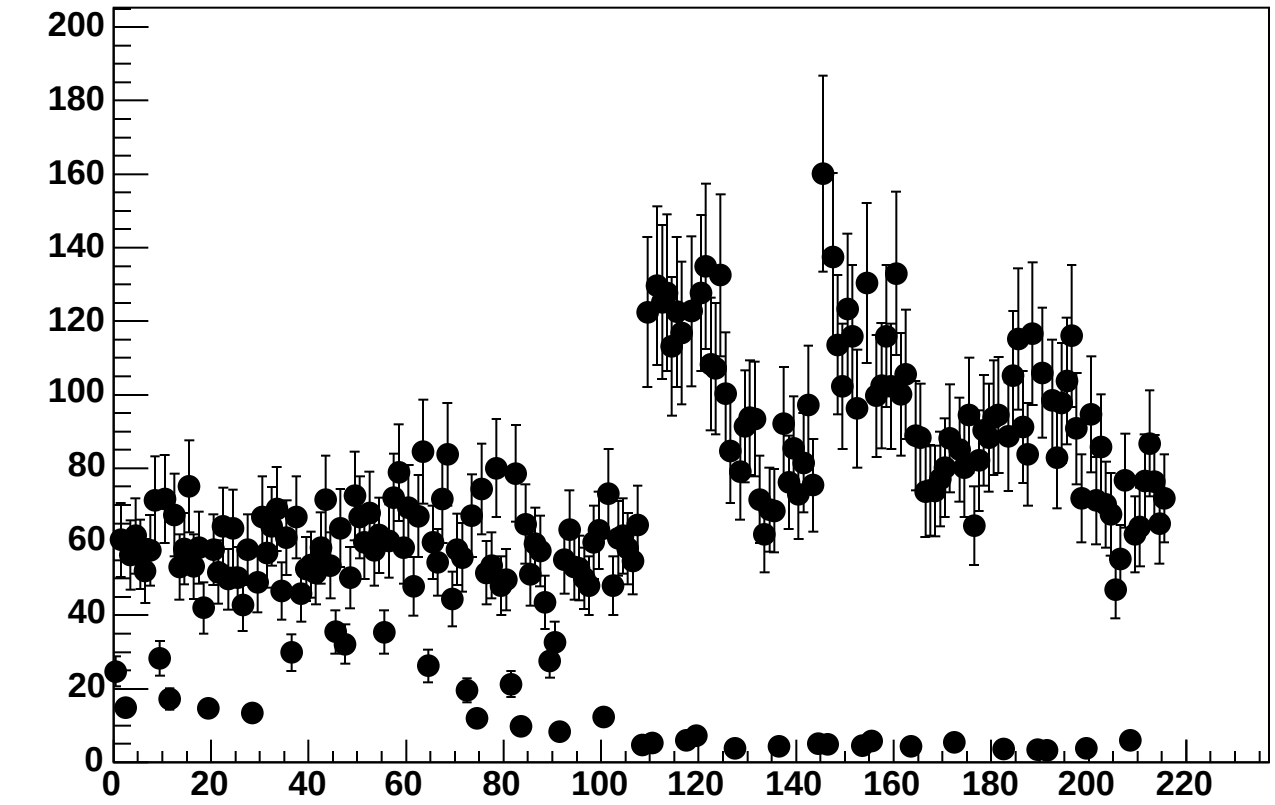
Enable 0, DAC=1600, Hold=25, ADC Noise vs 18*Chip+Chan



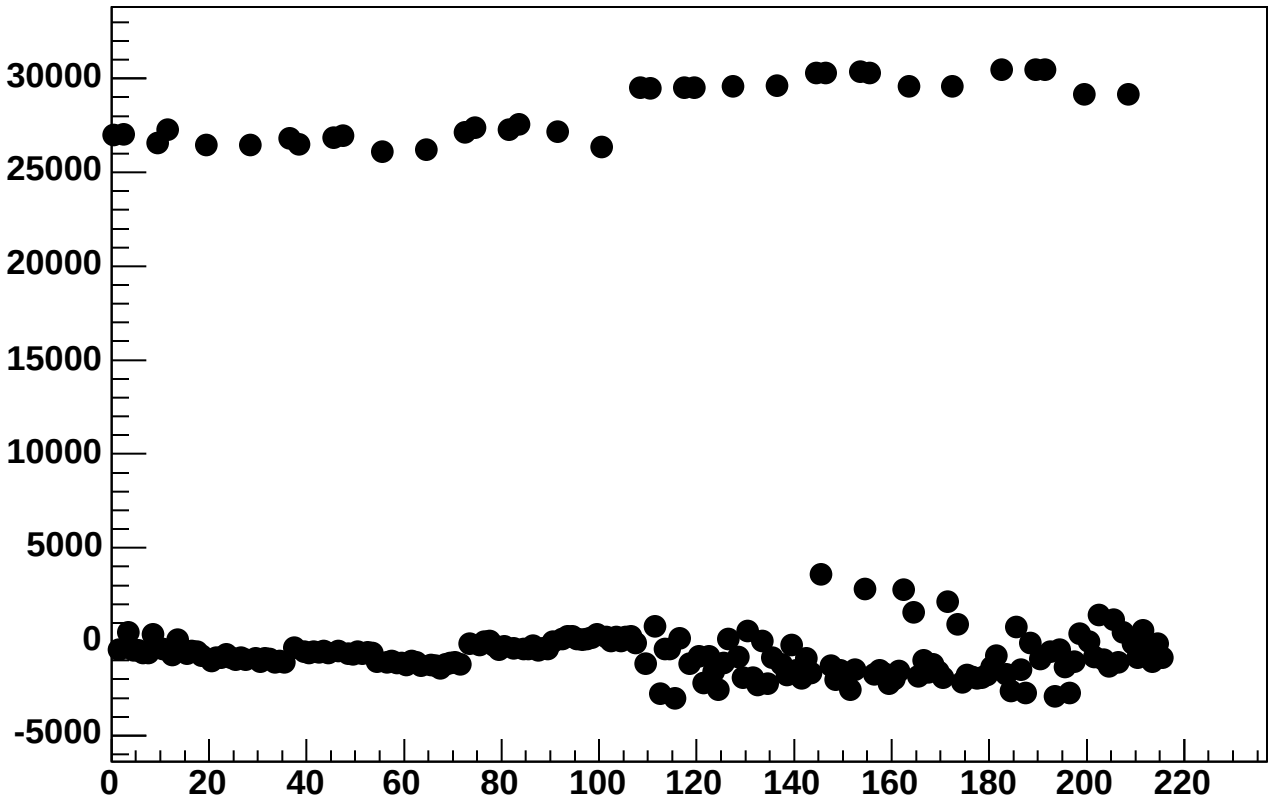
Enable 0, DAC=1600, Hold=30, ADC Mean vs 18*Chip+Chan



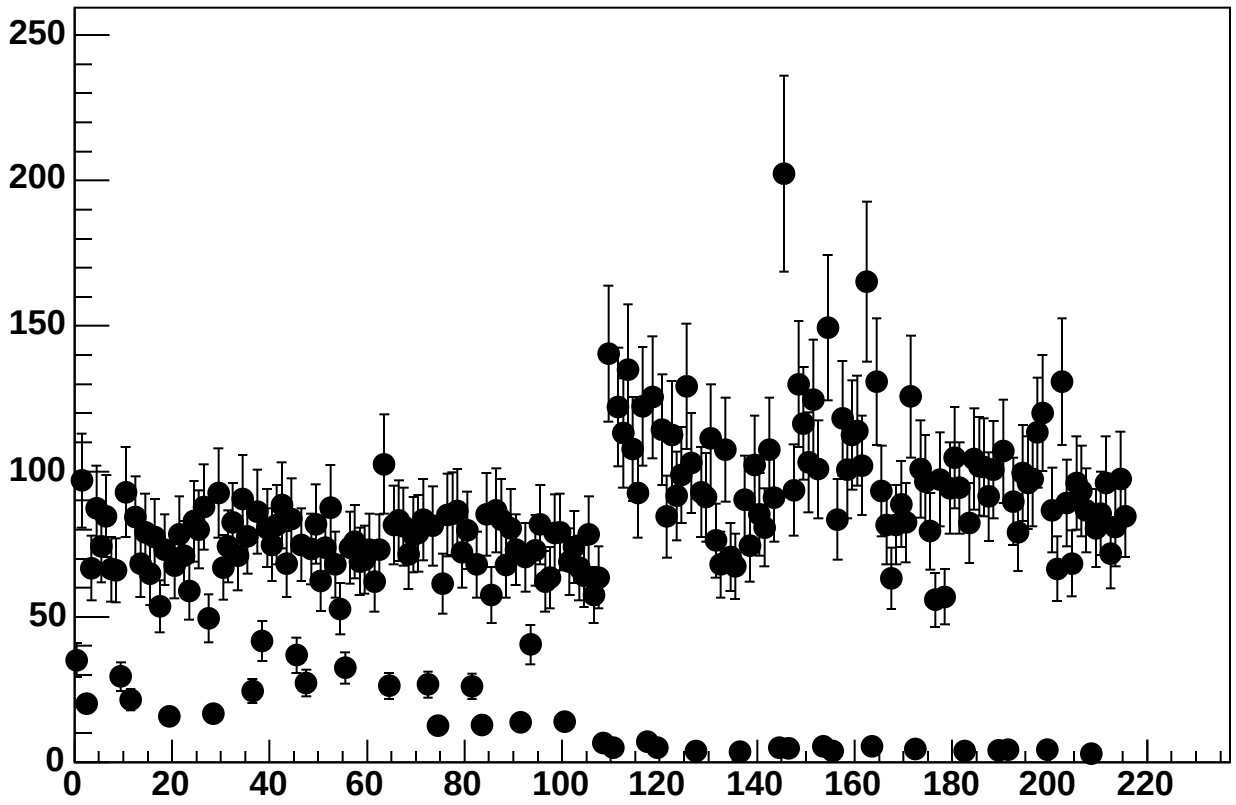
Enable 0, DAC=1600, Hold=30, ADC Noise vs 18*Chip+Chan



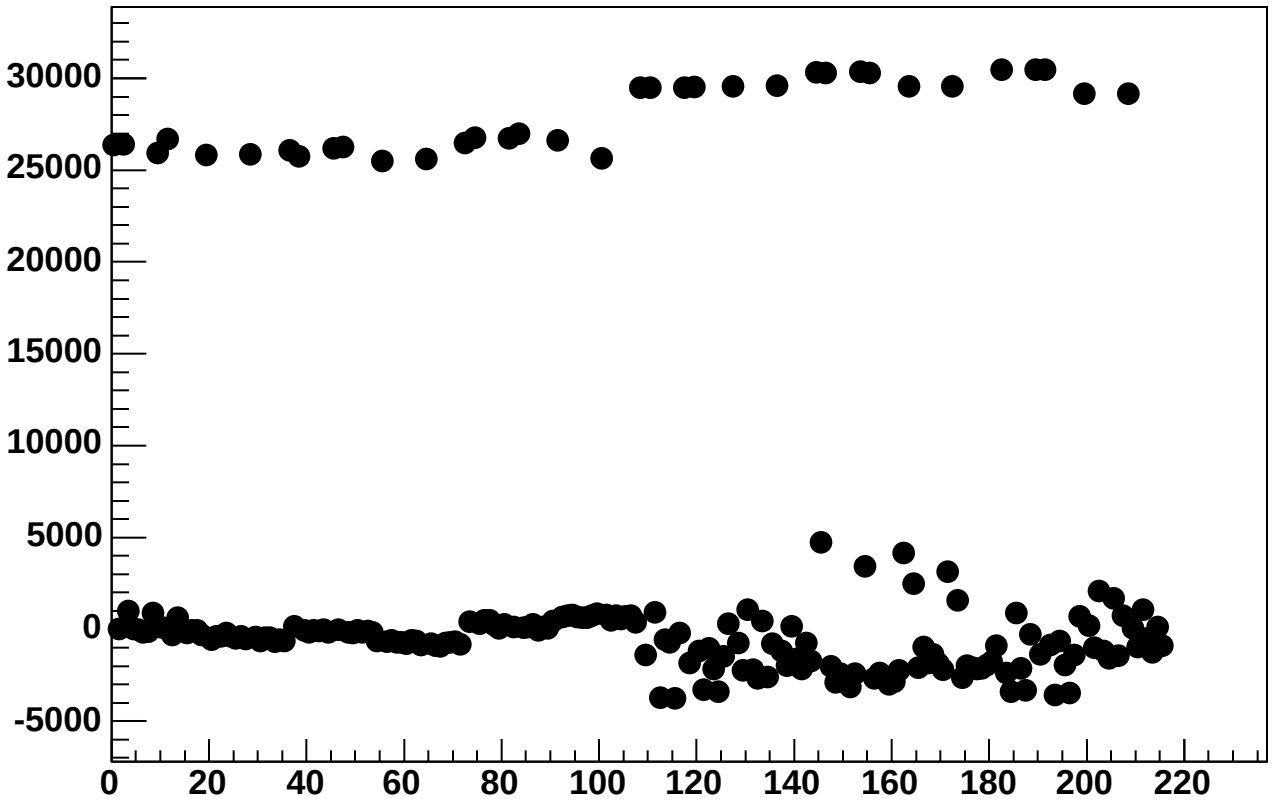
Enable 0, DAC=1600, Hold=35, ADC Mean vs 18*Chip+Chan



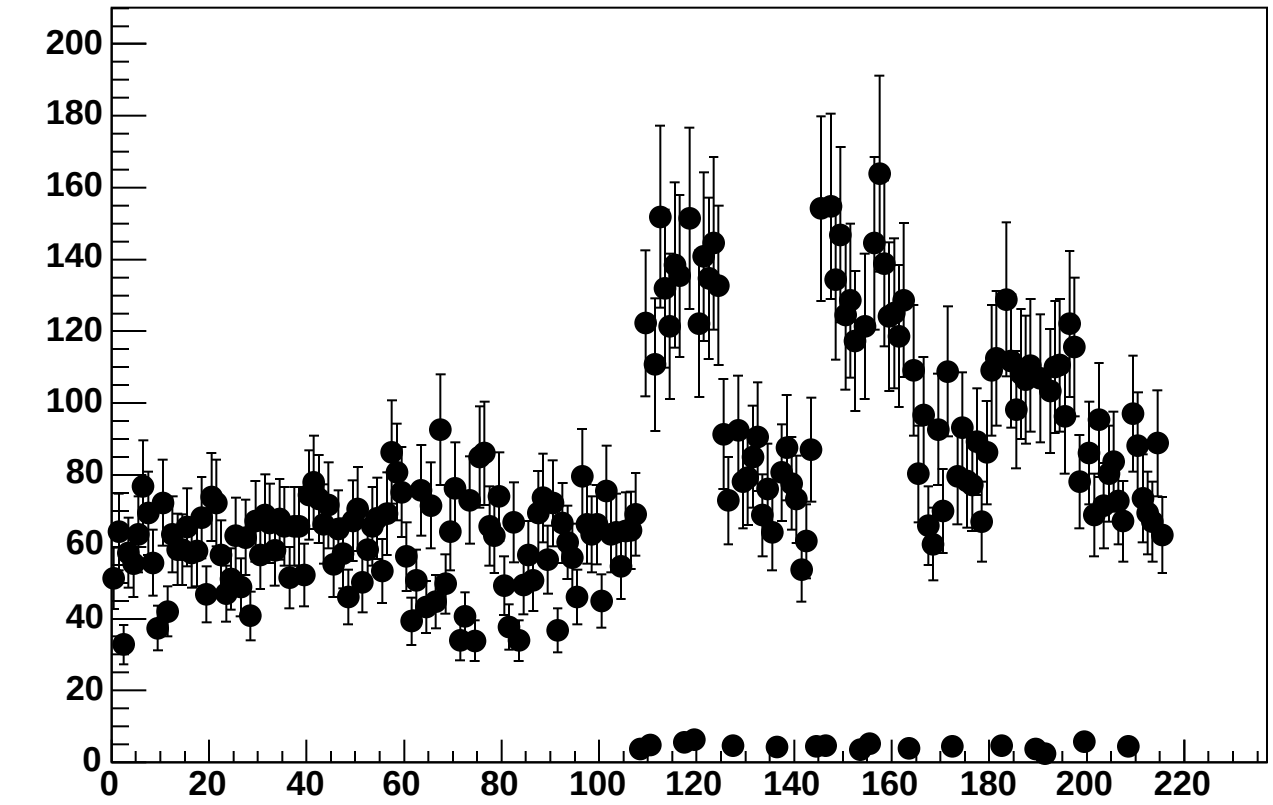
Enable 0, DAC=1600, Hold=35, ADC Noise vs 18*Chip+Chan



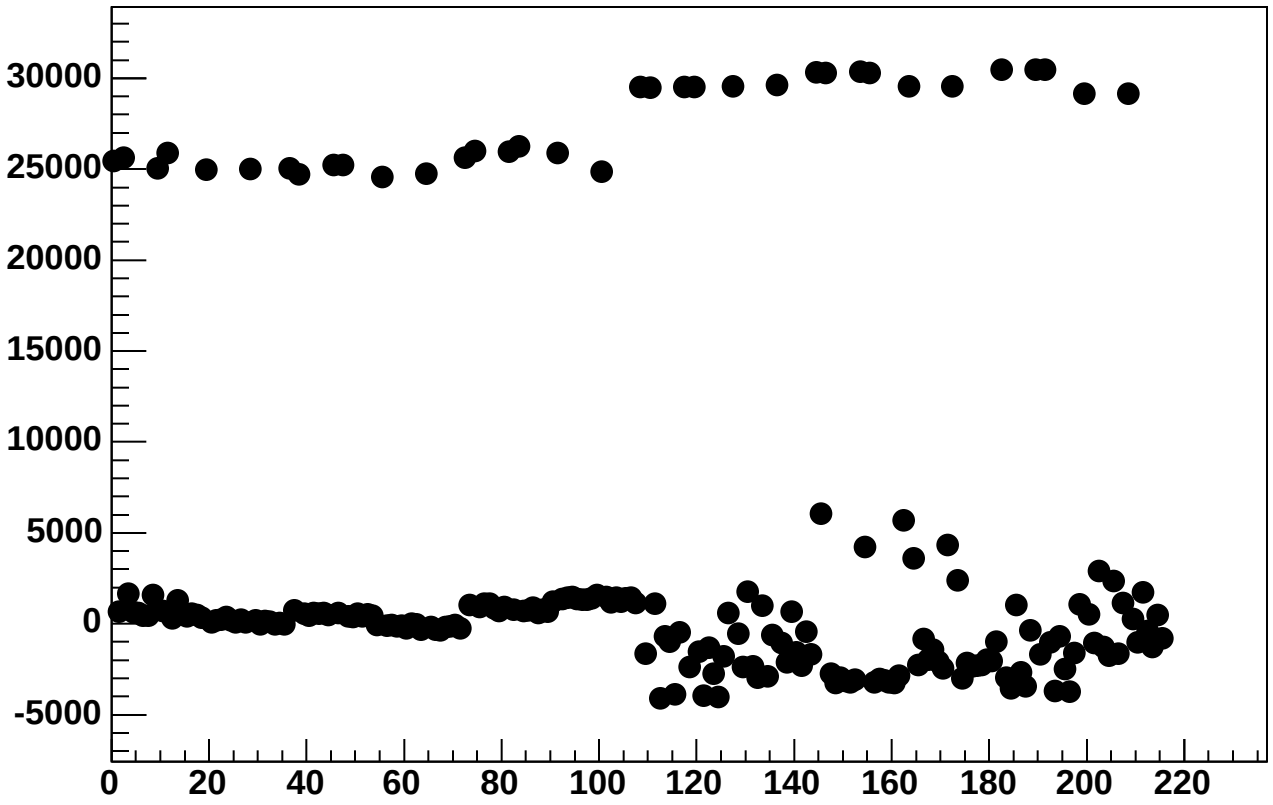
Enable 0, DAC=1600, Hold=40, ADC Mean vs 18*Chip+Chan



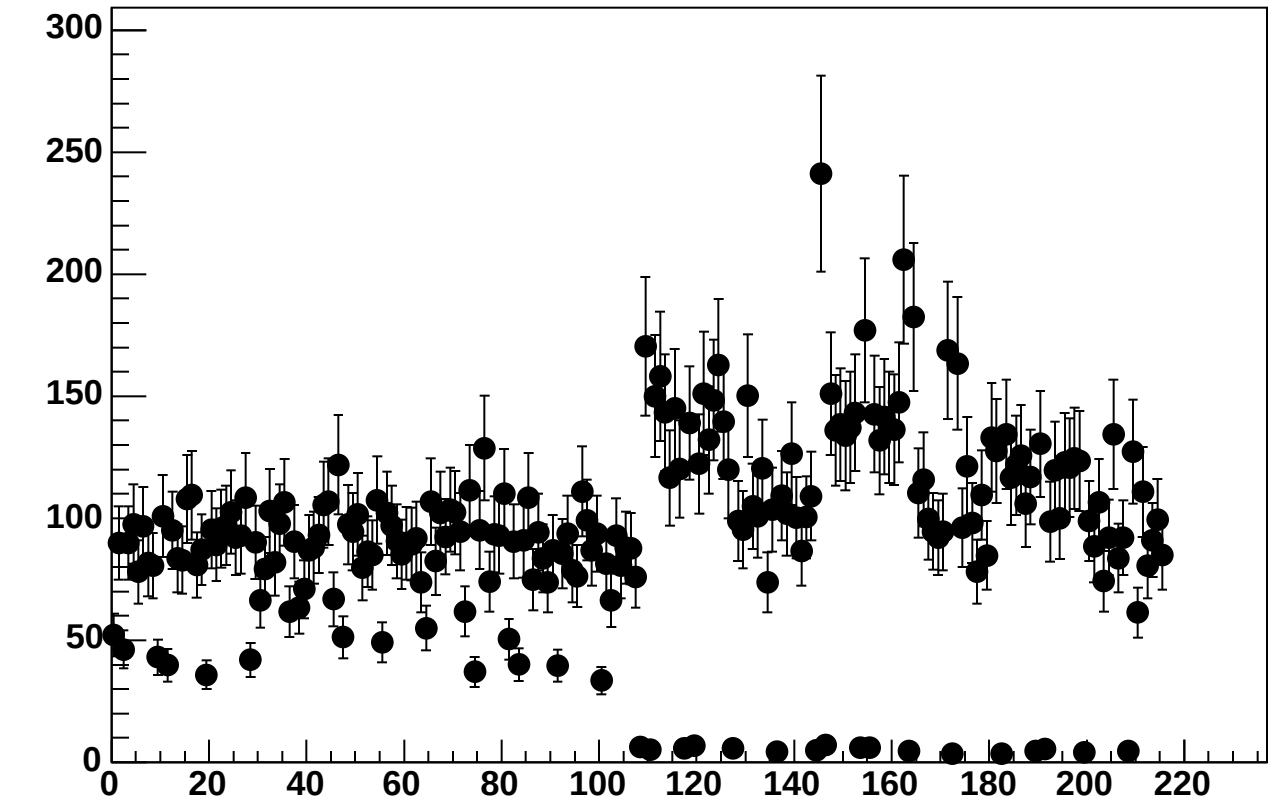
Enable 0, DAC=1600, Hold=40, ADC Noise vs 18*Chip+Chan



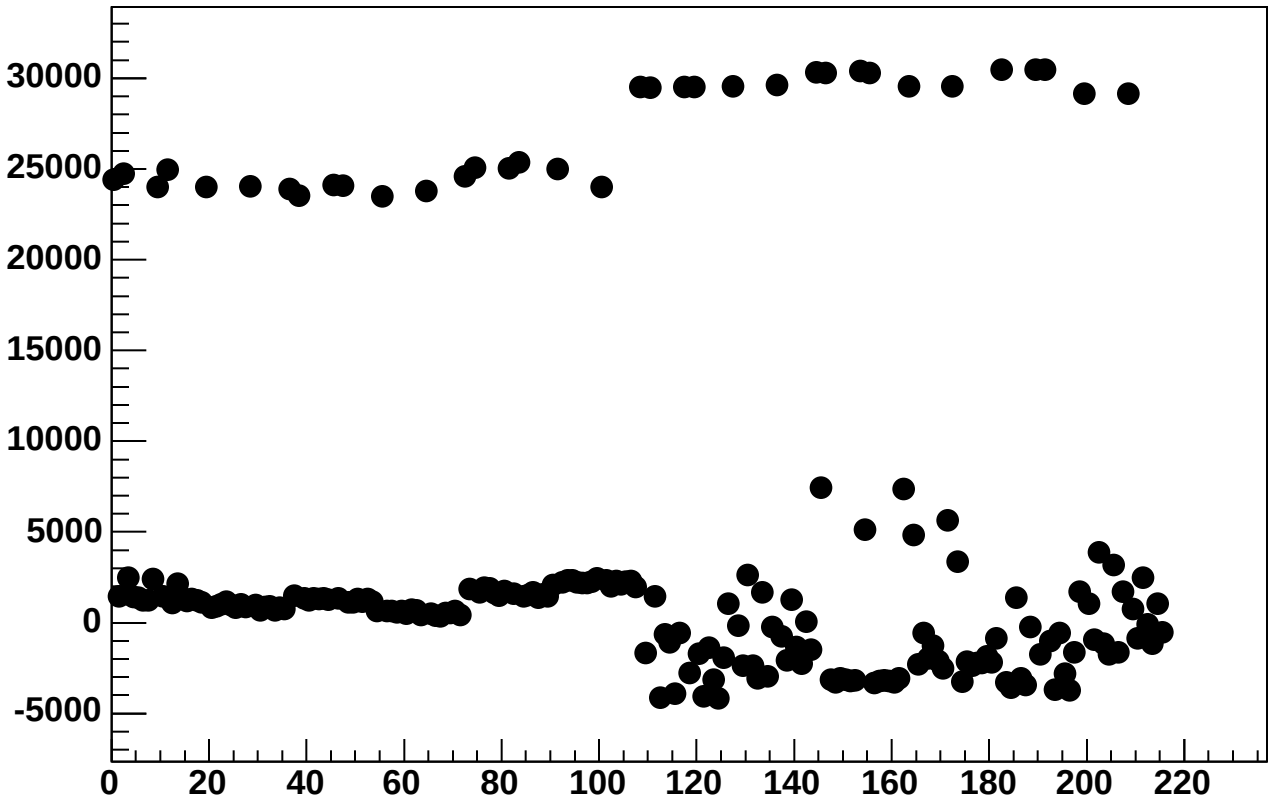
Enable 0, DAC=1600, Hold=45, ADC Mean vs 18*Chip+Chan



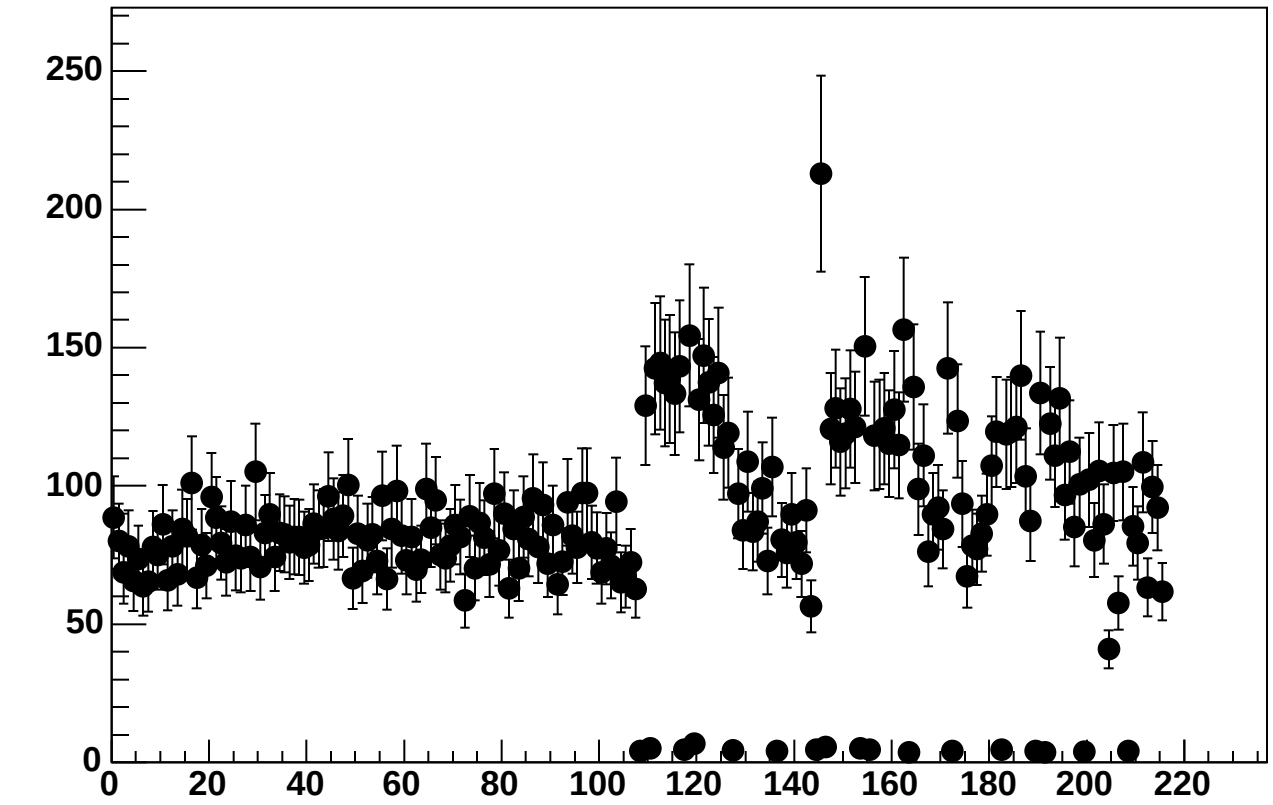
Enable 0, DAC=1600, Hold=45, ADC Noise vs 18*Chip+Chan



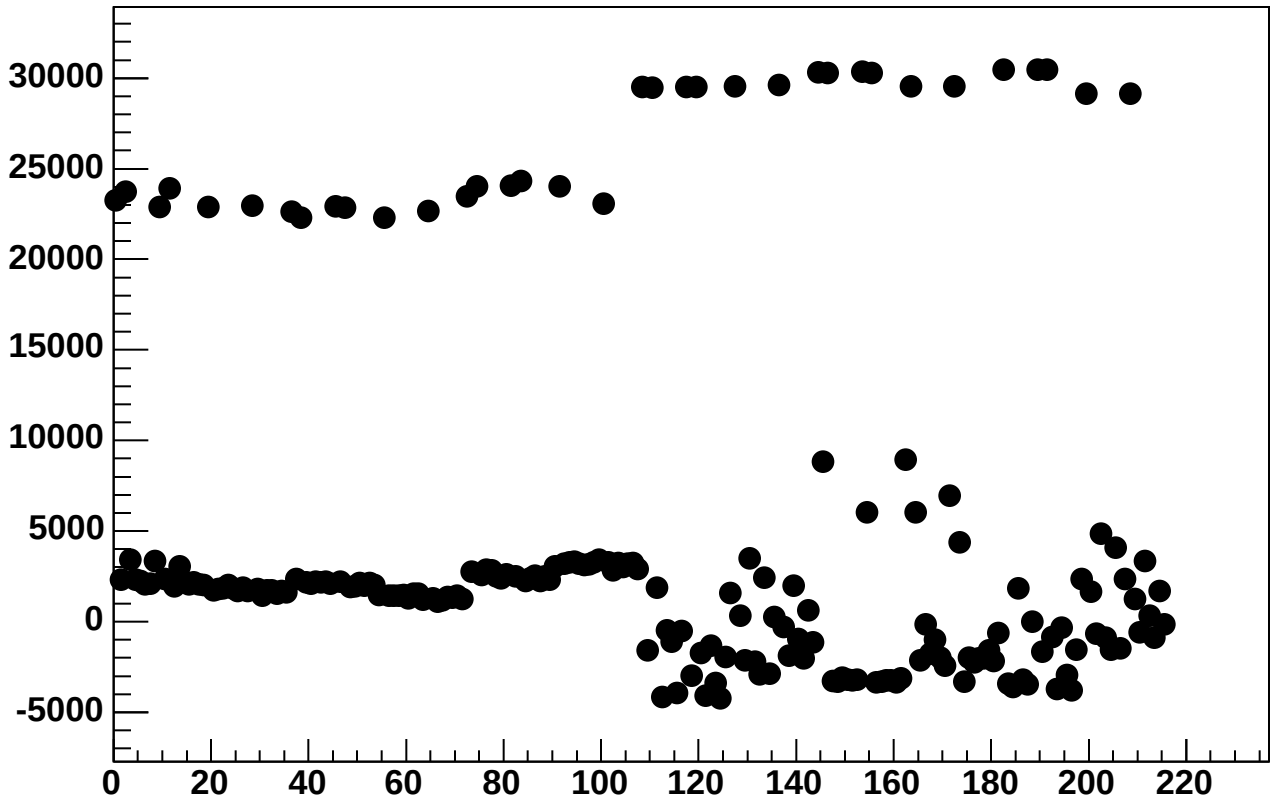
Enable 0, DAC=1600, Hold=50, ADC Mean vs 18*Chip+Chan



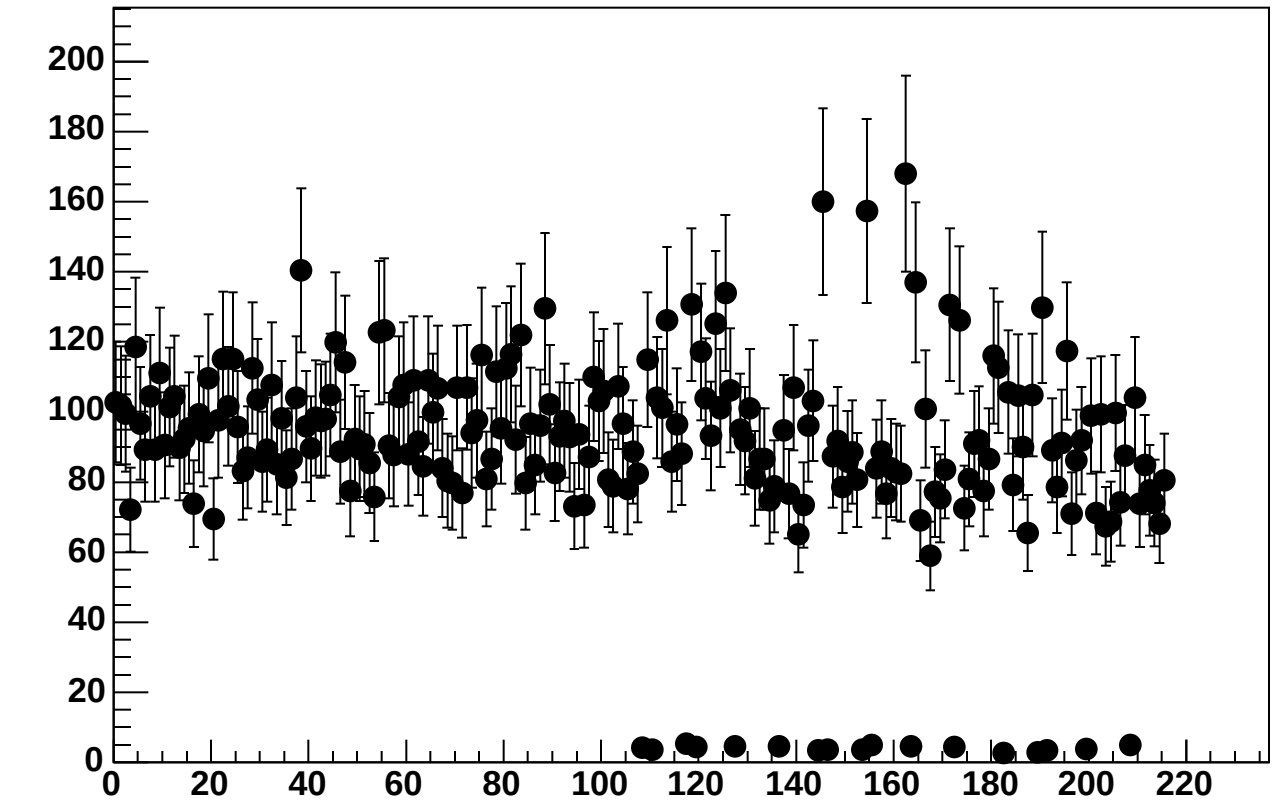
Enable 0, DAC=1600, Hold=50, ADC Noise vs 18*Chip+Chan



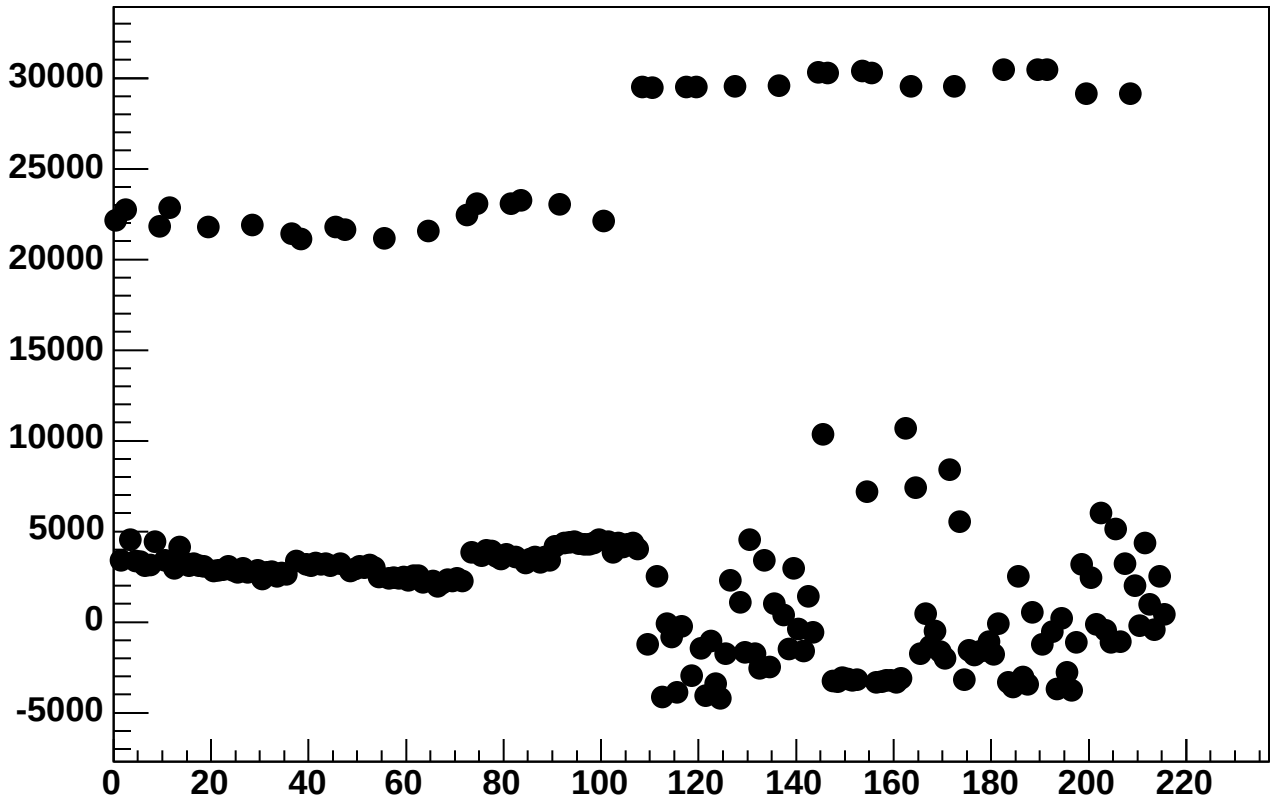
Enable 0, DAC=1600, Hold=55, ADC Mean vs 18*Chip+Chan



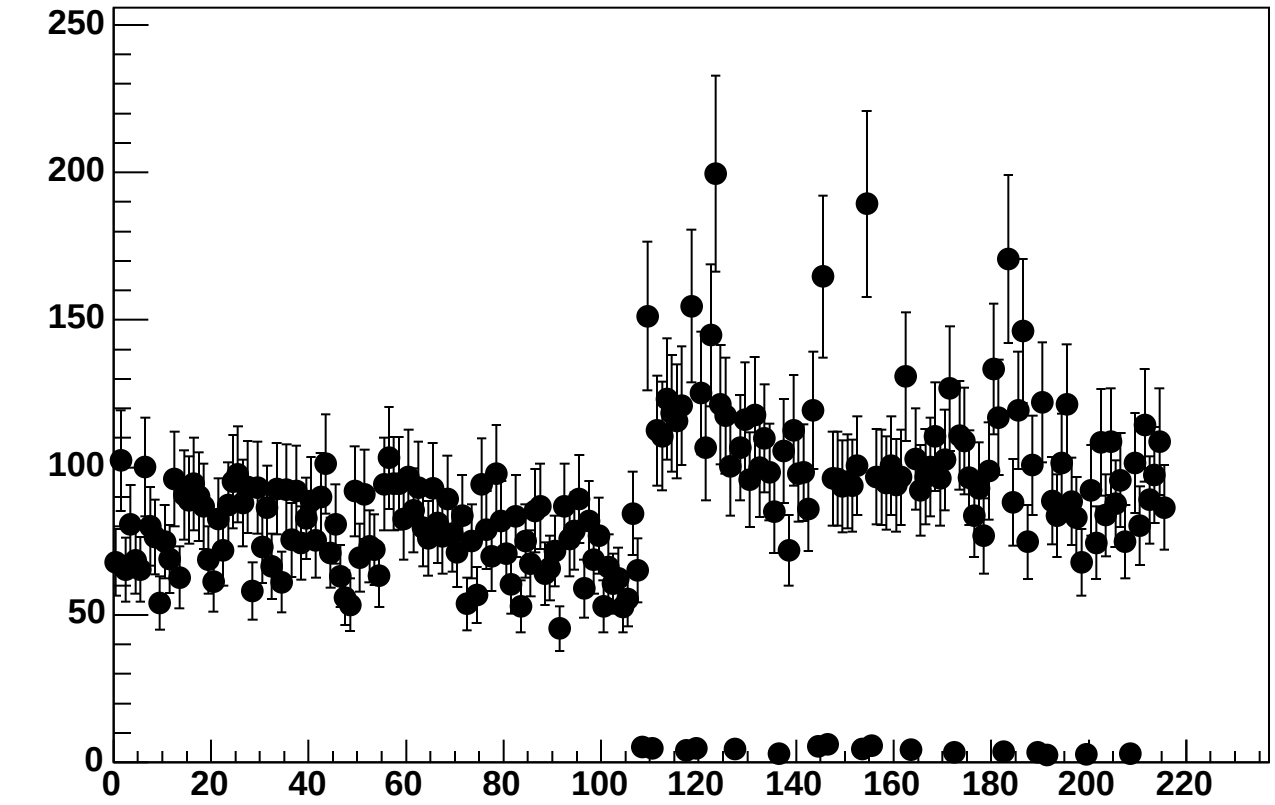
Enable 0, DAC=1600, Hold=55, ADC Noise vs 18*Chip+Chan



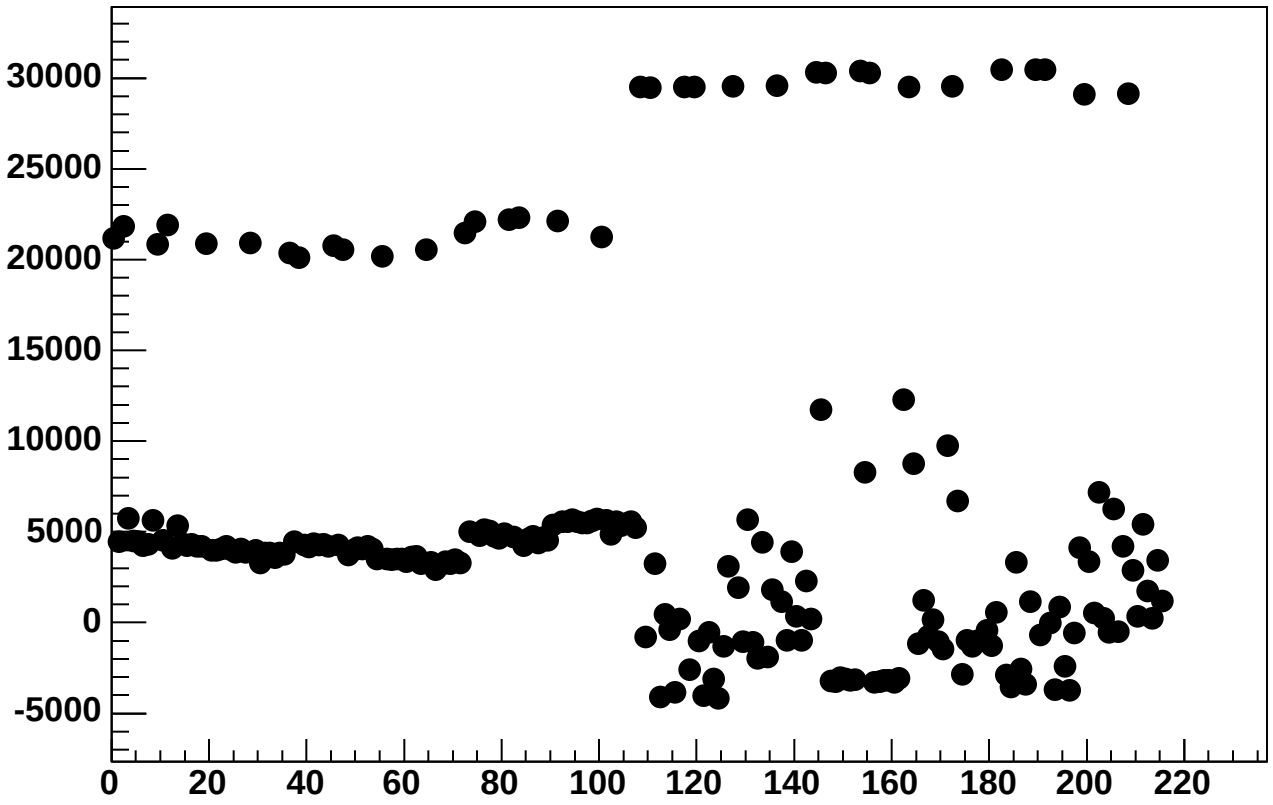
Enable 0, DAC=1600, Hold=60, ADC Mean vs 18*Chip+Chan



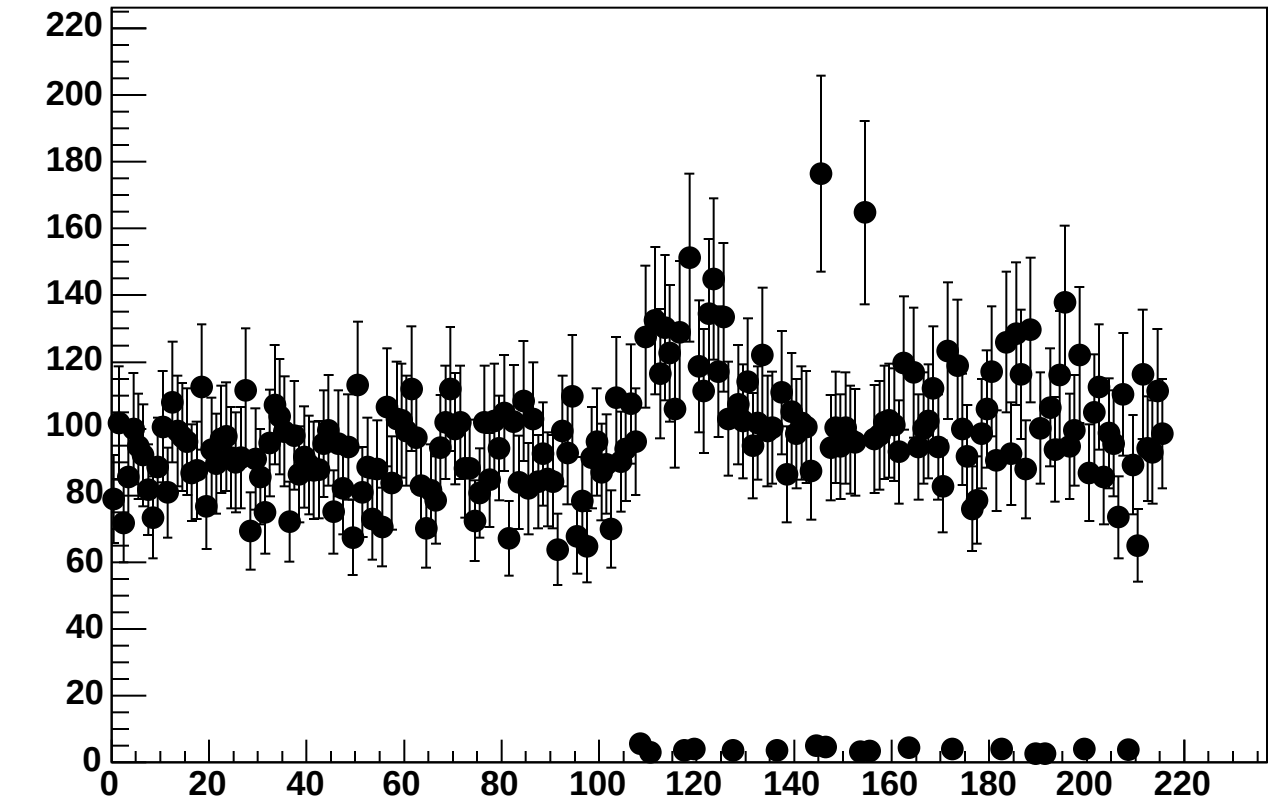
Enable 0, DAC=1600, Hold=60, ADC Noise vs 18*Chip+Chan



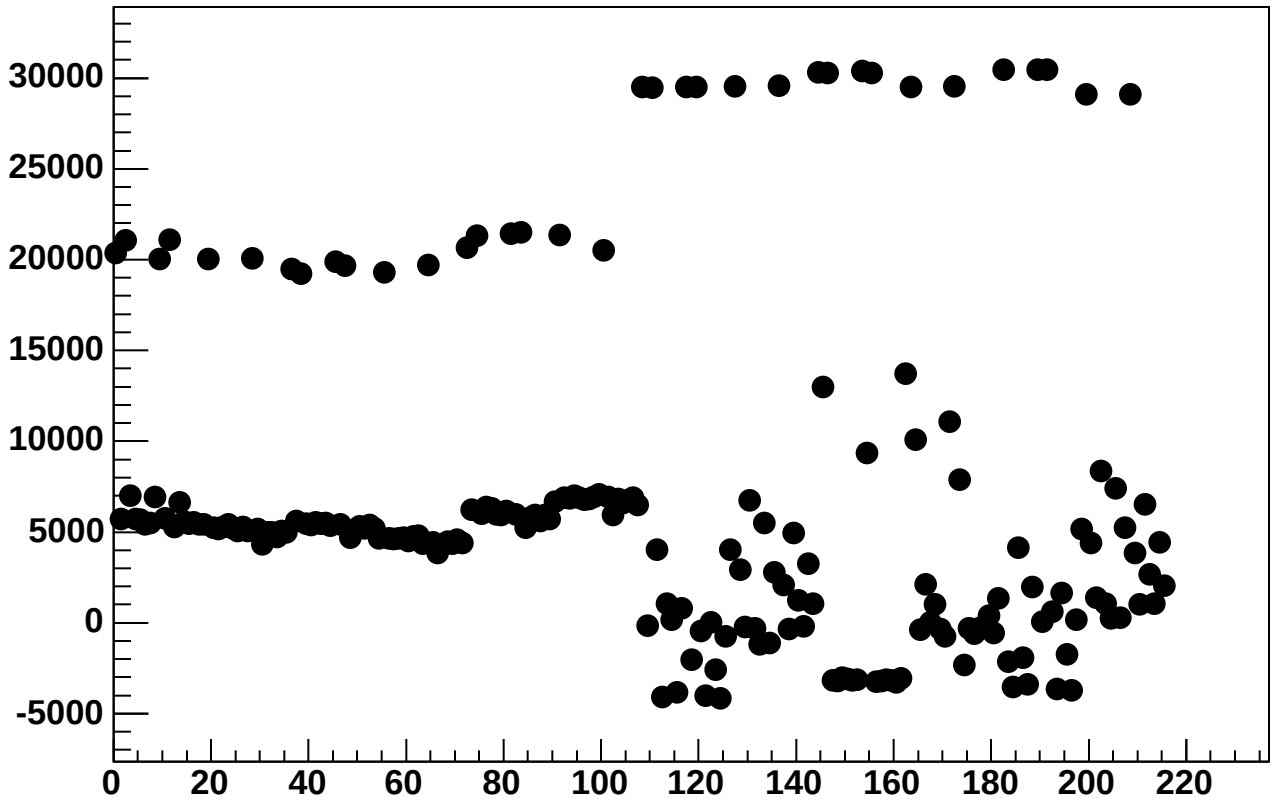
Enable 0, DAC=1600, Hold=65, ADC Mean vs 18*Chip+Chan



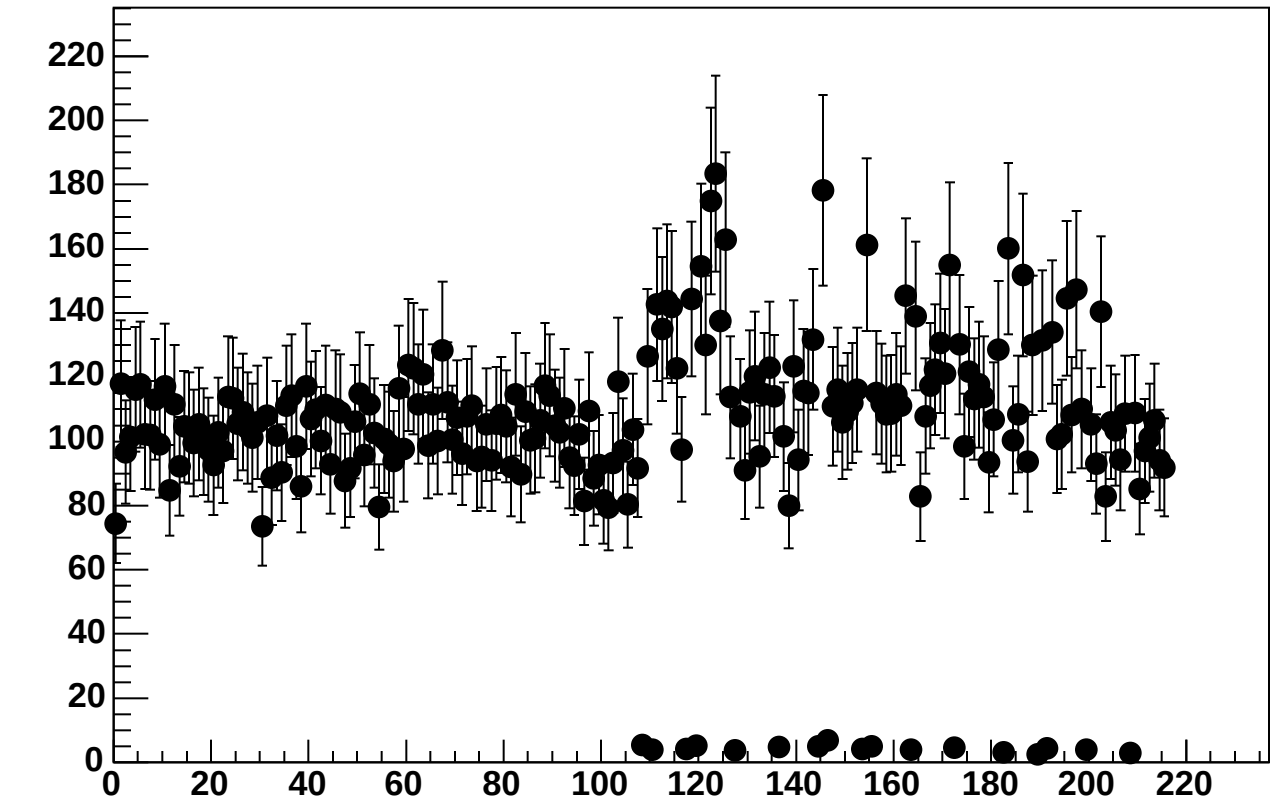
Enable 0, DAC=1600, Hold=65, ADC Noise vs 18*Chip+Chan



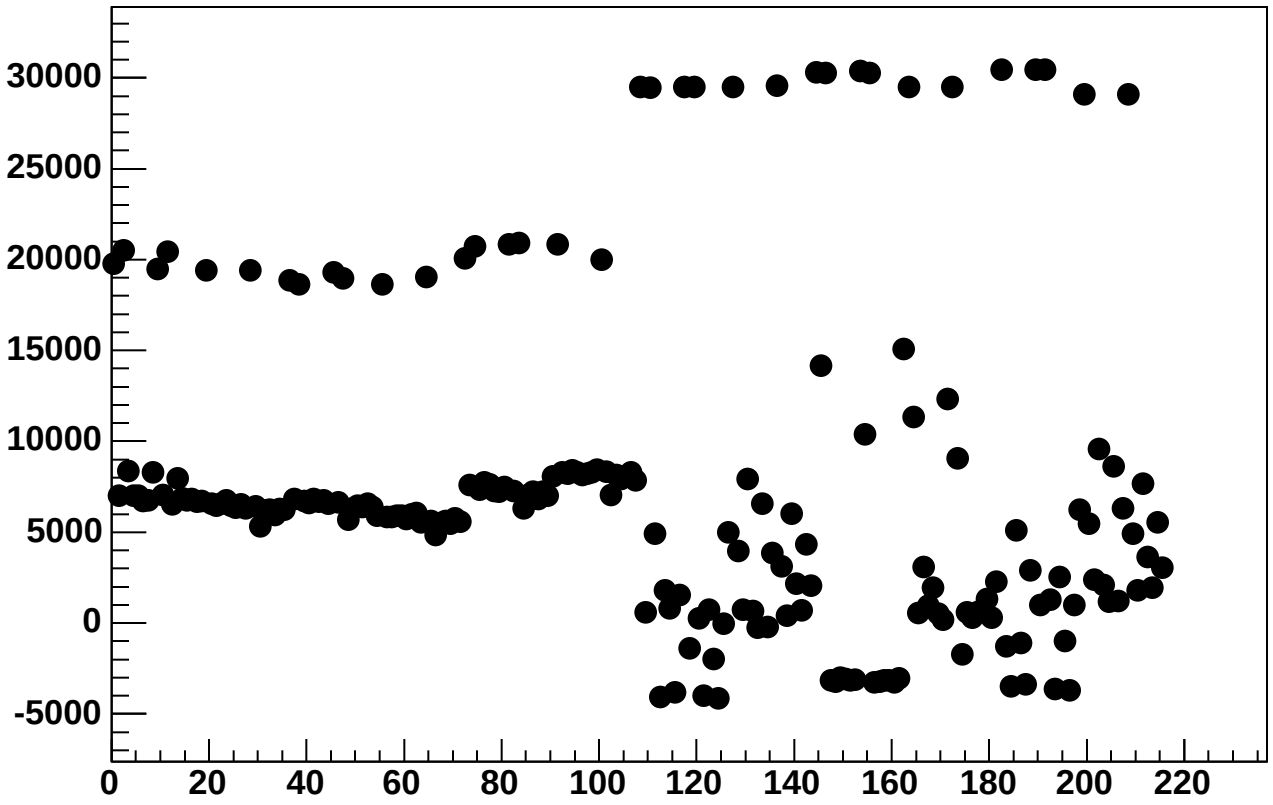
Enable 0, DAC=1600, Hold=70, ADC Mean vs 18*Chip+Chan



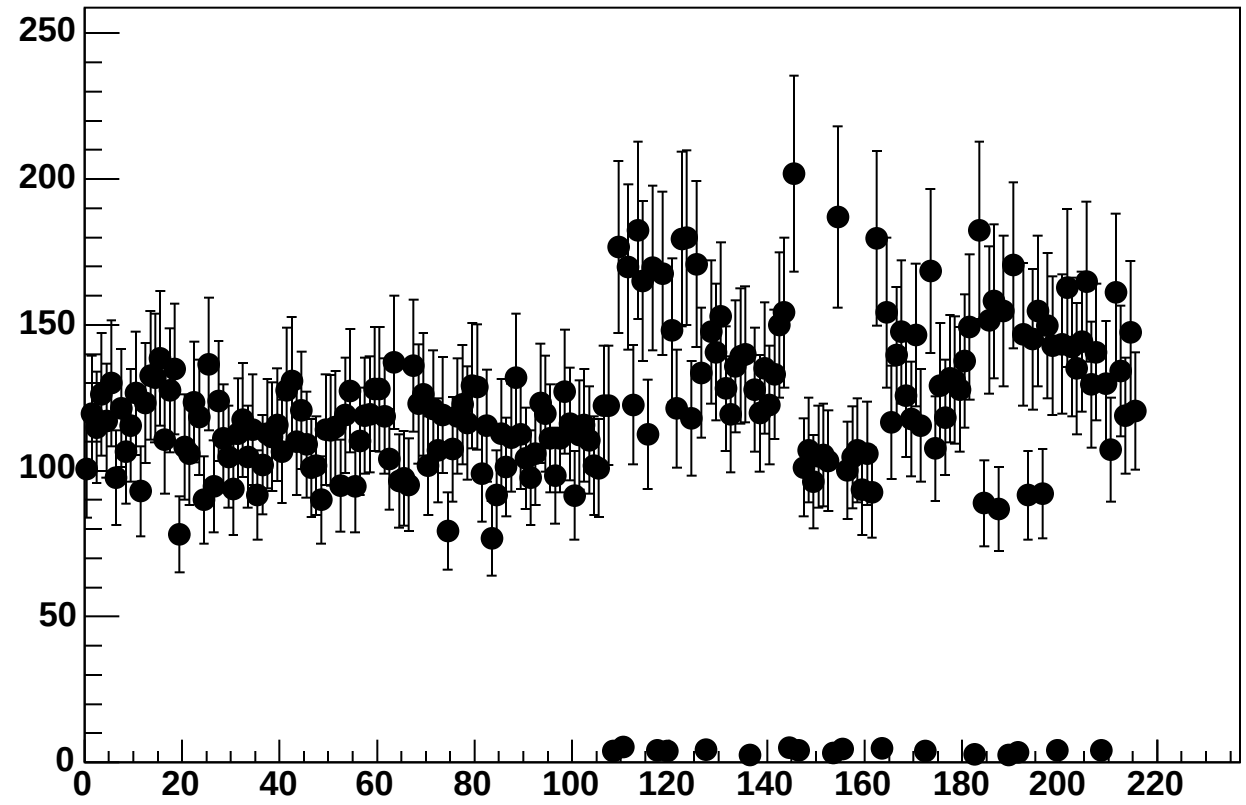
Enable 0, DAC=1600, Hold=70, ADC Noise vs 18*Chip+Chan



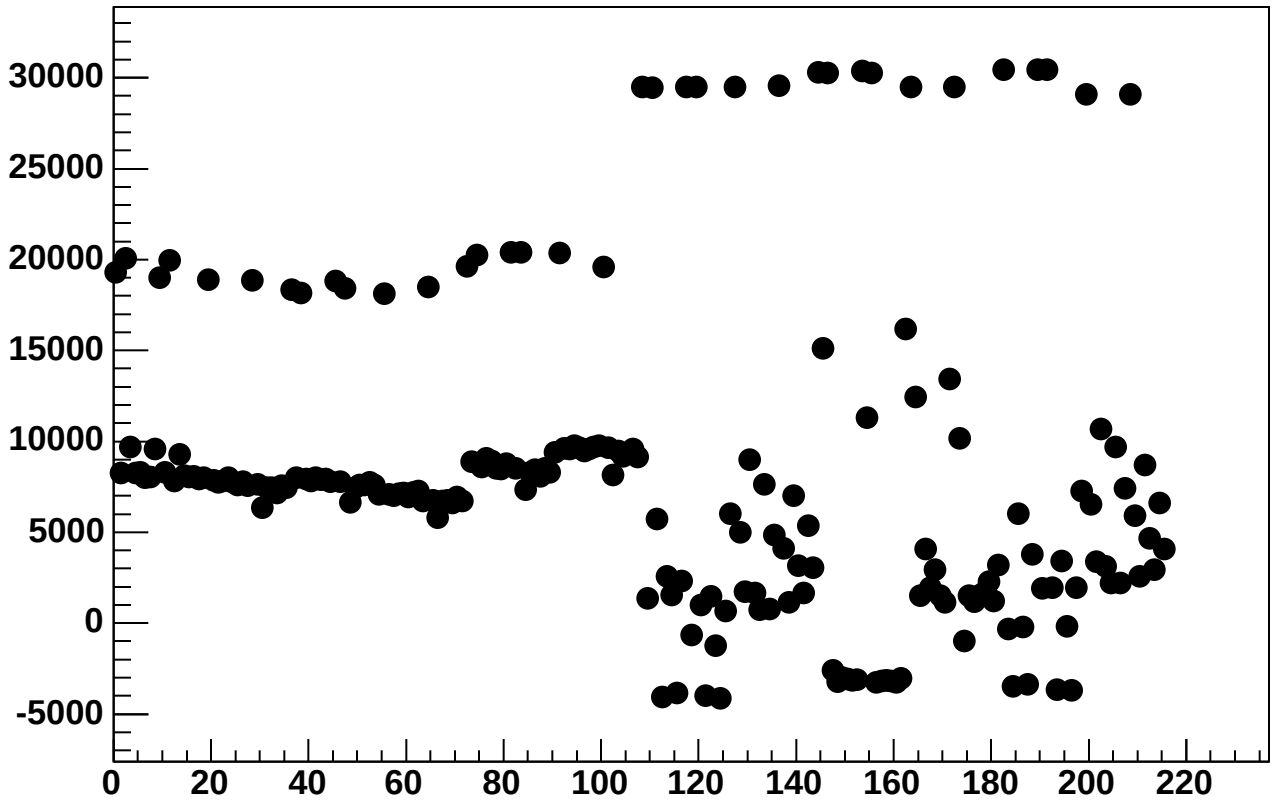
Enable 0, DAC=1600, Hold=75, ADC Mean vs 18*Chip+Chan



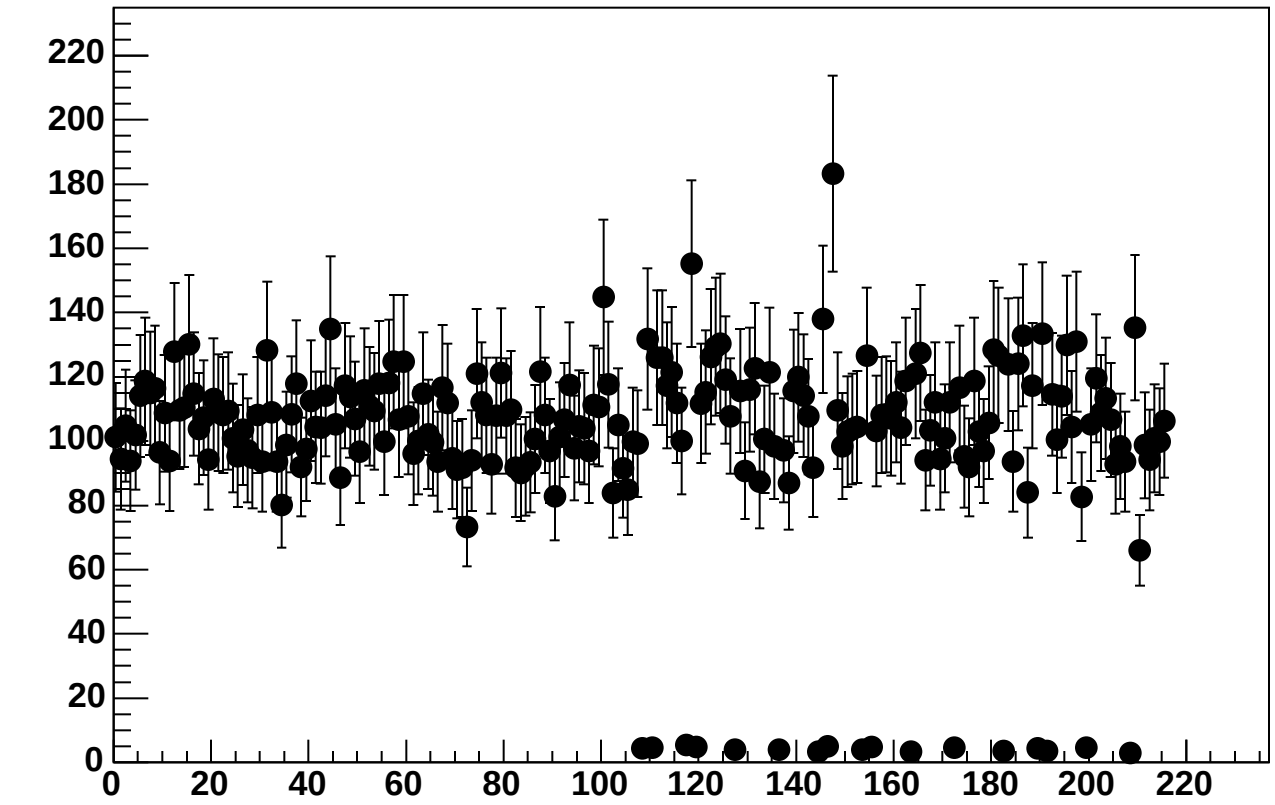
Enable 0, DAC=1600, Hold=75, ADC Noise vs 18*Chip+Chan



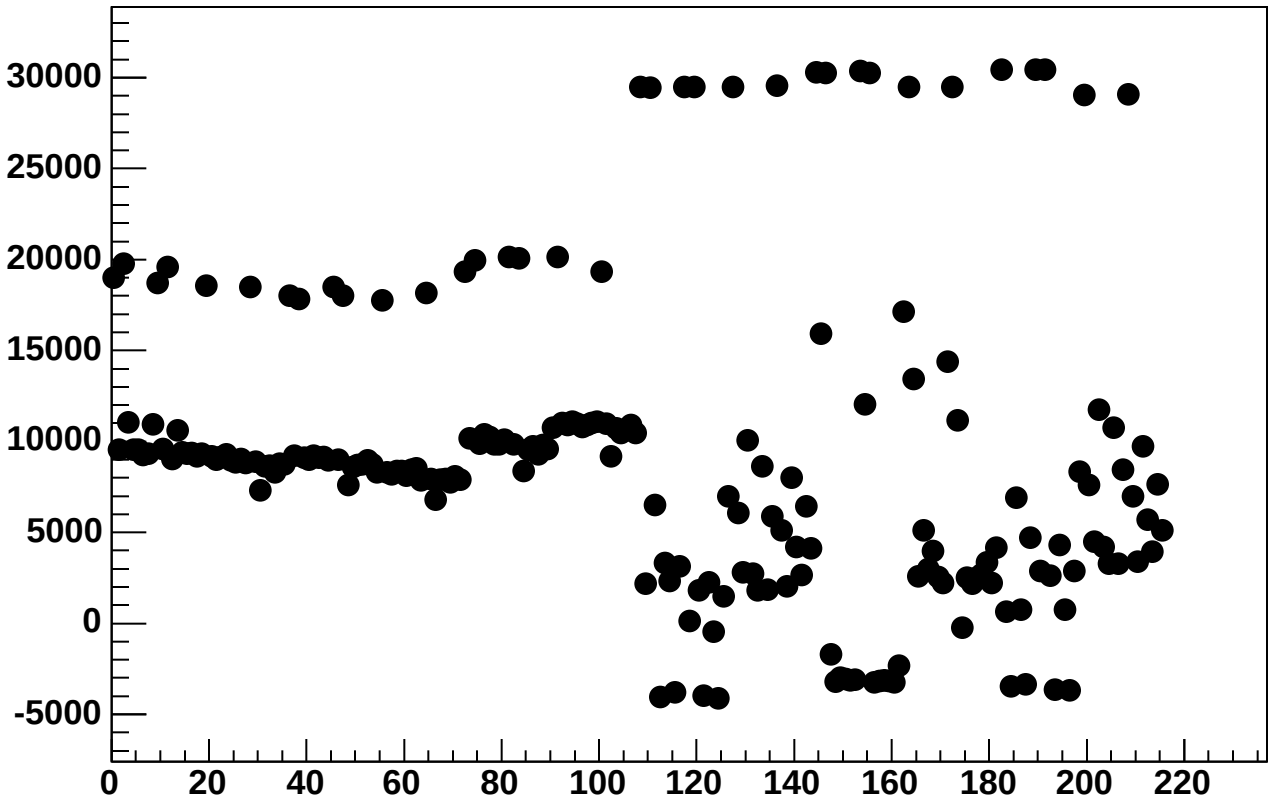
Enable 0, DAC=1600, Hold=80, ADC Mean vs 18*Chip+Chan



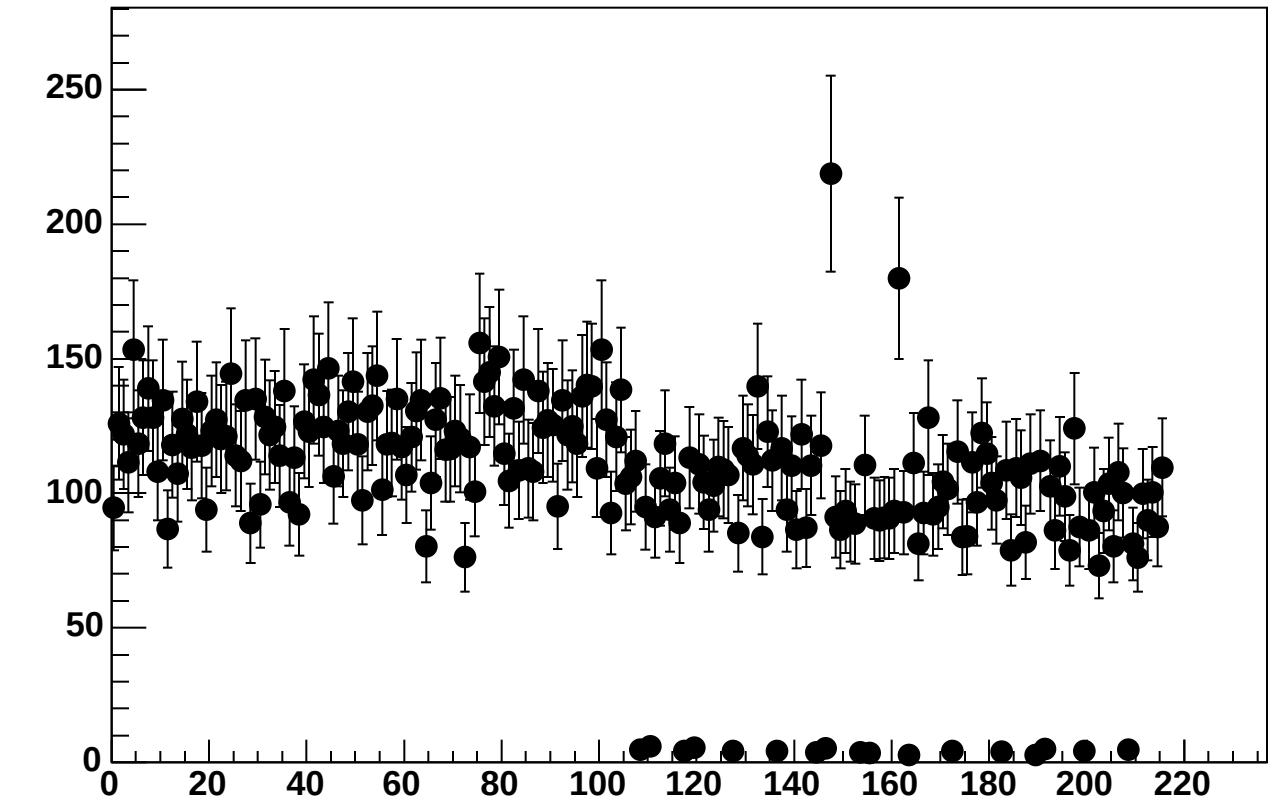
Enable 0, DAC=1600, Hold=80, ADC Noise vs 18*Chip+Chan



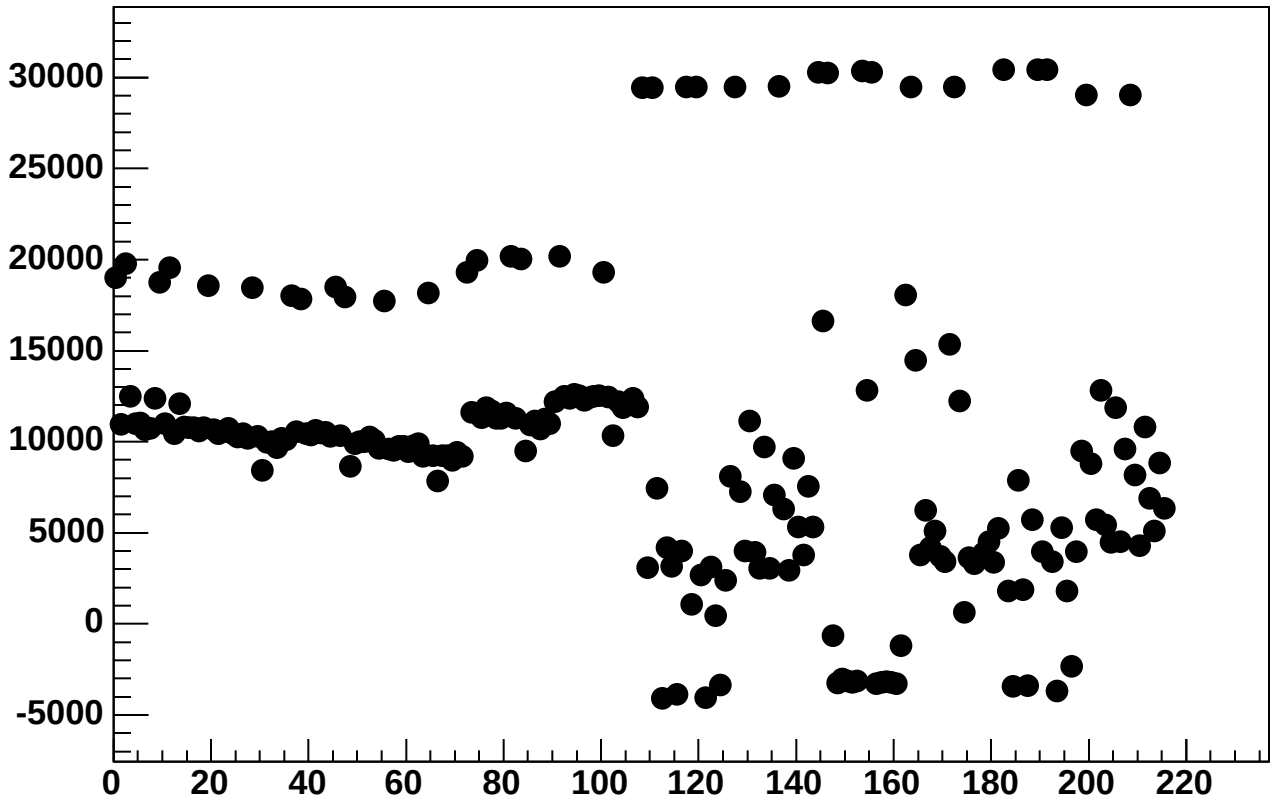
Enable 0, DAC=1600, Hold=85, ADC Mean vs 18*Chip+Chan



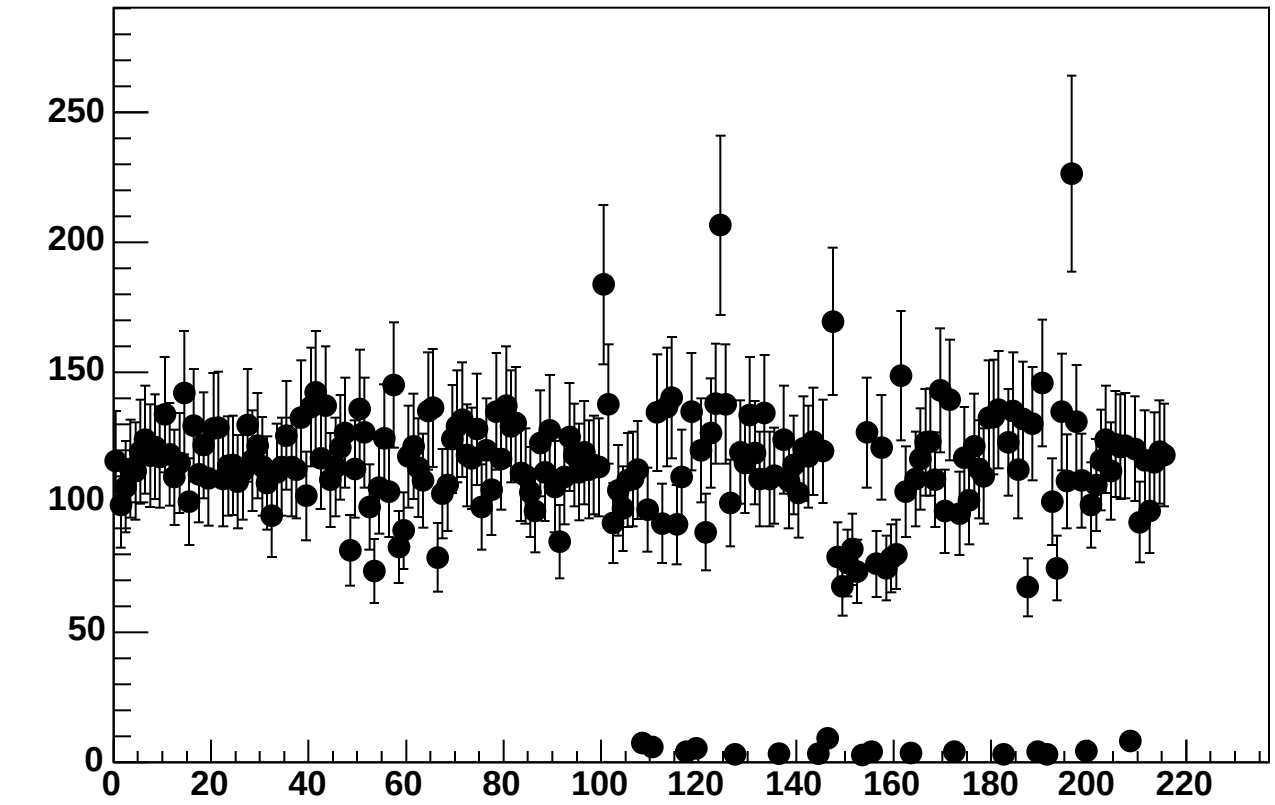
Enable 0, DAC=1600, Hold=85, ADC Noise vs 18*Chip+Chan



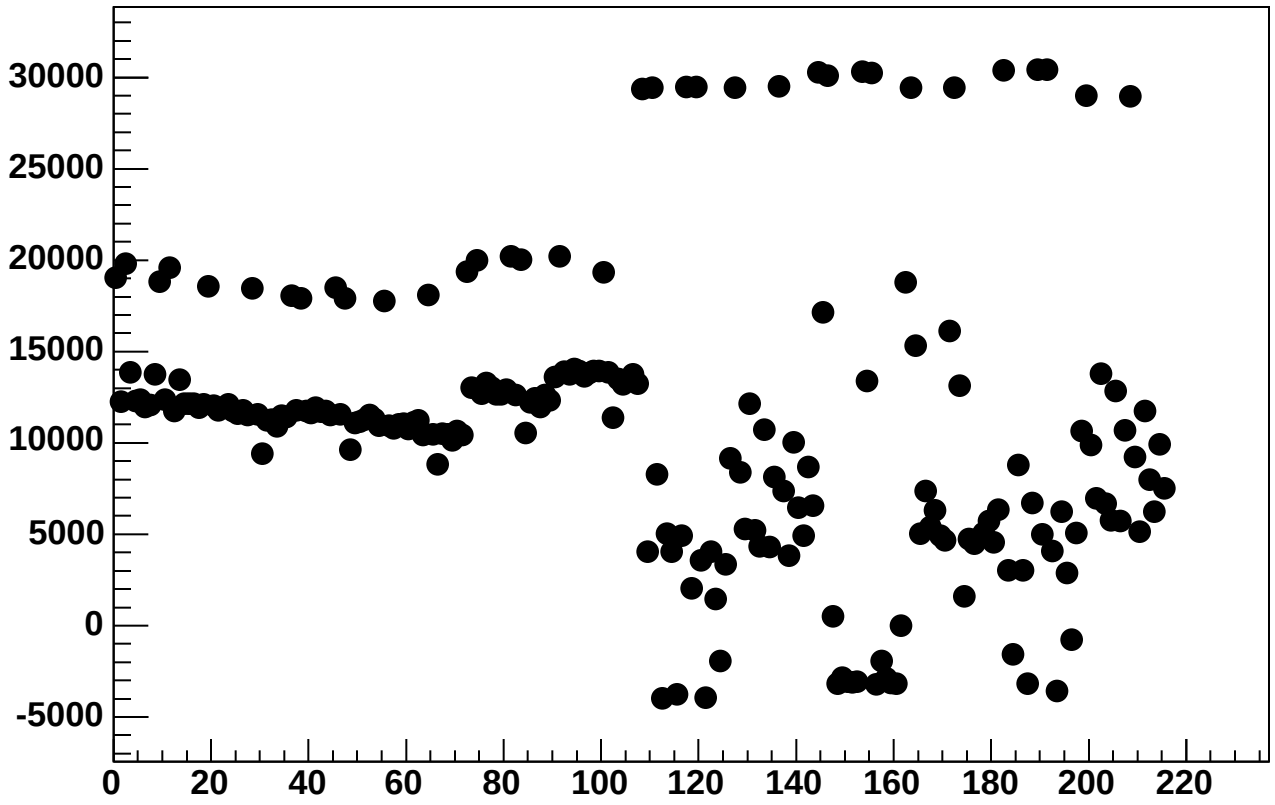
Enable 0, DAC=1600, Hold=90, ADC Mean vs 18*Chip+Chan



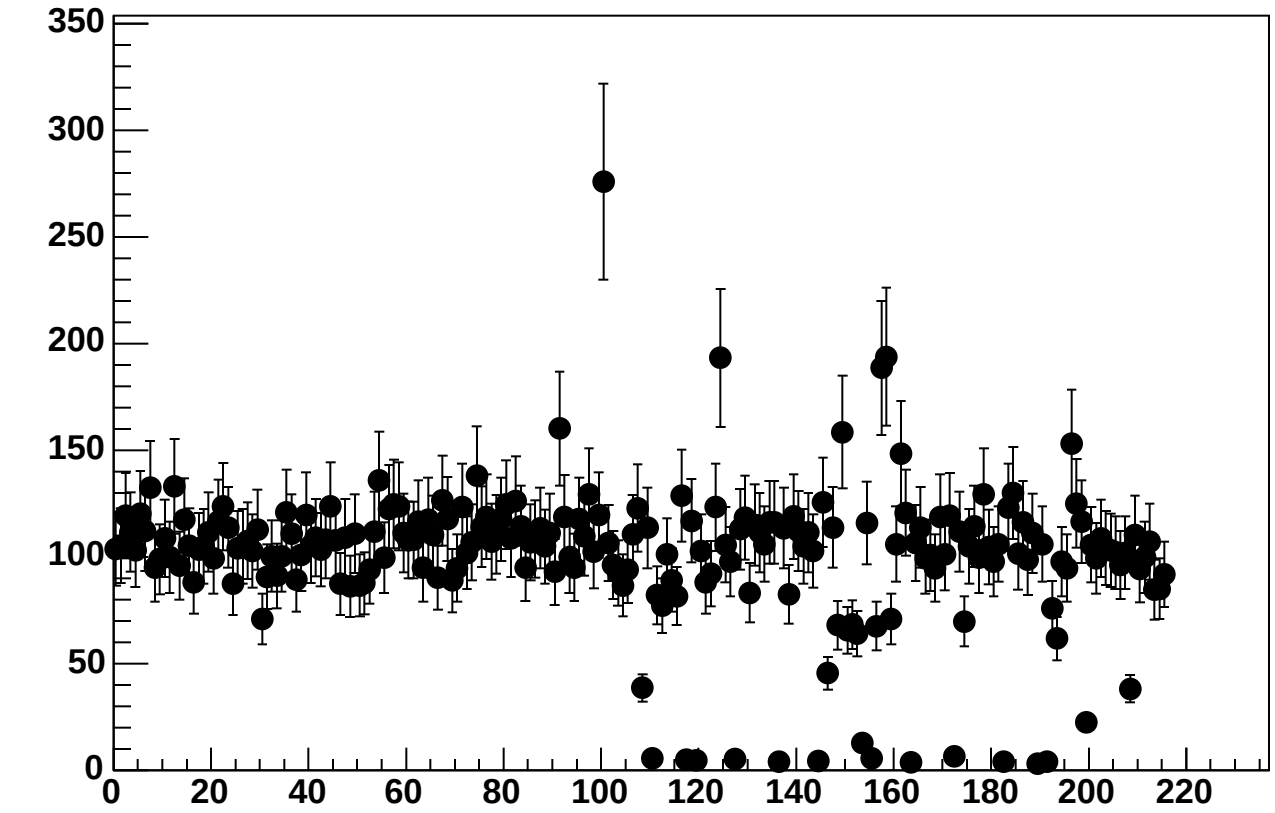
Enable 0, DAC=1600, Hold=90, ADC Noise vs 18*Chip+Chan



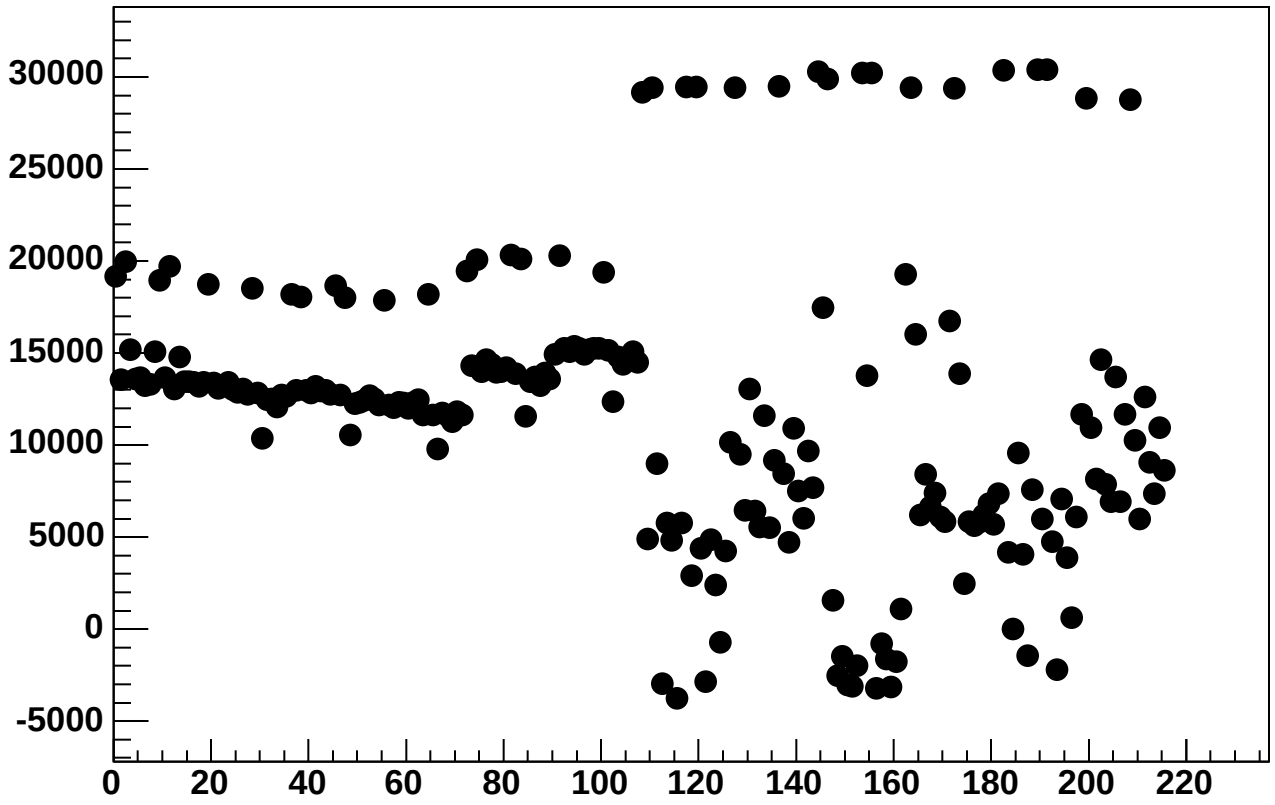
Enable 0, DAC=1600, Hold=95, ADC Mean vs 18*Chip+Chan



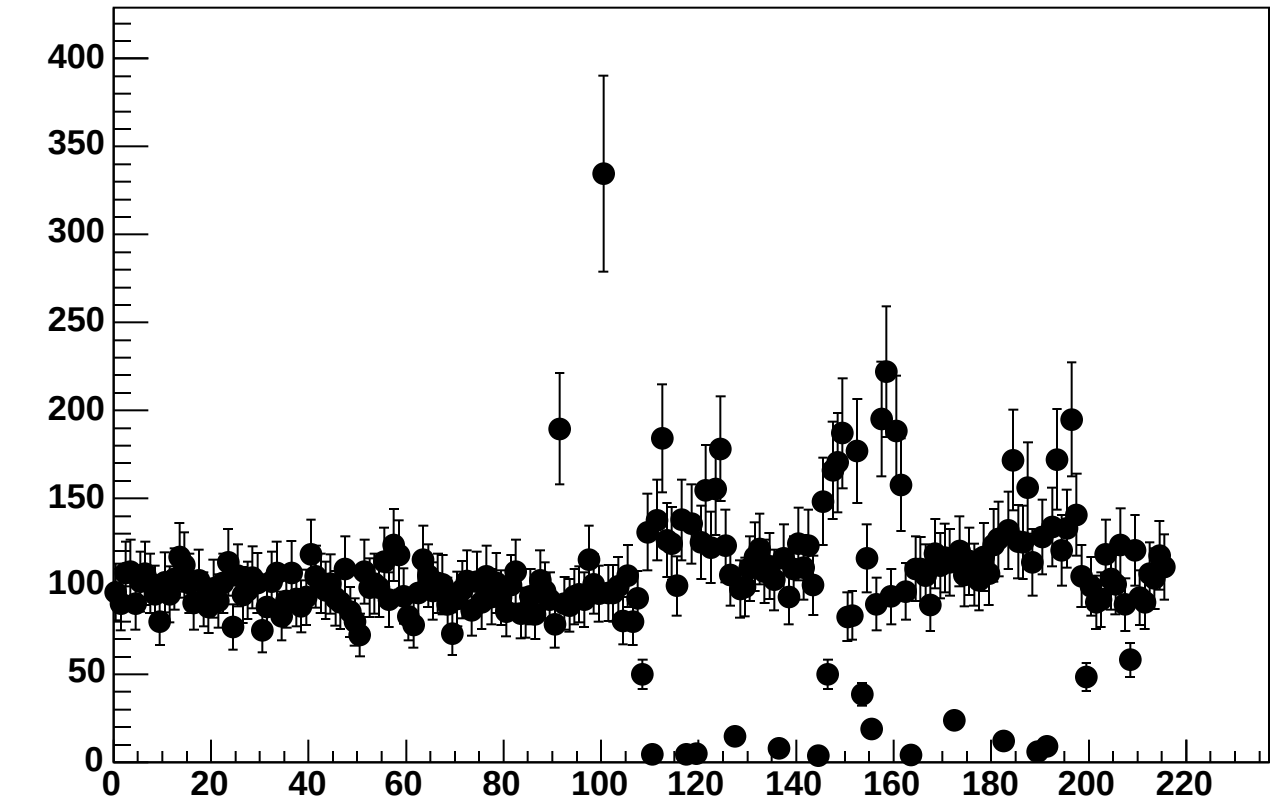
Enable 0, DAC=1600, Hold=95, ADC Noise vs 18*Chip+Chan



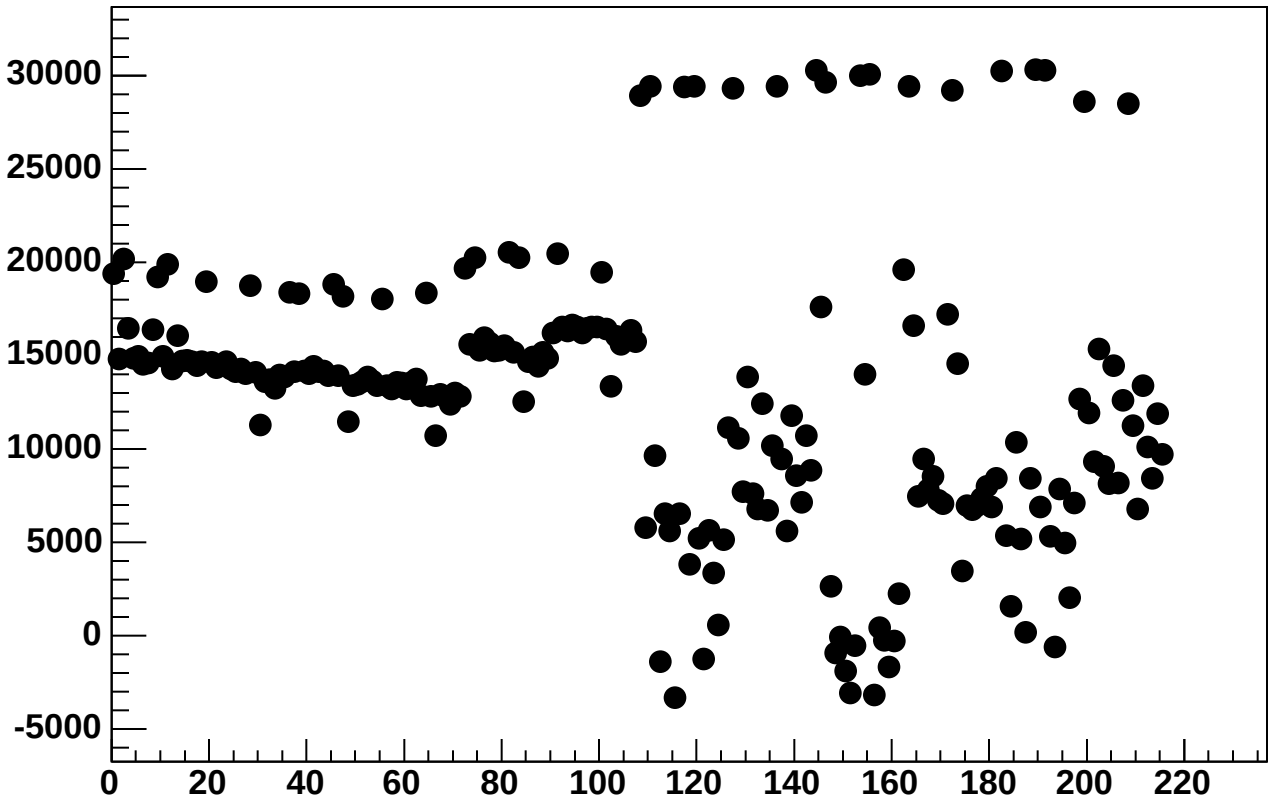
Enable 0, DAC=1600, Hold=100, ADC Mean vs 18*Chip+Chan



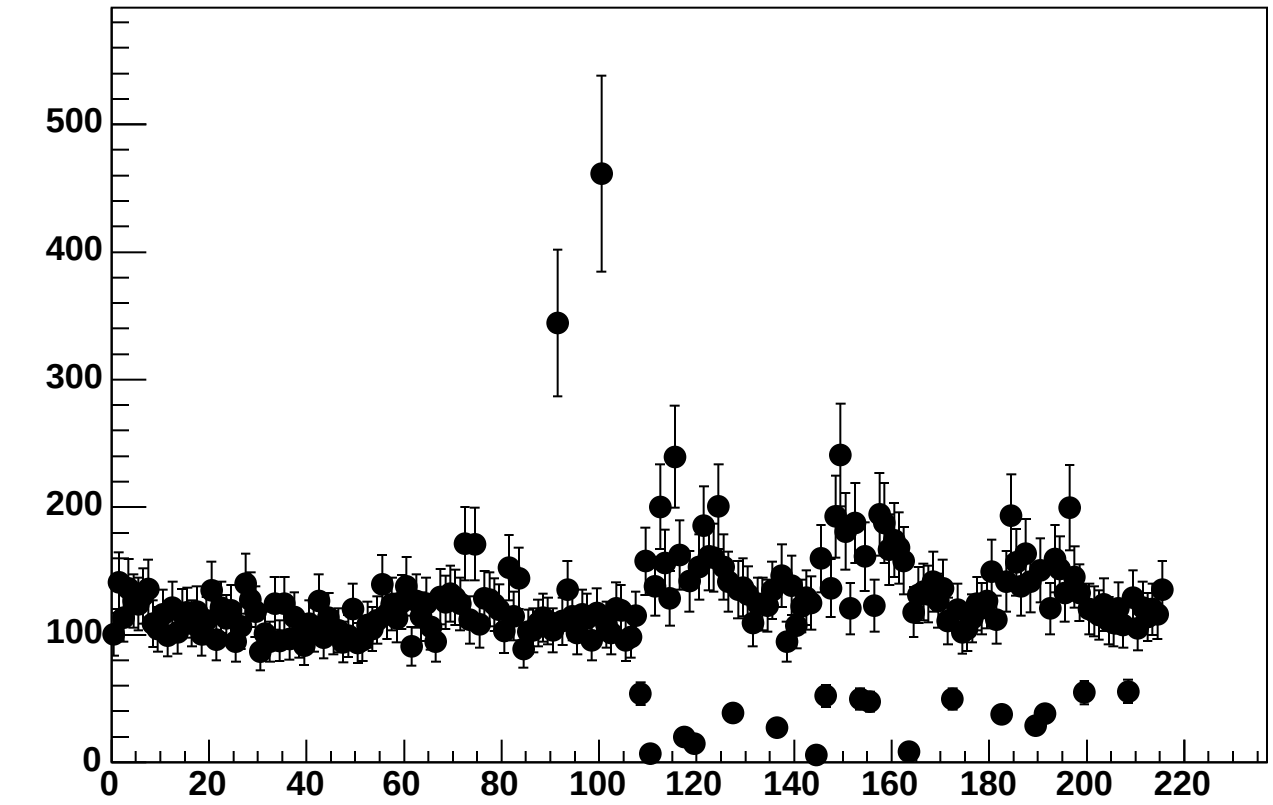
Enable 0, DAC=1600, Hold=100, ADC Noise vs 18*Chip+Chan



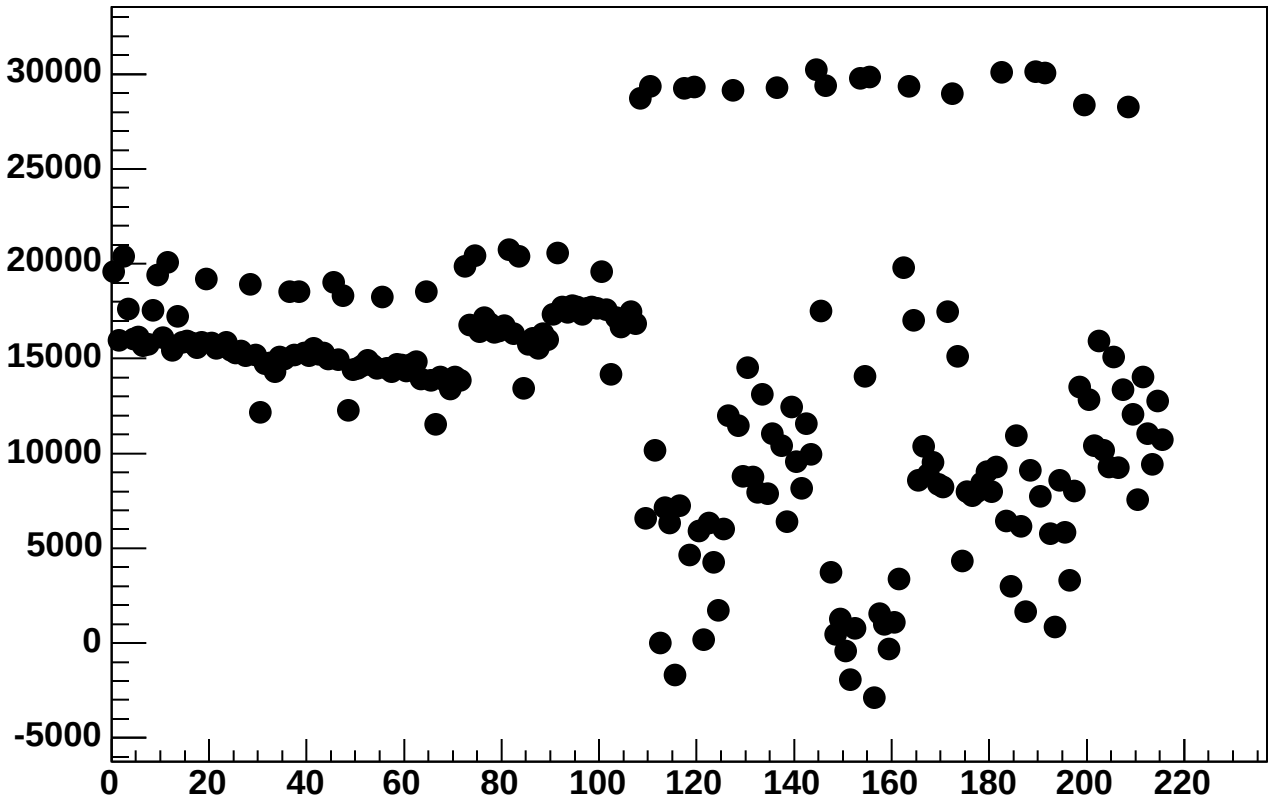
Enable 0, DAC=1600, Hold=105, ADC Mean vs 18*Chip+Chan



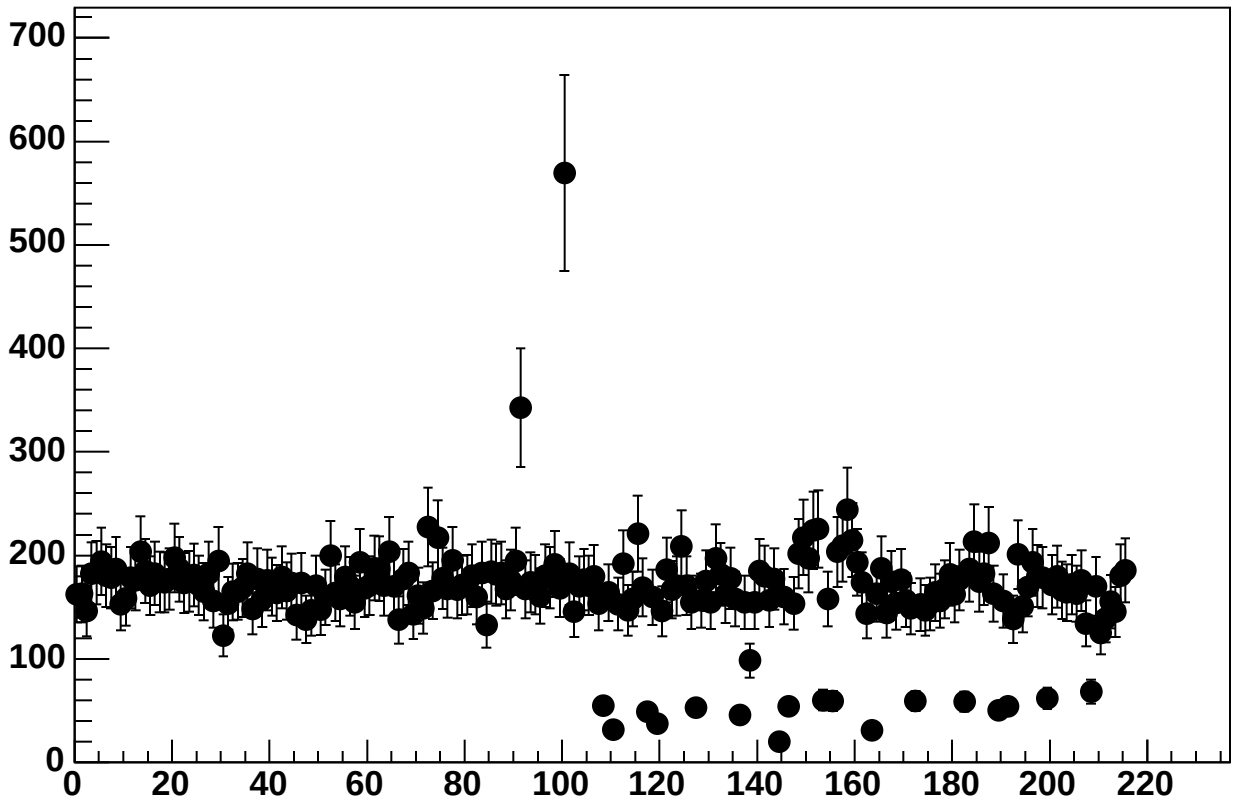
Enable 0, DAC=1600, Hold=105, ADC Noise vs 18*Chip+Chan



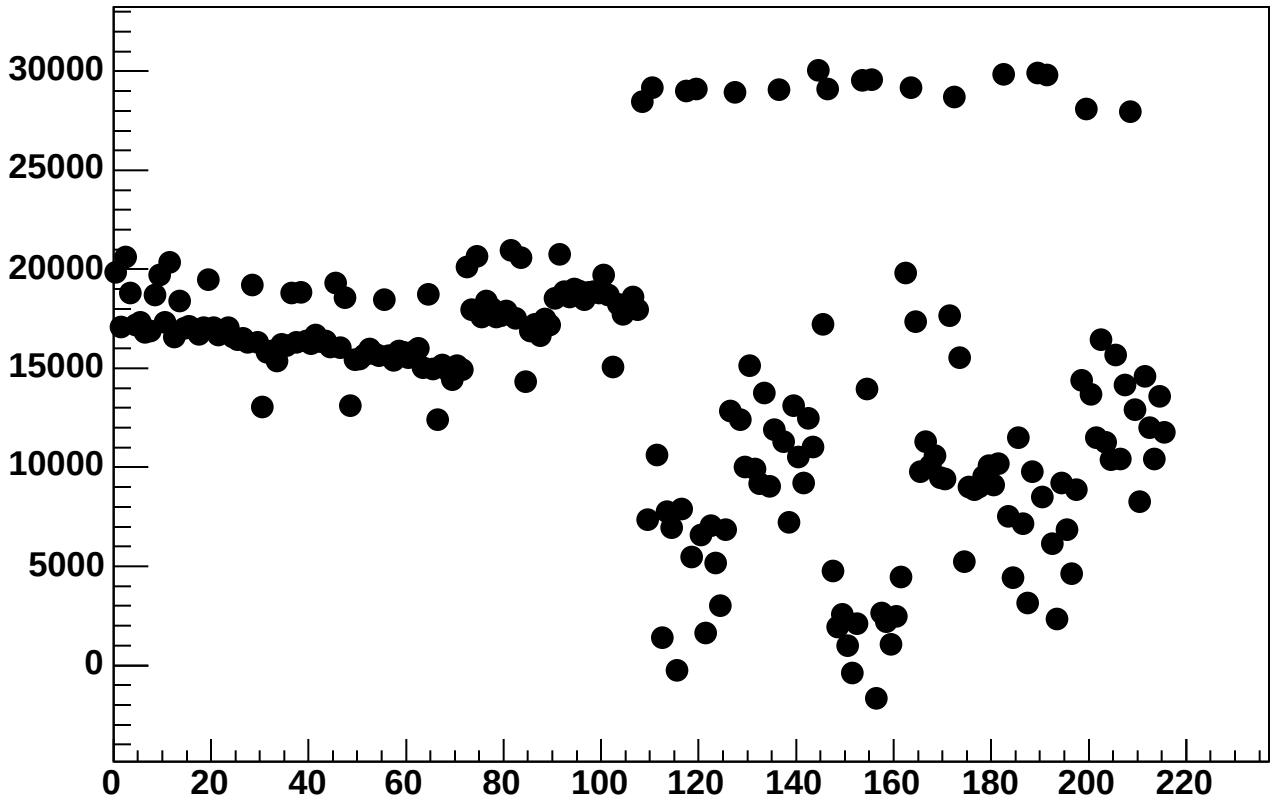
Enable 0, DAC=1600, Hold=110, ADC Mean vs 18*Chip+Chan



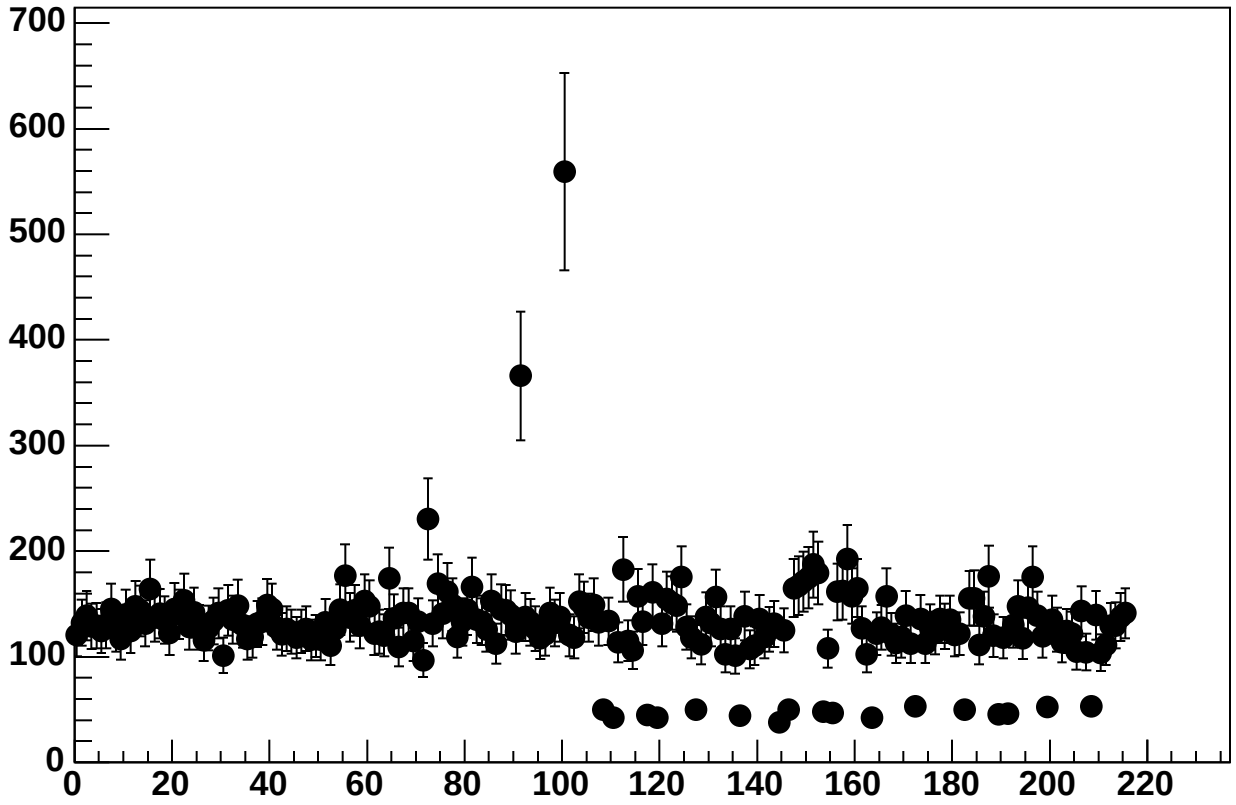
Enable 0, DAC=1600, Hold=110, ADC Noise vs 18*Chip+Chan



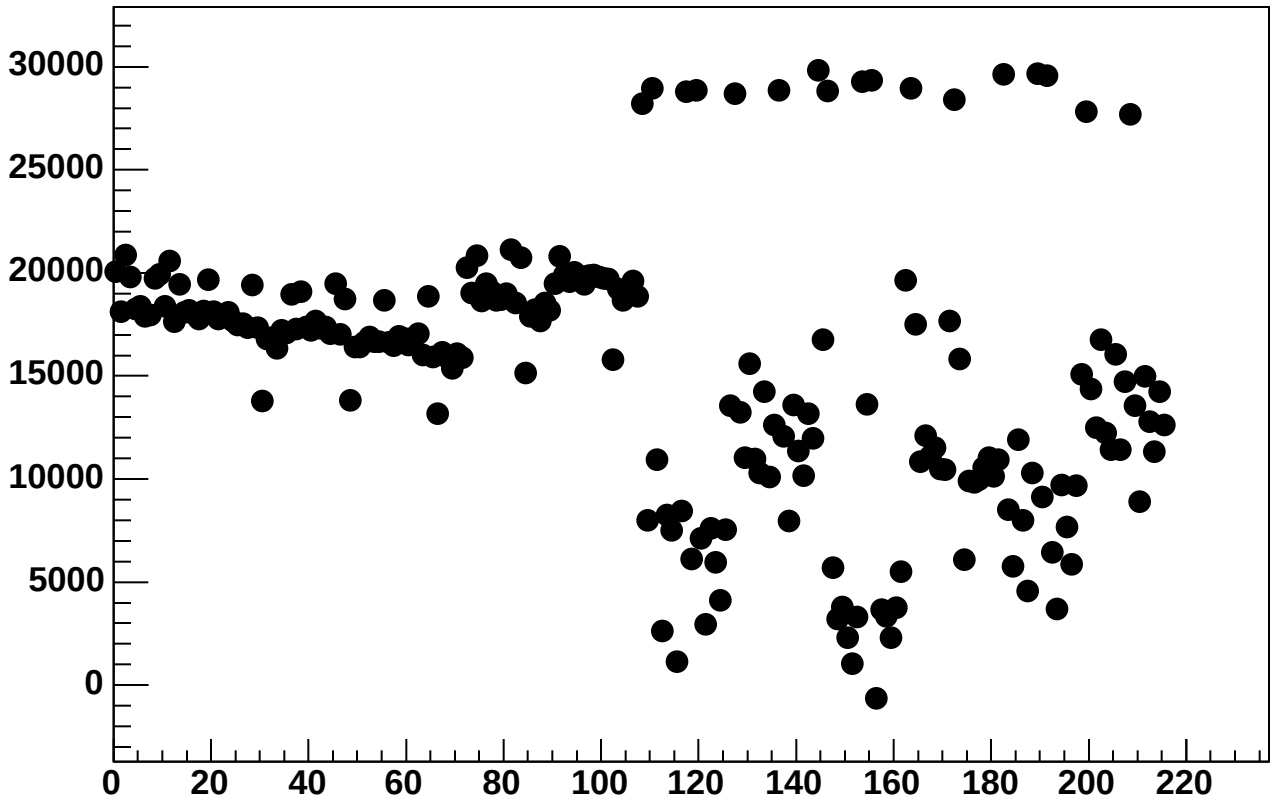
Enable 0, DAC=1600, Hold=115, ADC Mean vs 18*Chip+Chan



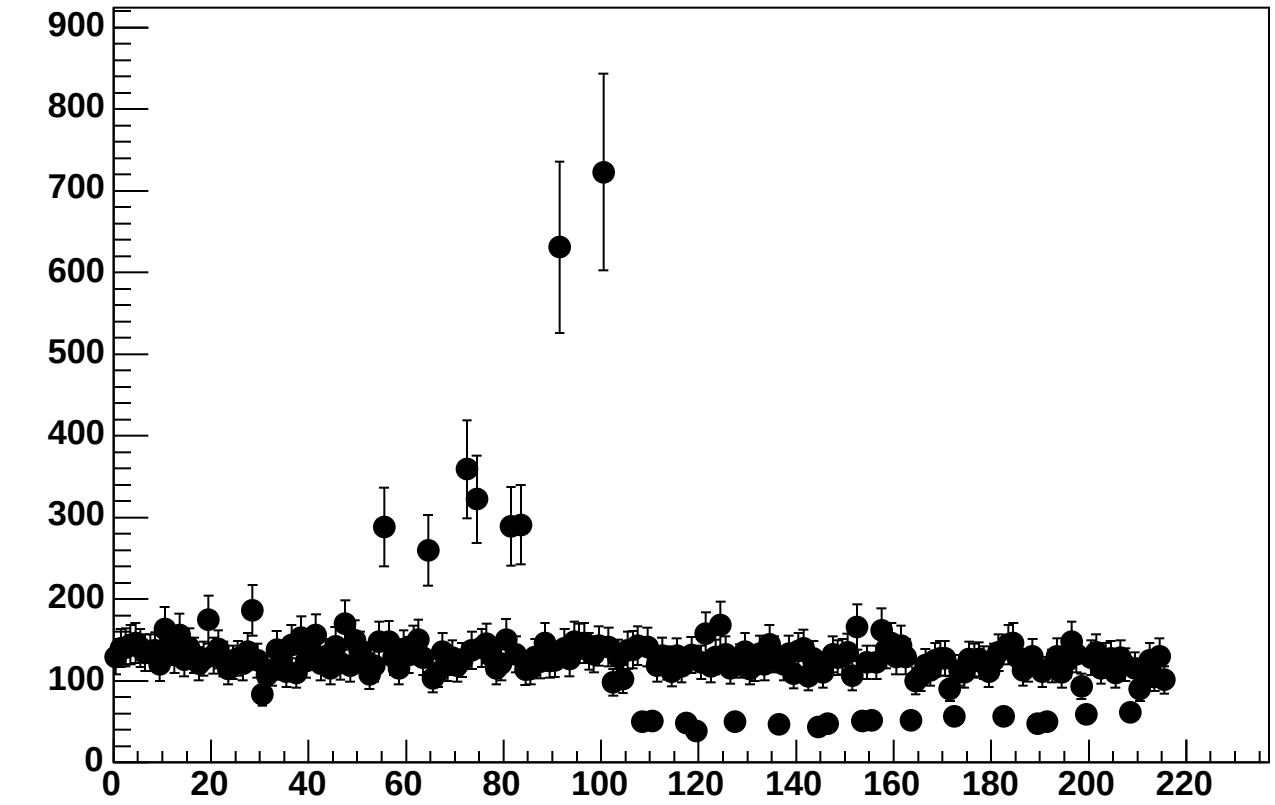
Enable 0, DAC=1600, Hold=115, ADC Noise vs 18*Chip+Chan



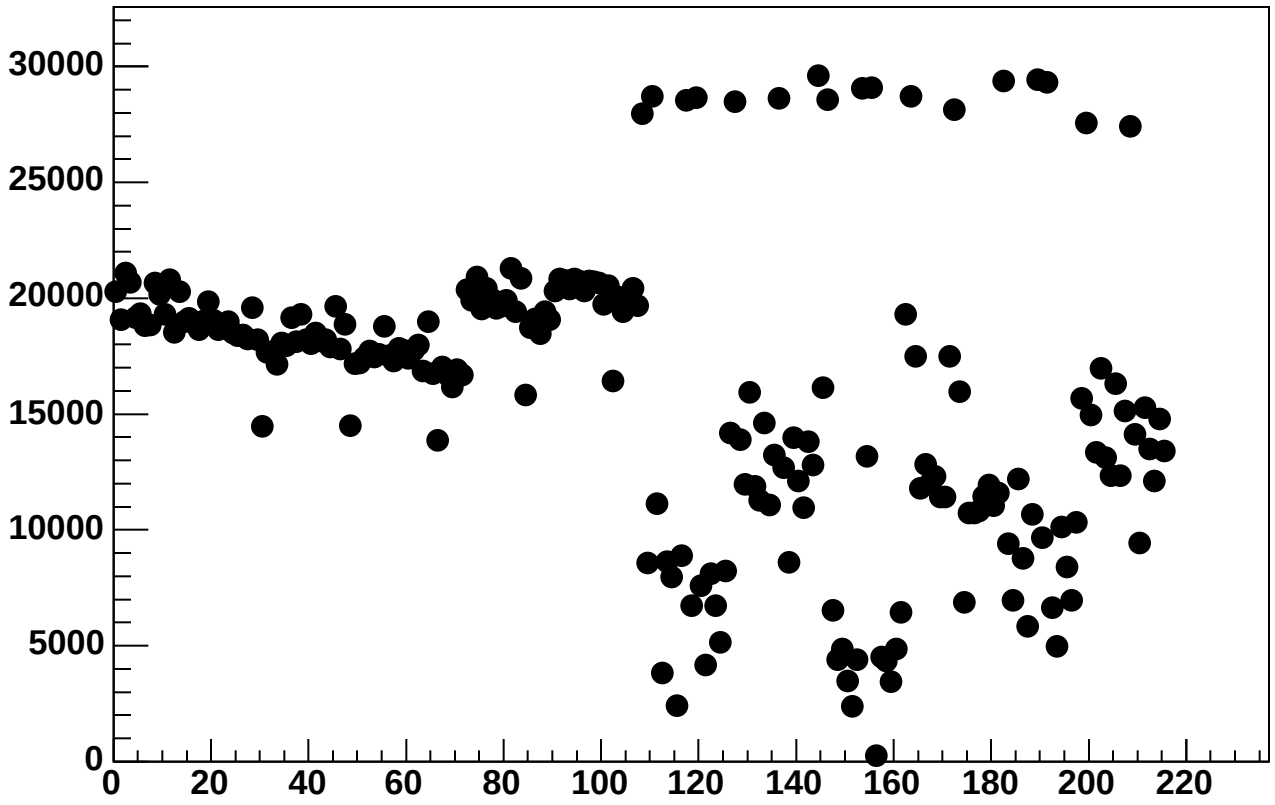
Enable 0, DAC=1600, Hold=120, ADC Mean vs 18*Chip+Chan



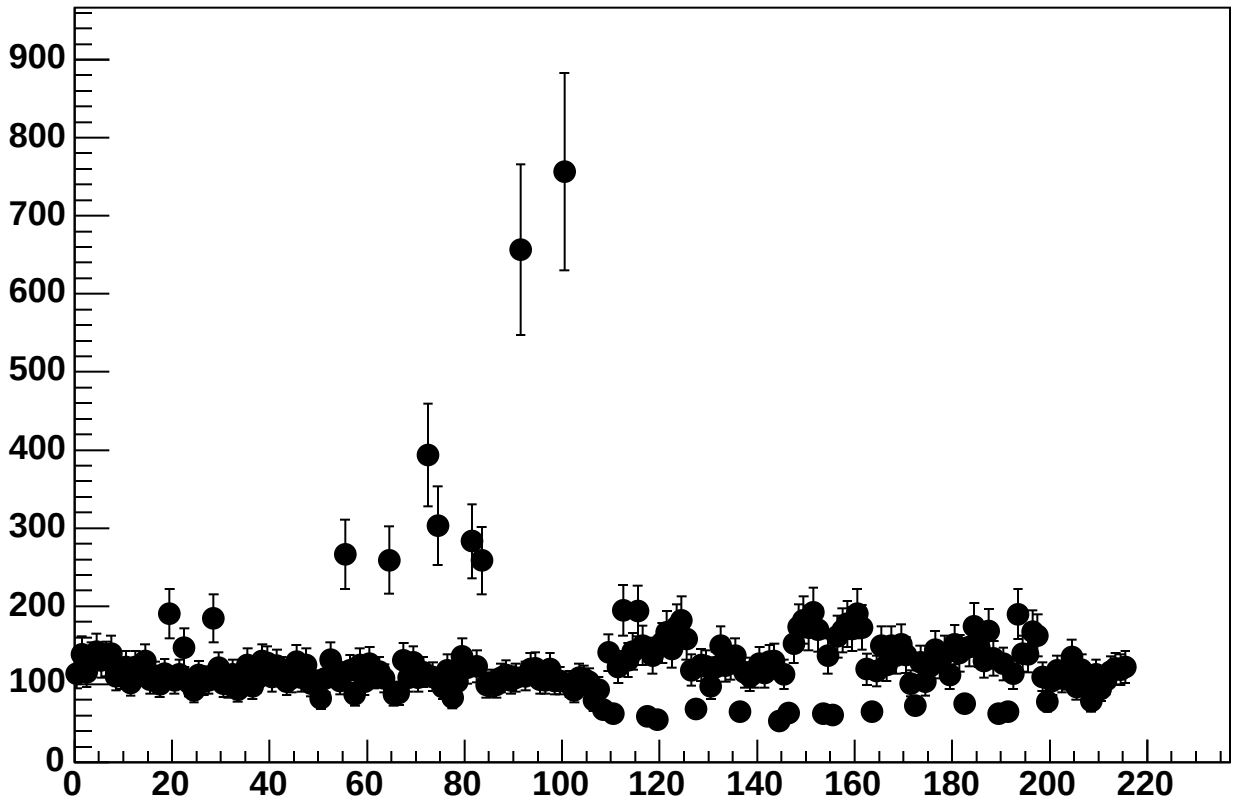
Enable 0, DAC=1600, Hold=120, ADC Noise vs 18*Chip+Chan



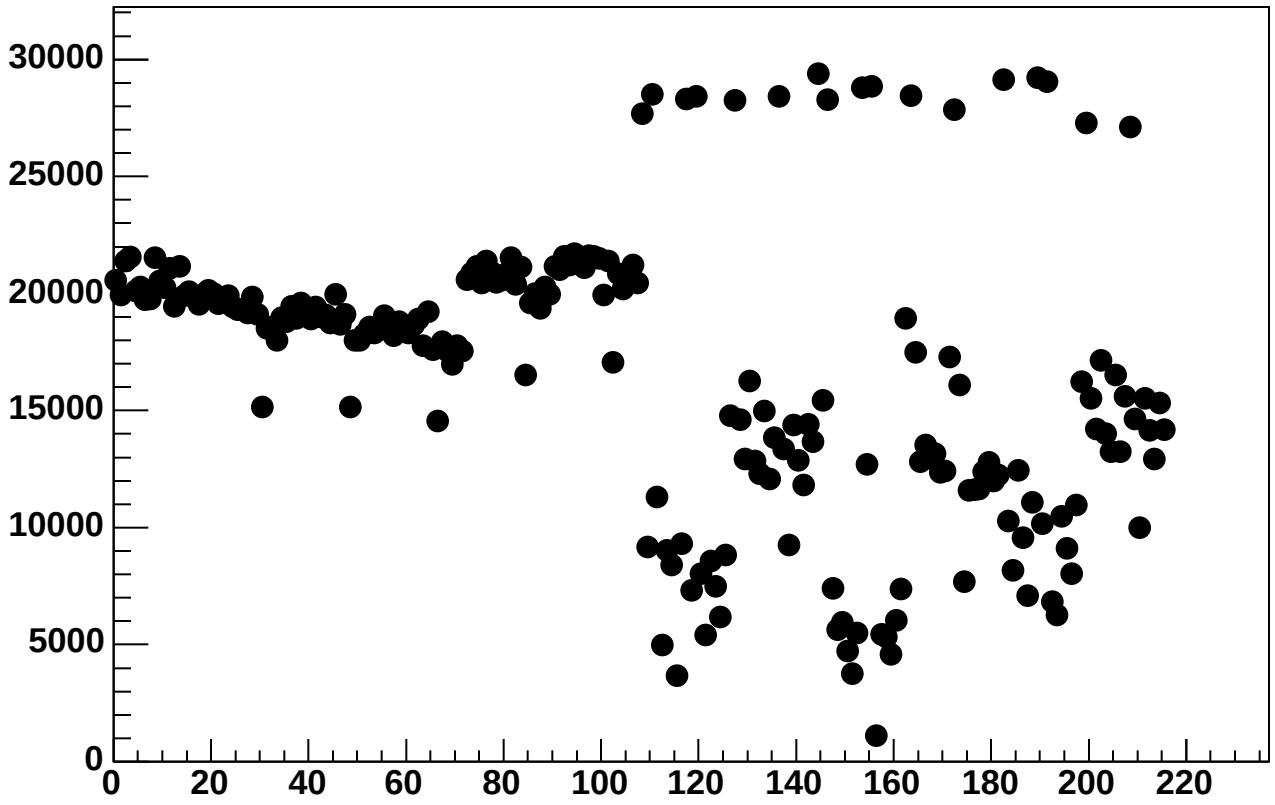
Enable 0, DAC=1600, Hold=125, ADC Mean vs 18*Chip+Chan



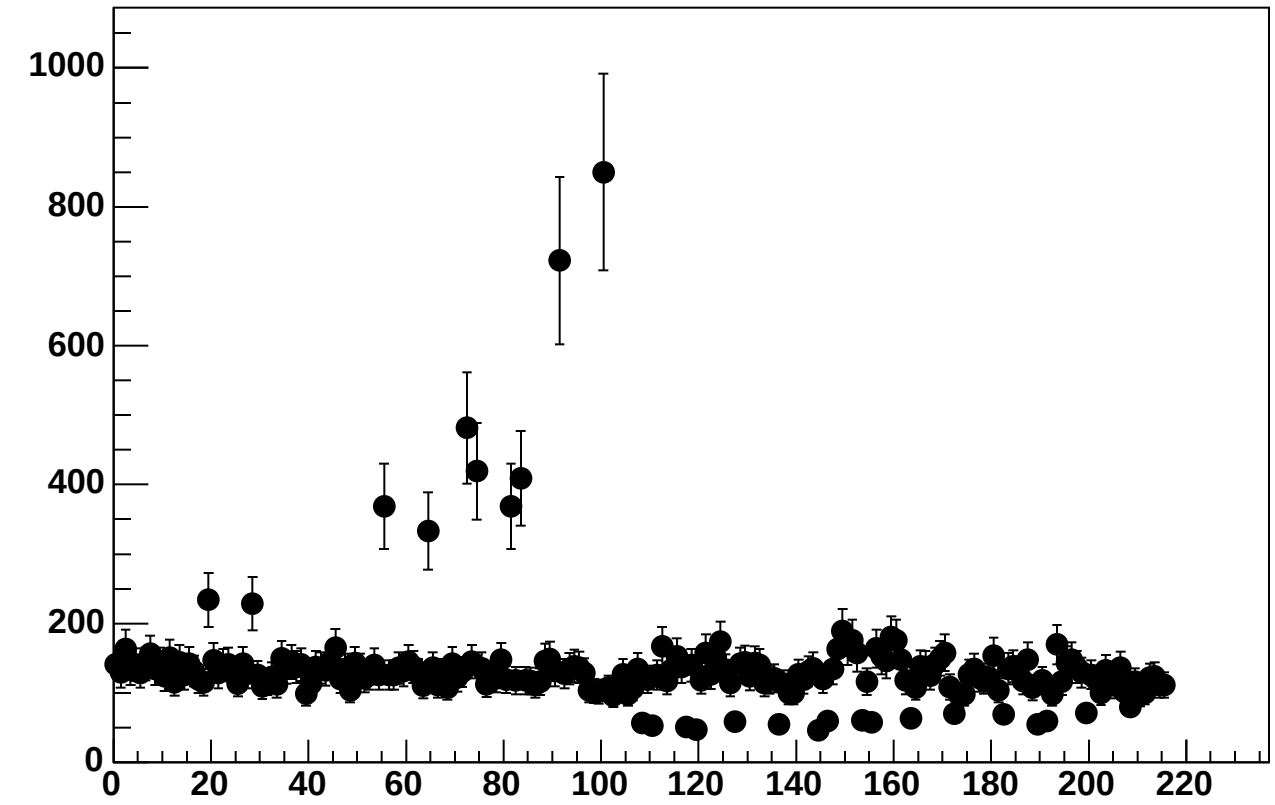
Enable 0, DAC=1600, Hold=125, ADC Noise vs 18*Chip+Chan



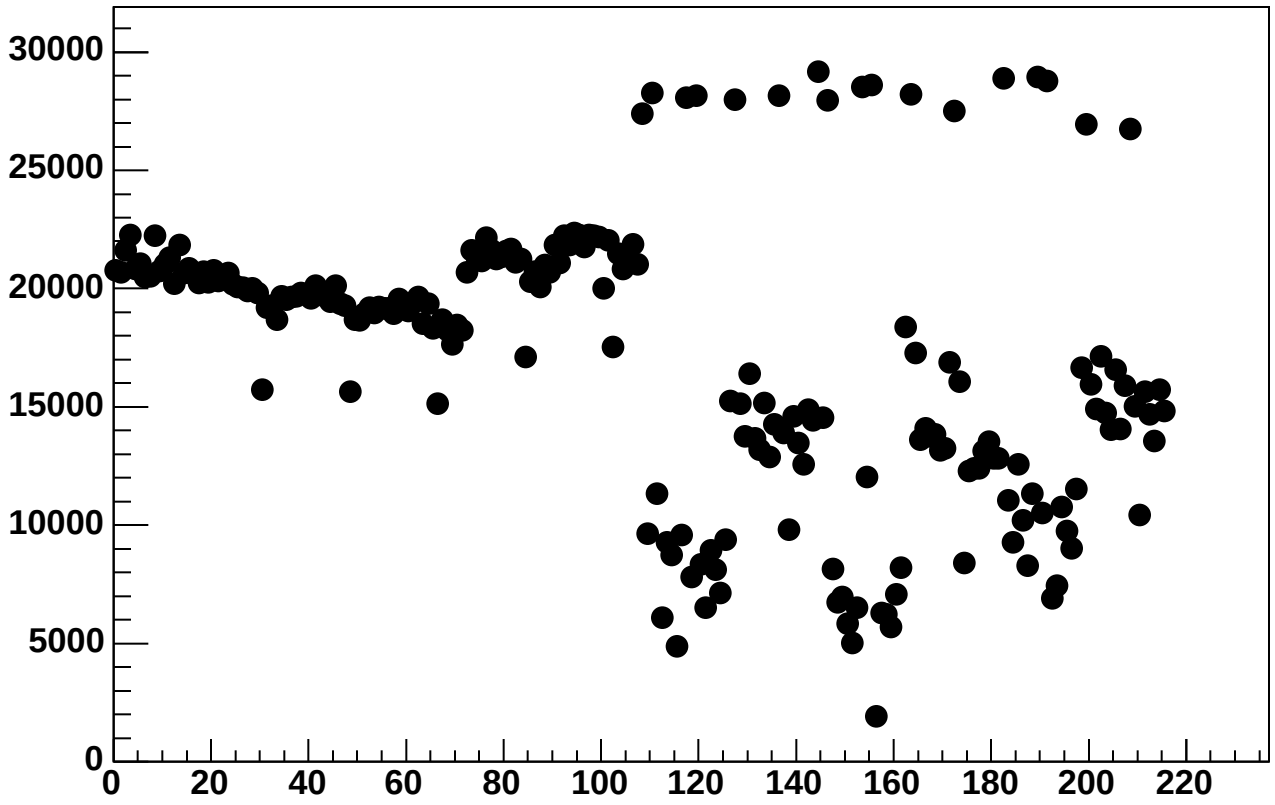
Enable 0, DAC=1600, Hold=130, ADC Mean vs 18*Chip+Chan



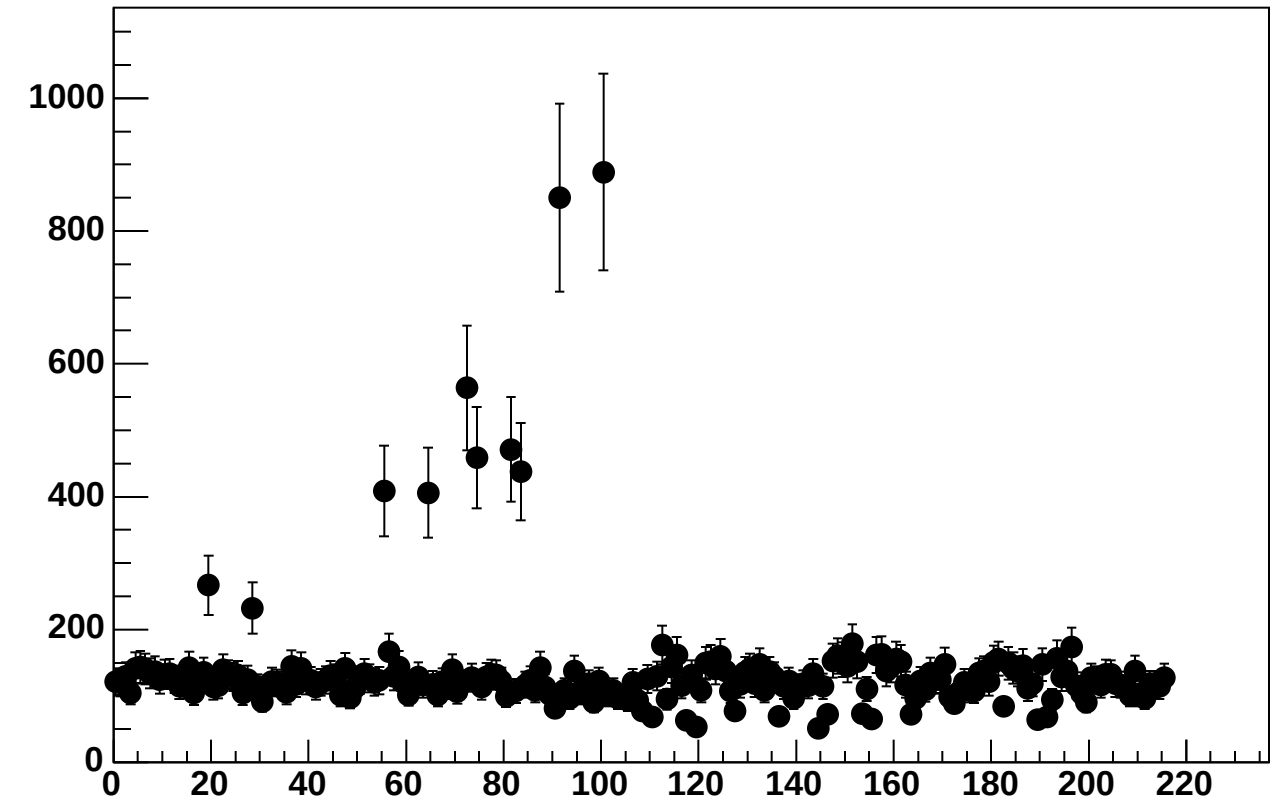
Enable 0, DAC=1600, Hold=130, ADC Noise vs 18*Chip+Chan



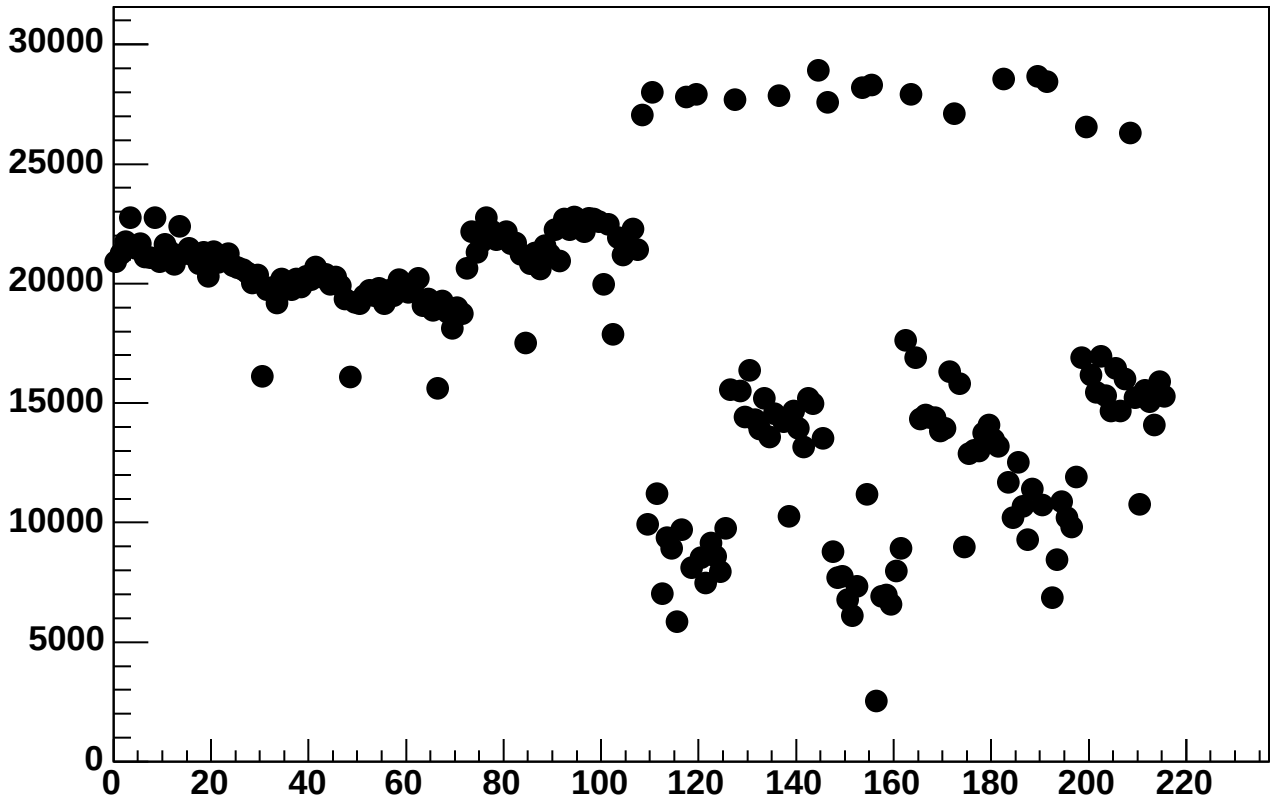
Enable 0, DAC=1600, Hold=135, ADC Mean vs 18*Chip+Chan



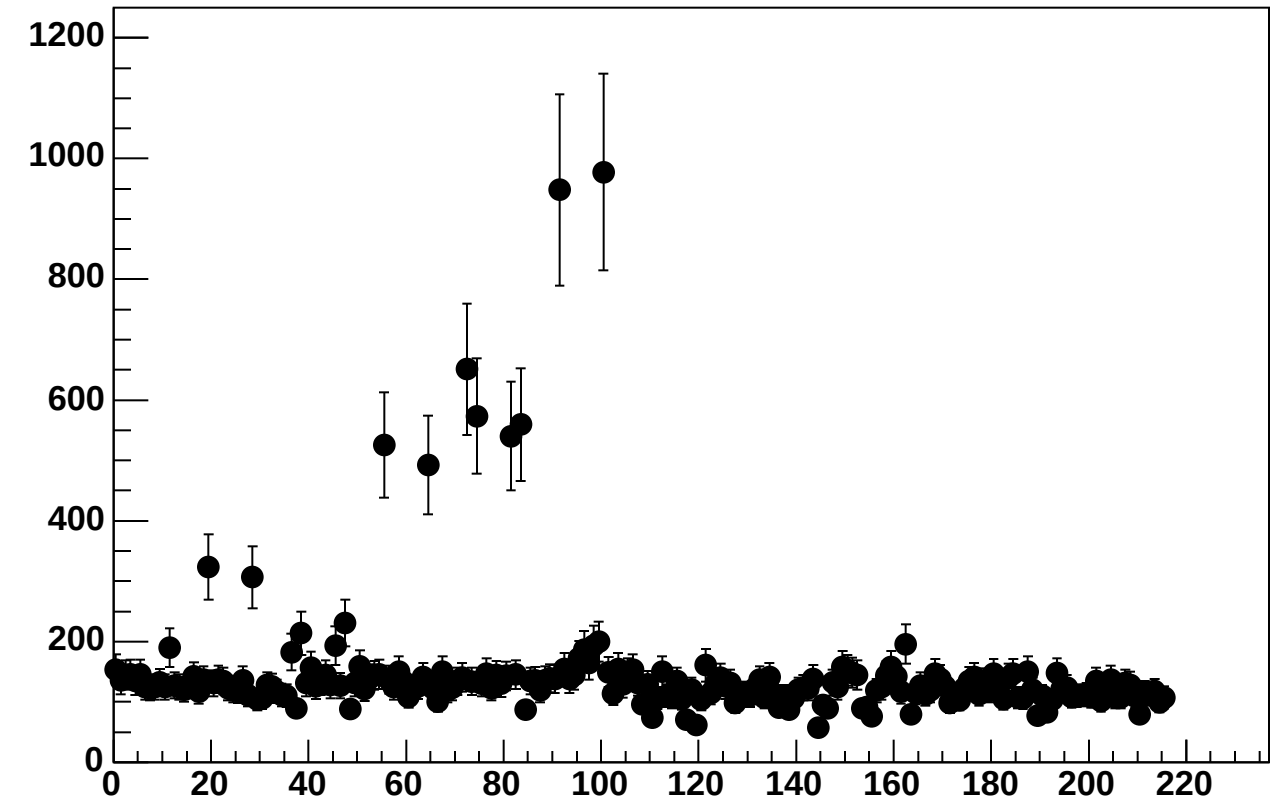
Enable 0, DAC=1600, Hold=135, ADC Noise vs 18*Chip+Chan



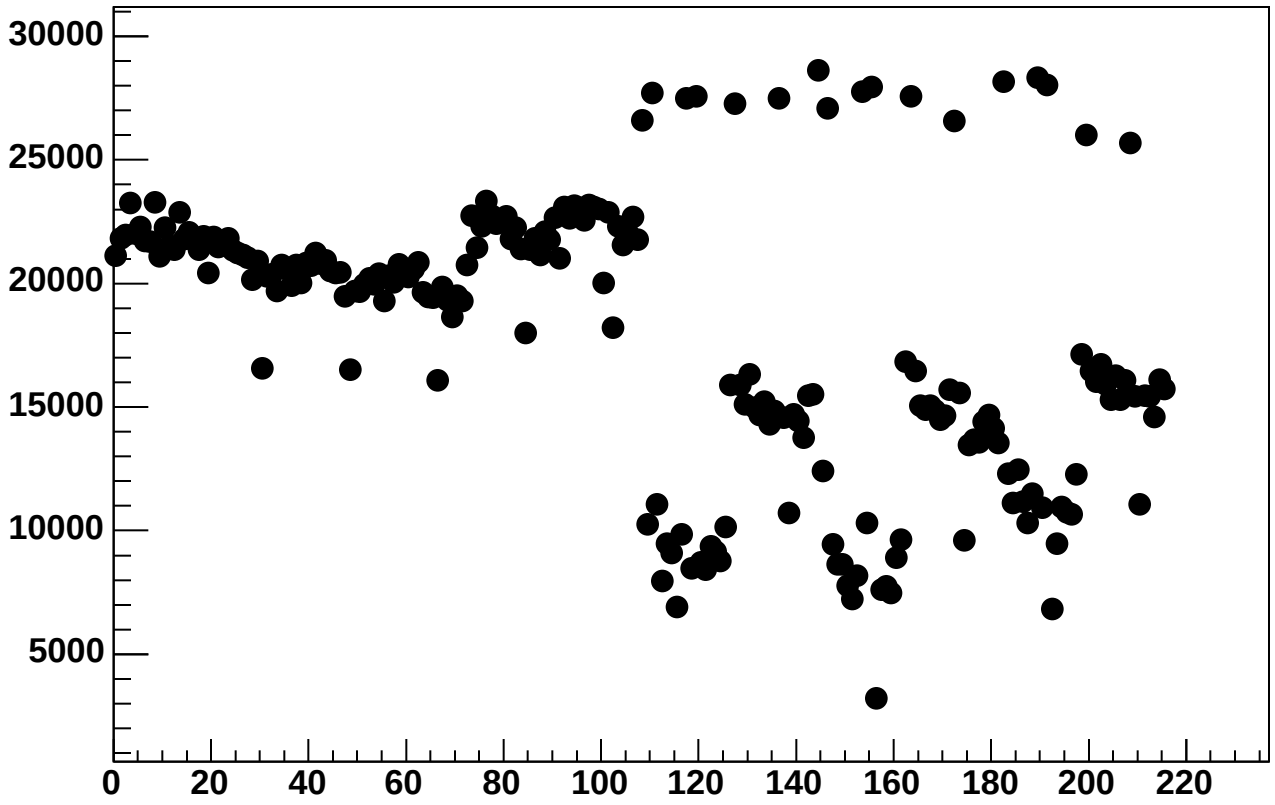
Enable 0, DAC=1600, Hold=140, ADC Mean vs 18*Chip+Chan



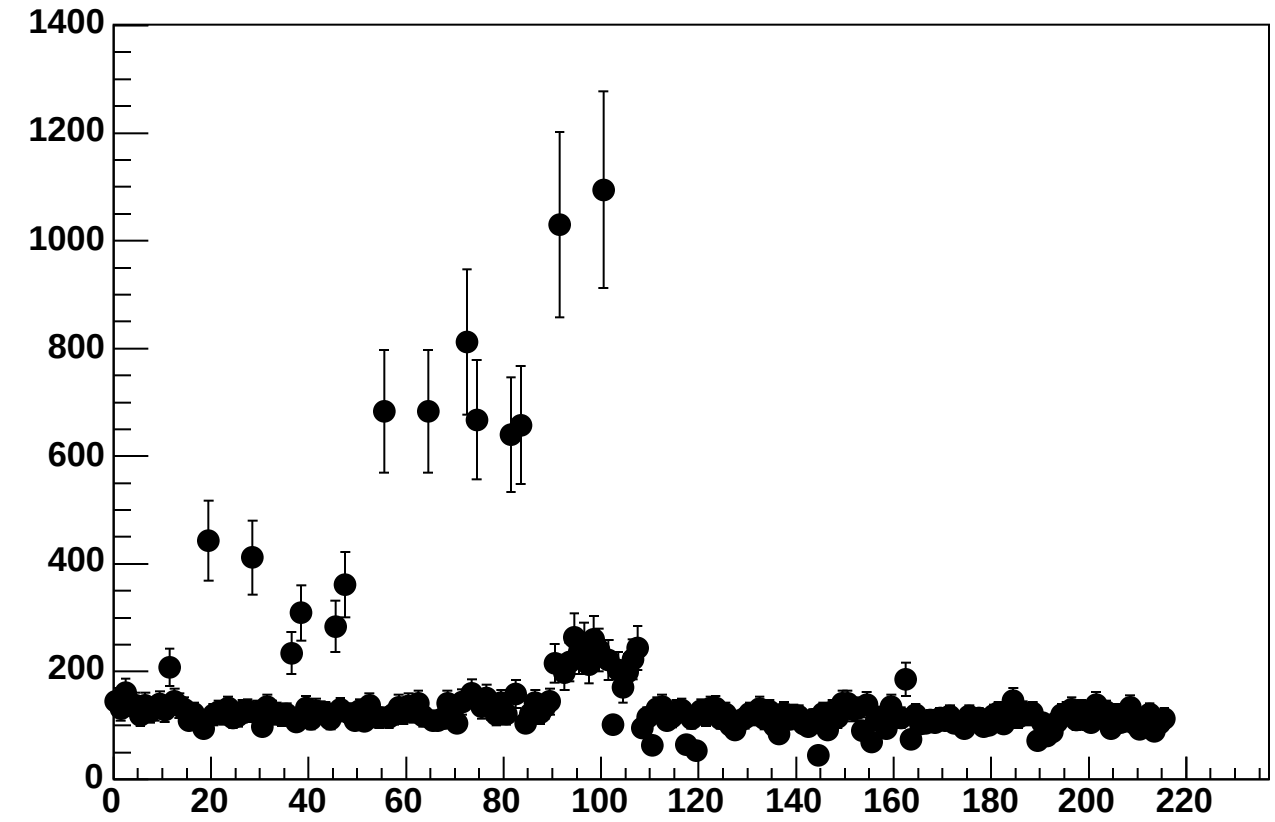
Enable 0, DAC=1600, Hold=140, ADC Noise vs 18*Chip+Chan



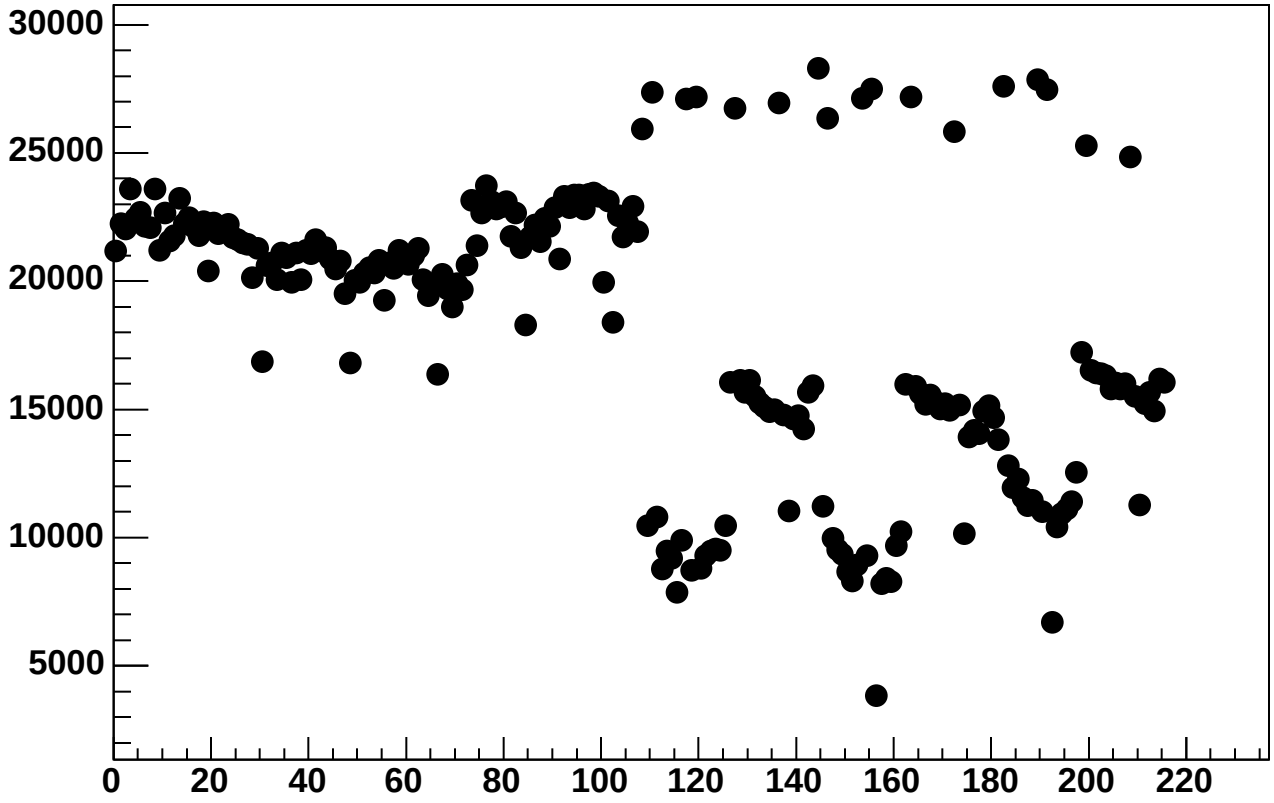
Enable 0, DAC=1600, Hold=145, ADC Mean vs 18*Chip+Chan



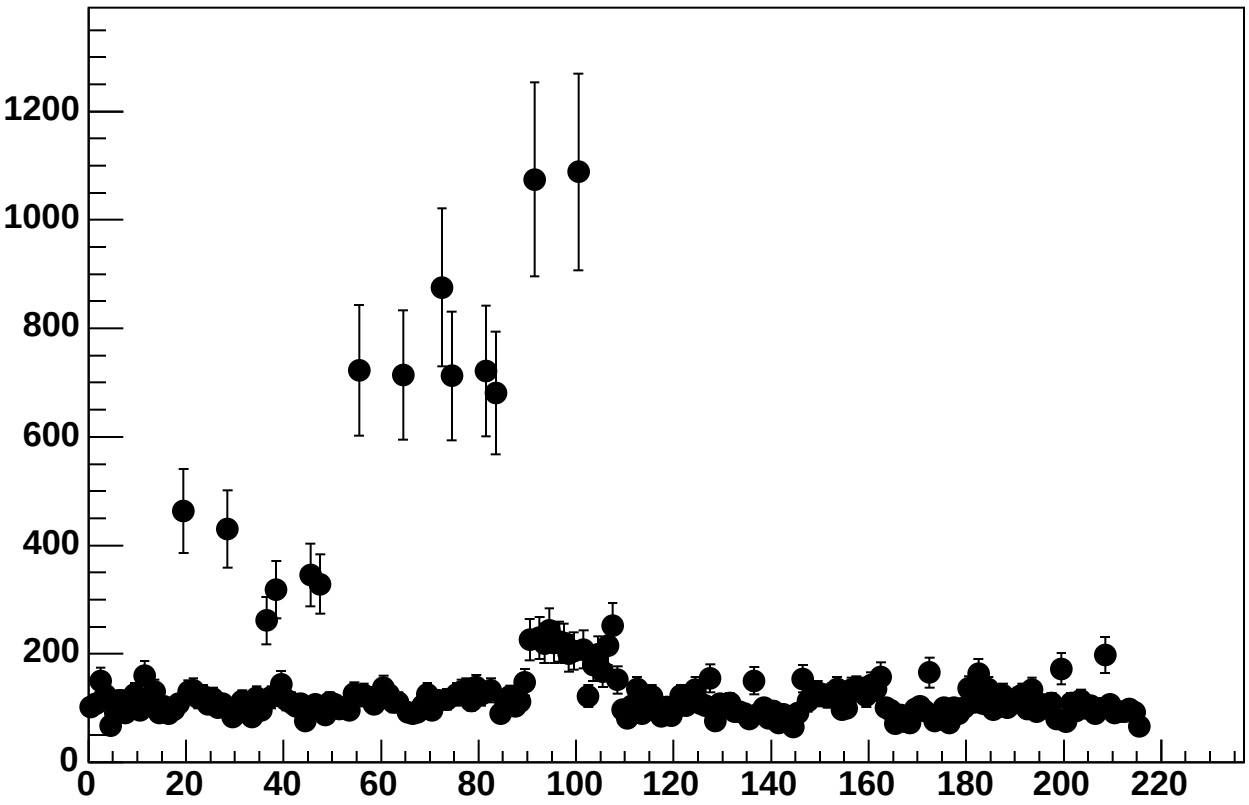
Enable 0, DAC=1600, Hold=145, ADC Noise vs 18*Chip+Chan



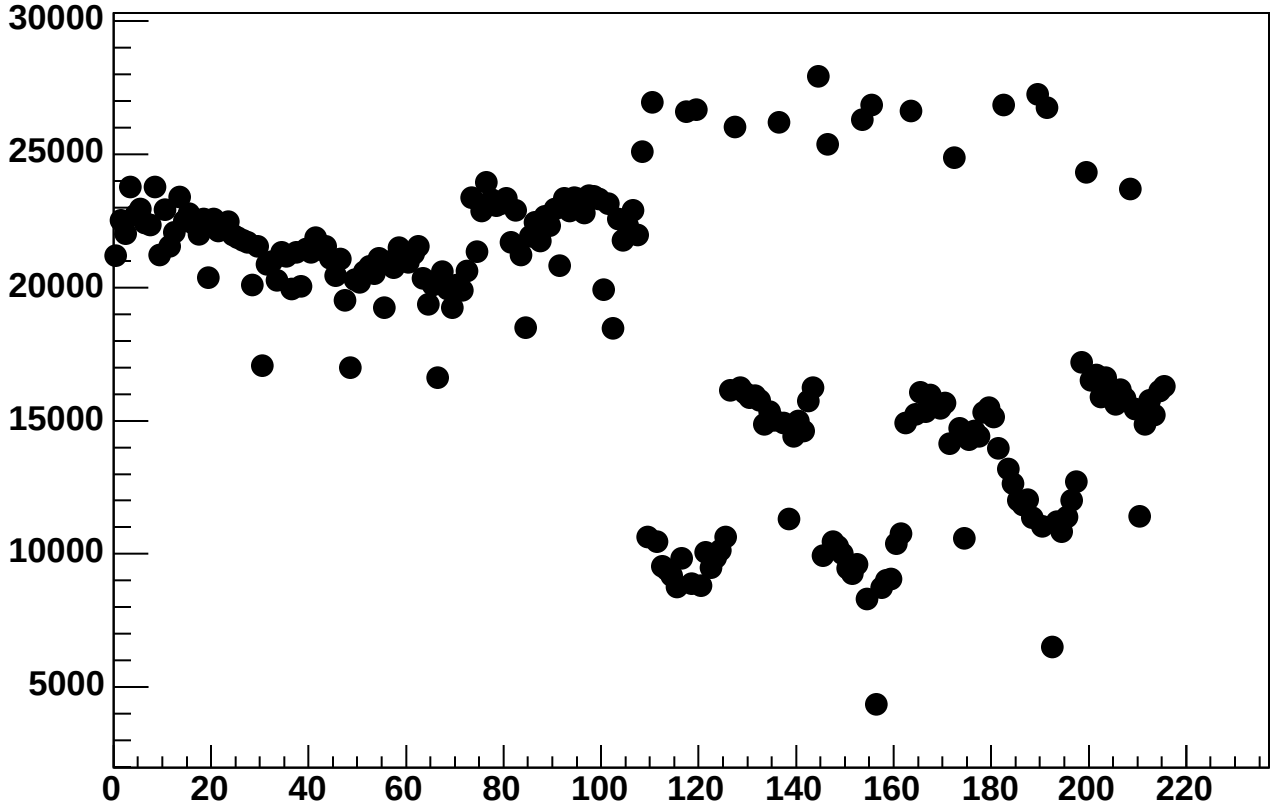
Enable 0, DAC=1600, Hold=150, ADC Mean vs 18*Chip+Chan



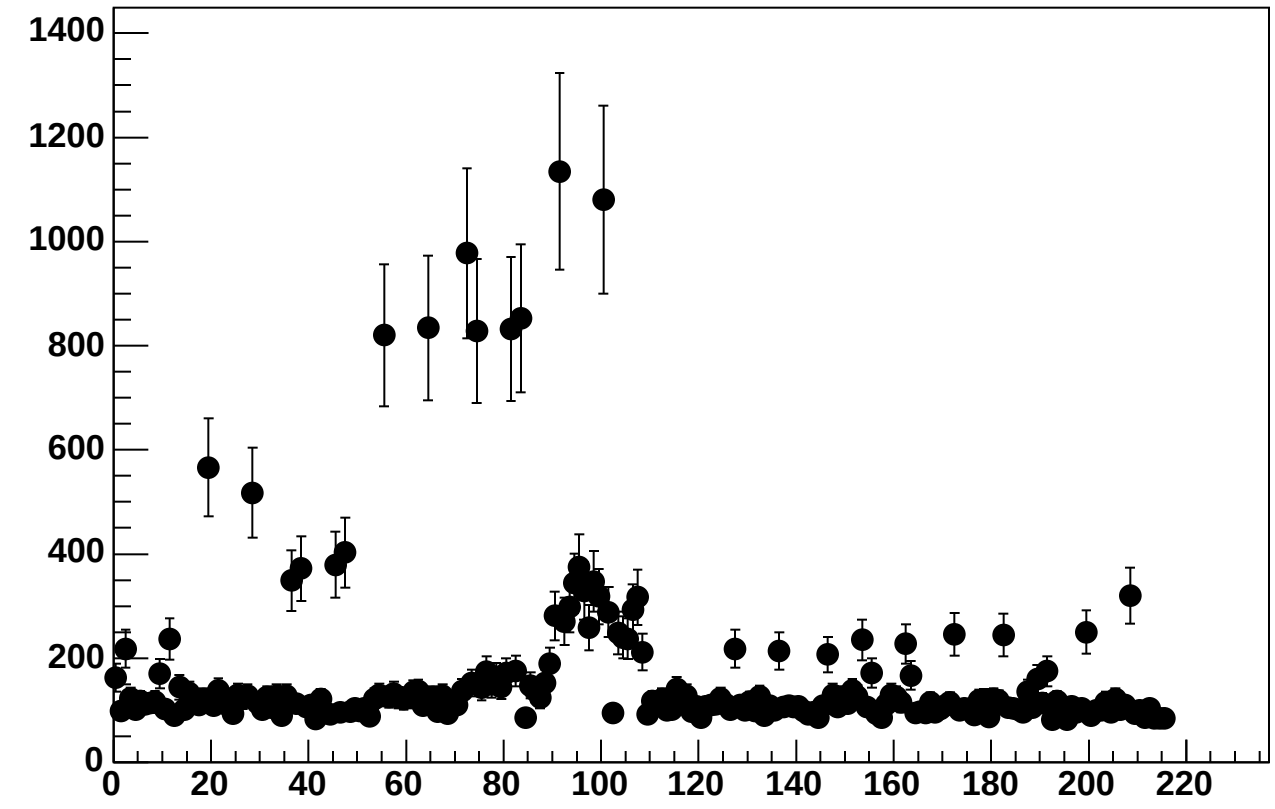
Enable 0, DAC=1600, Hold=150, ADC Noise vs 18*Chip+Chan



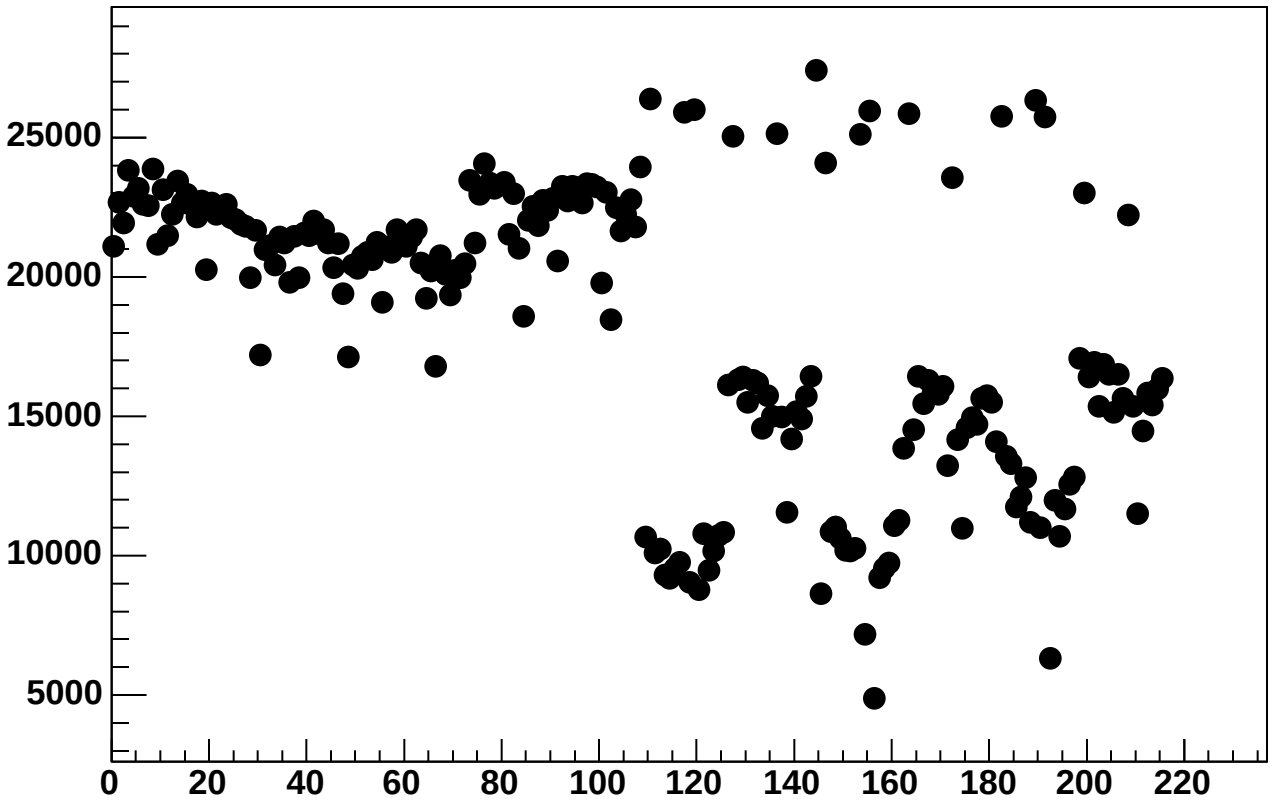
Enable 0, DAC=1600, Hold=155, ADC Mean vs 18*Chip+Chan



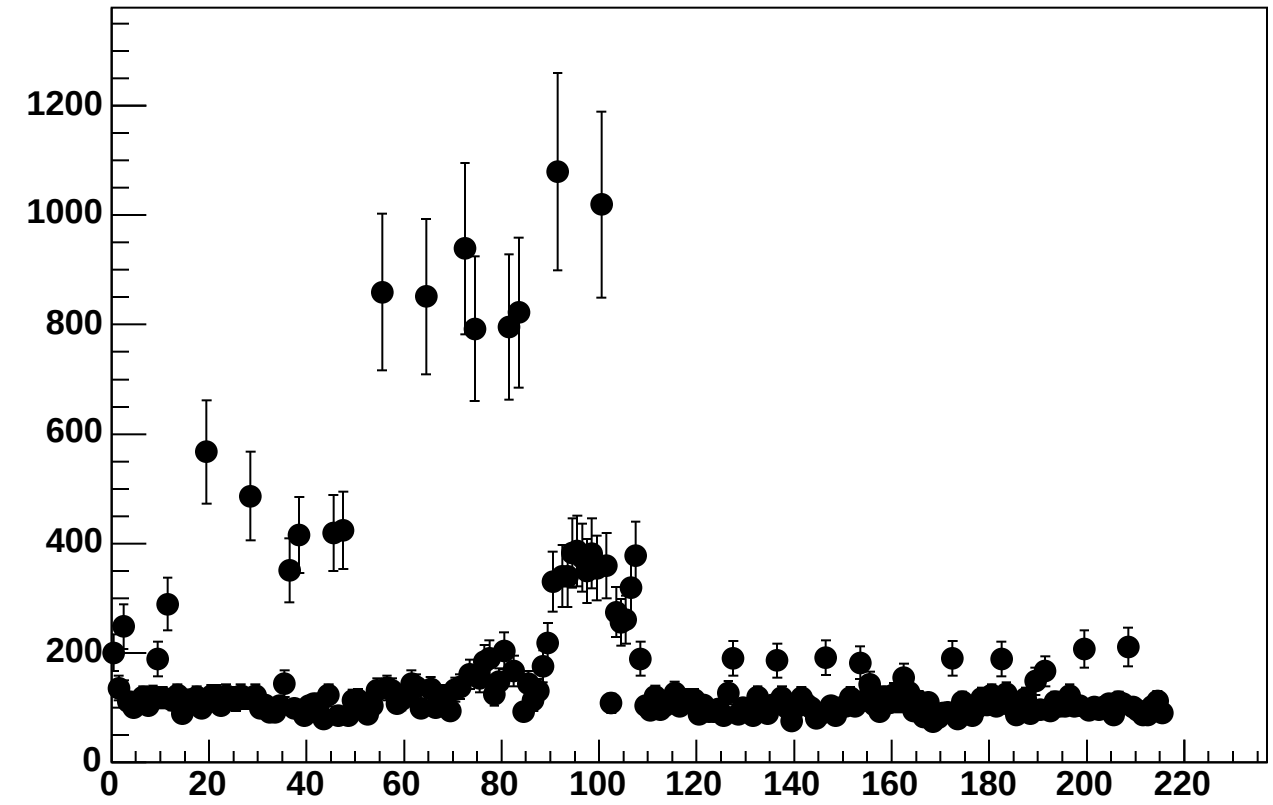
Enable 0, DAC=1600, Hold=155, ADC Noise vs 18*Chip+Chan



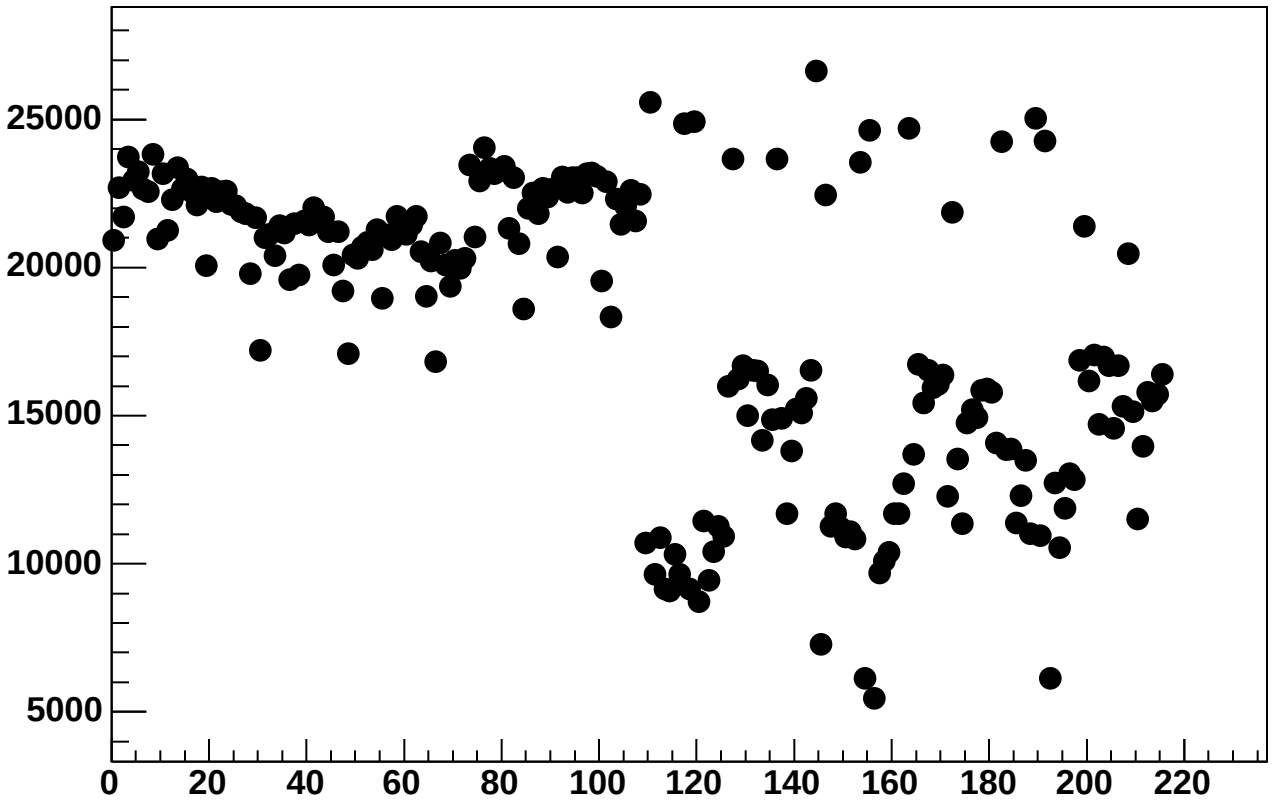
Enable 0, DAC=1600, Hold=160, ADC Mean vs 18*Chip+Chan



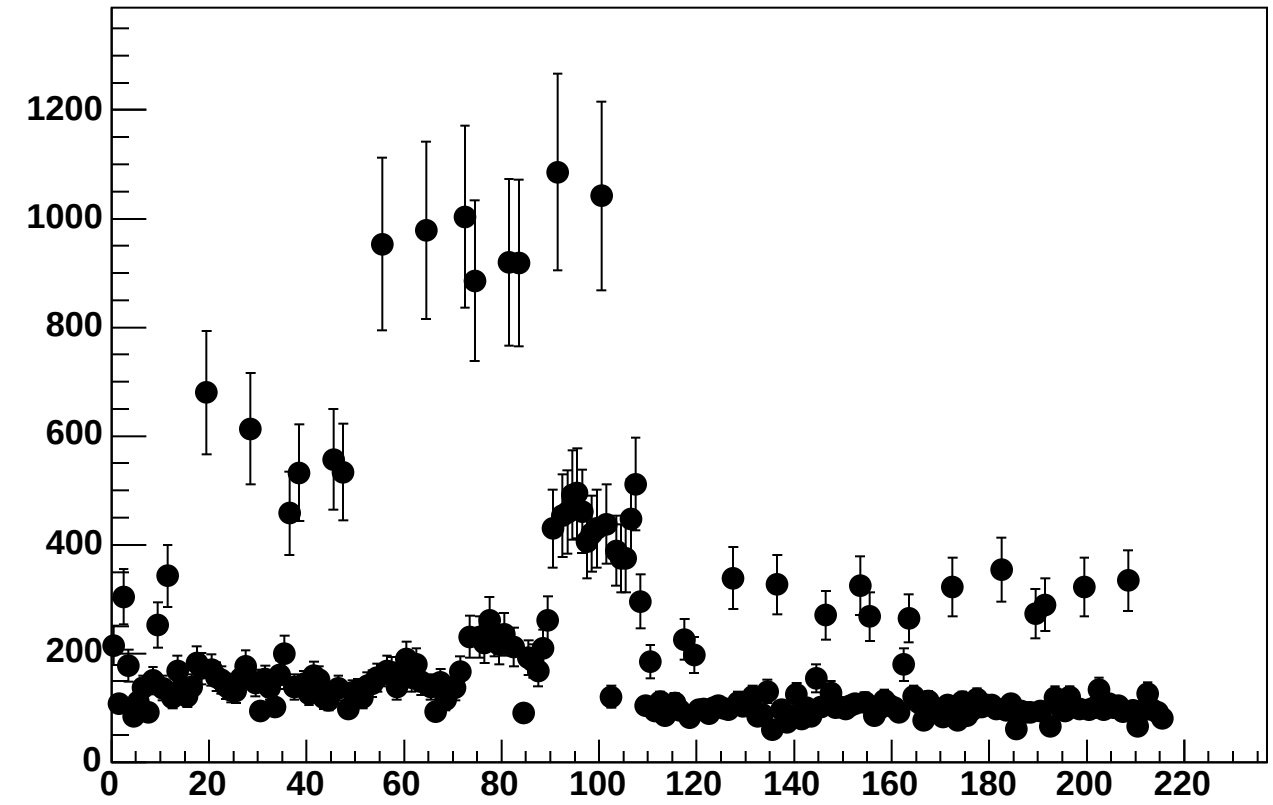
Enable 0, DAC=1600, Hold=160, ADC Noise vs 18*Chip+Chan



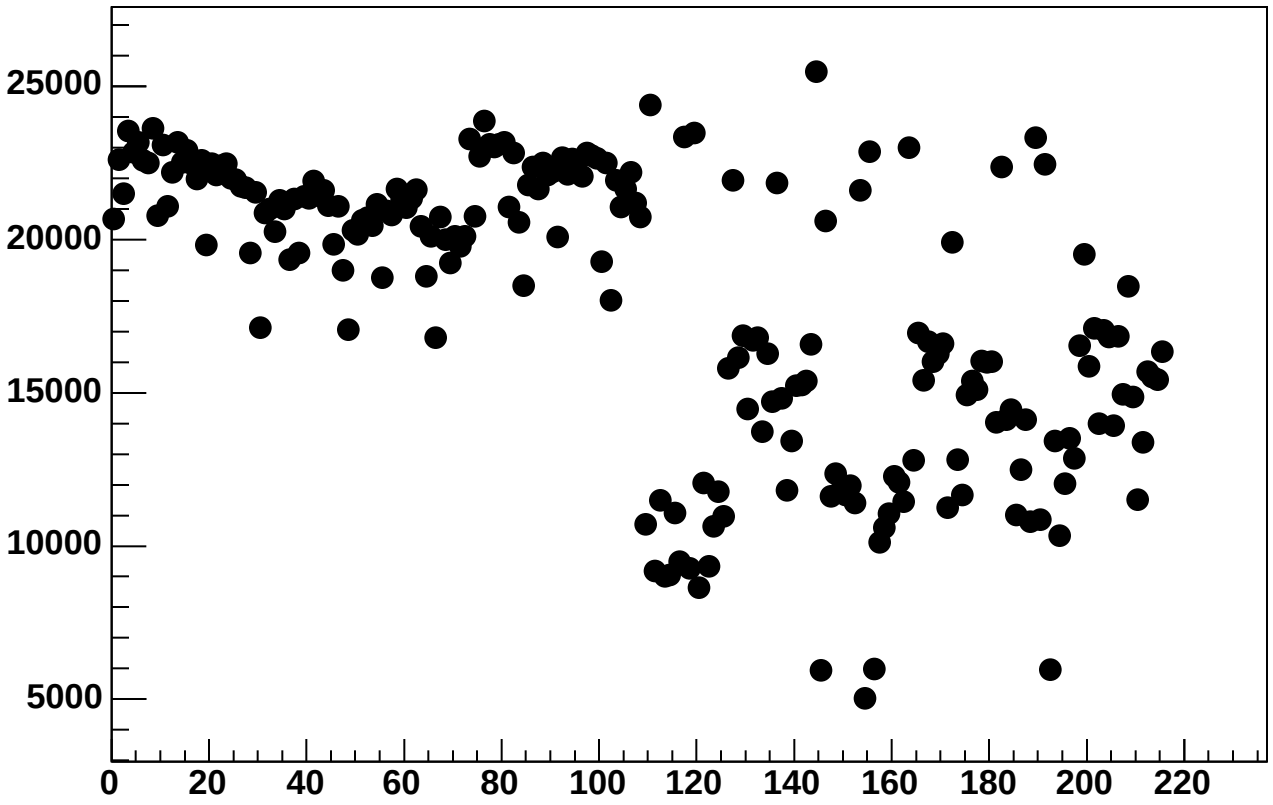
Enable 0, DAC=1600, Hold=165, ADC Mean vs 18*Chip+Chan



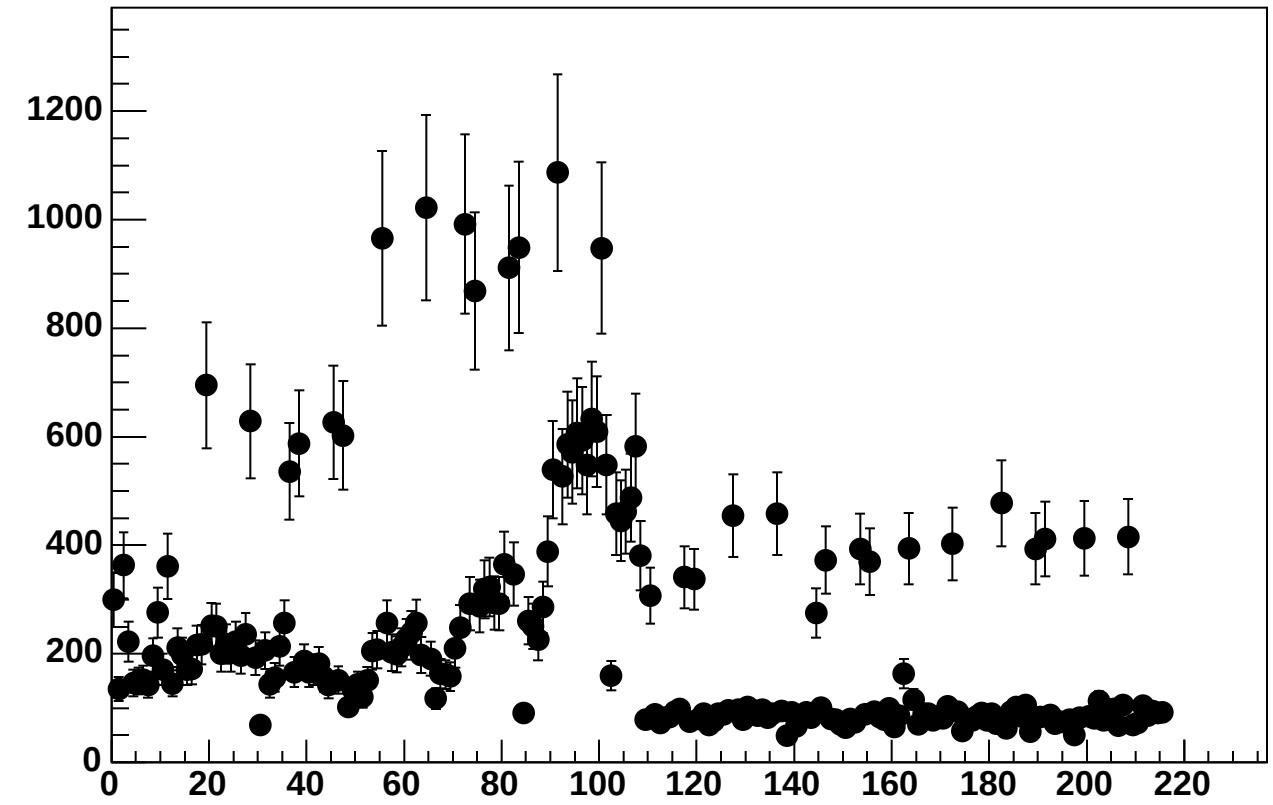
Enable 0, DAC=1600, Hold=165, ADC Noise vs 18*Chip+Chan



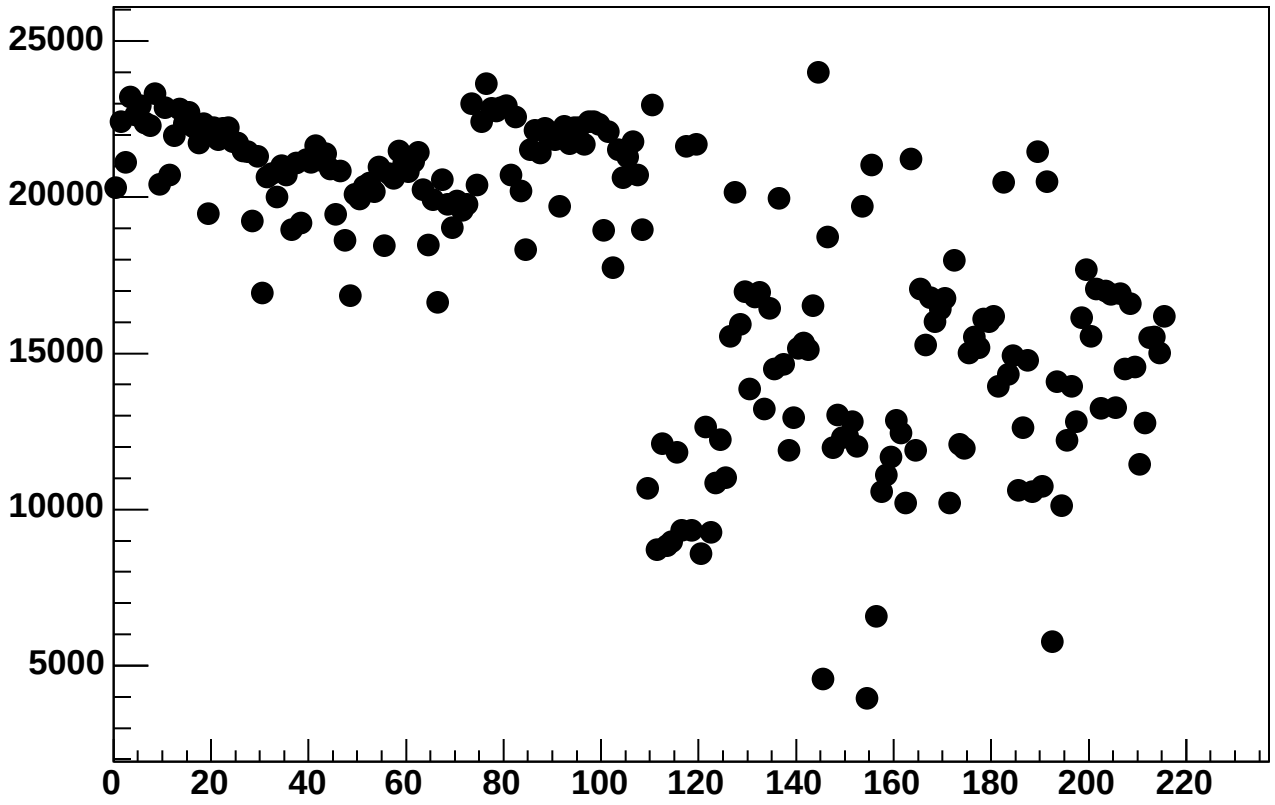
Enable 0, DAC=1600, Hold=170, ADC Mean vs 18*Chip+Chan



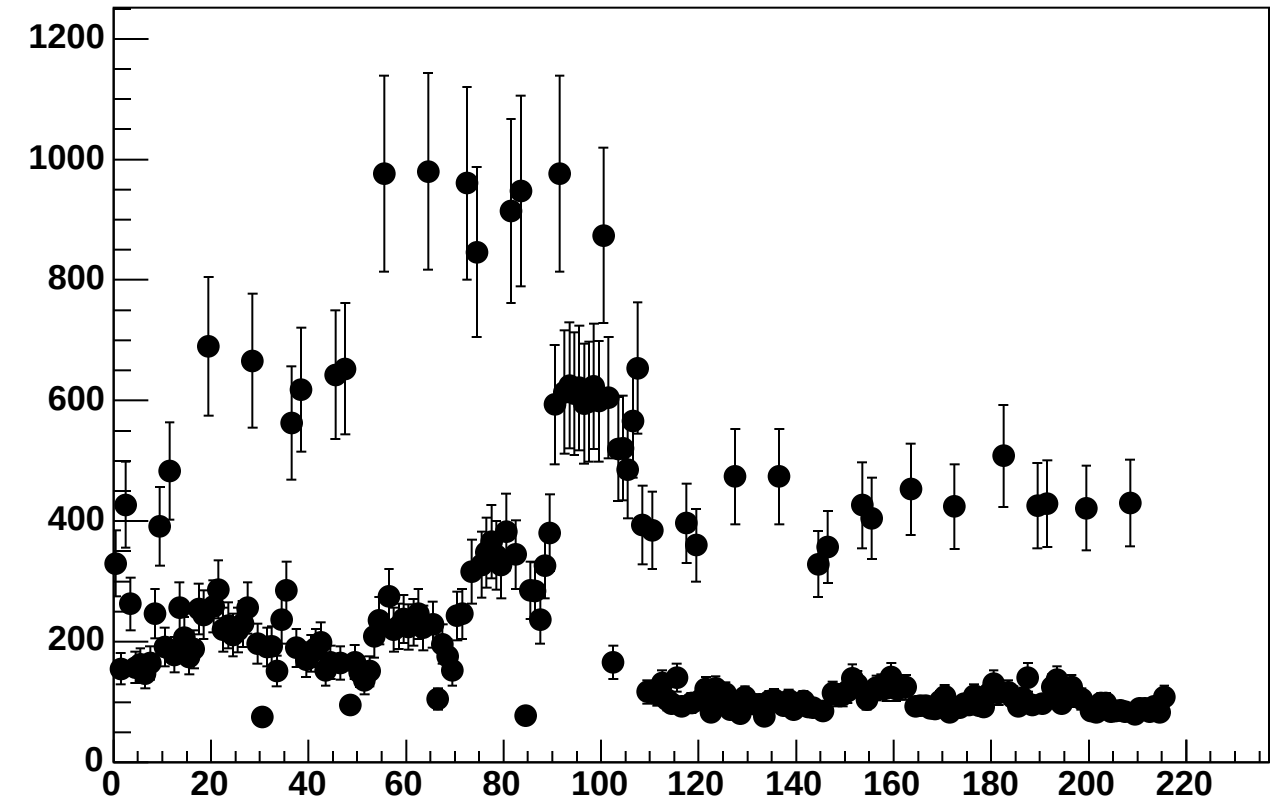
Enable 0, DAC=1600, Hold=170, ADC Noise vs 18*Chip+Chan



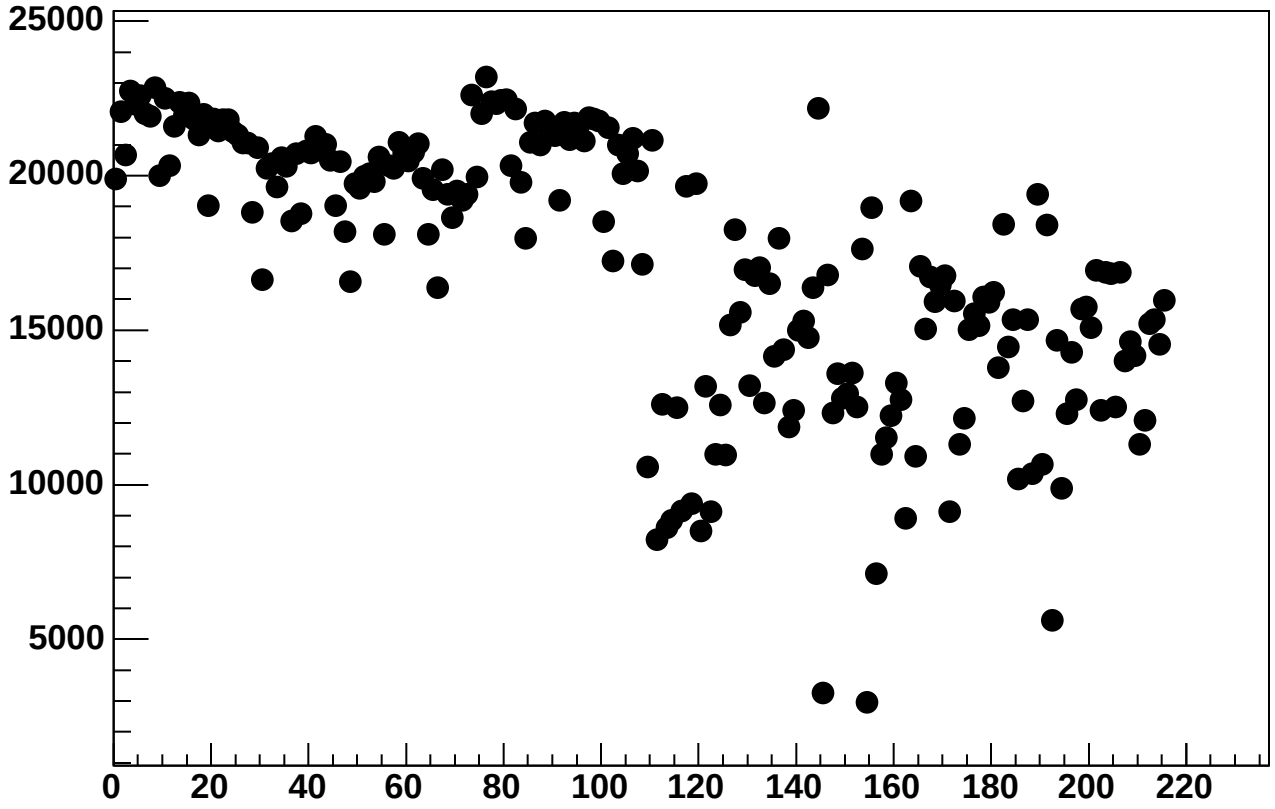
Enable 0, DAC=1600, Hold=175, ADC Mean vs 18*Chip+Chan



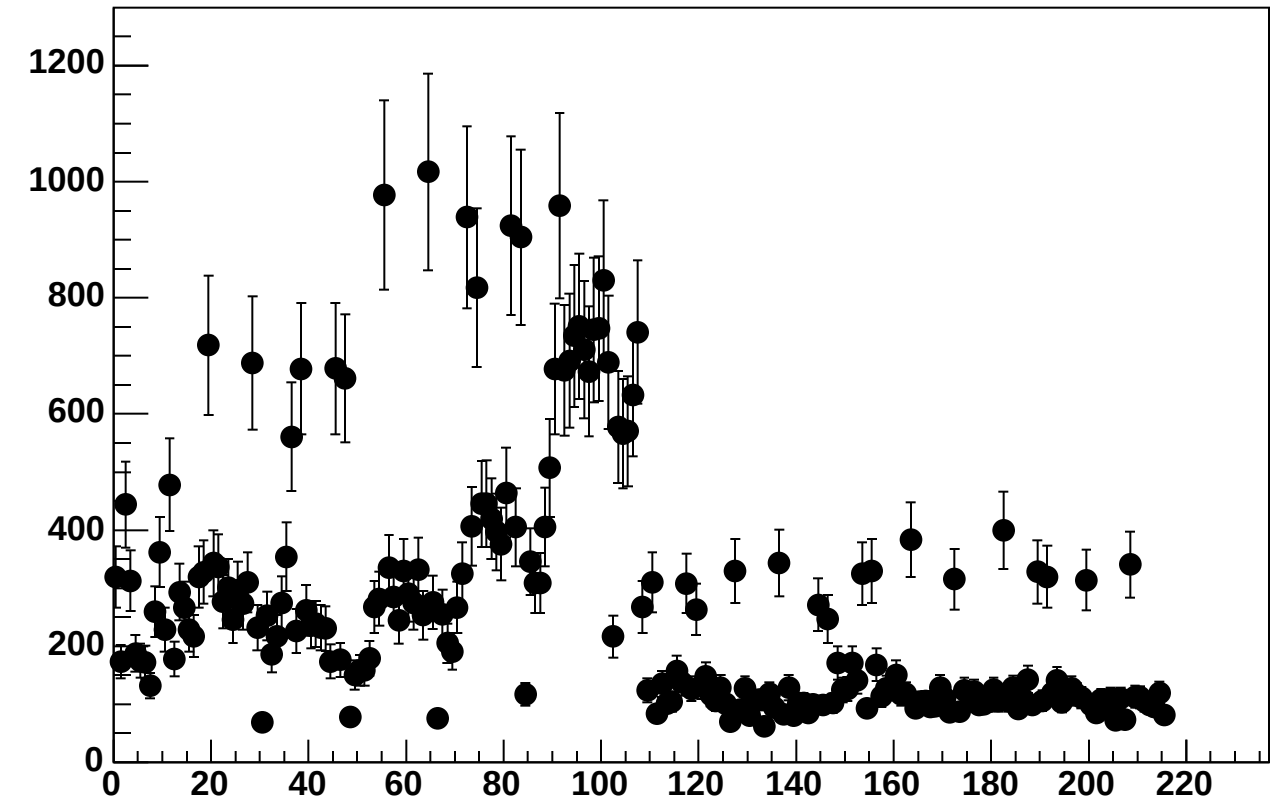
Enable 0, DAC=1600, Hold=175, ADC Noise vs 18*Chip+Chan



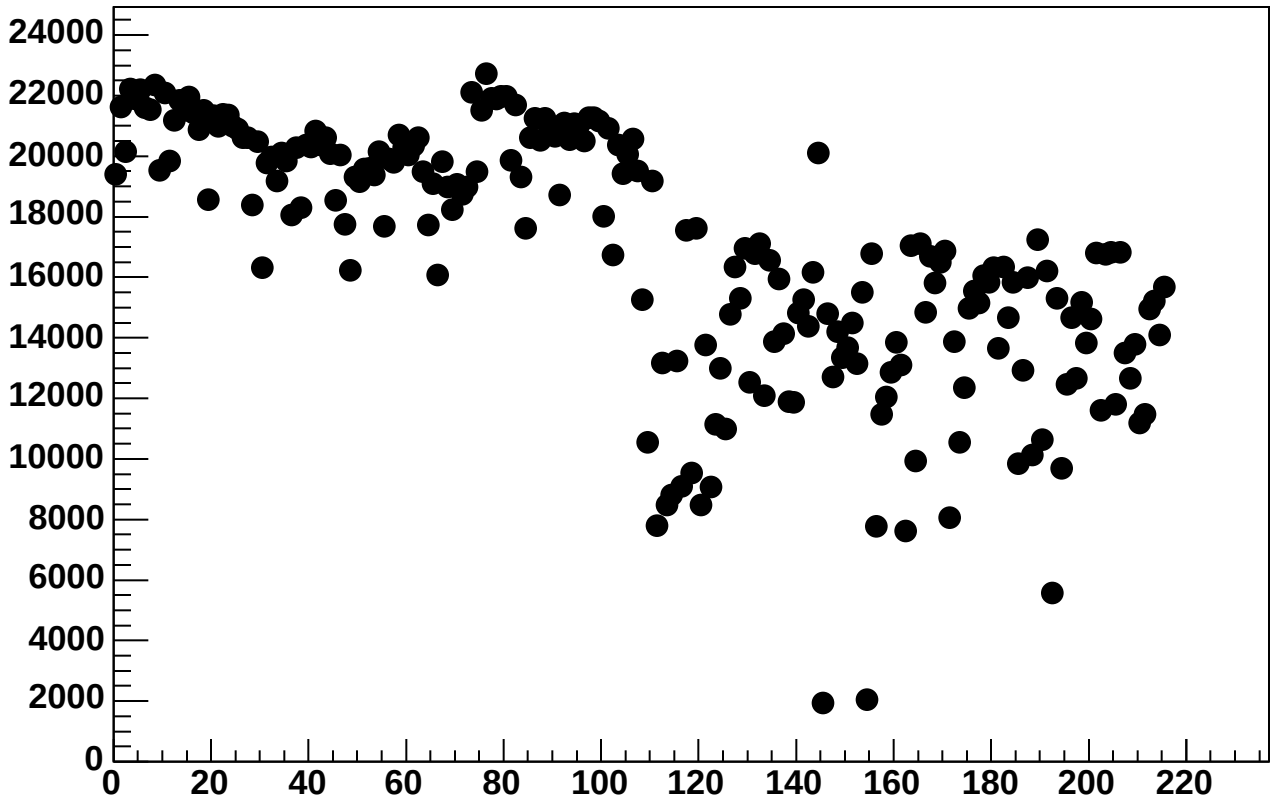
Enable 0, DAC=1600, Hold=180, ADC Mean vs 18*Chip+Chan



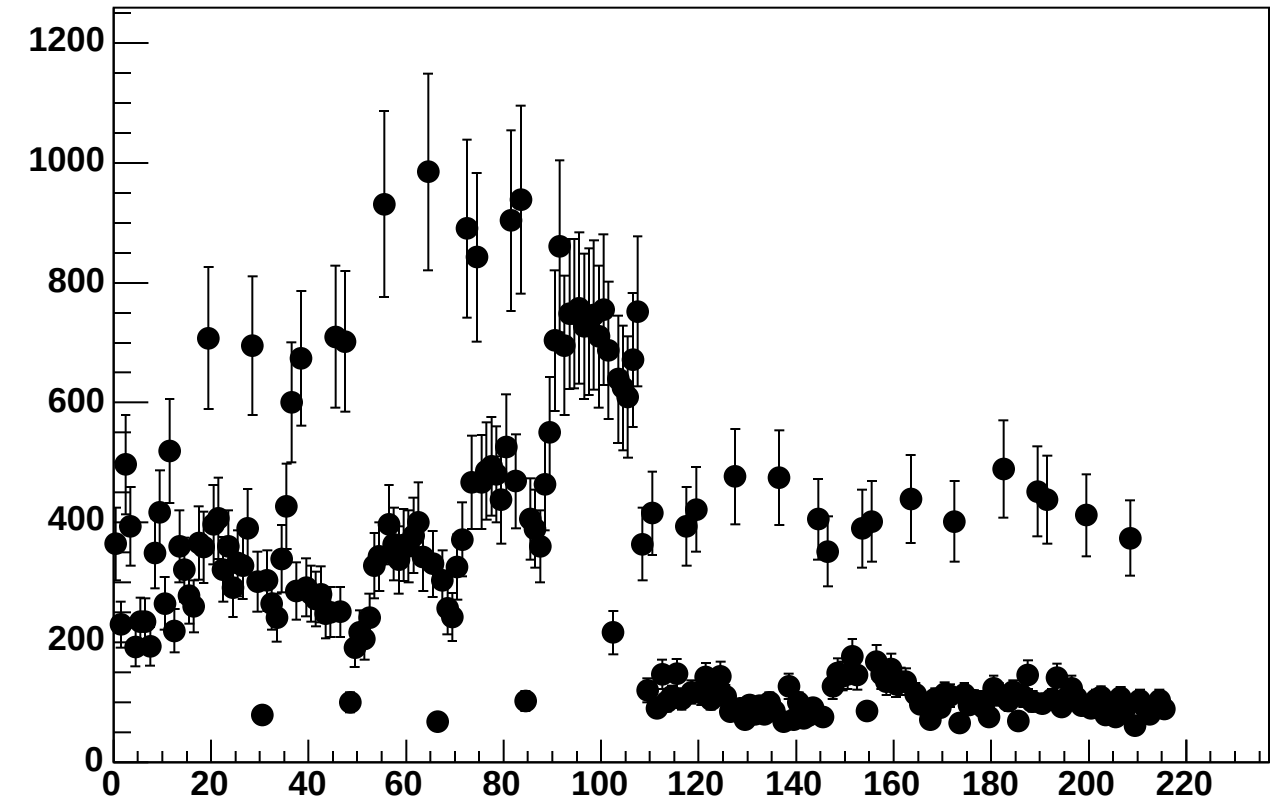
Enable 0, DAC=1600, Hold=180, ADC Noise vs 18*Chip+Chan



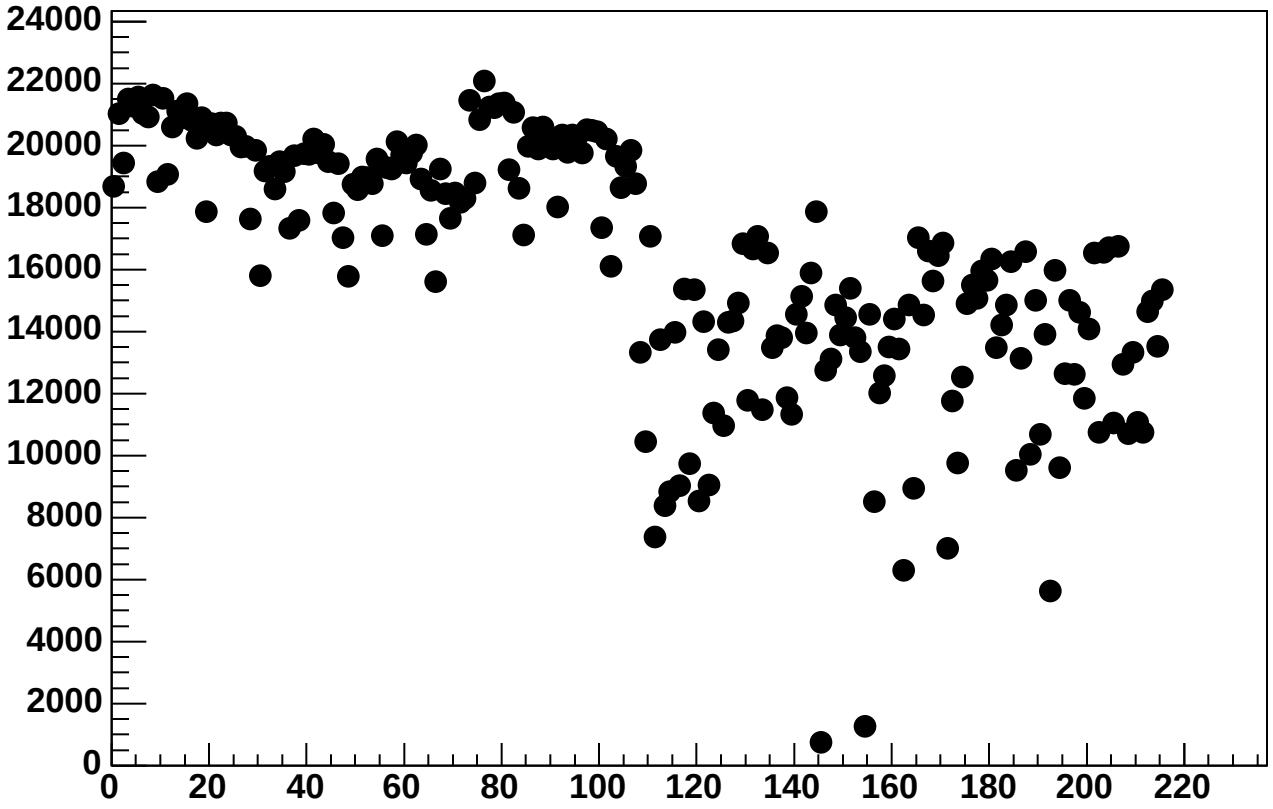
Enable 0, DAC=1600, Hold=185, ADC Mean vs 18*Chip+Chan



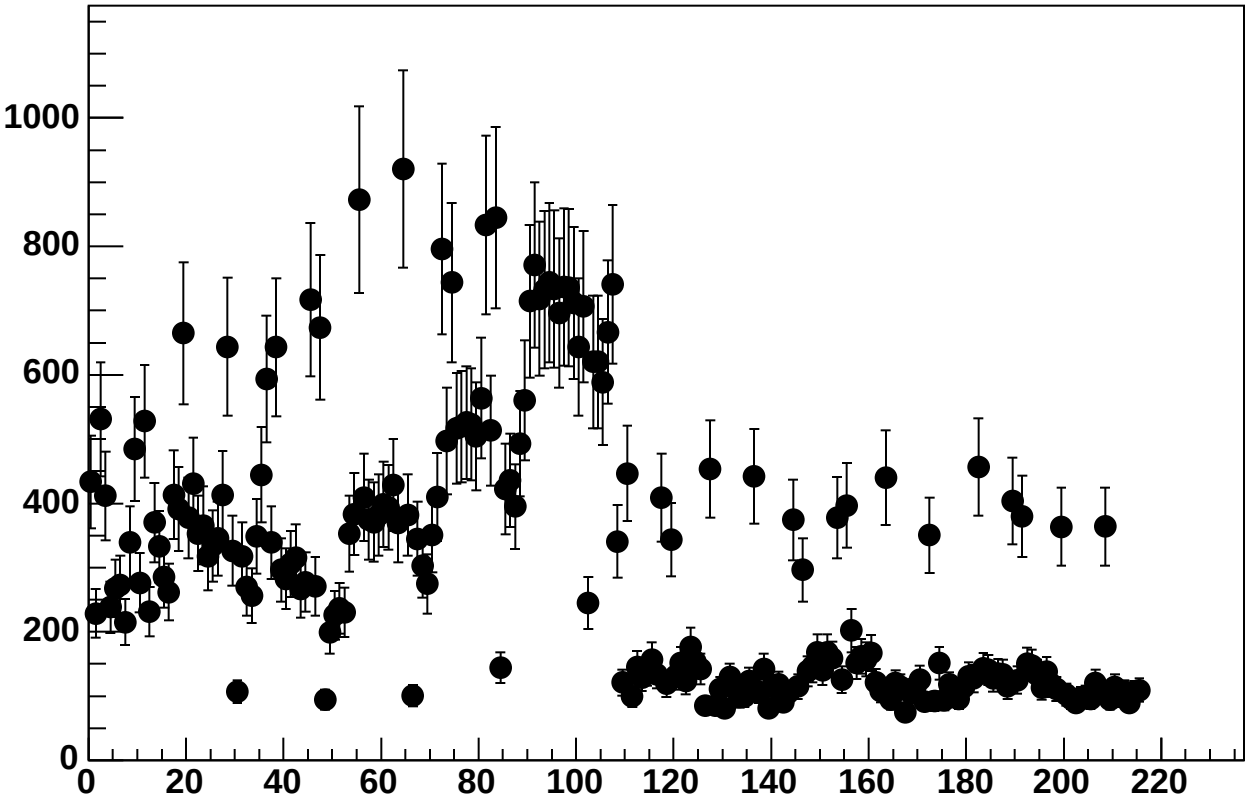
Enable 0, DAC=1600, Hold=185, ADC Noise vs 18*Chip+Chan



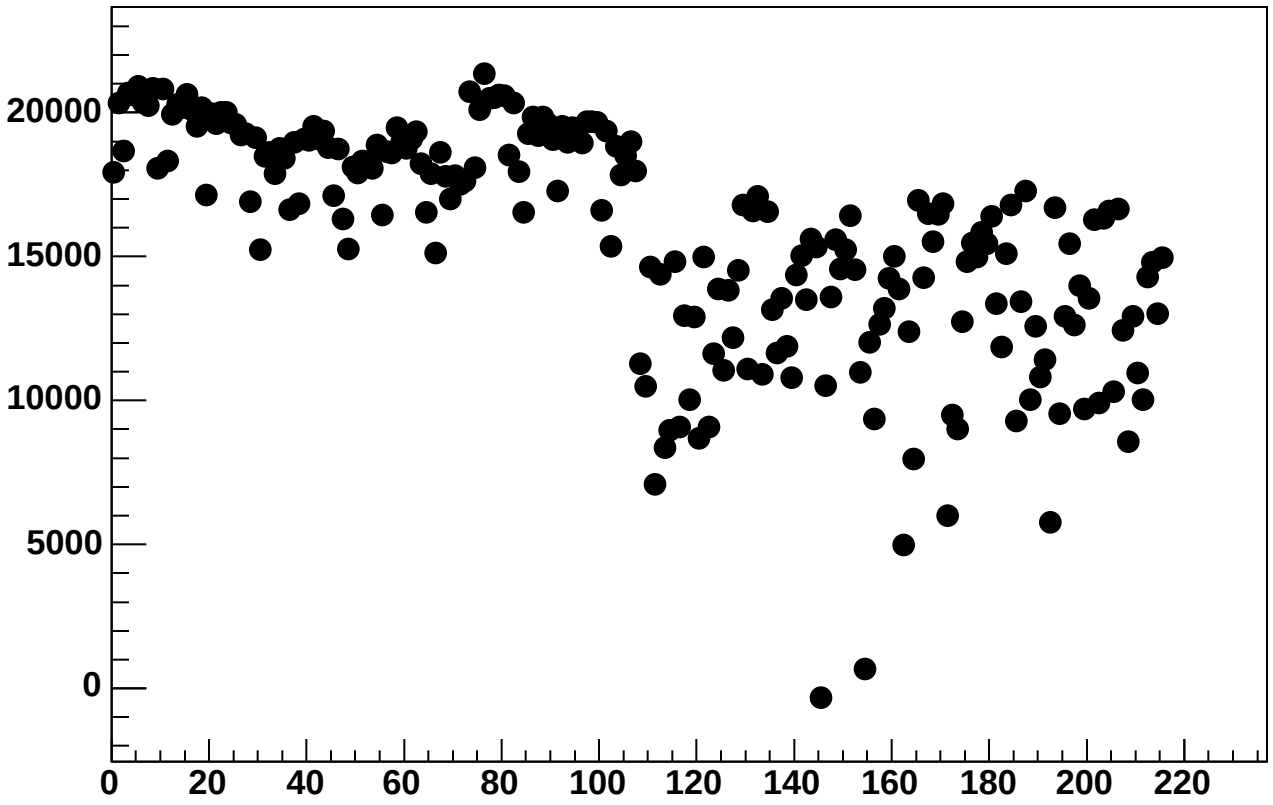
Enable 0, DAC=1600, Hold=190, ADC Mean vs 18*Chip+Chan



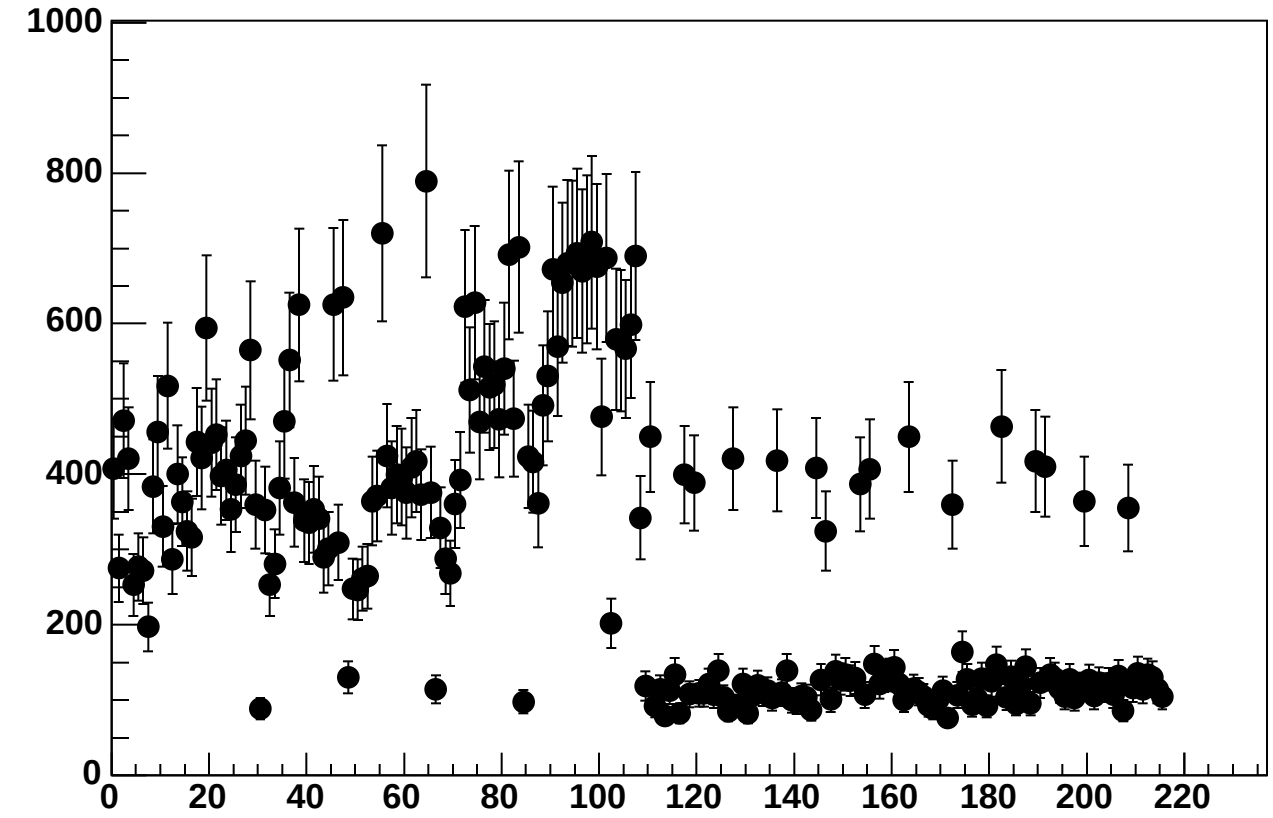
Enable 0, DAC=1600, Hold=190, ADC Noise vs 18*Chip+Chan



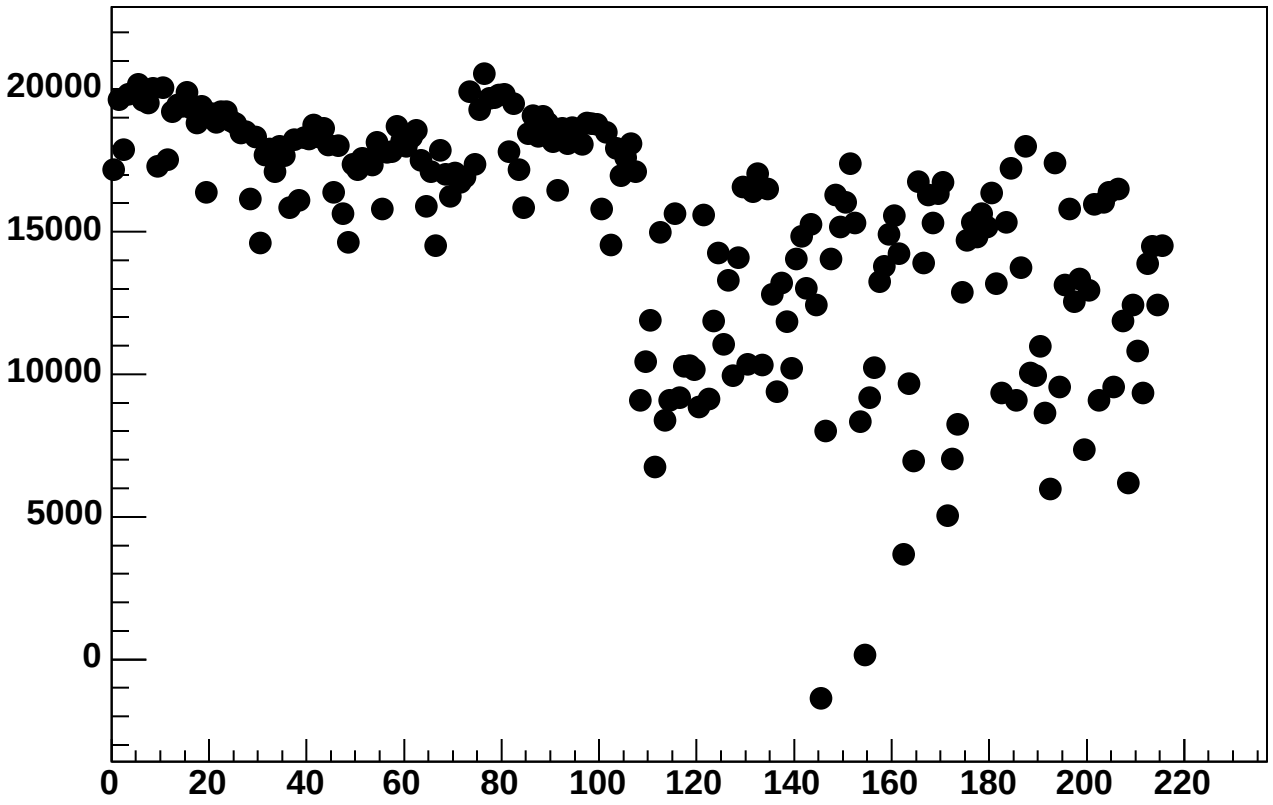
Enable 0, DAC=1600, Hold=195, ADC Mean vs 18*Chip+Chan



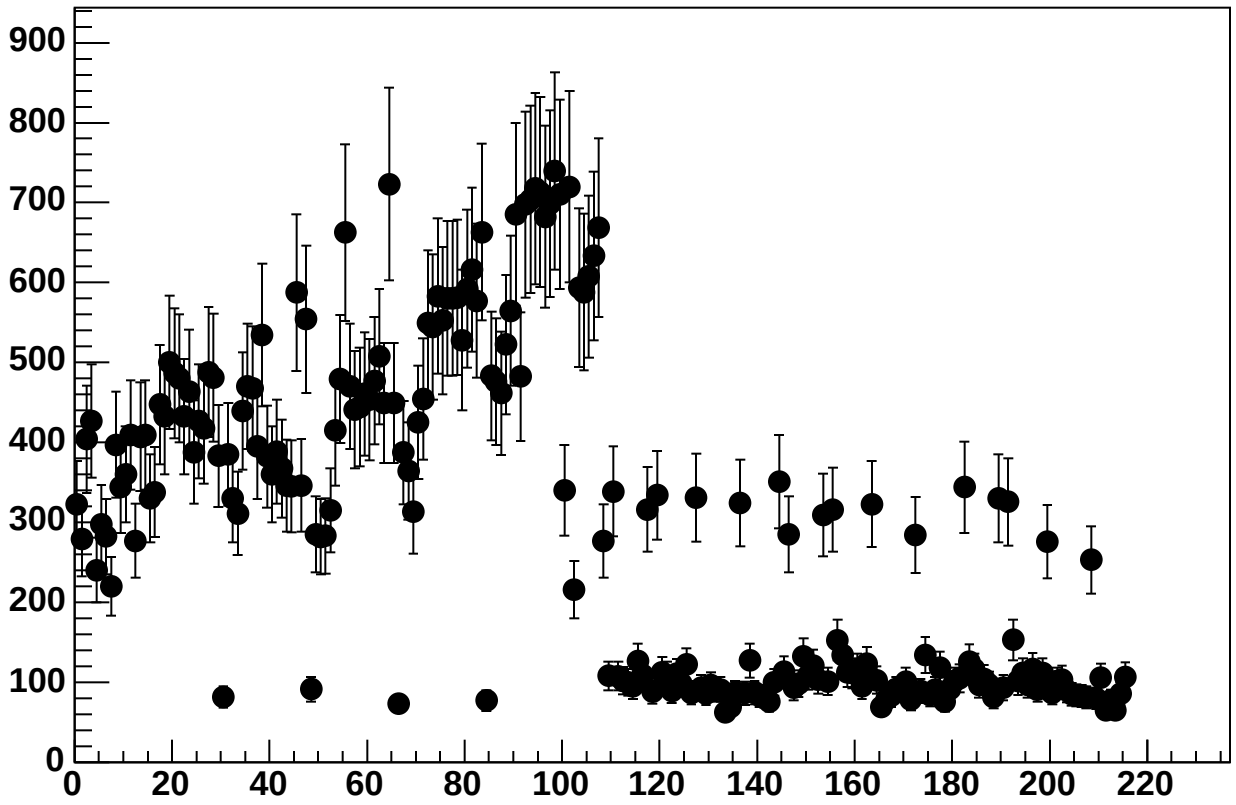
Enable 0, DAC=1600, Hold=195, ADC Noise vs 18*Chip+Chan



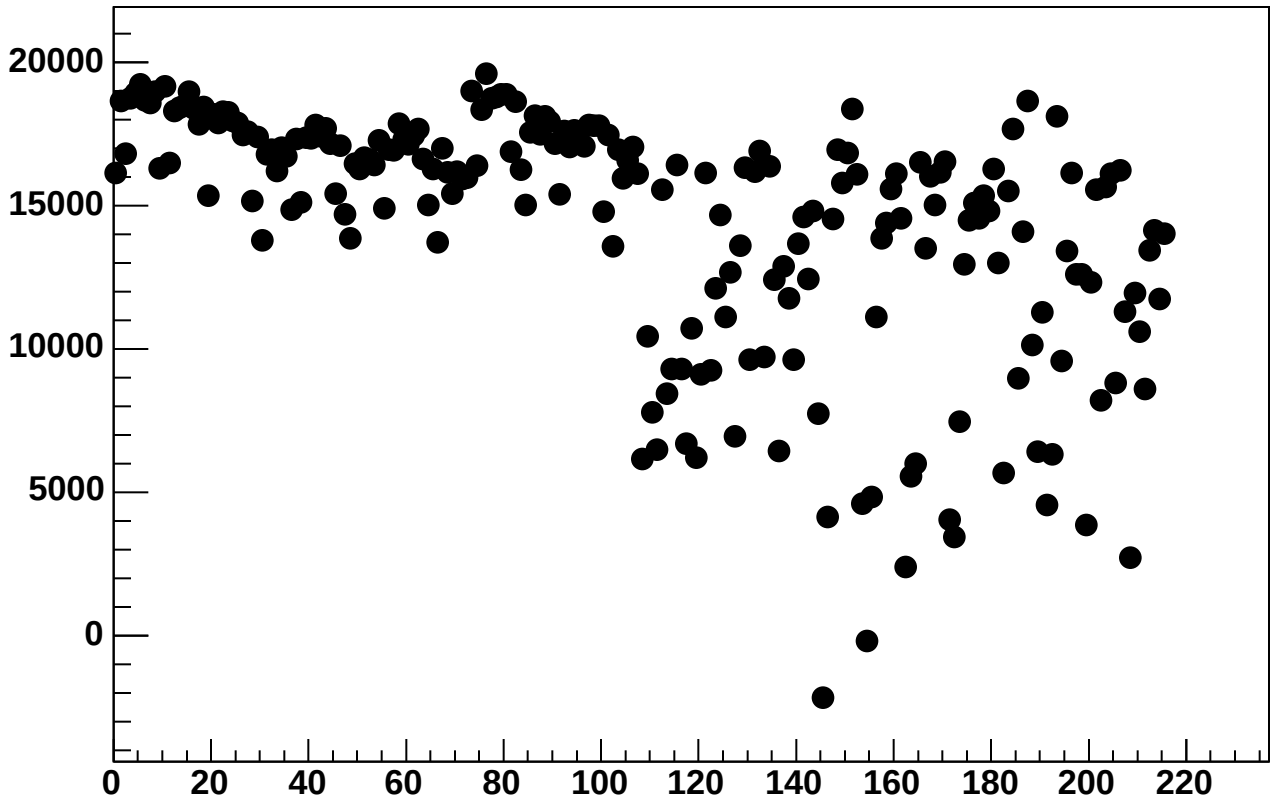
Enable 0, DAC=1600, Hold=200, ADC Mean vs 18*Chip+Chan



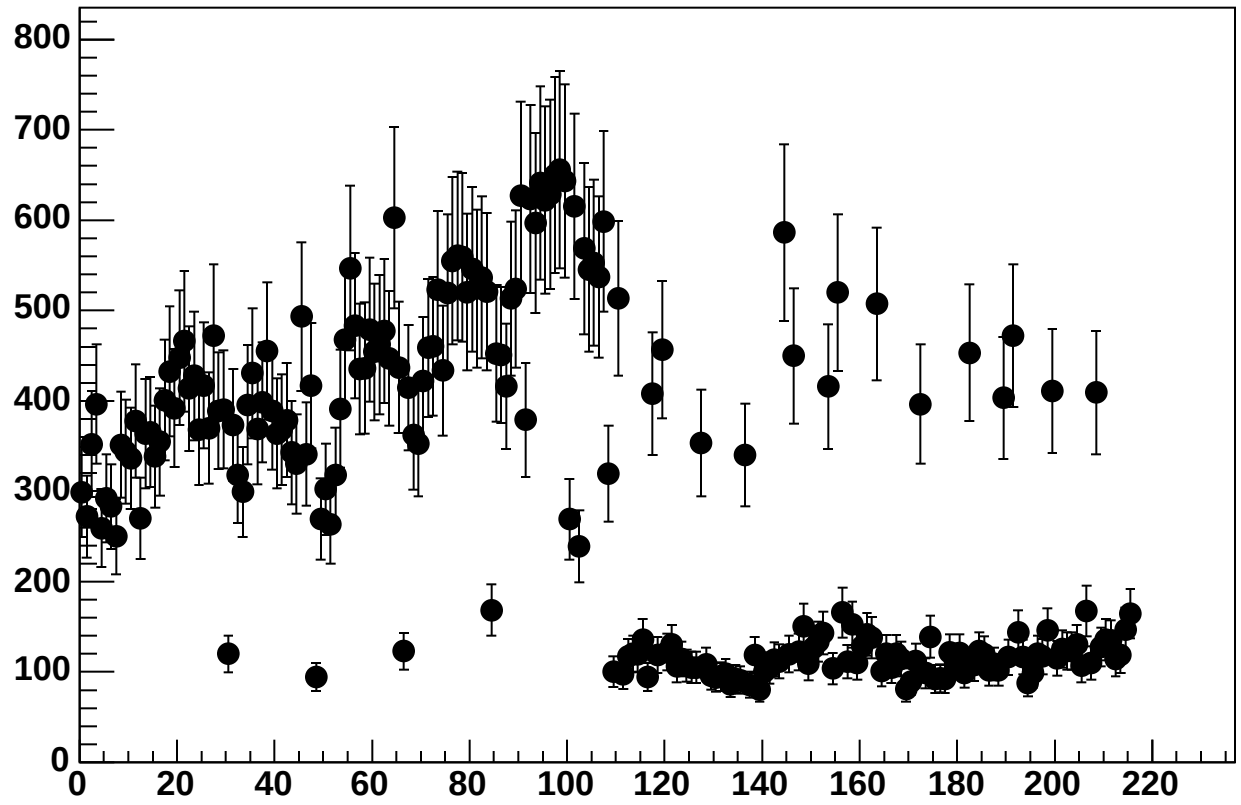
Enable 0, DAC=1600, Hold=200, ADC Noise vs 18*Chip+Chan



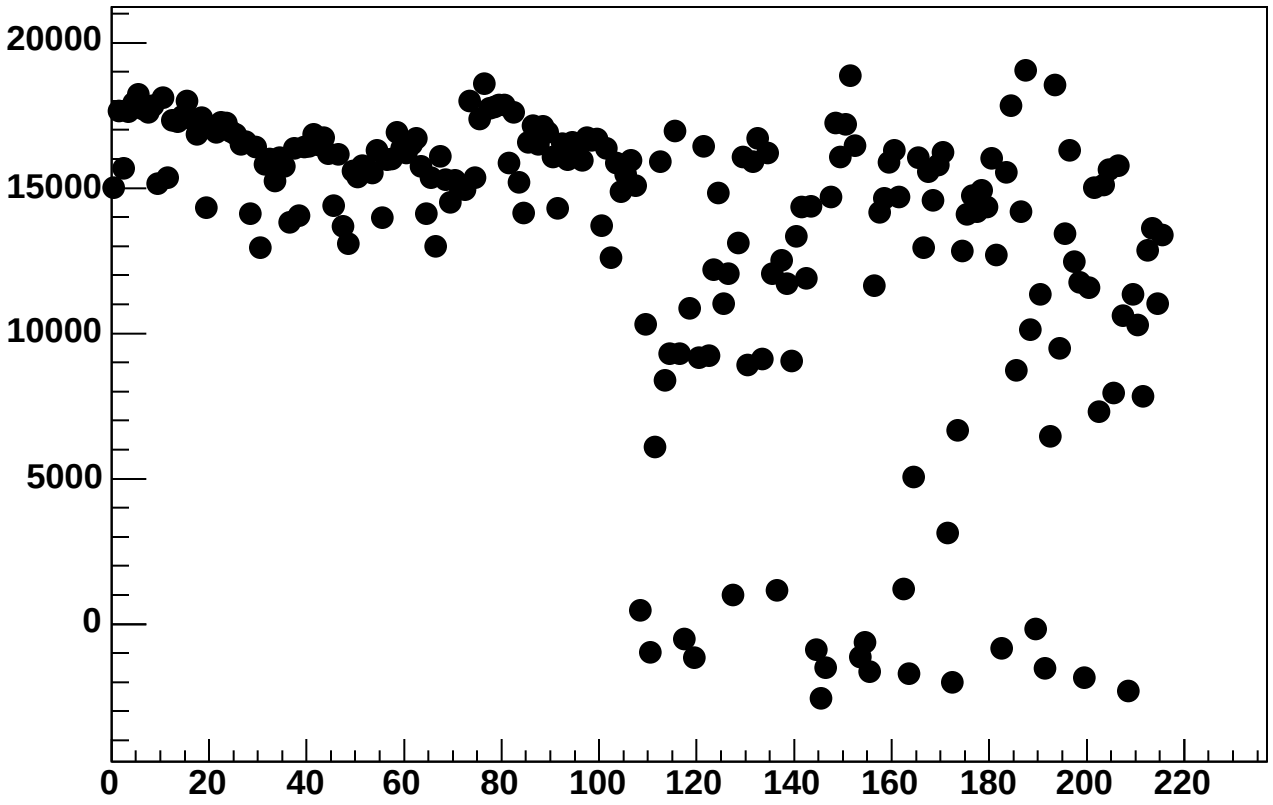
Enable 0, DAC=1600, Hold=205, ADC Mean vs 18*Chip+Chan



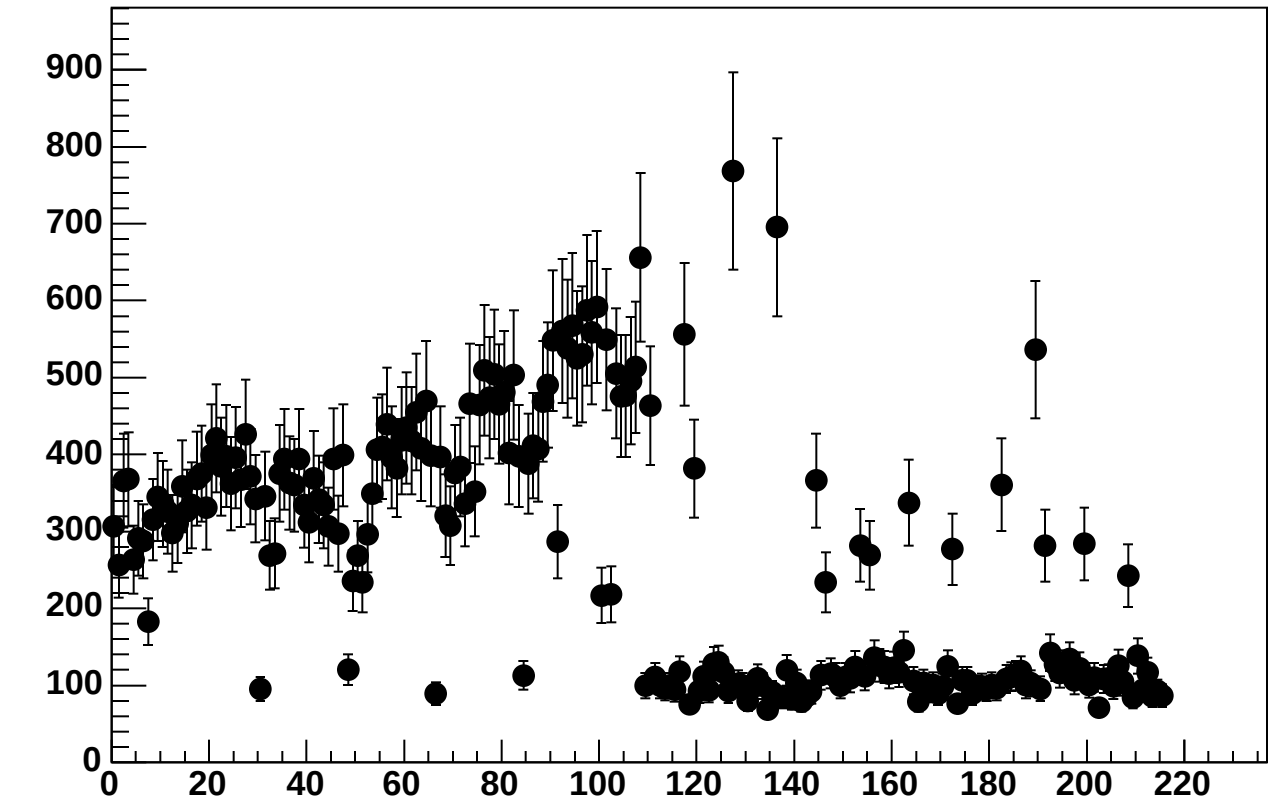
Enable 0, DAC=1600, Hold=205, ADC Noise vs 18*Chip+Chan



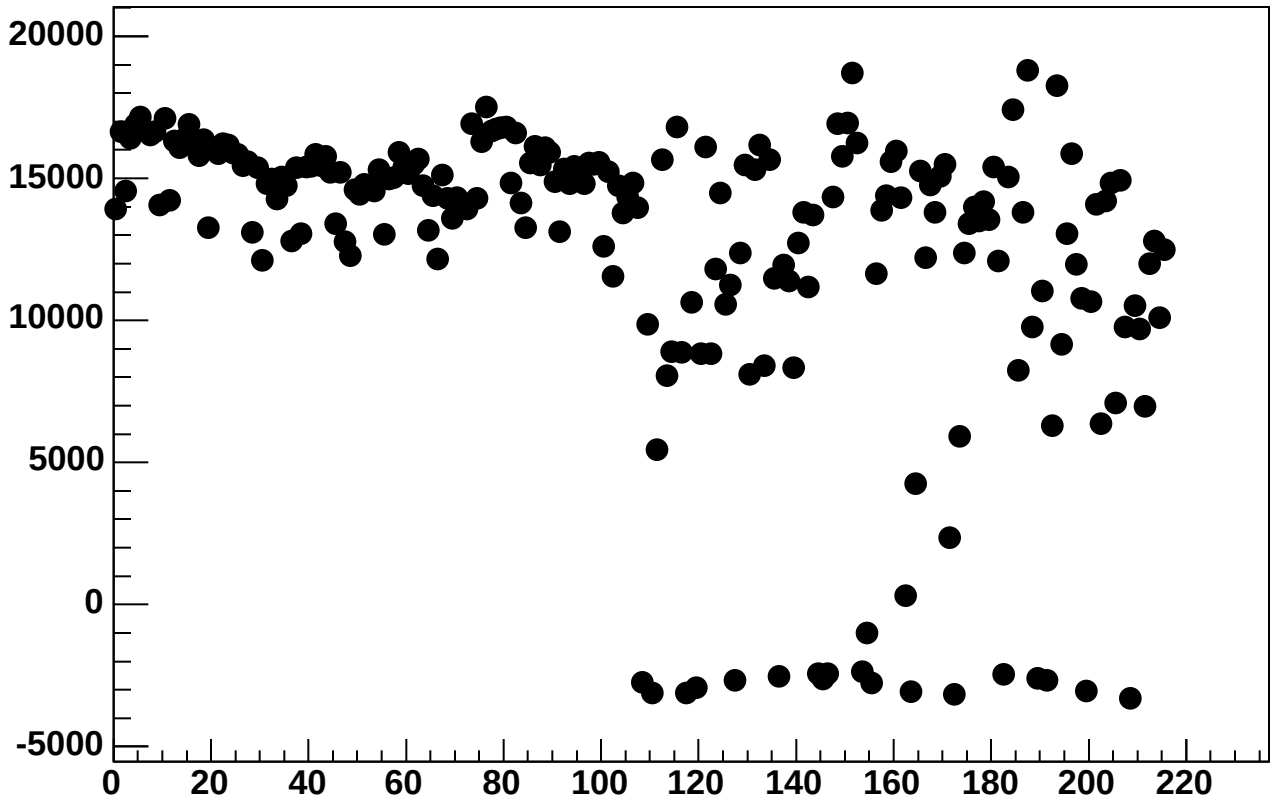
Enable 0, DAC=1600, Hold=210, ADC Mean vs 18*Chip+Chan



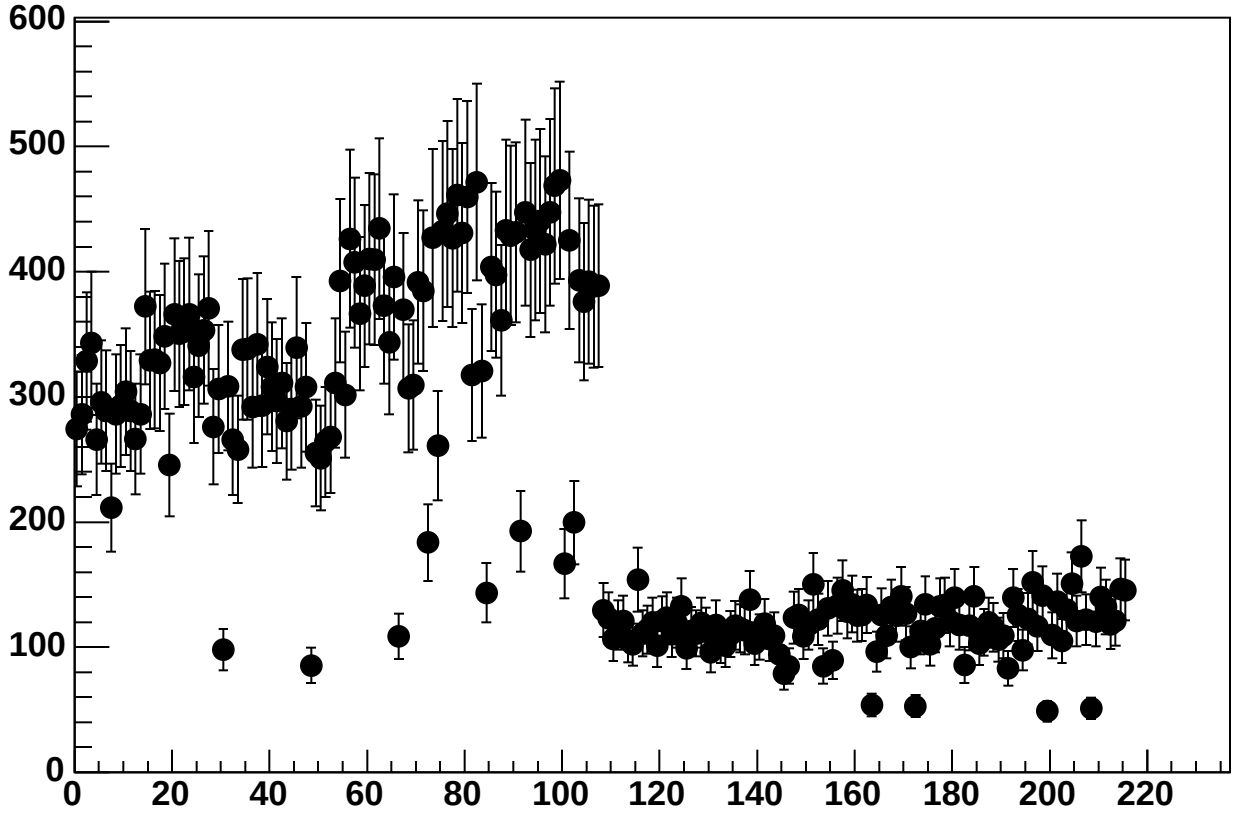
Enable 0, DAC=1600, Hold=210, ADC Noise vs 18*Chip+Chan



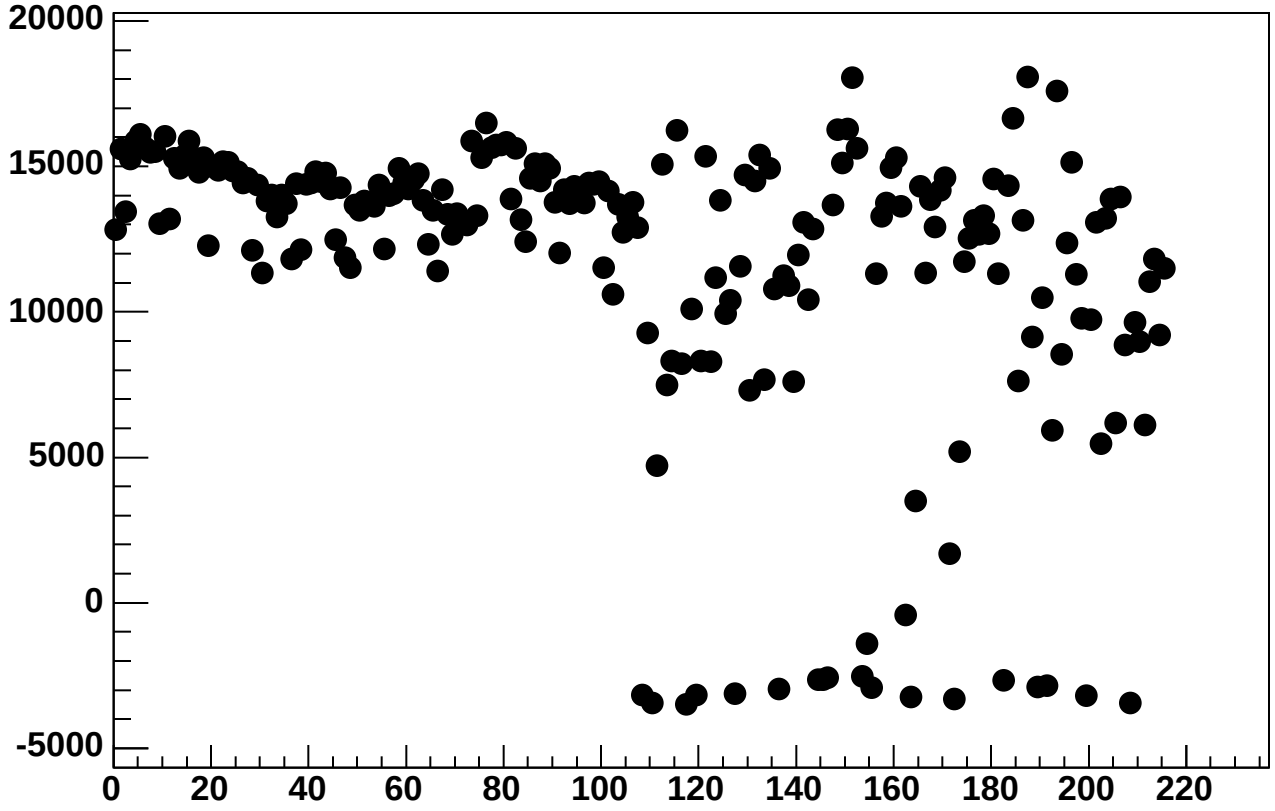
Enable 0, DAC=1600, Hold=215, ADC Mean vs 18*Chip+Chan



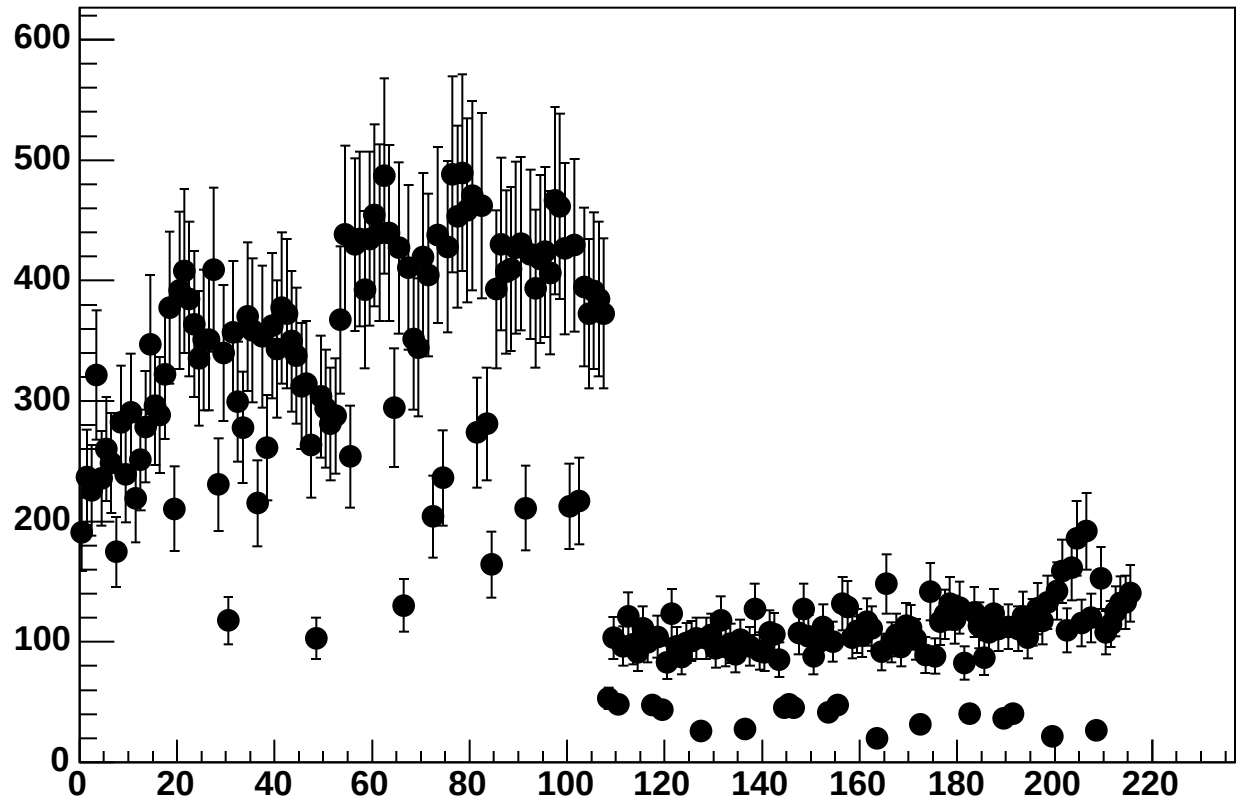
Enable 0, DAC=1600, Hold=215, ADC Noise vs 18*Chip+Chan



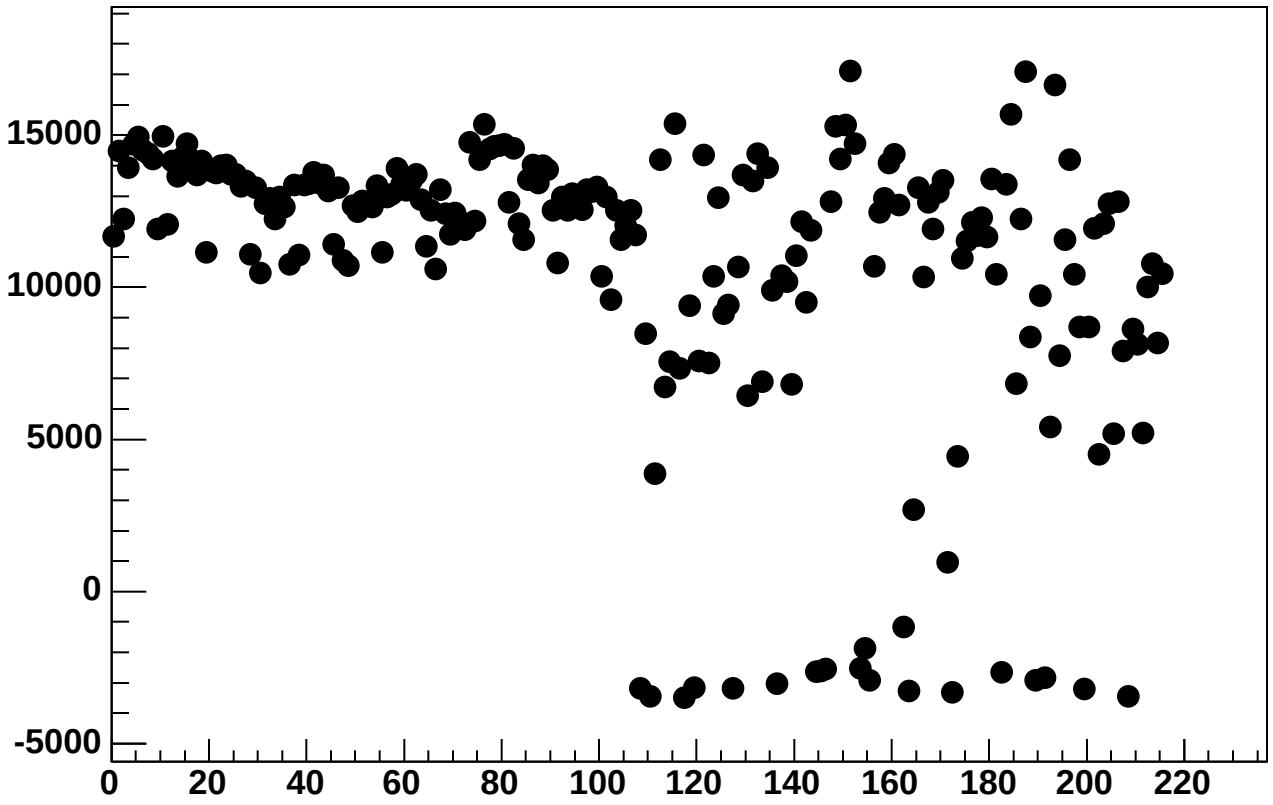
Enable 0, DAC=1600, Hold=220, ADC Mean vs 18*Chip+Chan



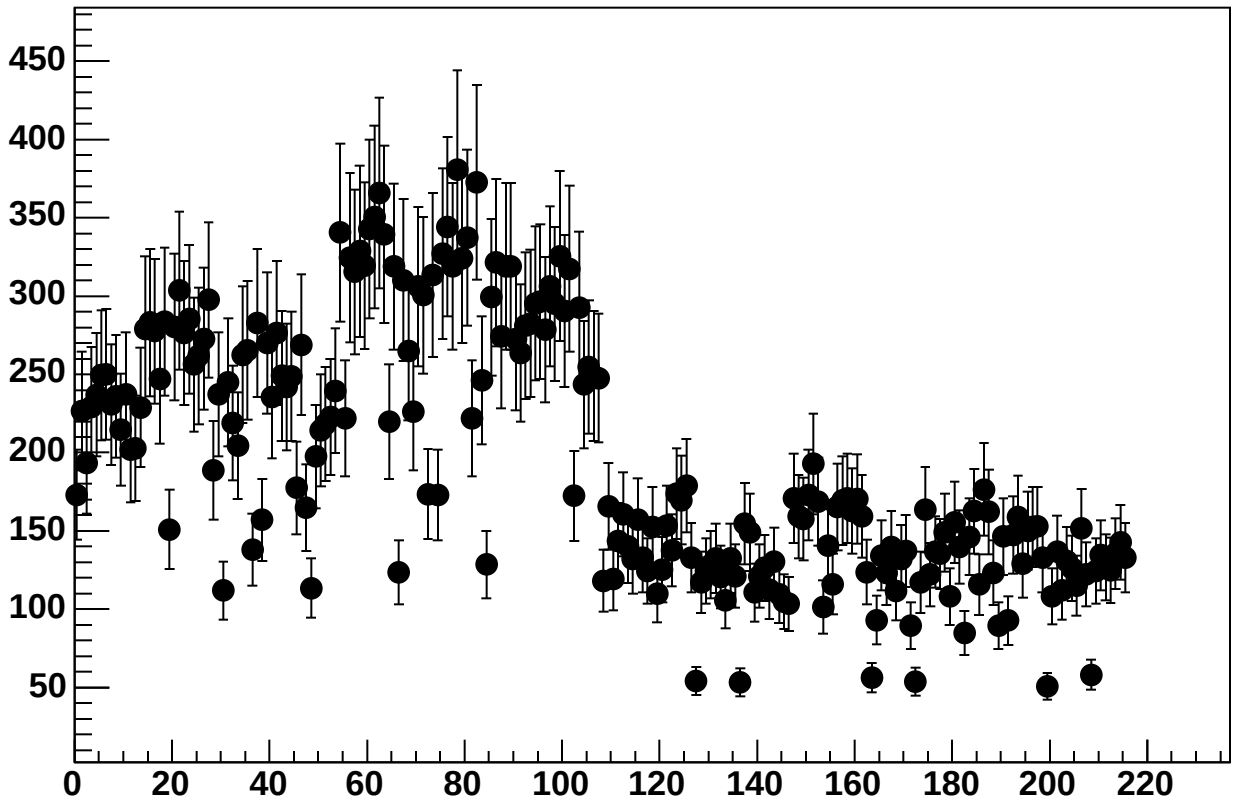
Enable 0, DAC=1600, Hold=220, ADC Noise vs 18*Chip+Chan



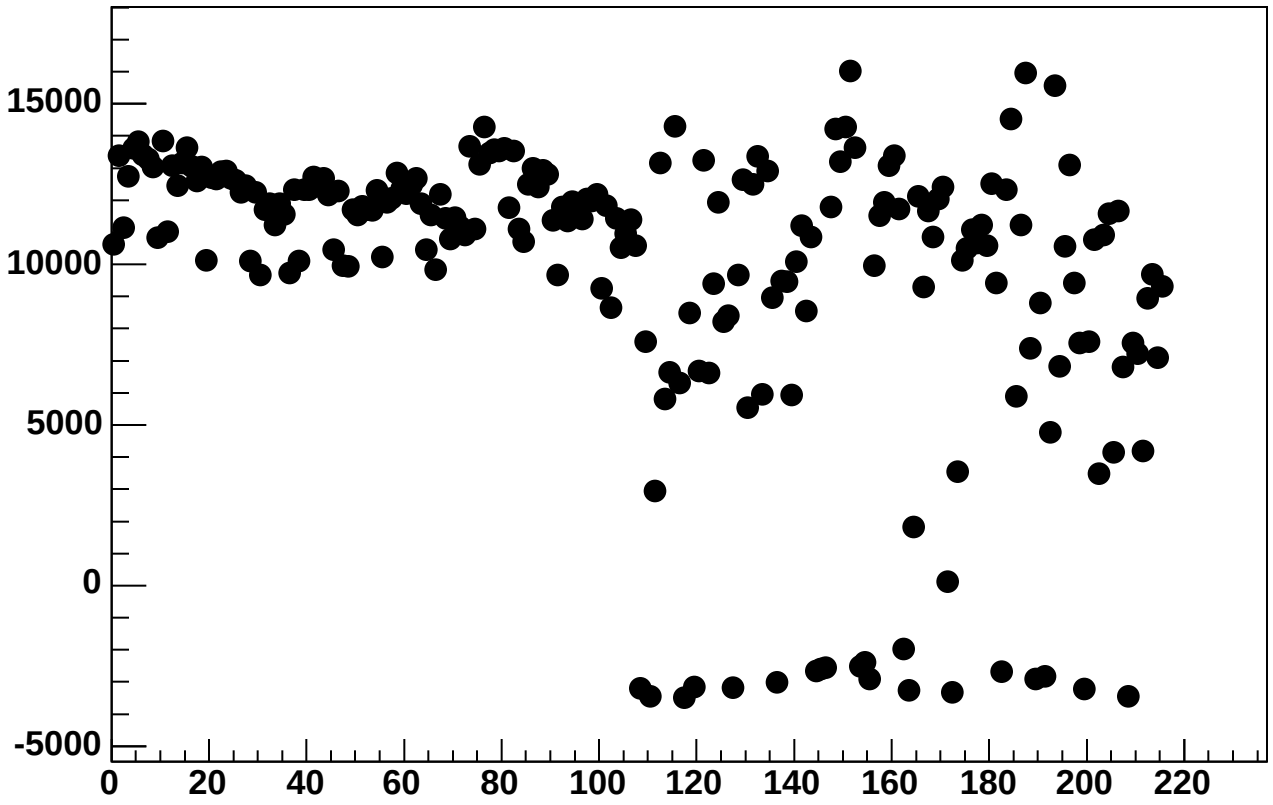
Enable 0, DAC=1600, Hold=225, ADC Mean vs 18*Chip+Chan



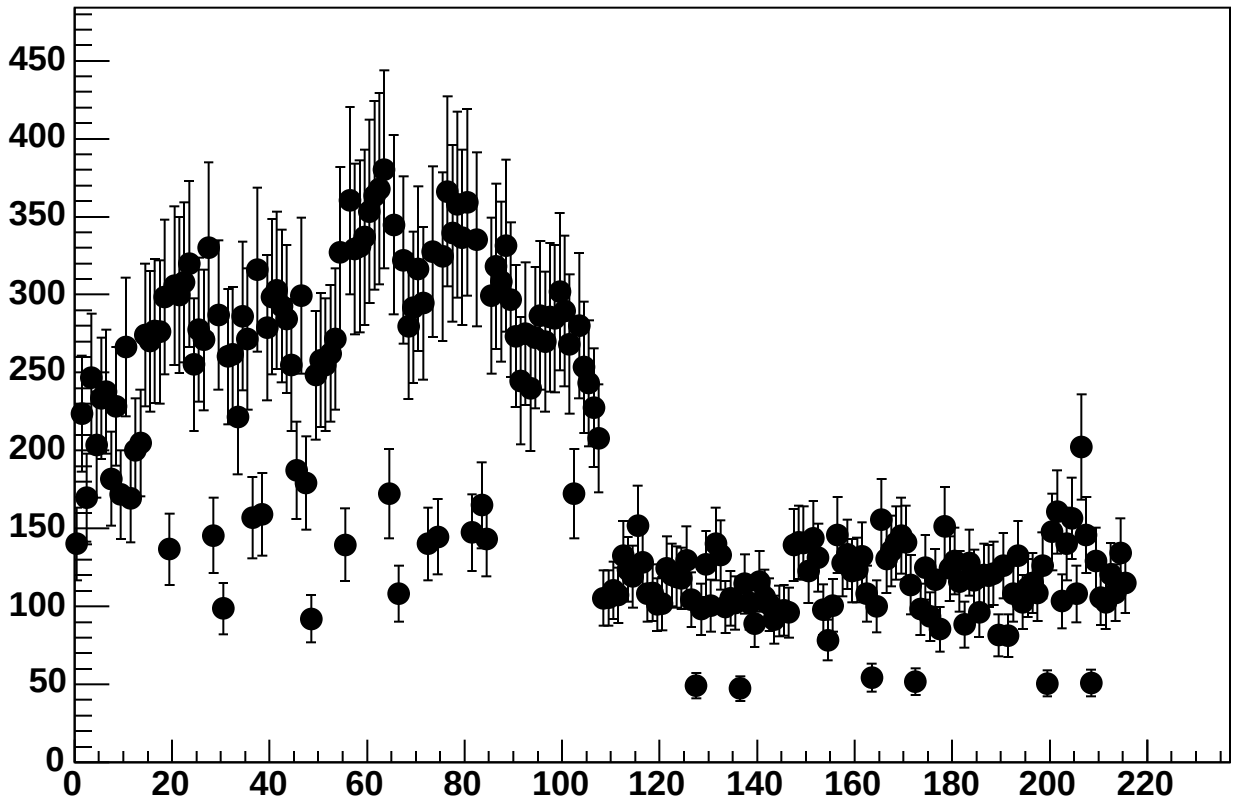
Enable 0, DAC=1600, Hold=225, ADC Noise vs 18*Chip+Chan



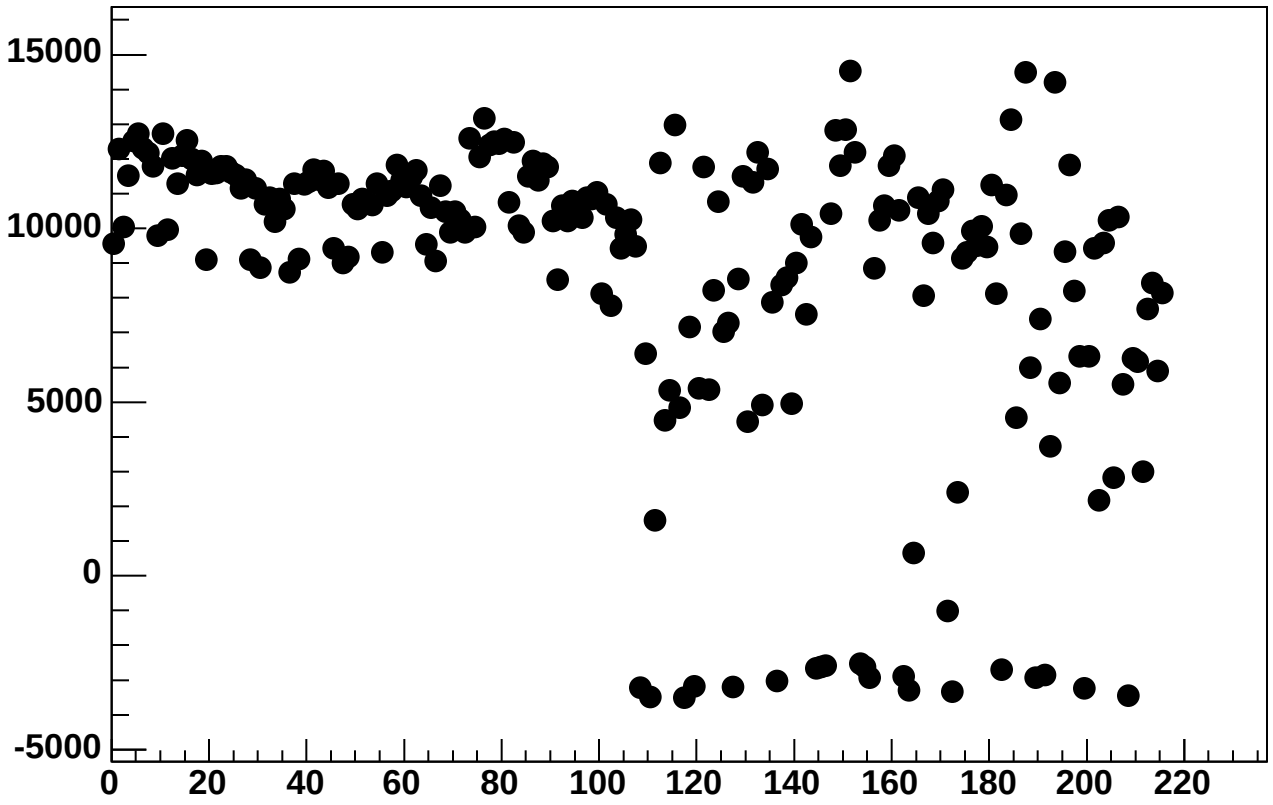
Enable 0, DAC=1600, Hold=230, ADC Mean vs 18*Chip+Chan



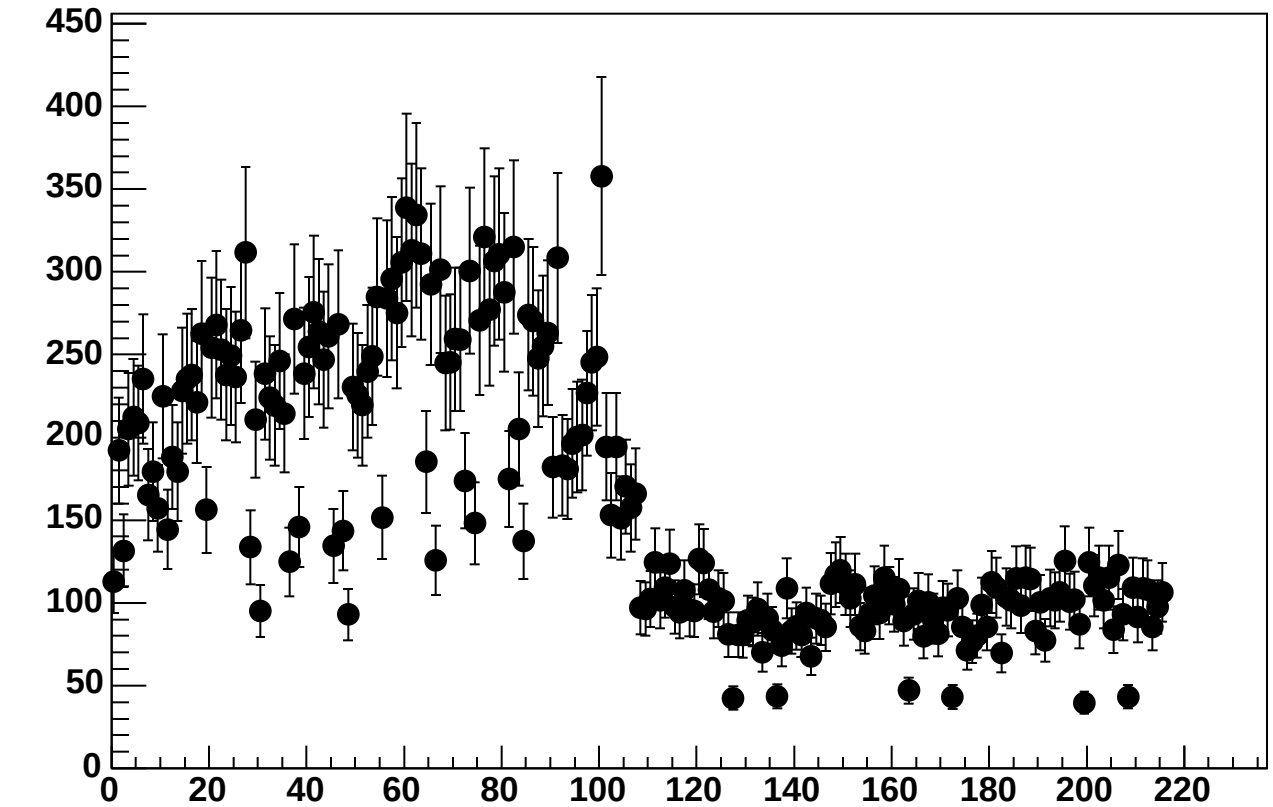
Enable 0, DAC=1600, Hold=230, ADC Noise vs 18*Chip+Chan



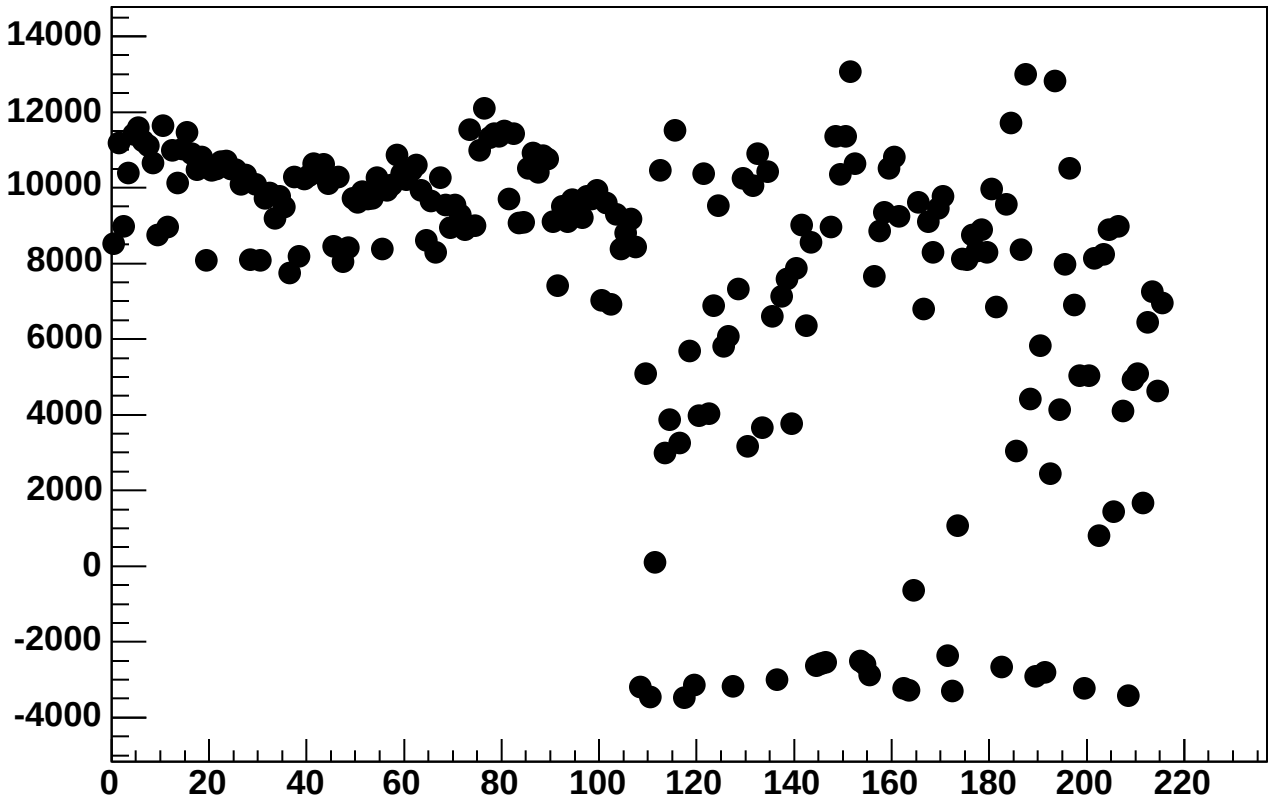
Enable 0, DAC=1600, Hold=235, ADC Mean vs 18*Chip+Chan



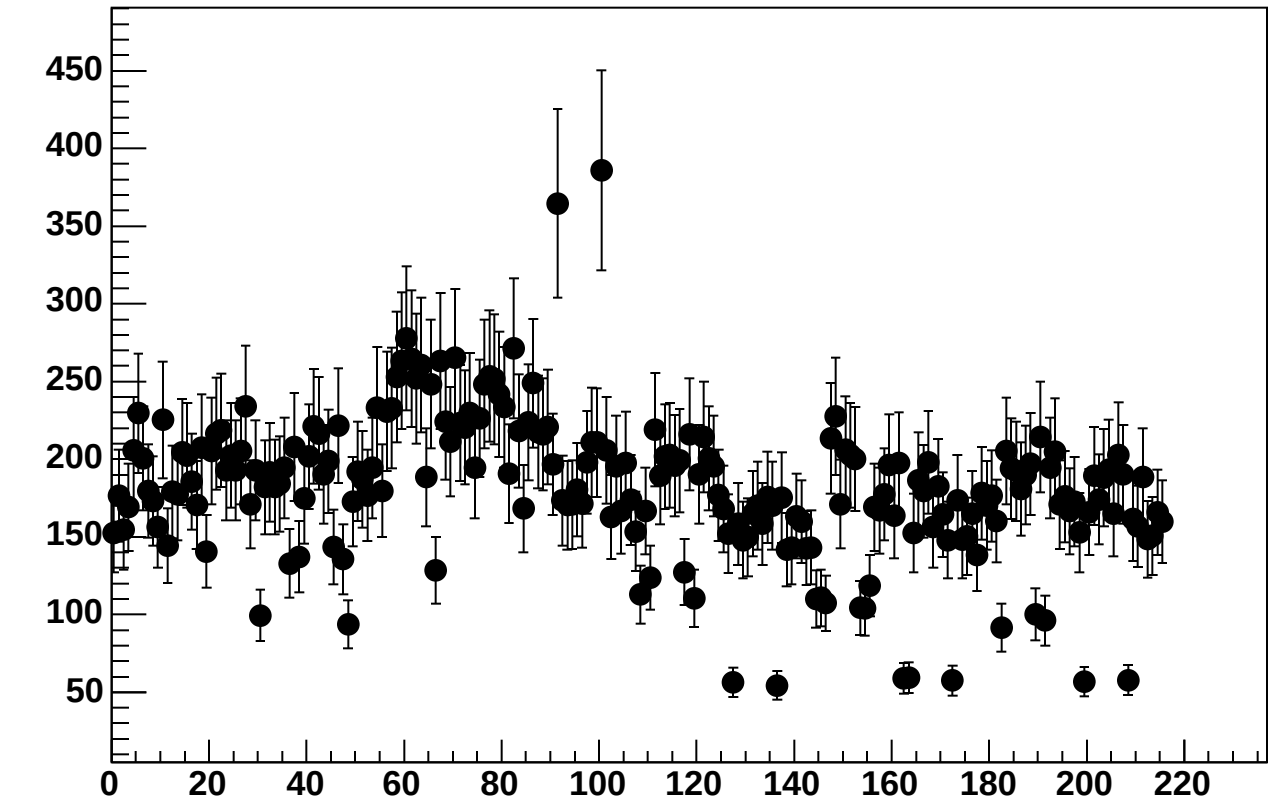
Enable 0, DAC=1600, Hold=235, ADC Noise vs 18*Chip+Chan



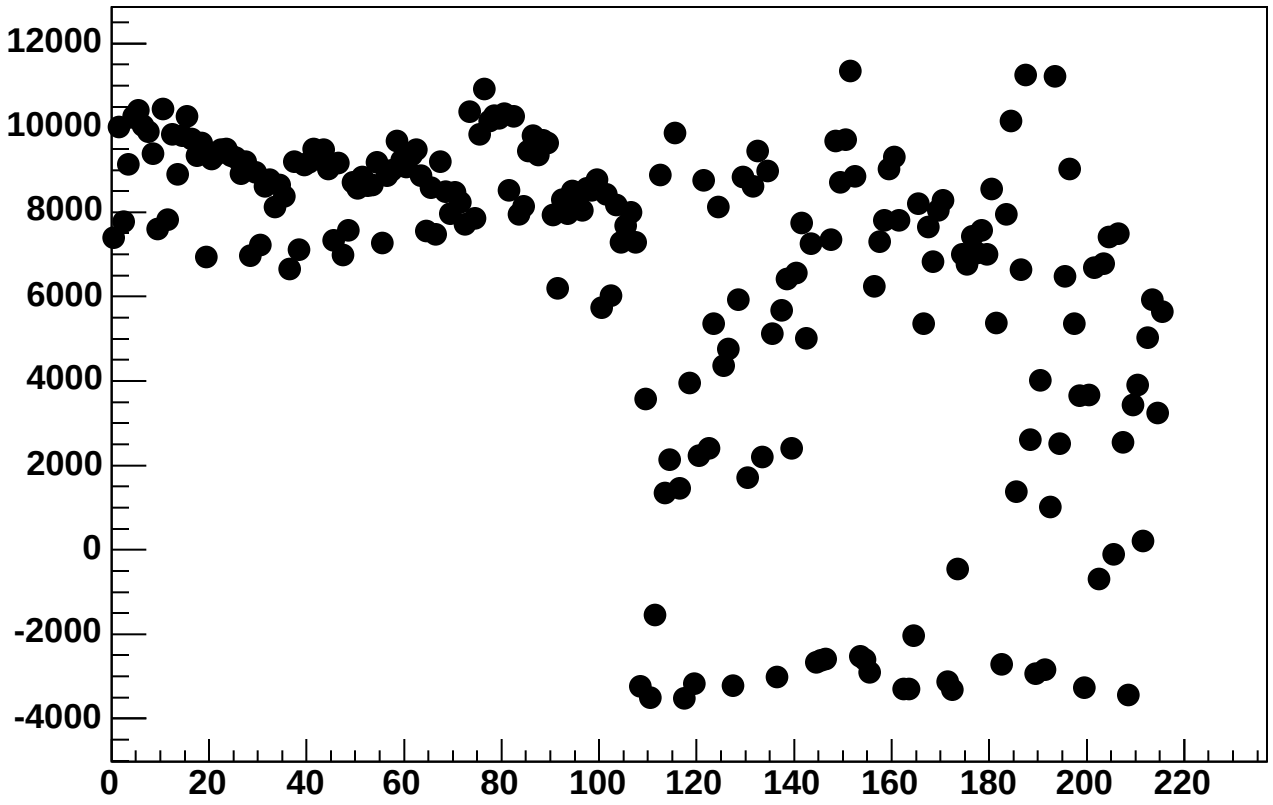
Enable 0, DAC=1600, Hold=240, ADC Mean vs 18*Chip+Chan



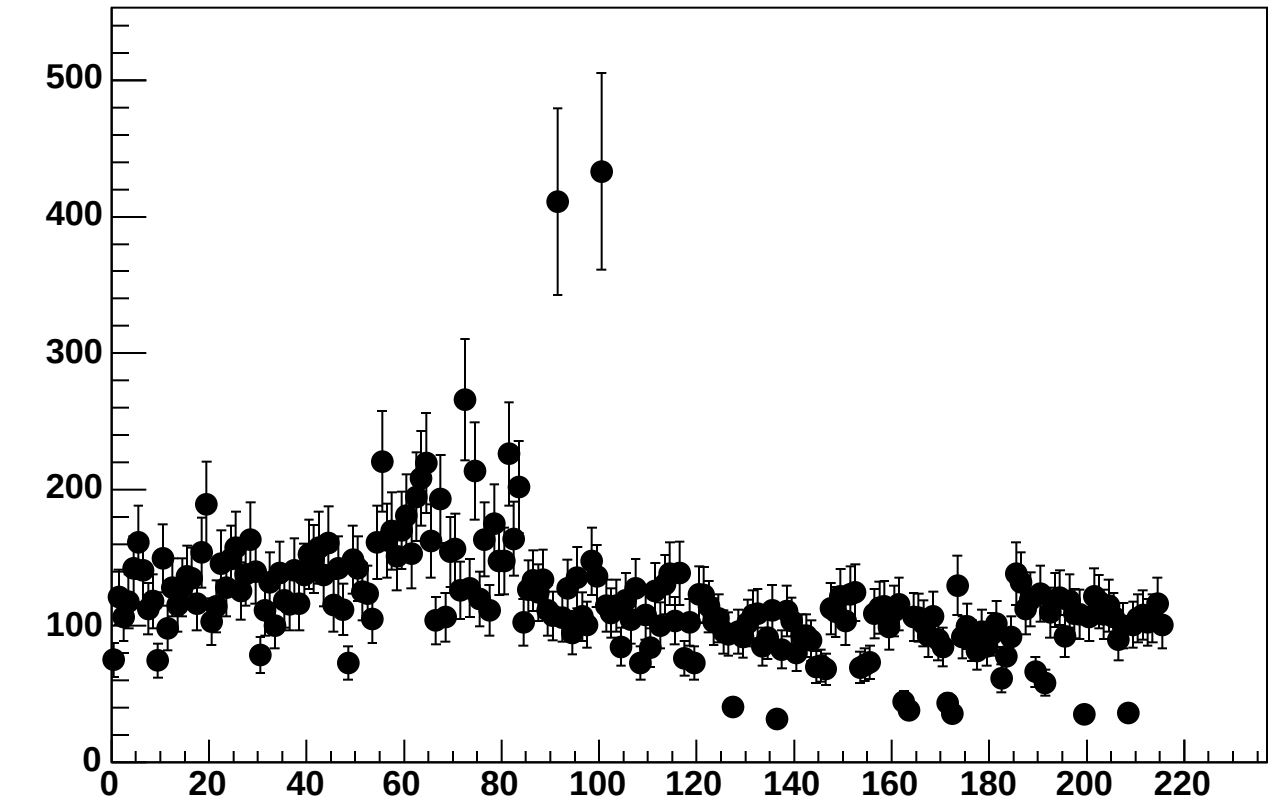
Enable 0, DAC=1600, Hold=240, ADC Noise vs 18*Chip+Chan



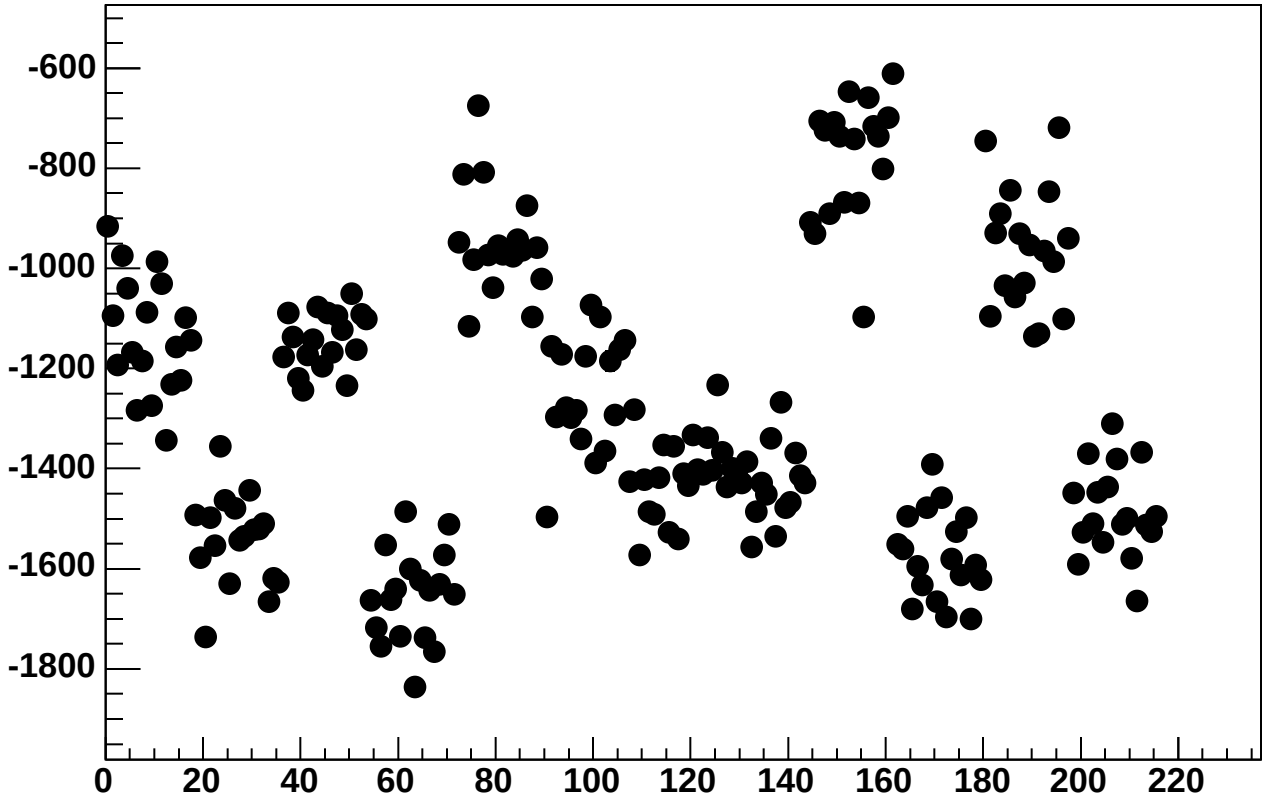
Enable 0, DAC=1600, Hold=245, ADC Mean vs 18*Chip+Chan



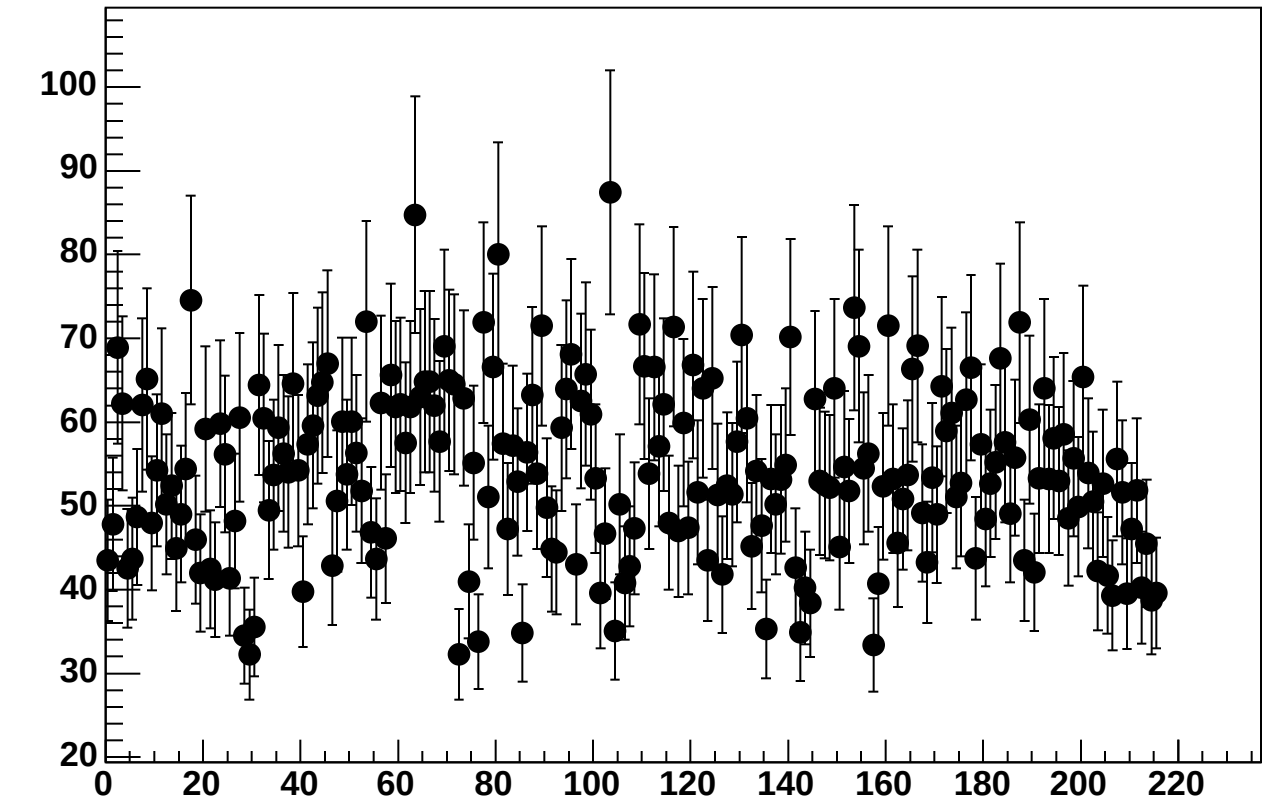
Enable 0, DAC=1600, Hold=245, ADC Noise vs 18*Chip+Chan



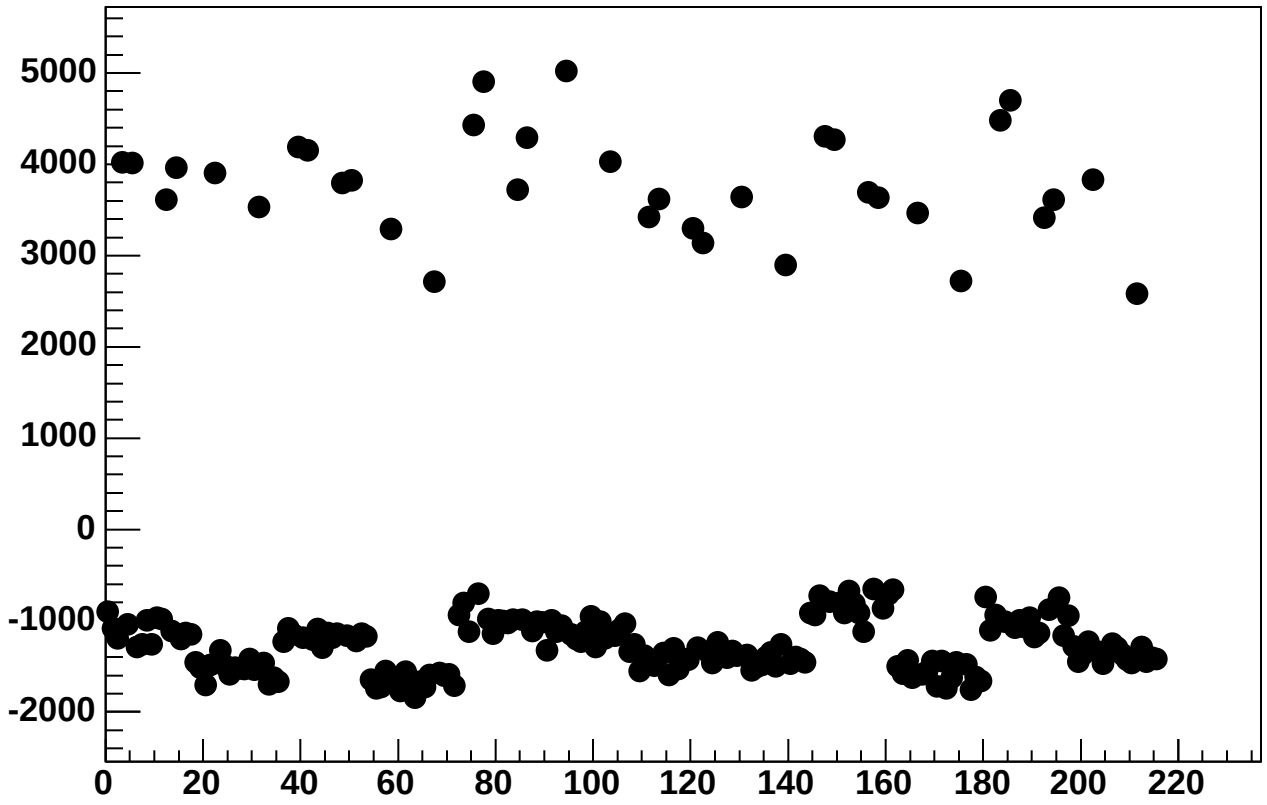
Enable 1, DAC=1600, Hold=0, ADC Mean vs 18*Chip+Chan



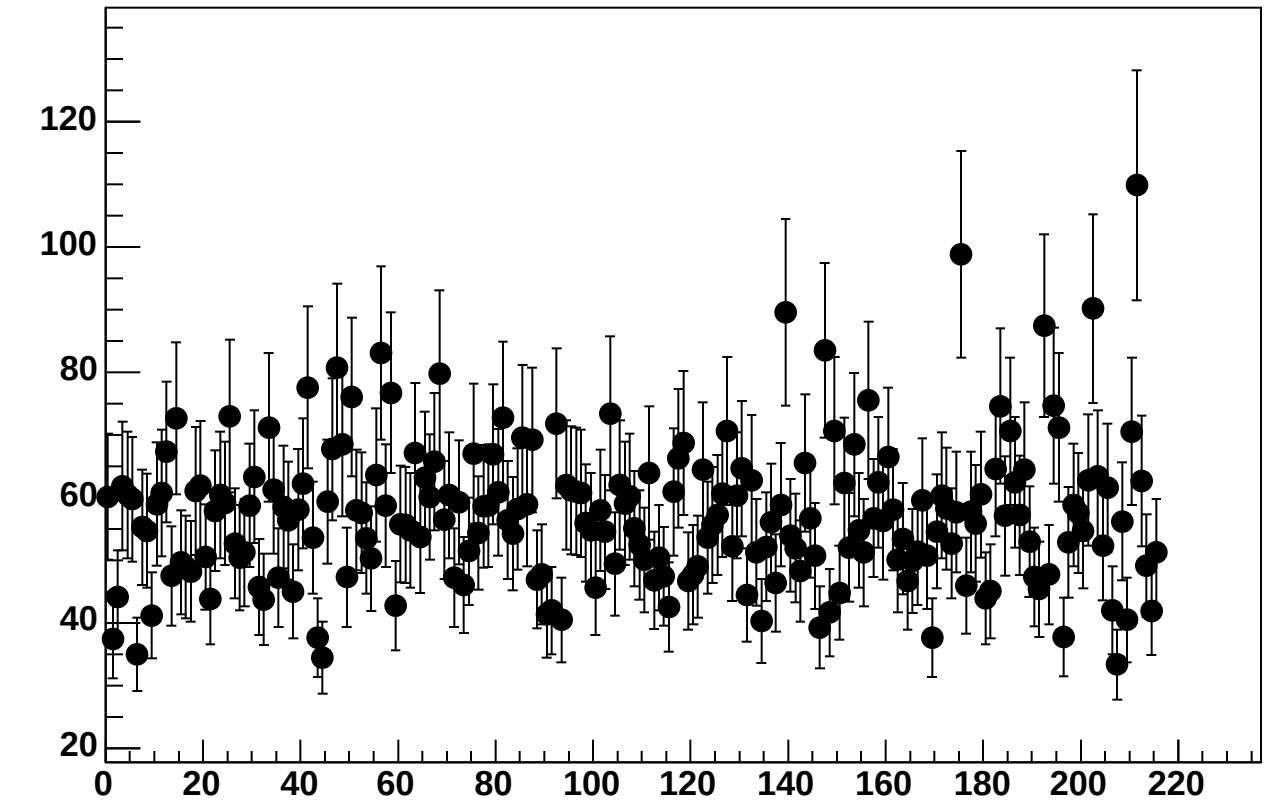
Enable 1, DAC=1600, Hold=0, ADC Noise vs 18*Chip+Chan



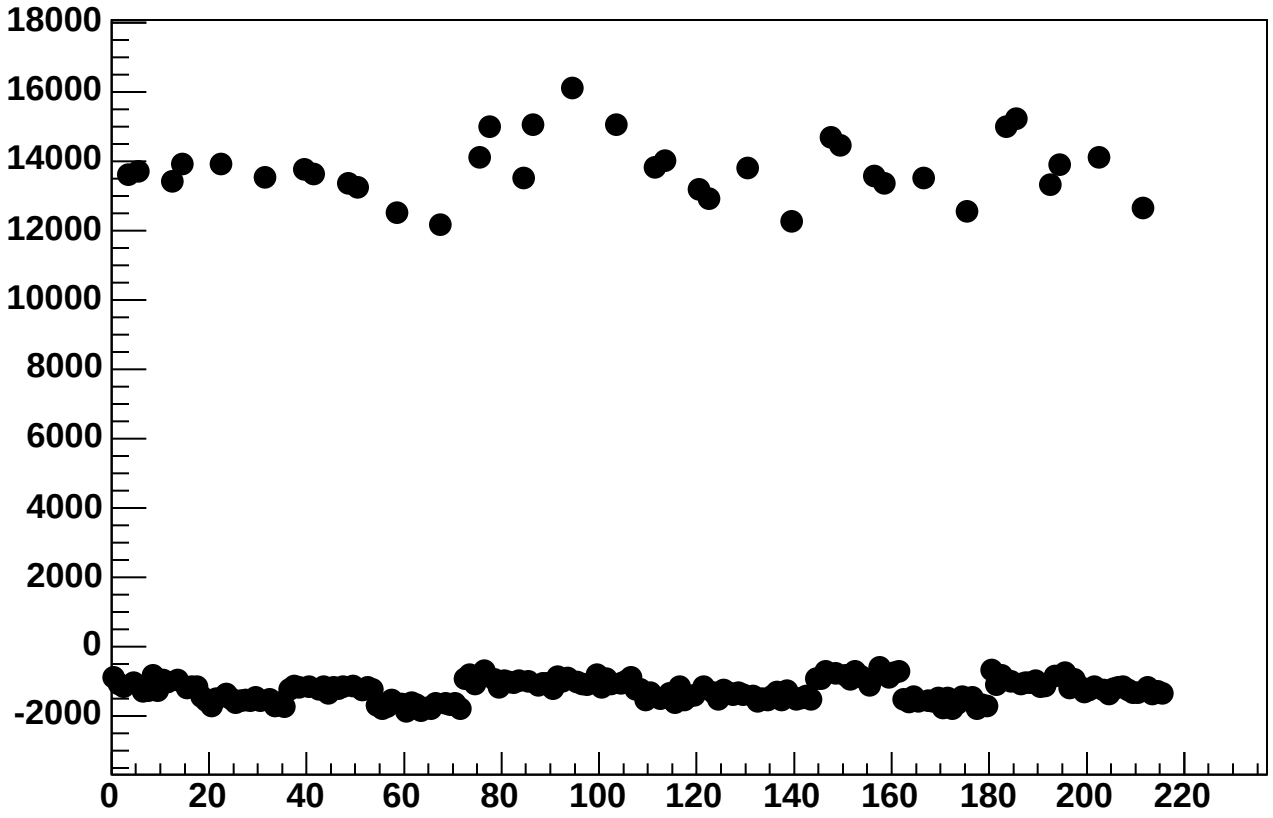
Enable 1, DAC=1600, Hold=5, ADC Mean vs 18*Chip+Chan



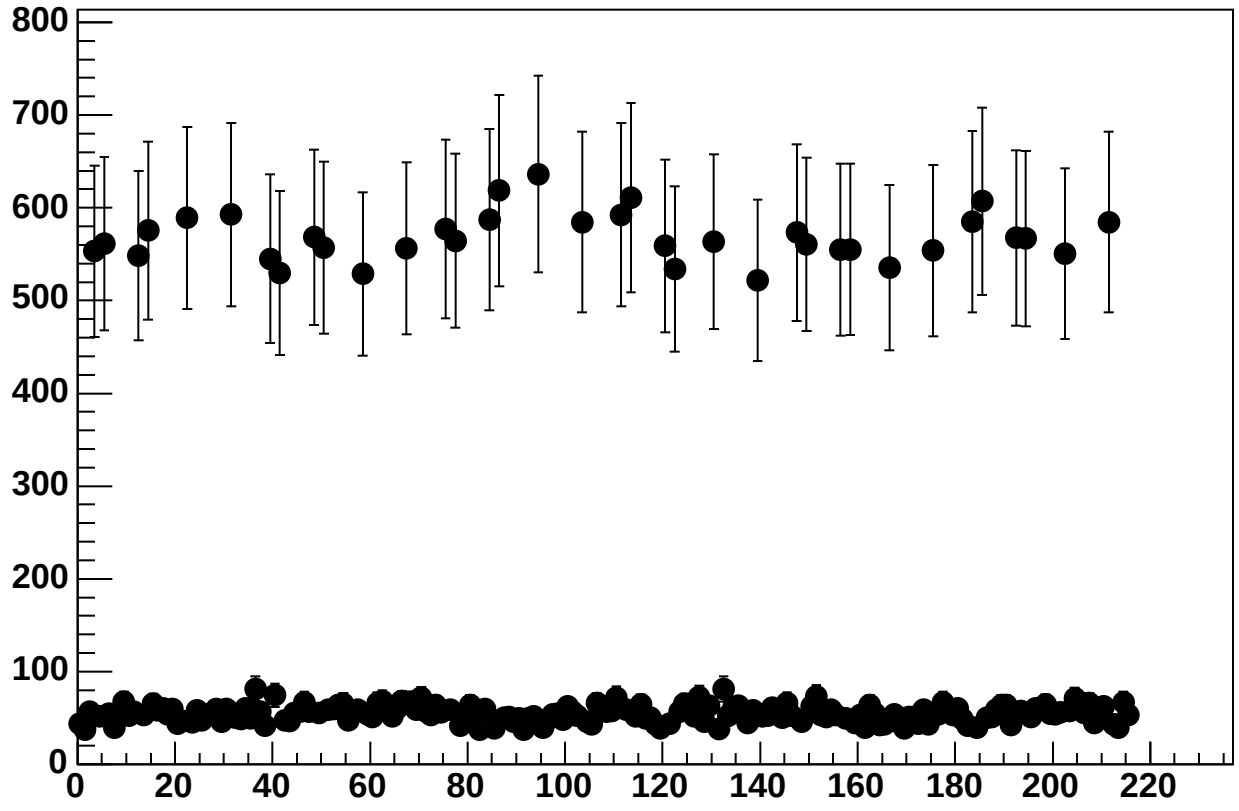
Enable 1, DAC=1600, Hold=5, ADC Noise vs 18*Chip+Chan



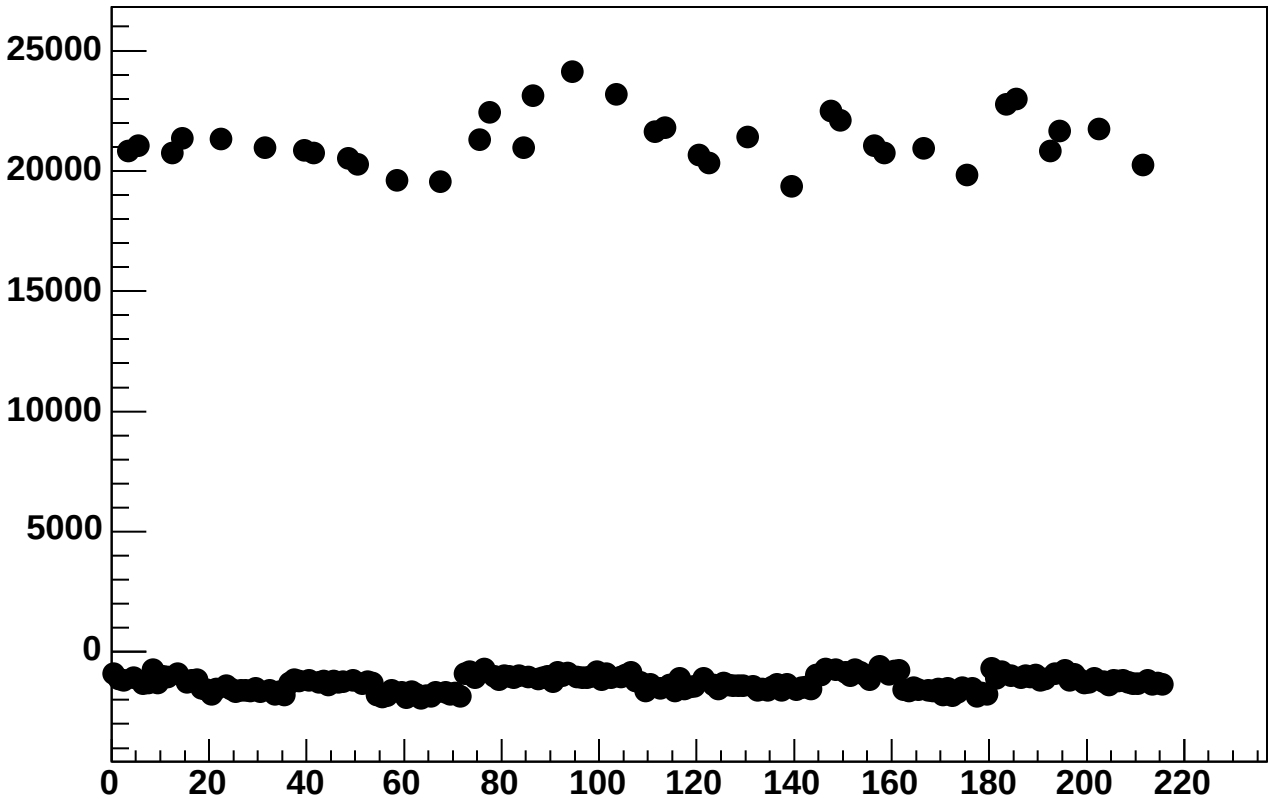
Enable 1, DAC=1600, Hold=10, ADC Mean vs 18*Chip+Chan



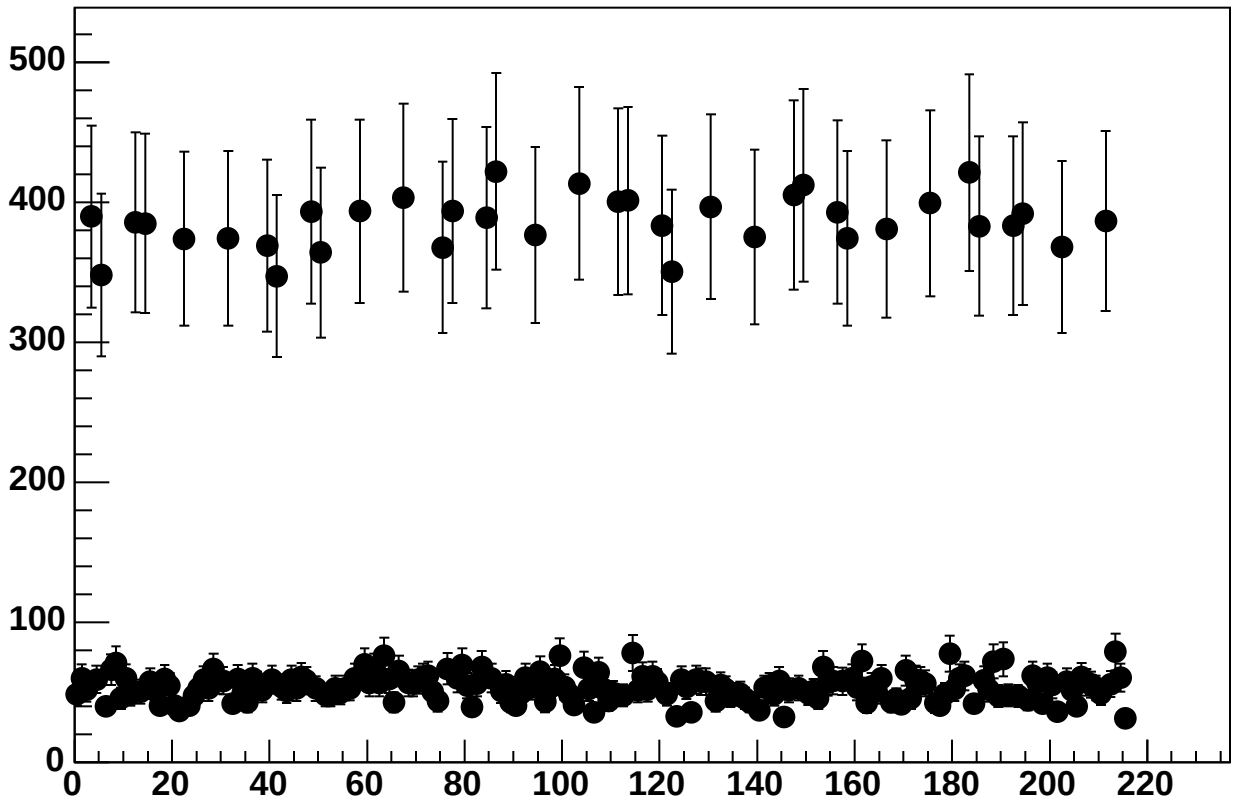
Enable 1, DAC=1600, Hold=10, ADC Noise vs 18*Chip+Chan



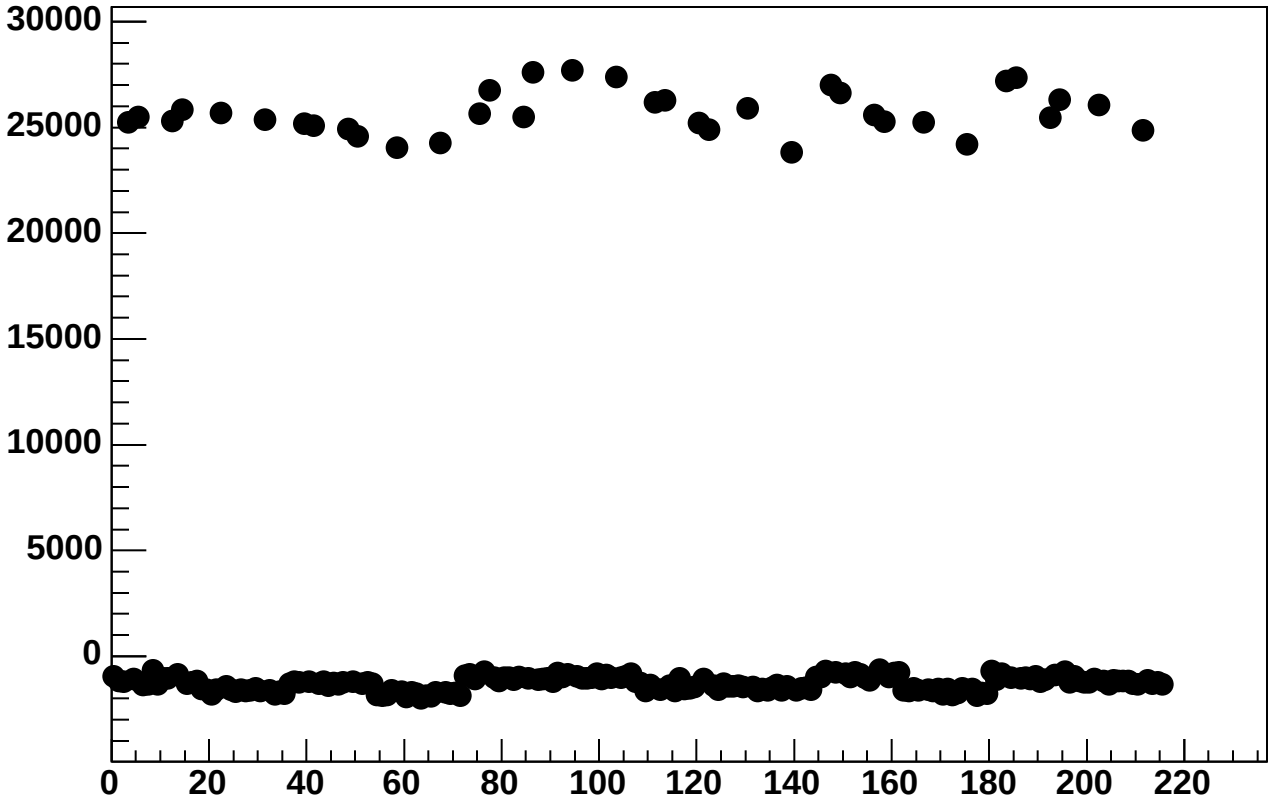
Enable 1, DAC=1600, Hold=15, ADC Mean vs 18*Chip+Chan



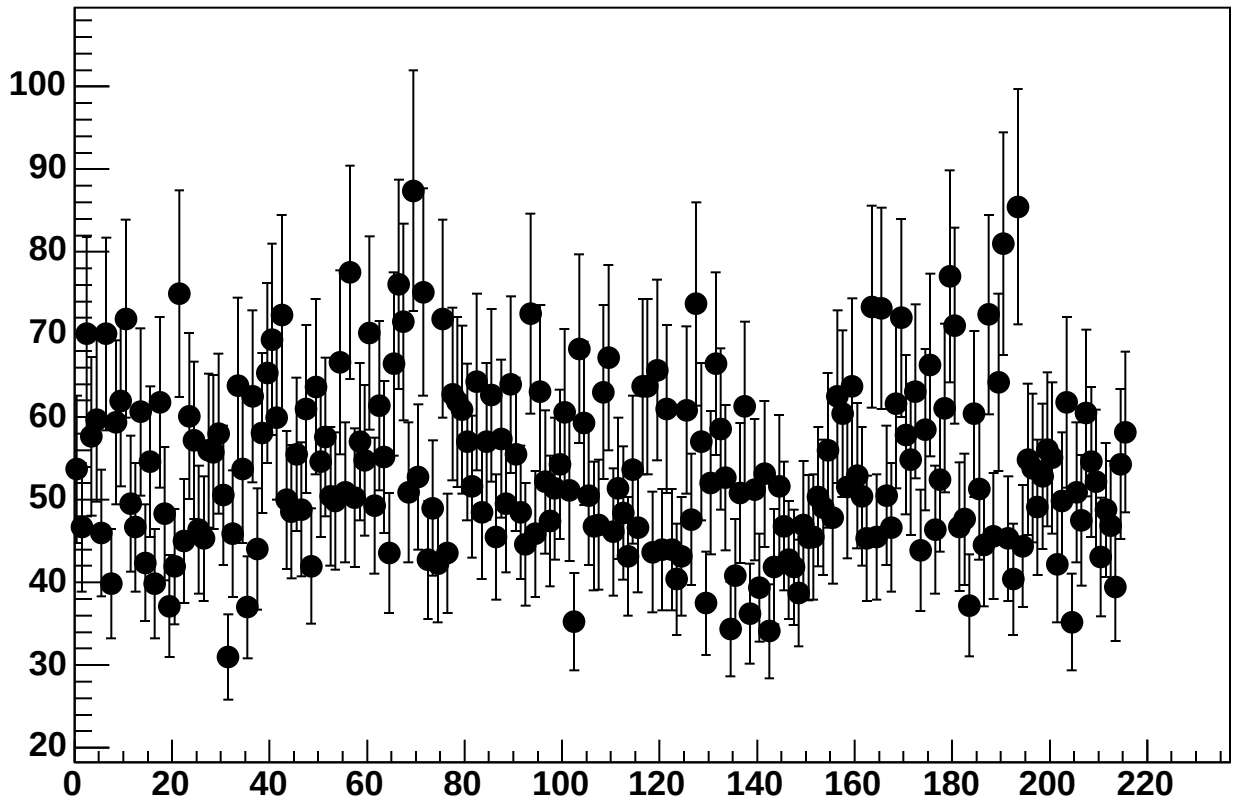
Enable 1, DAC=1600, Hold=15, ADC Noise vs 18*Chip+Chan



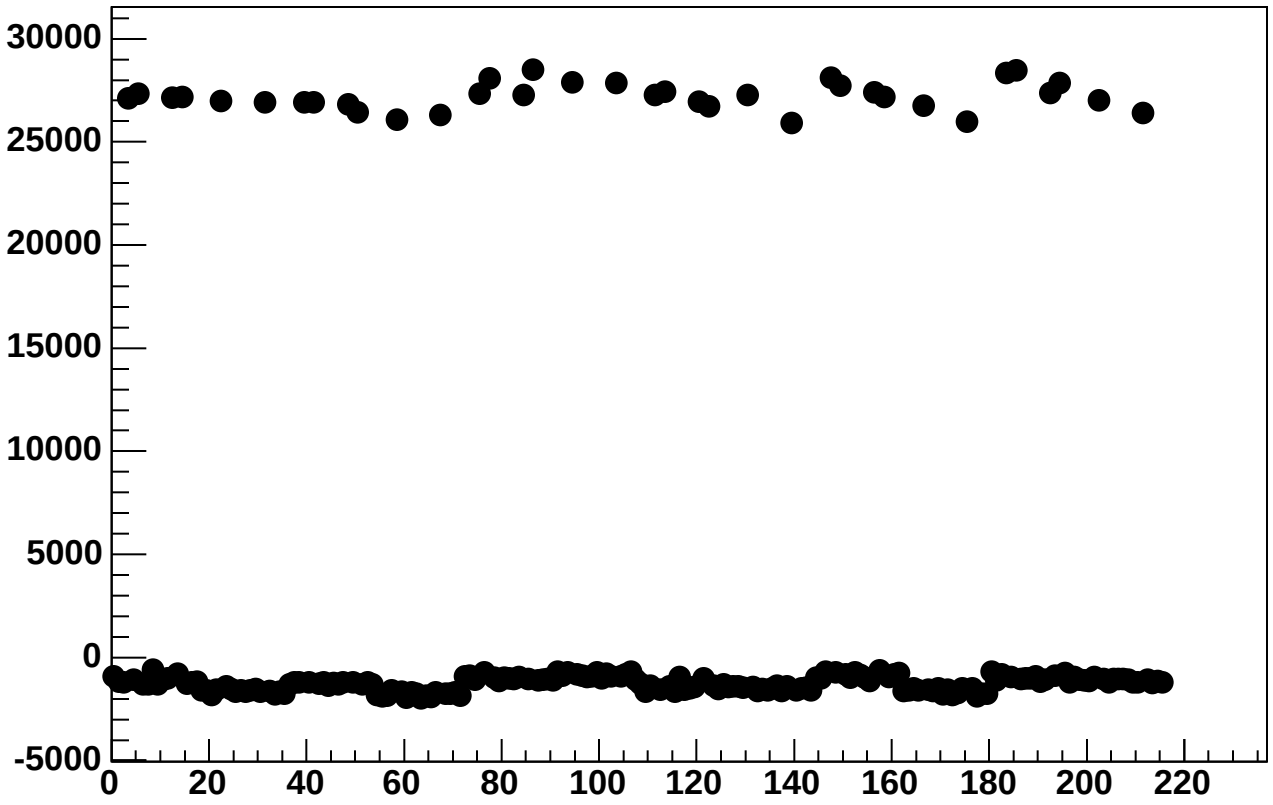
Enable 1, DAC=1600, Hold=20, ADC Mean vs 18*Chip+Chan



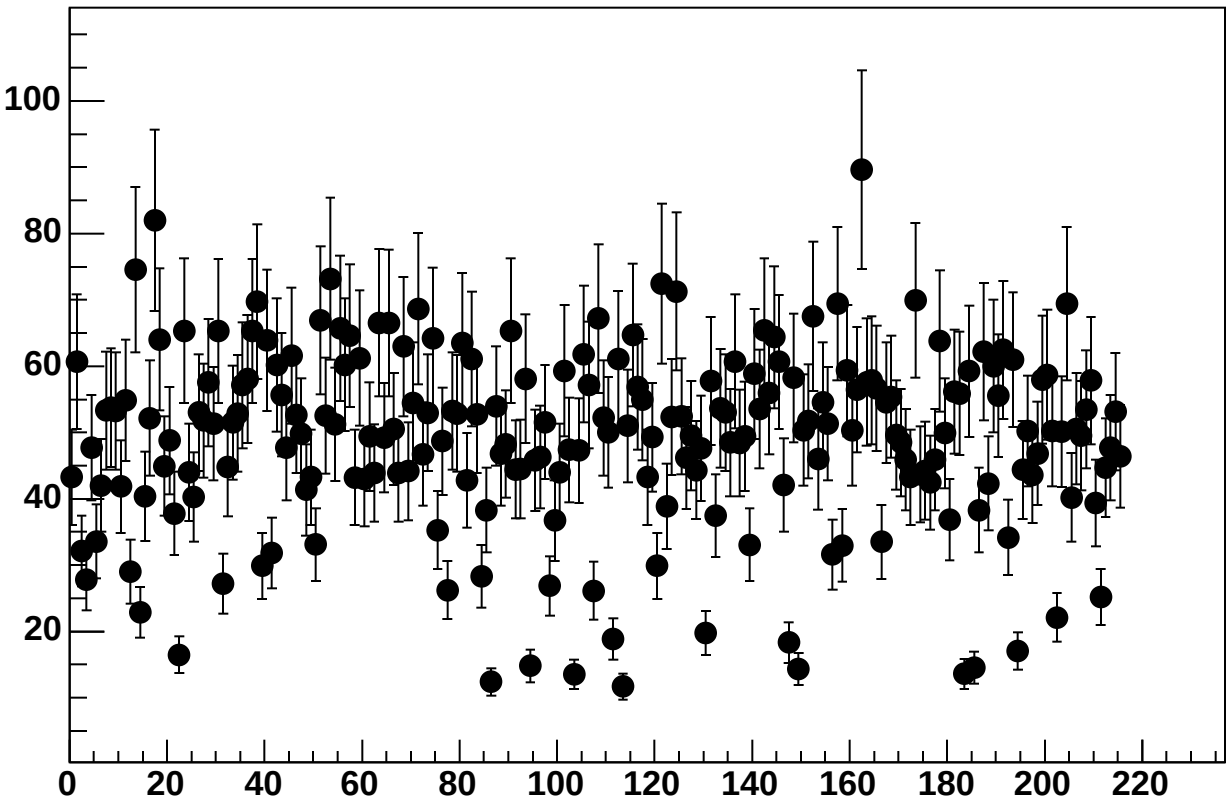
Enable 1, DAC=1600, Hold=20, ADC Noise vs 18*Chip+Chan



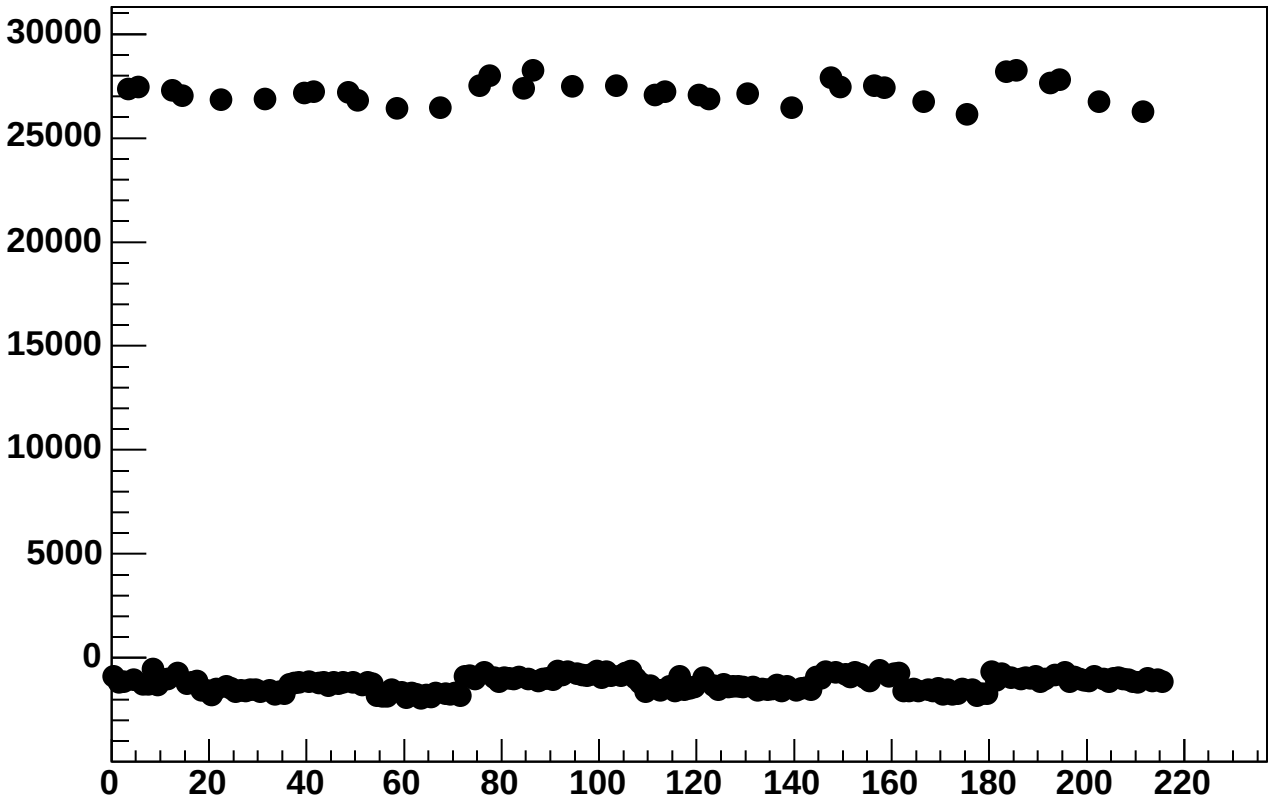
Enable 1, DAC=1600, Hold=25, ADC Mean vs 18*Chip+Chan



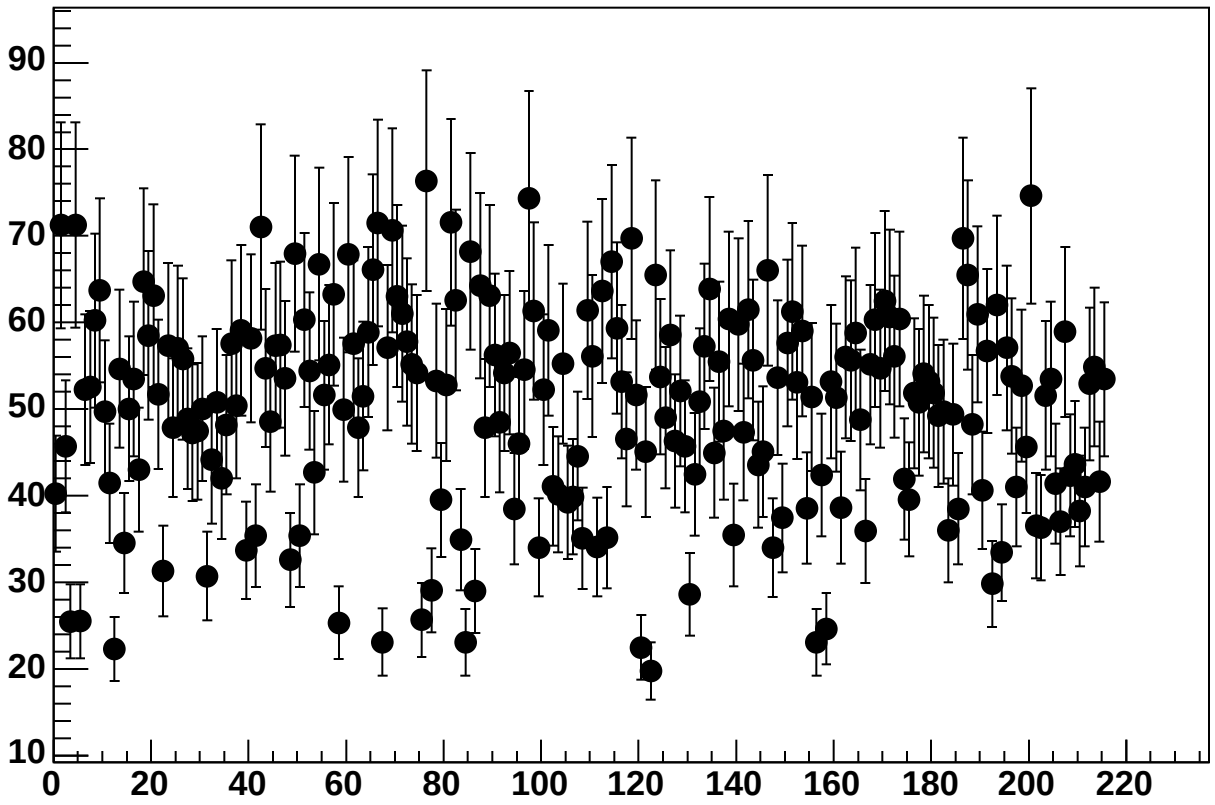
Enable 1, DAC=1600, Hold=25, ADC Noise vs 18*Chip+Chan



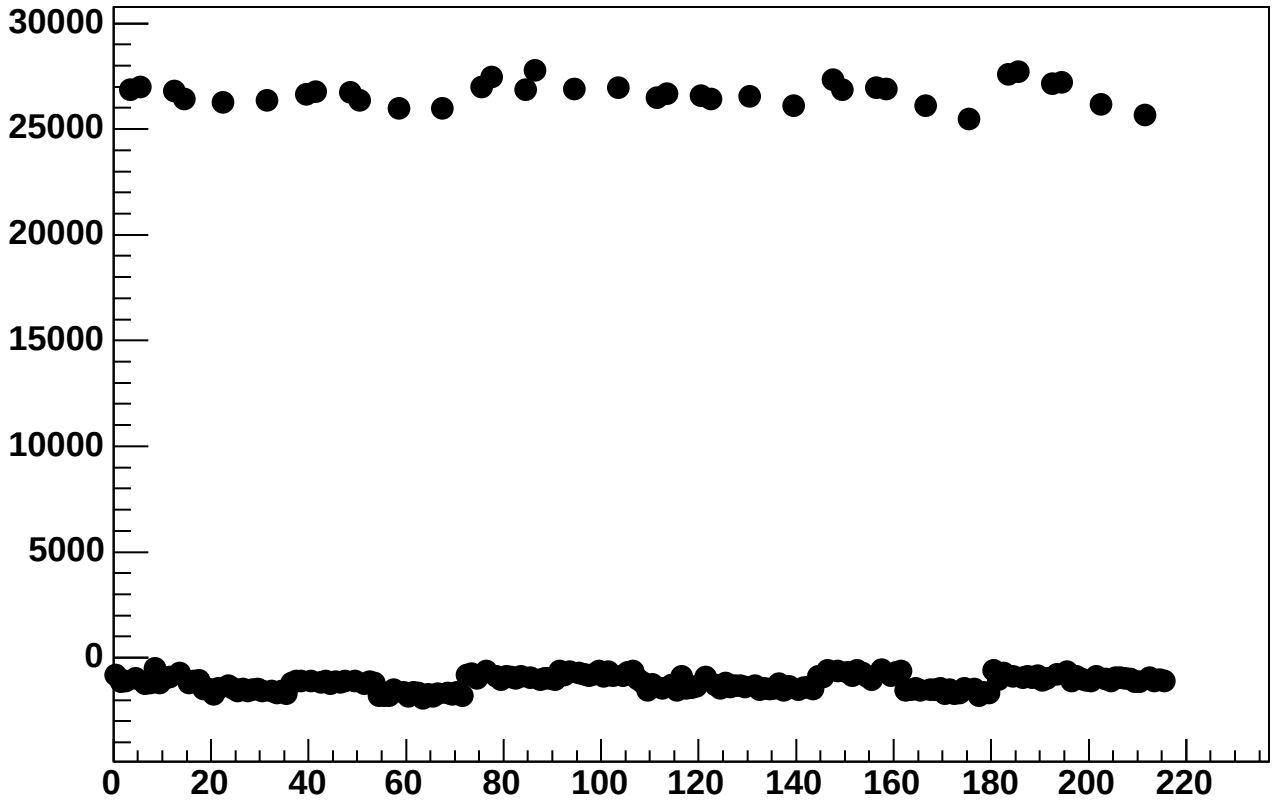
Enable 1, DAC=1600, Hold=30, ADC Mean vs 18*Chip+Chan



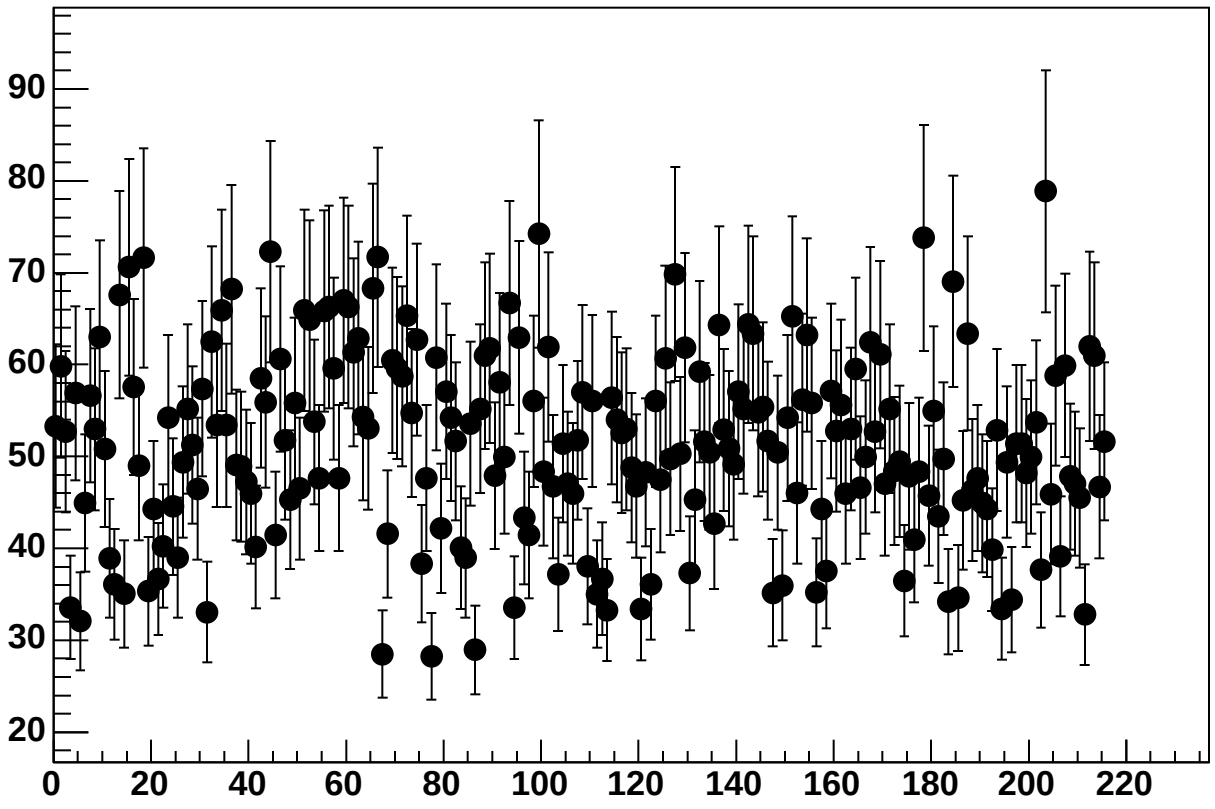
Enable 1, DAC=1600, Hold=30, ADC Noise vs 18*Chip+Chan



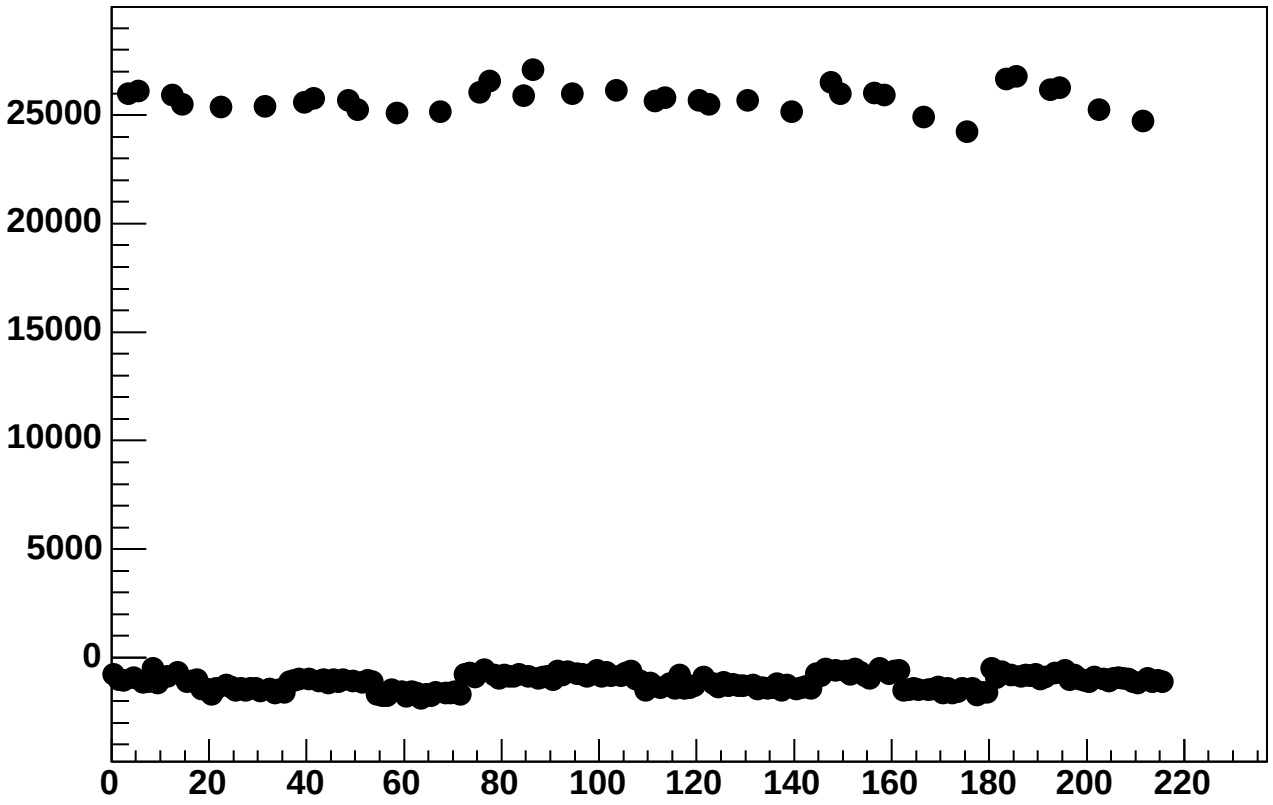
Enable 1, DAC=1600, Hold=35, ADC Mean vs 18*Chip+Chan



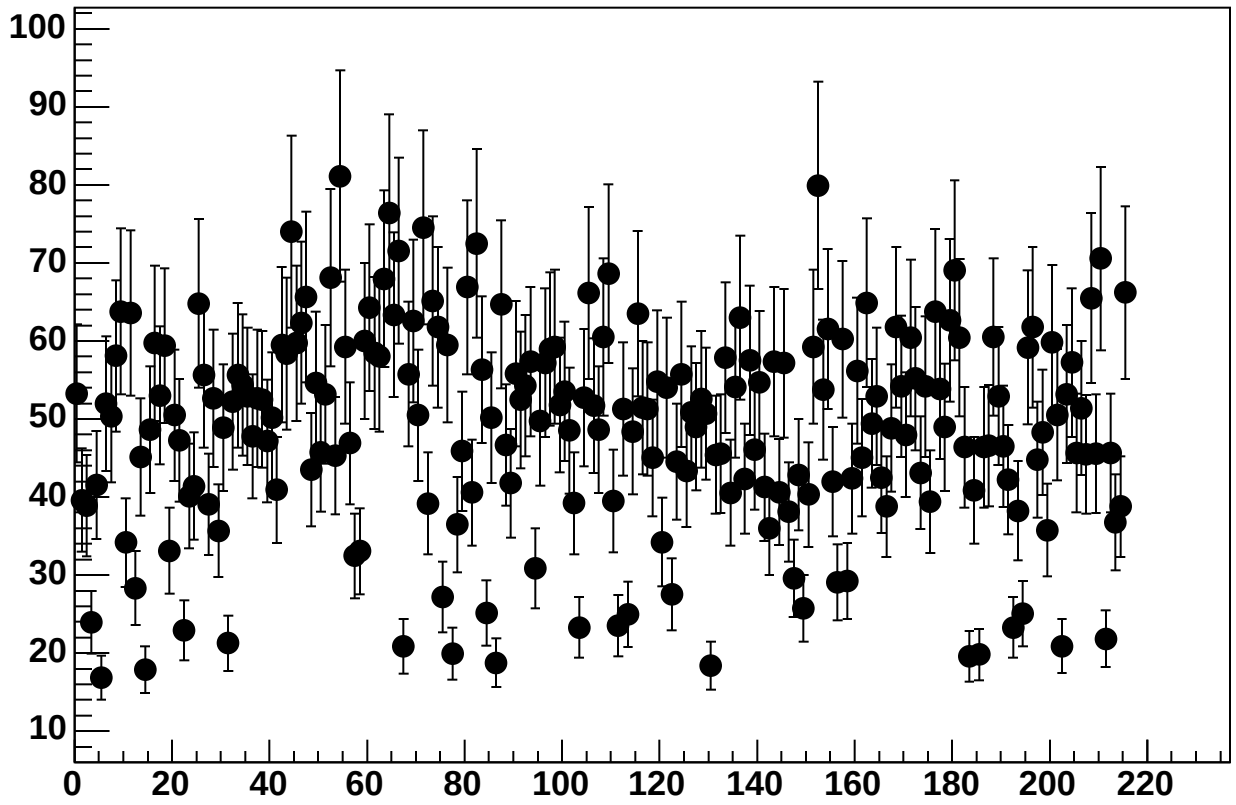
Enable 1, DAC=1600, Hold=35, ADC Noise vs 18*Chip+Chan



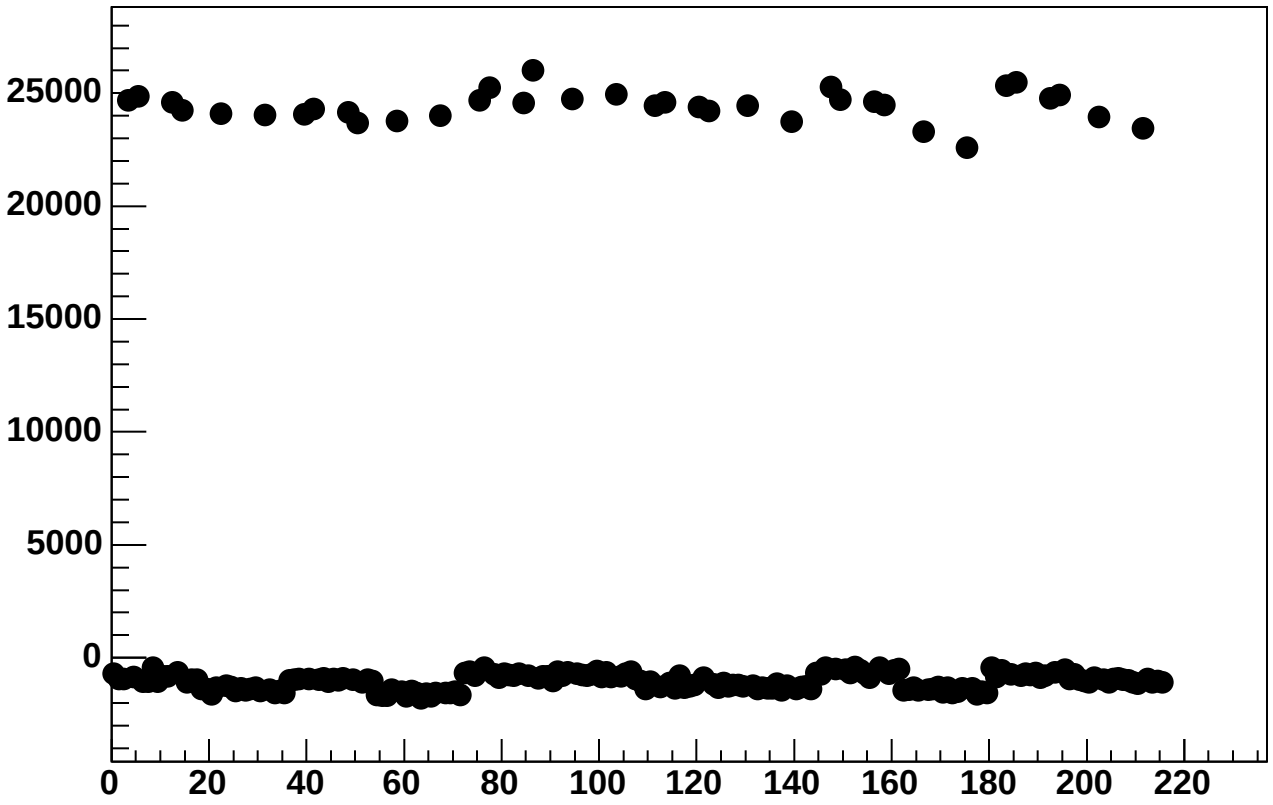
Enable 1, DAC=1600, Hold=40, ADC Mean vs 18*Chip+Chan



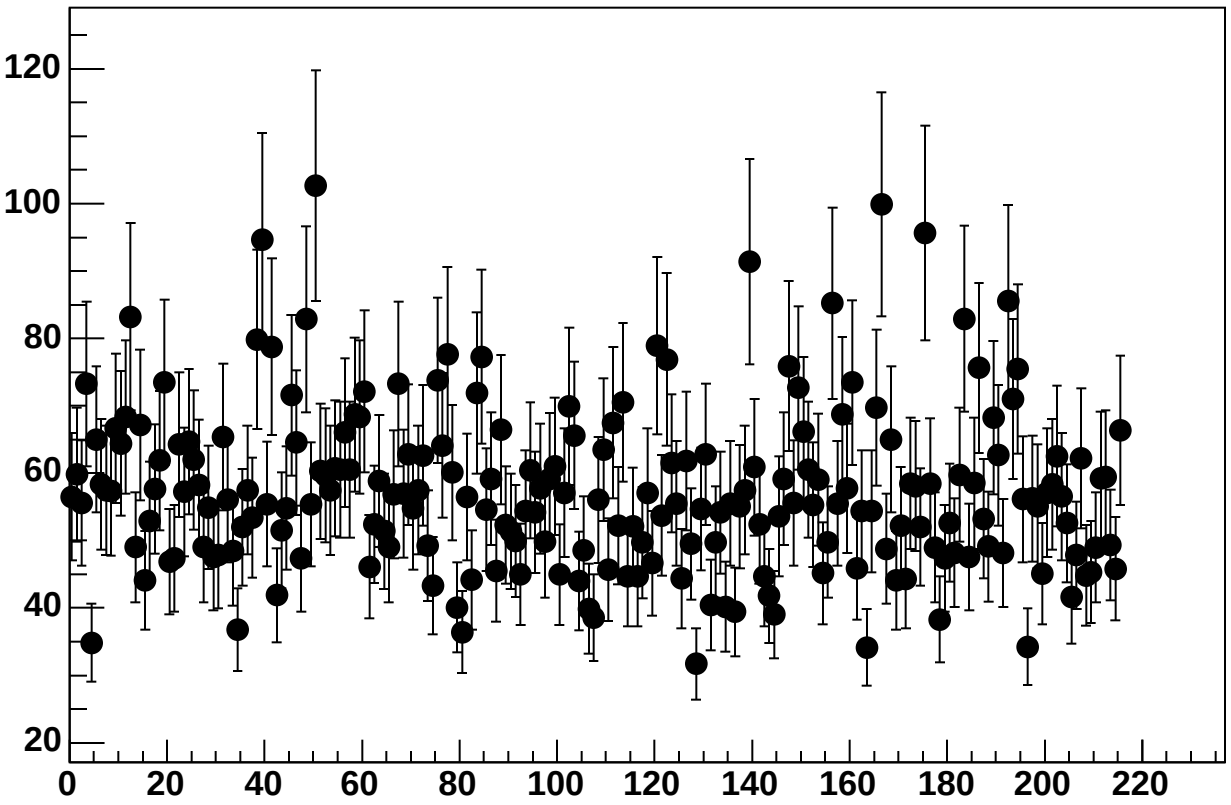
Enable 1, DAC=1600, Hold=40, ADC Noise vs 18*Chip+Chan



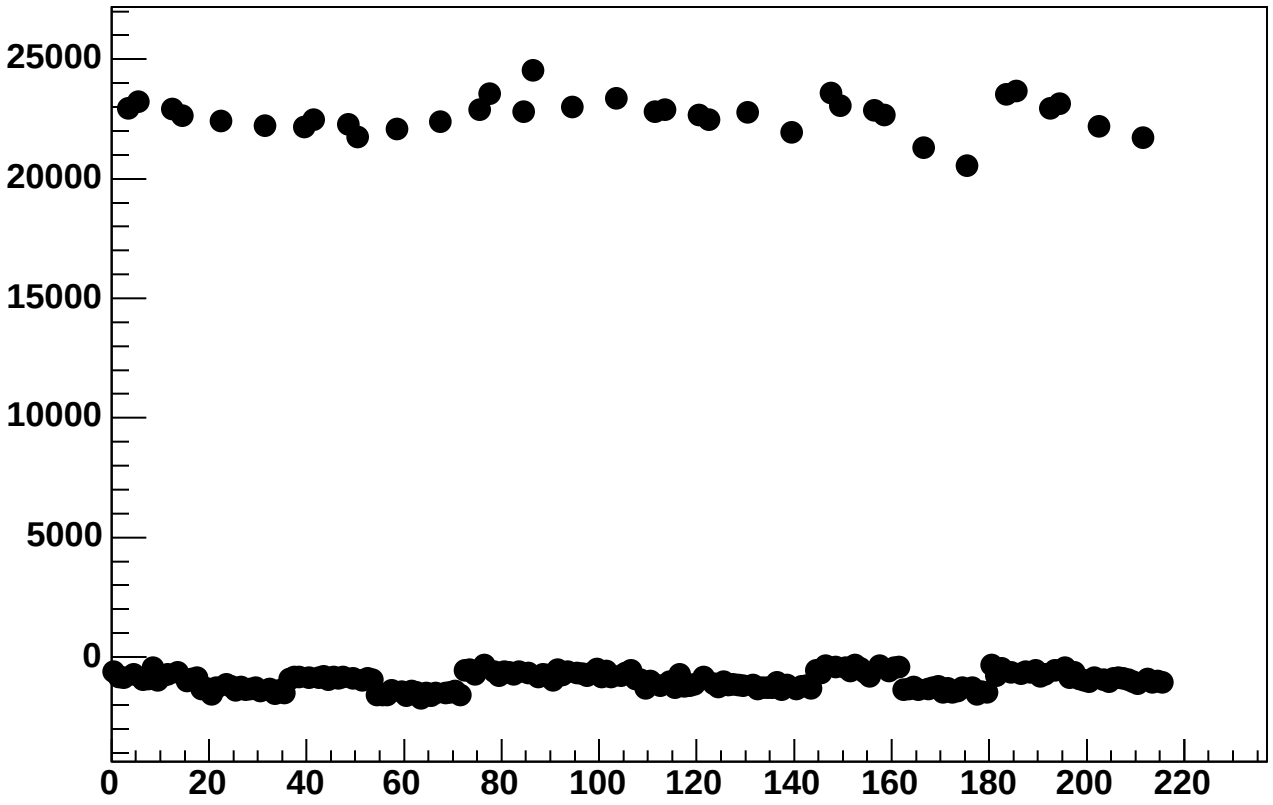
Enable 1, DAC=1600, Hold=45, ADC Mean vs 18*Chip+Chan



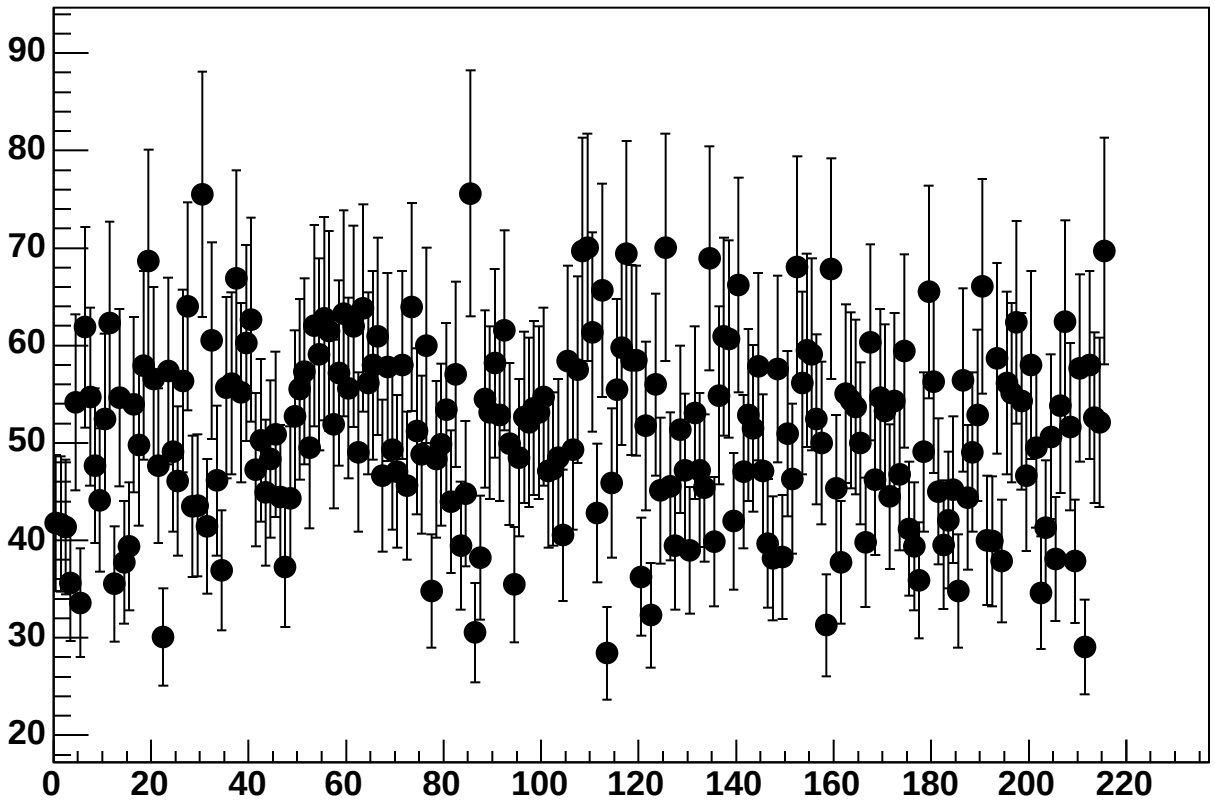
Enable 1, DAC=1600, Hold=45, ADC Noise vs 18*Chip+Chan



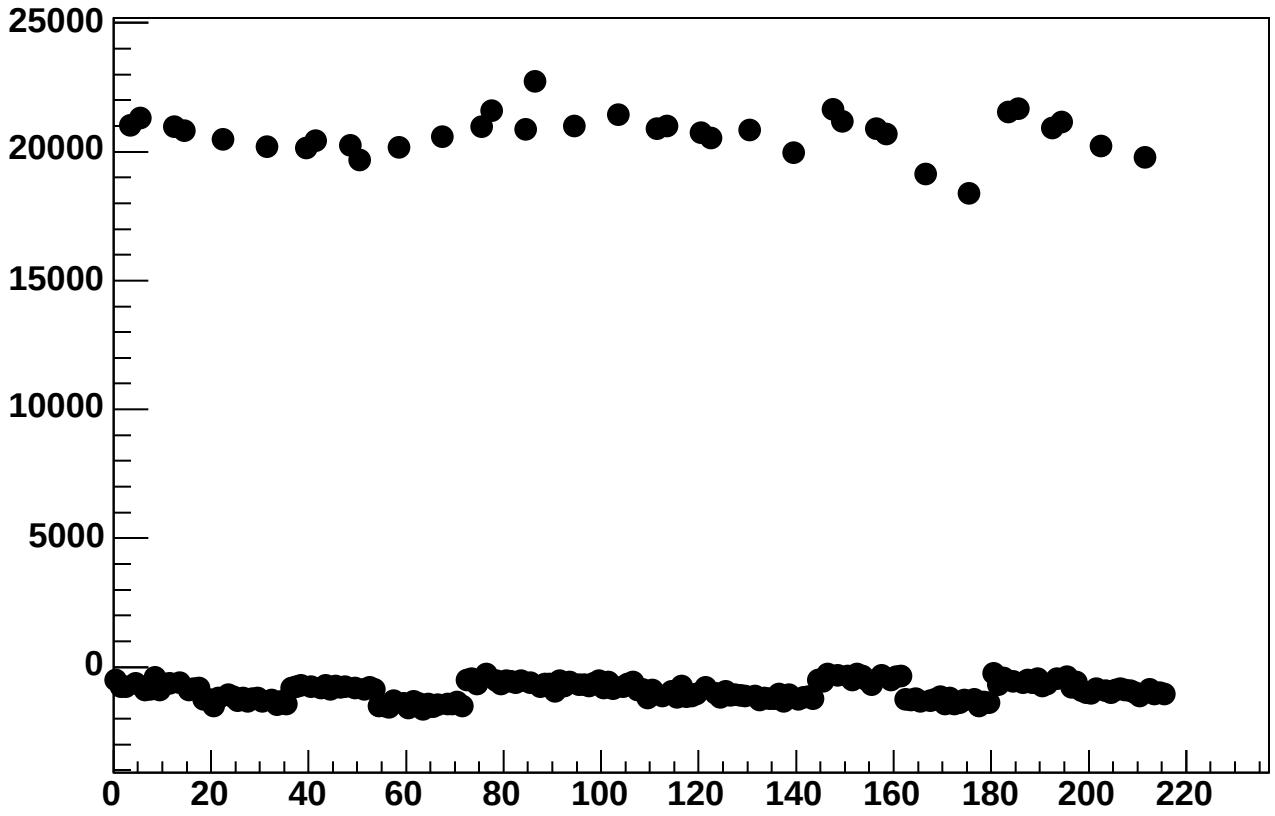
Enable 1, DAC=1600, Hold=50, ADC Mean vs 18*Chip+Chan



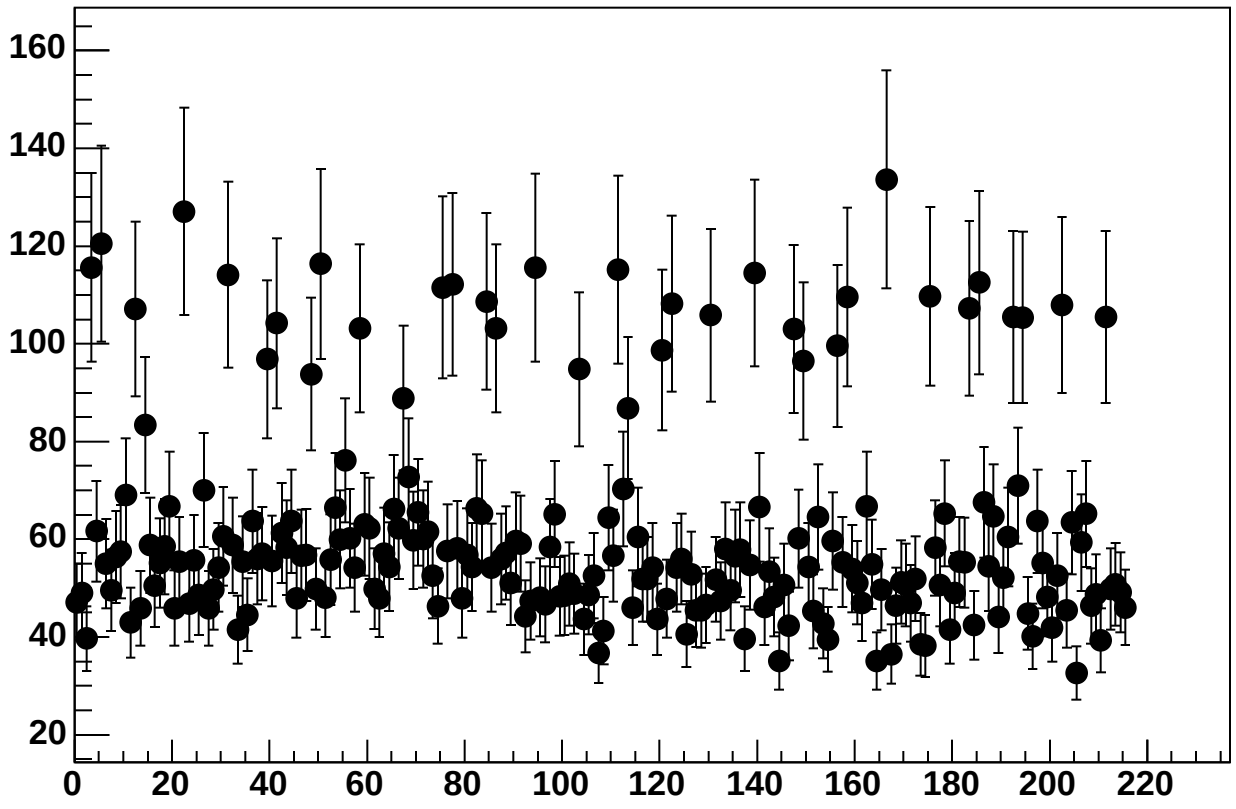
Enable 1, DAC=1600, Hold=50, ADC Noise vs 18*Chip+Chan



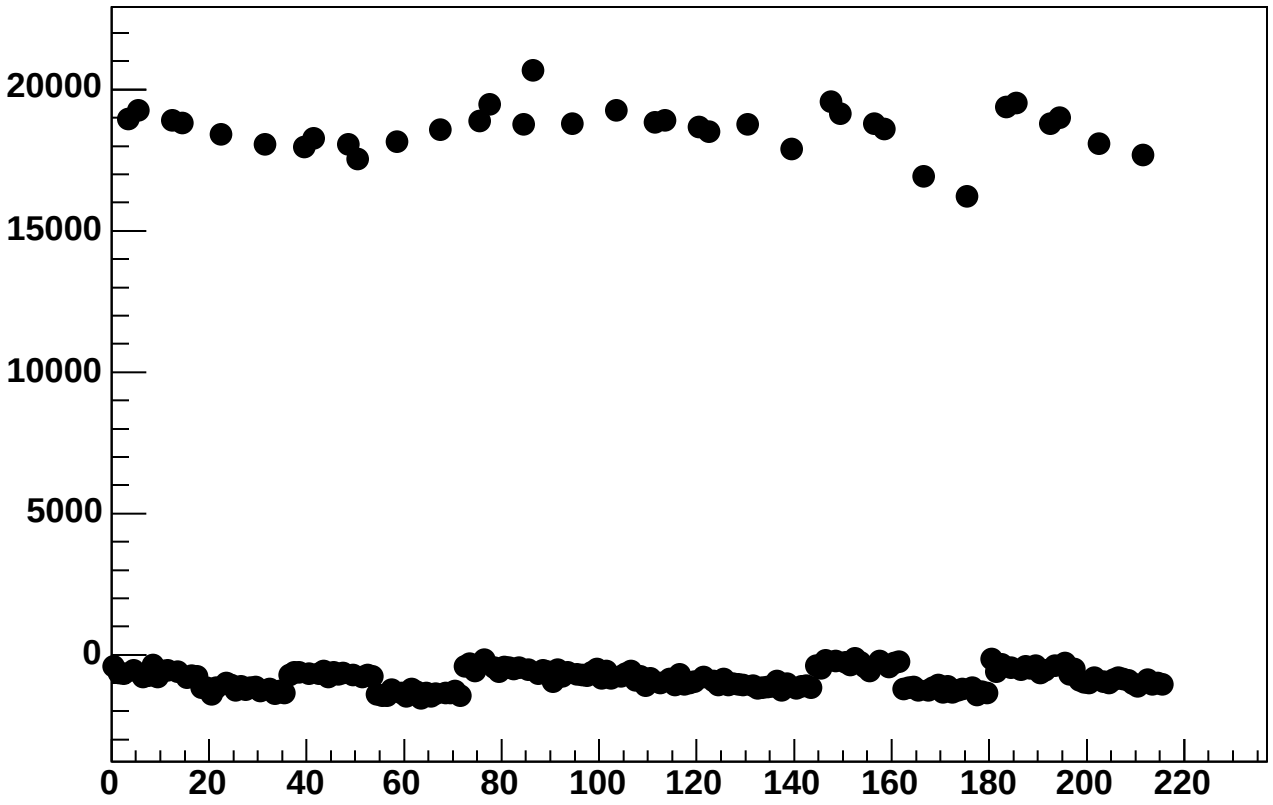
Enable 1, DAC=1600, Hold=55, ADC Mean vs 18*Chip+Chan



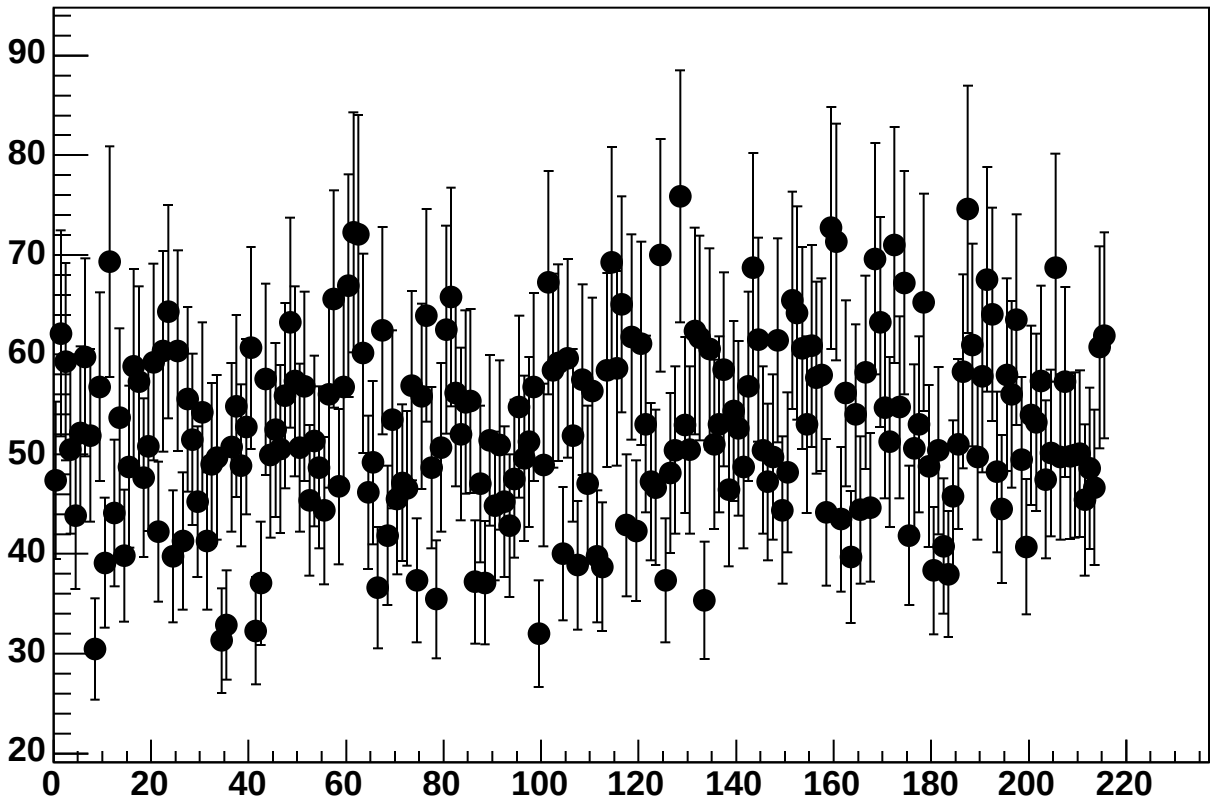
Enable 1, DAC=1600, Hold=55, ADC Noise vs 18*Chip+Chan



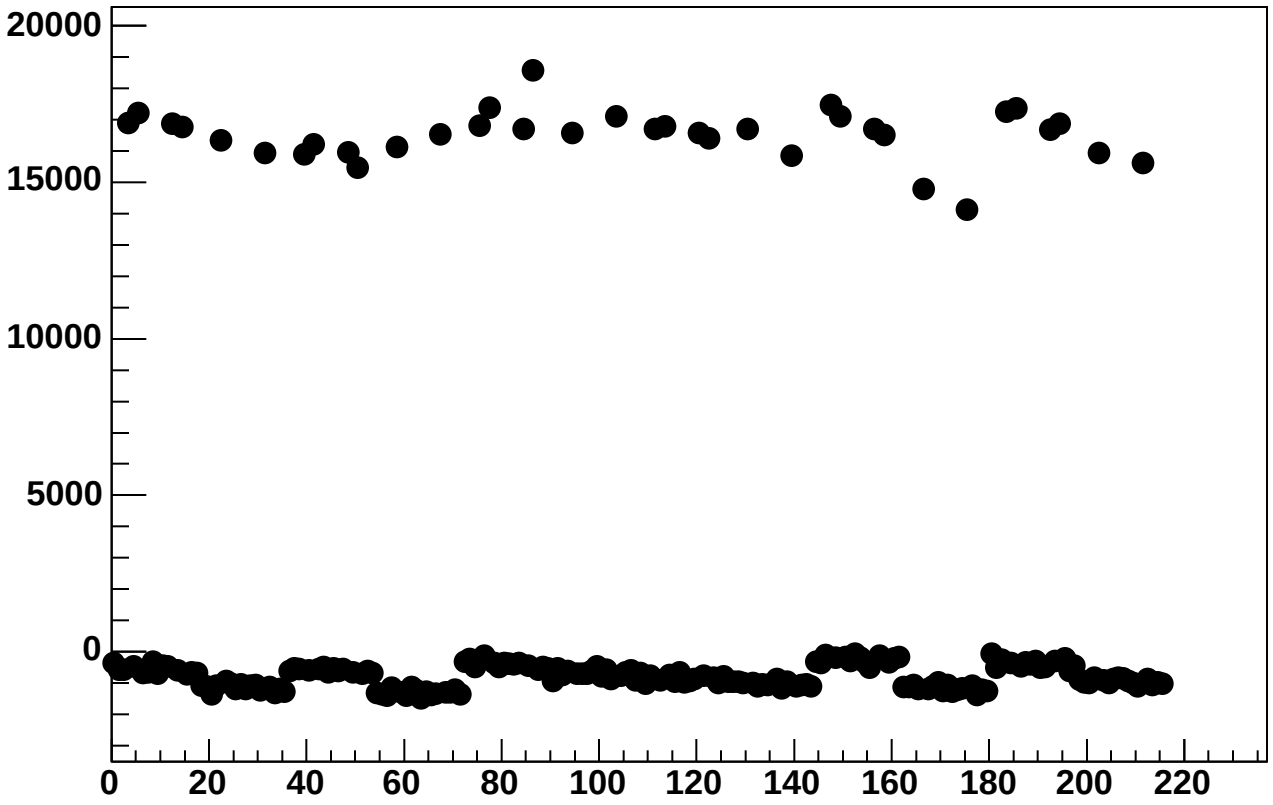
Enable 1, DAC=1600, Hold=60, ADC Mean vs 18*Chip+Chan



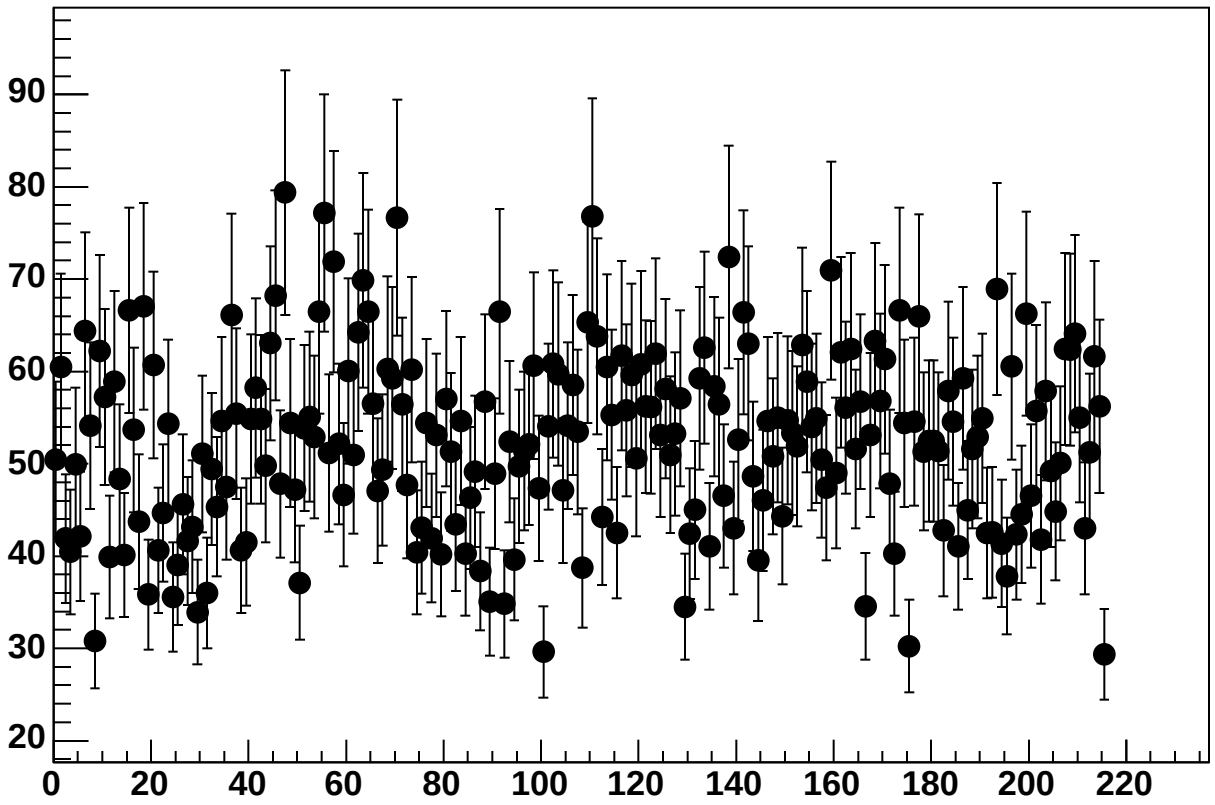
Enable 1, DAC=1600, Hold=60, ADC Noise vs 18*Chip+Chan



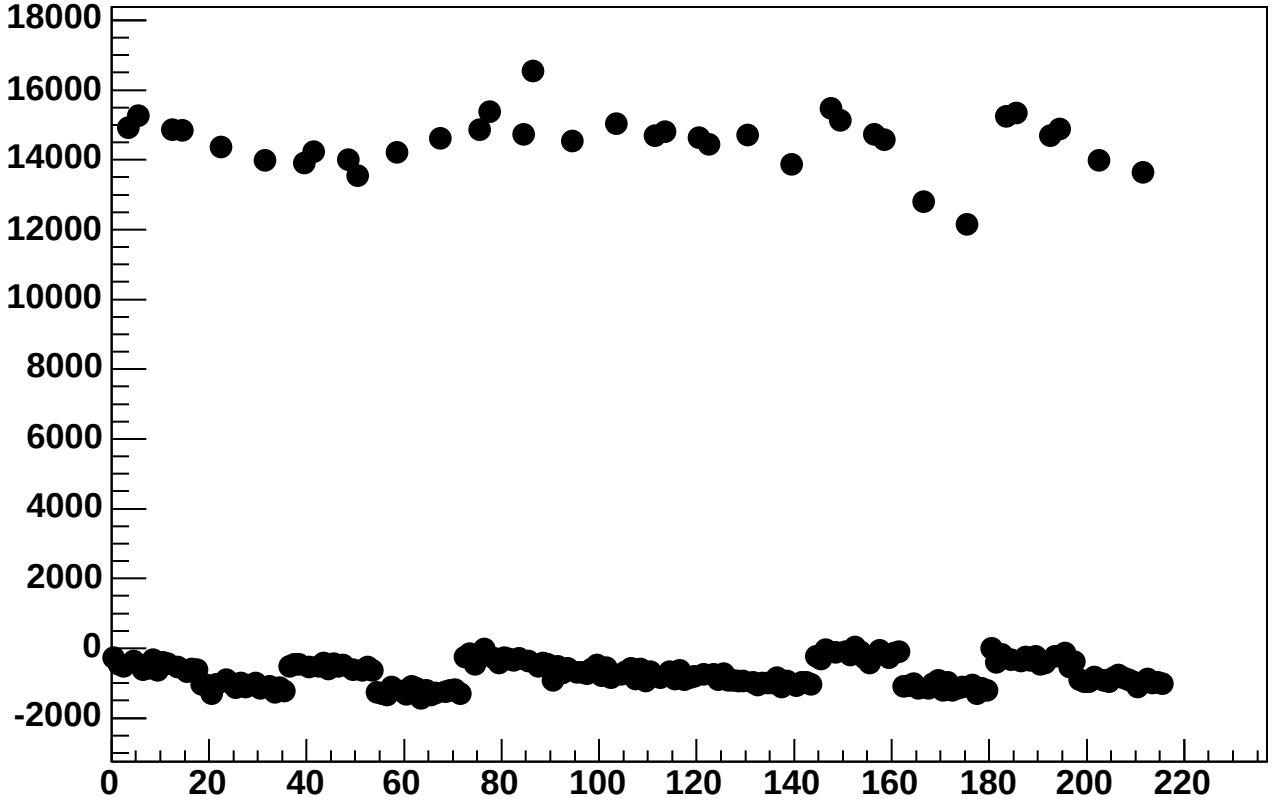
Enable 1, DAC=1600, Hold=65, ADC Mean vs 18*Chip+Chan



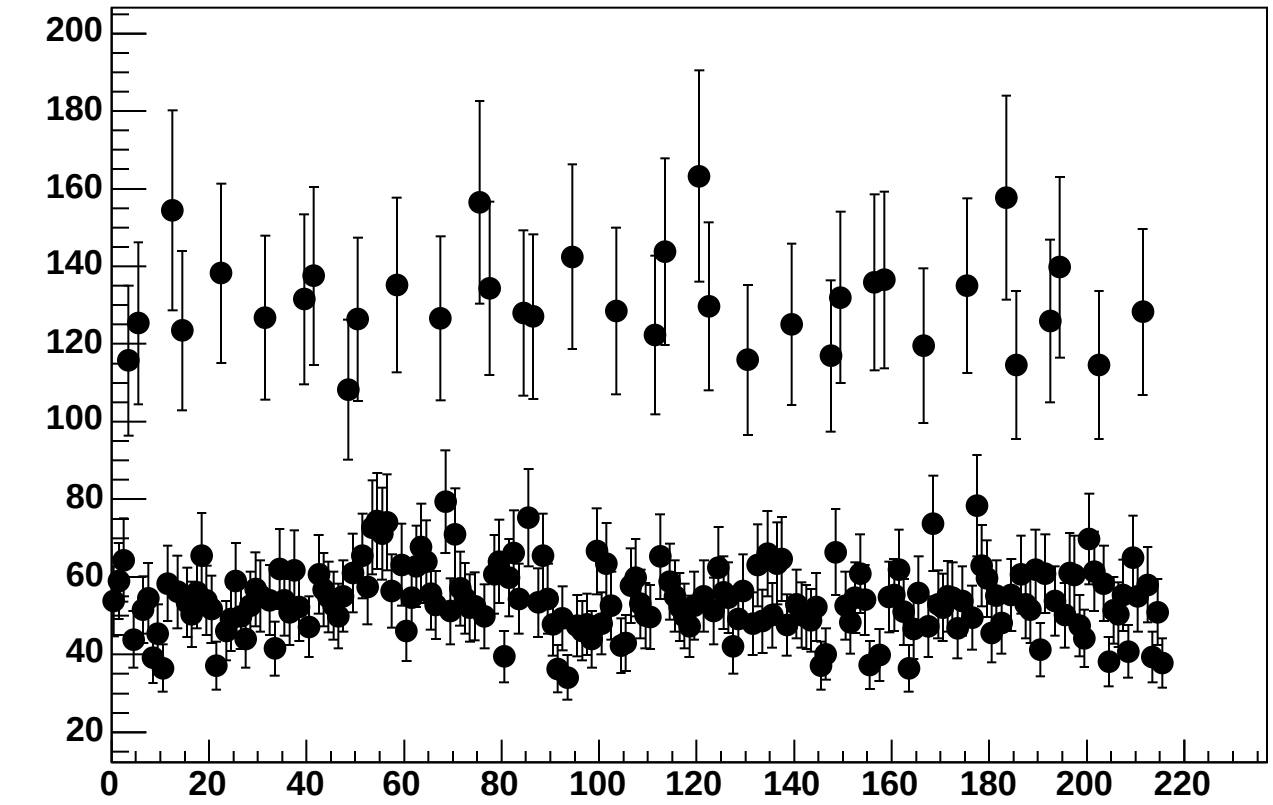
Enable 1, DAC=1600, Hold=65, ADC Noise vs 18*Chip+Chan



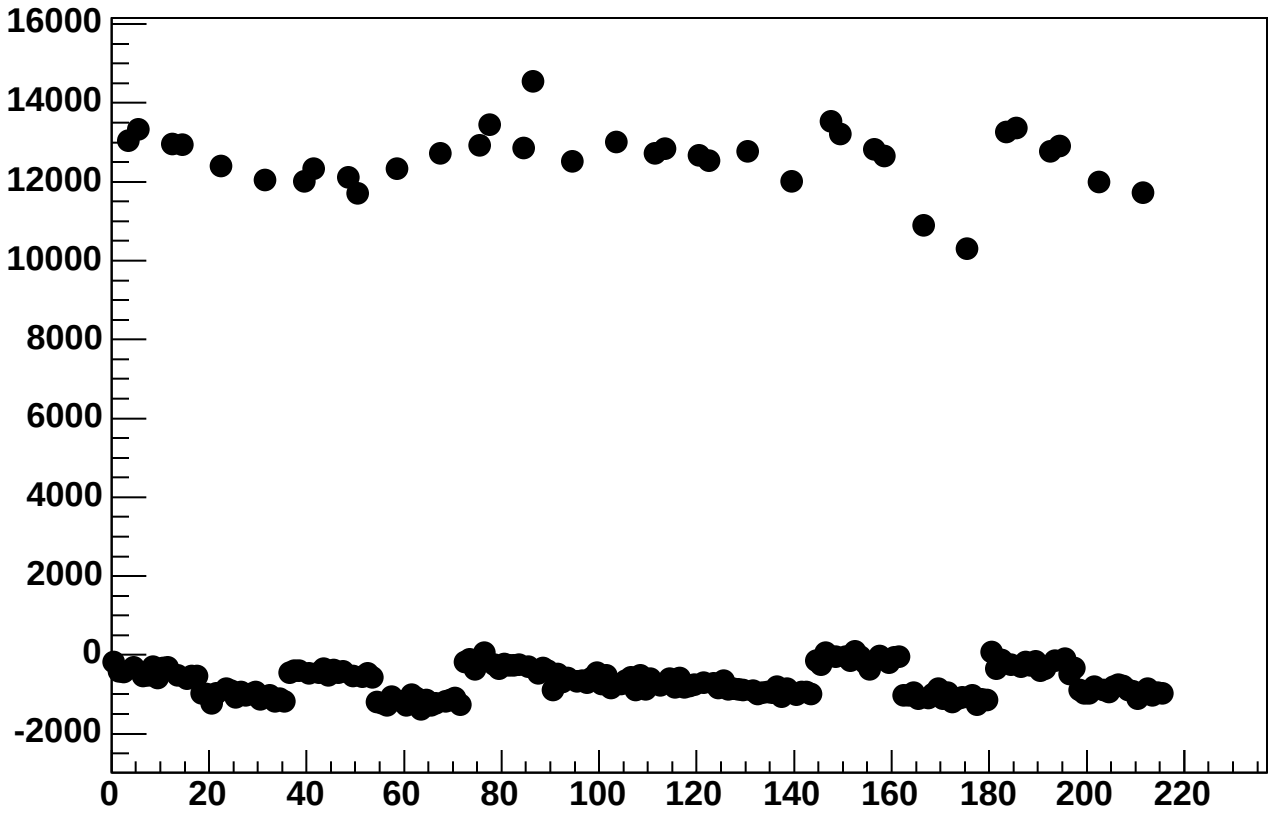
Enable 1, DAC=1600, Hold=70, ADC Mean vs 18*Chip+Chan



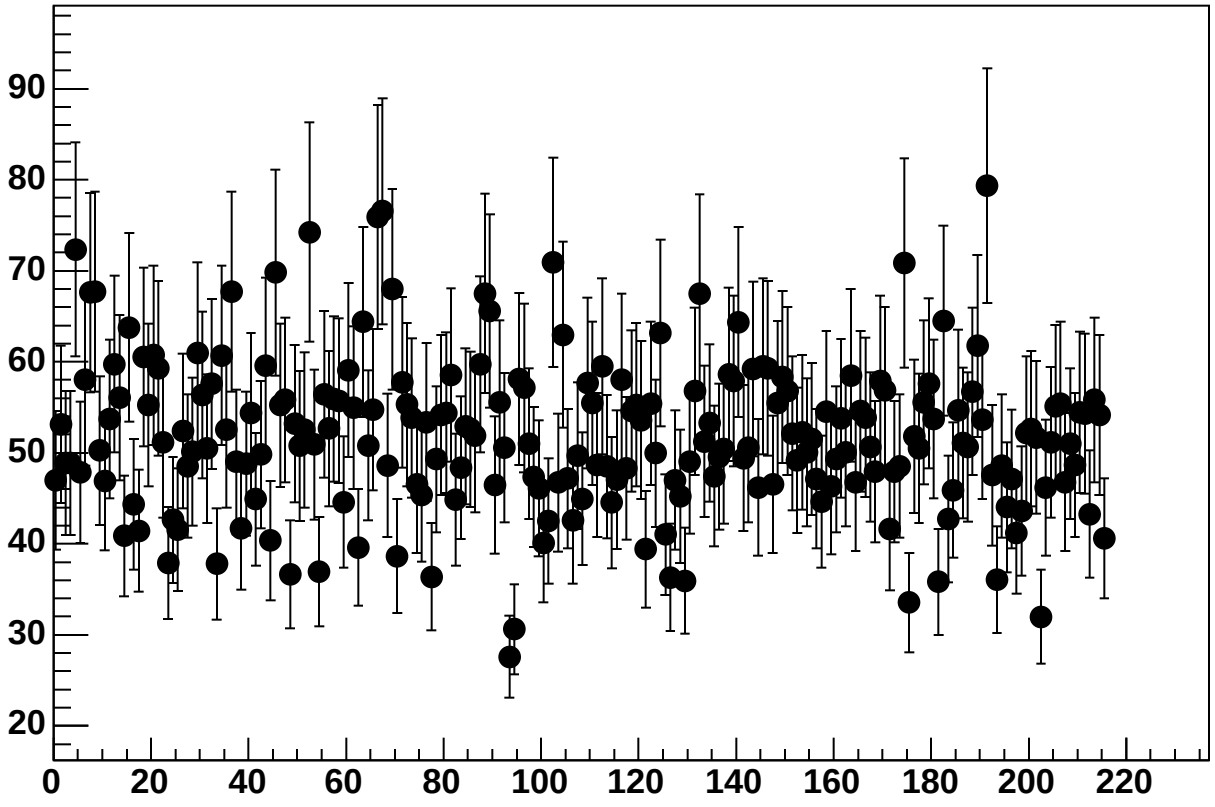
Enable 1, DAC=1600, Hold=70, ADC Noise vs 18*Chip+Chan



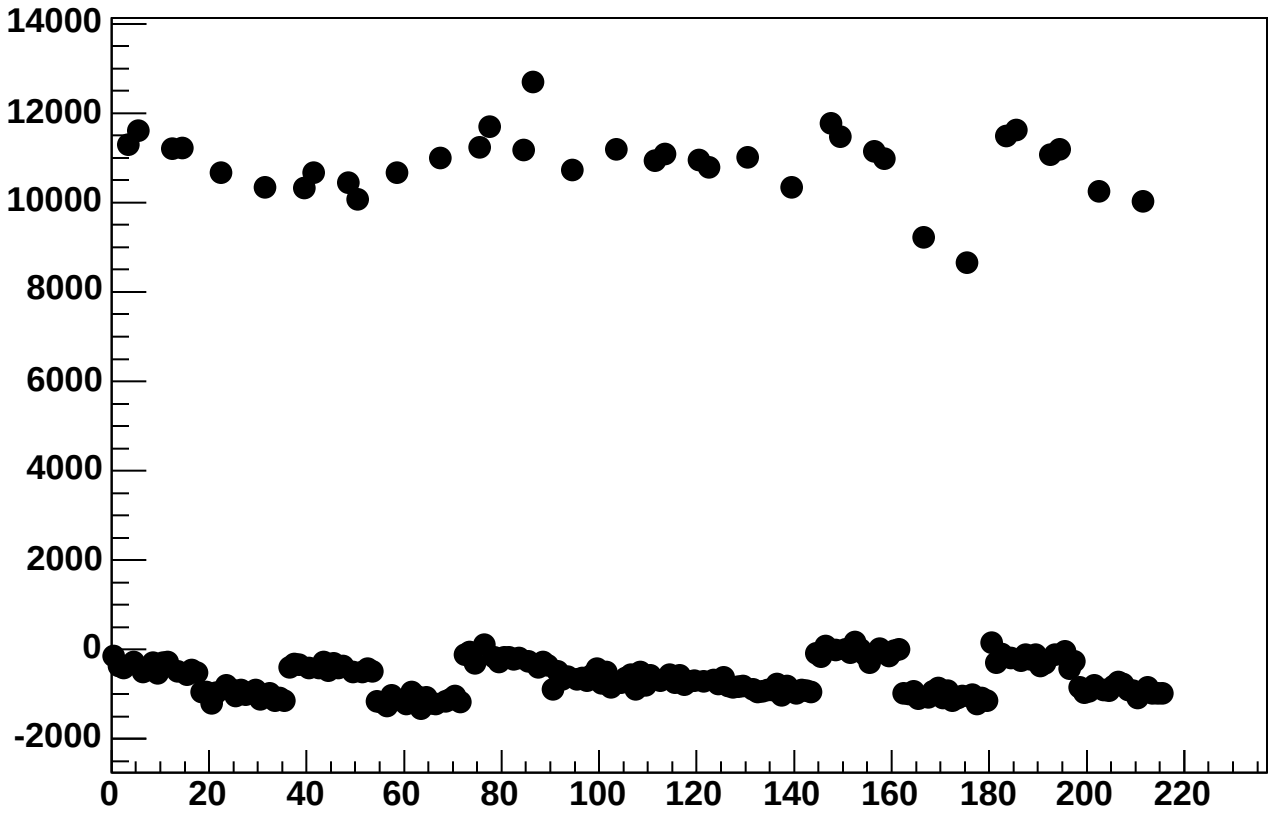
Enable 1, DAC=1600, Hold=75, ADC Mean vs 18*Chip+Chan



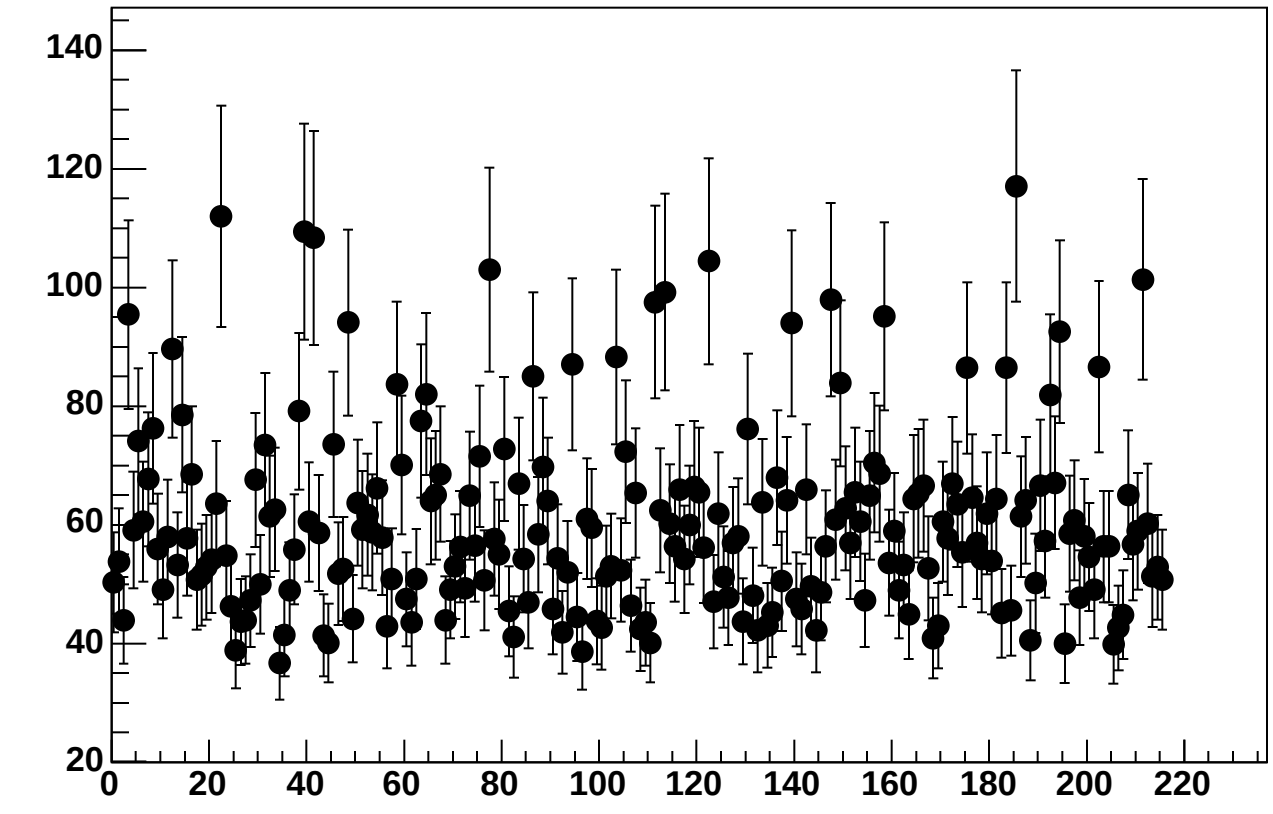
Enable 1, DAC=1600, Hold=75, ADC Noise vs 18*Chip+Chan



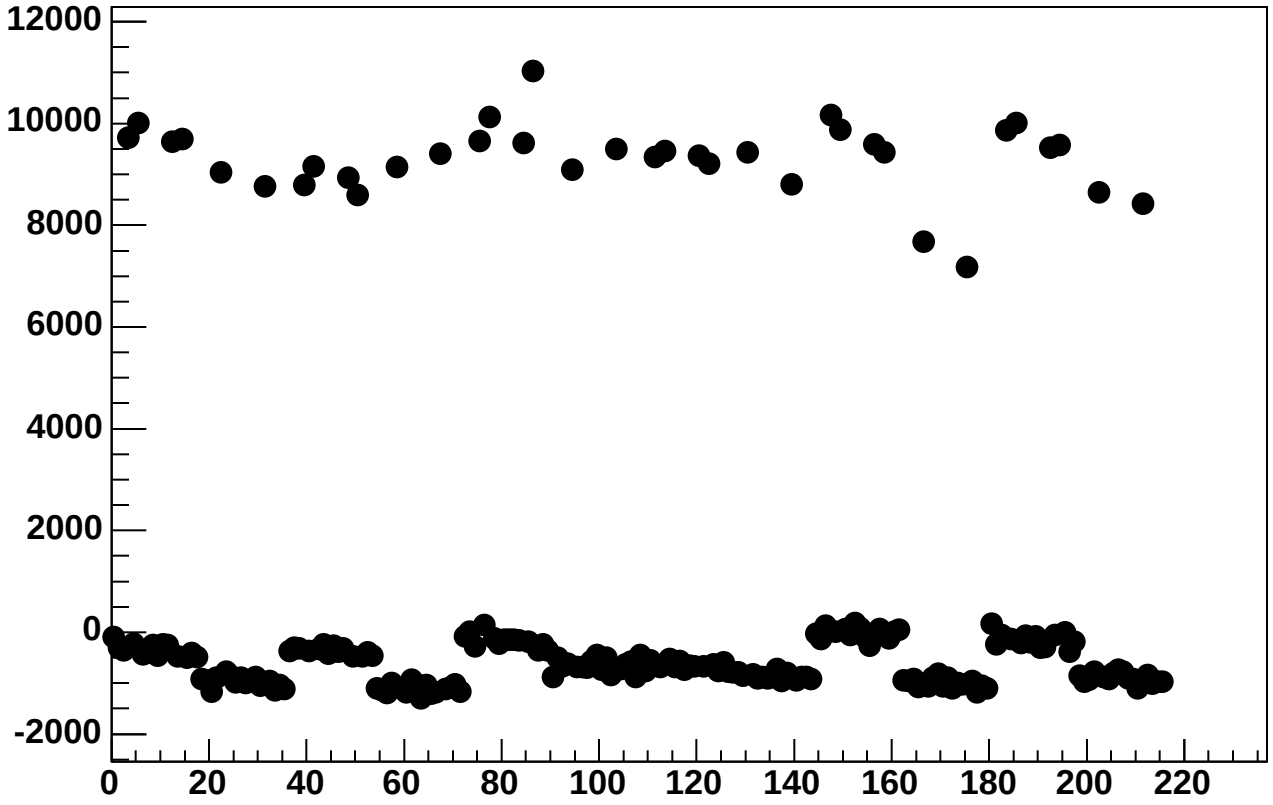
Enable 1, DAC=1600, Hold=80, ADC Mean vs 18*Chip+Chan



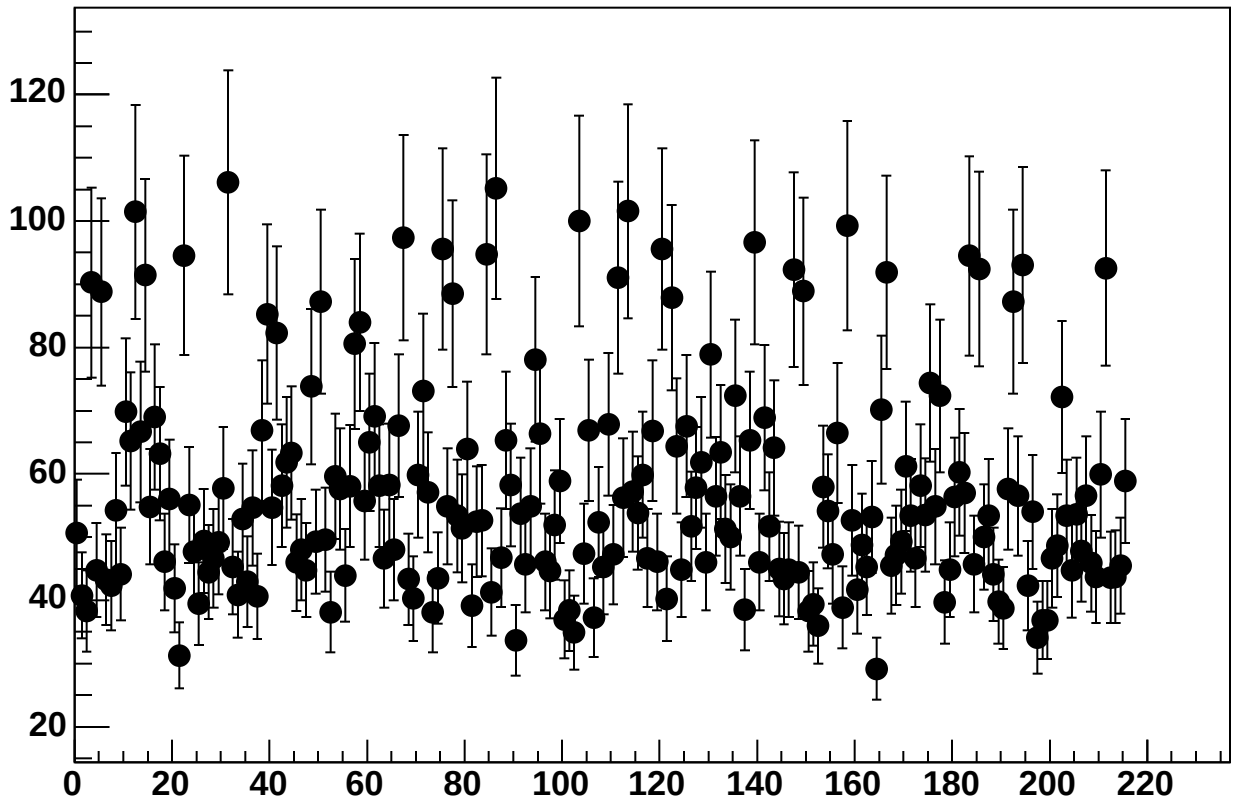
Enable 1, DAC=1600, Hold=80, ADC Noise vs 18*Chip+Chan



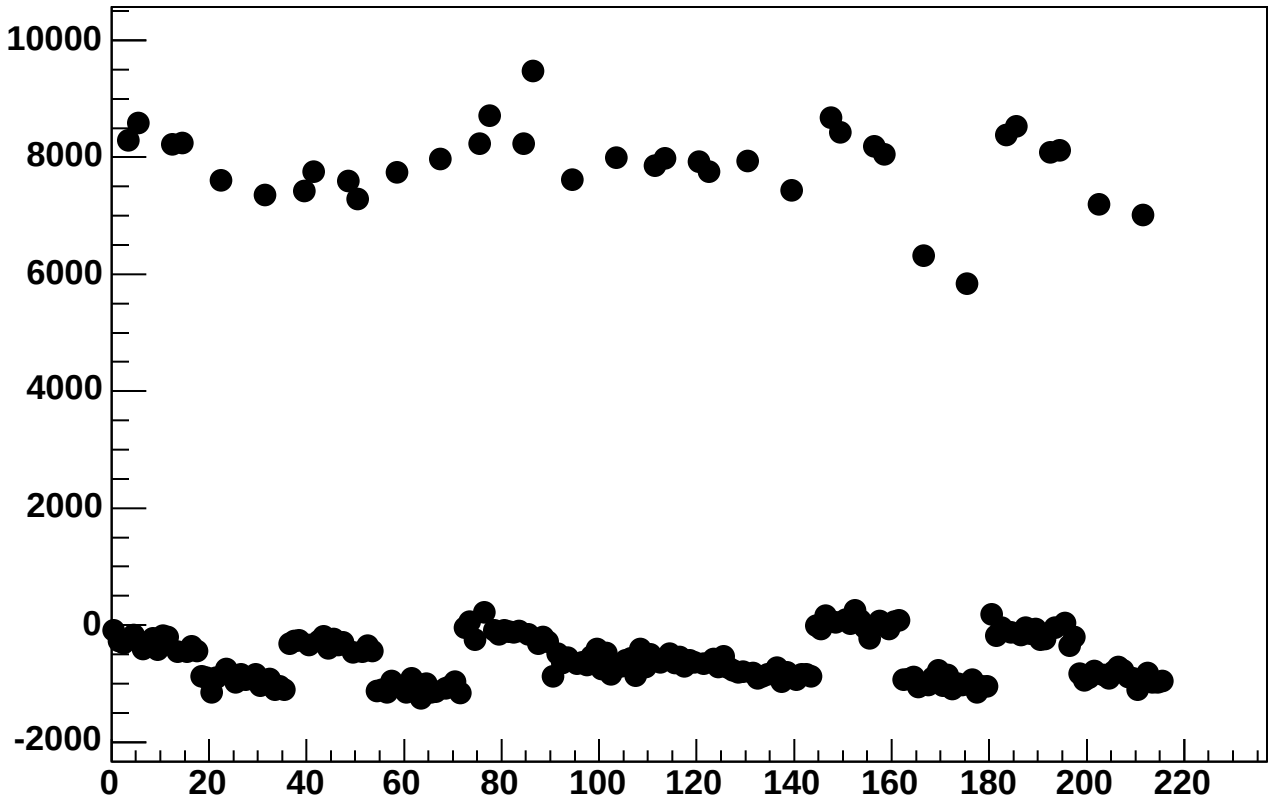
Enable 1, DAC=1600, Hold=85, ADC Mean vs 18*Chip+Chan



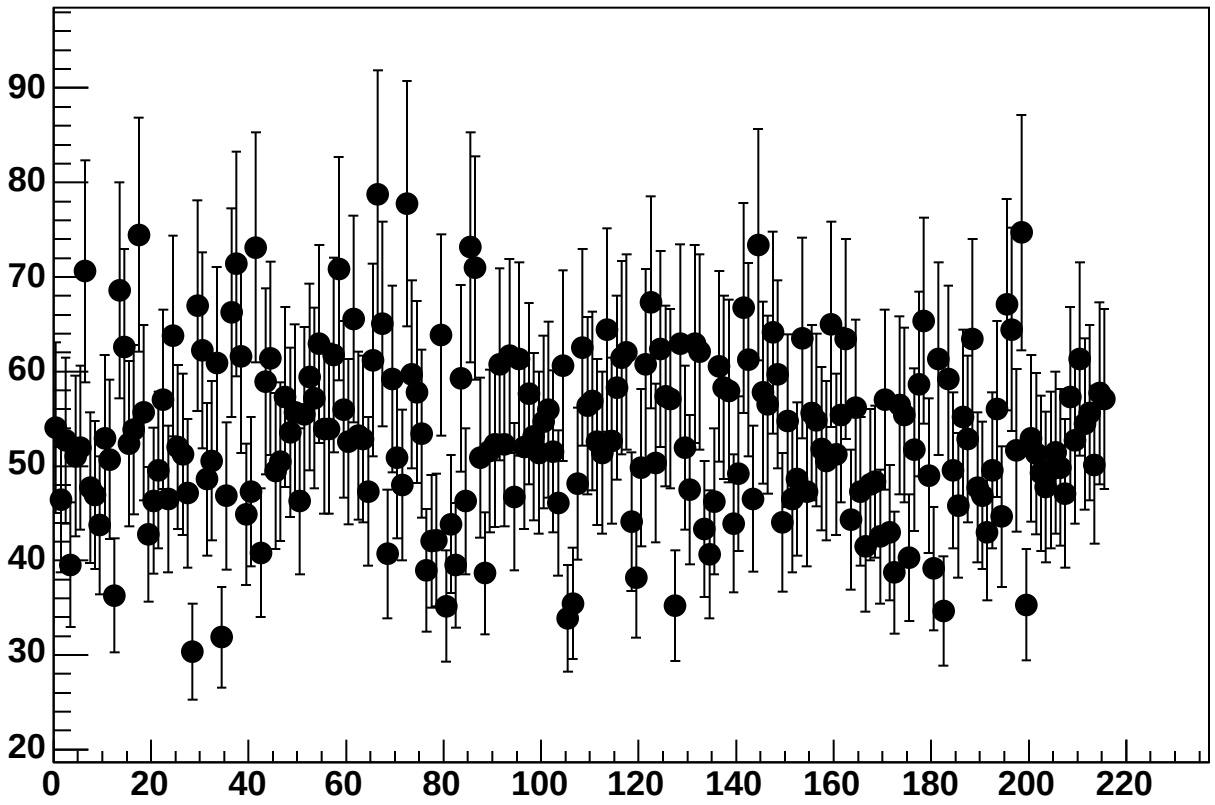
Enable 1, DAC=1600, Hold=85, ADC Noise vs 18*Chip+Chan



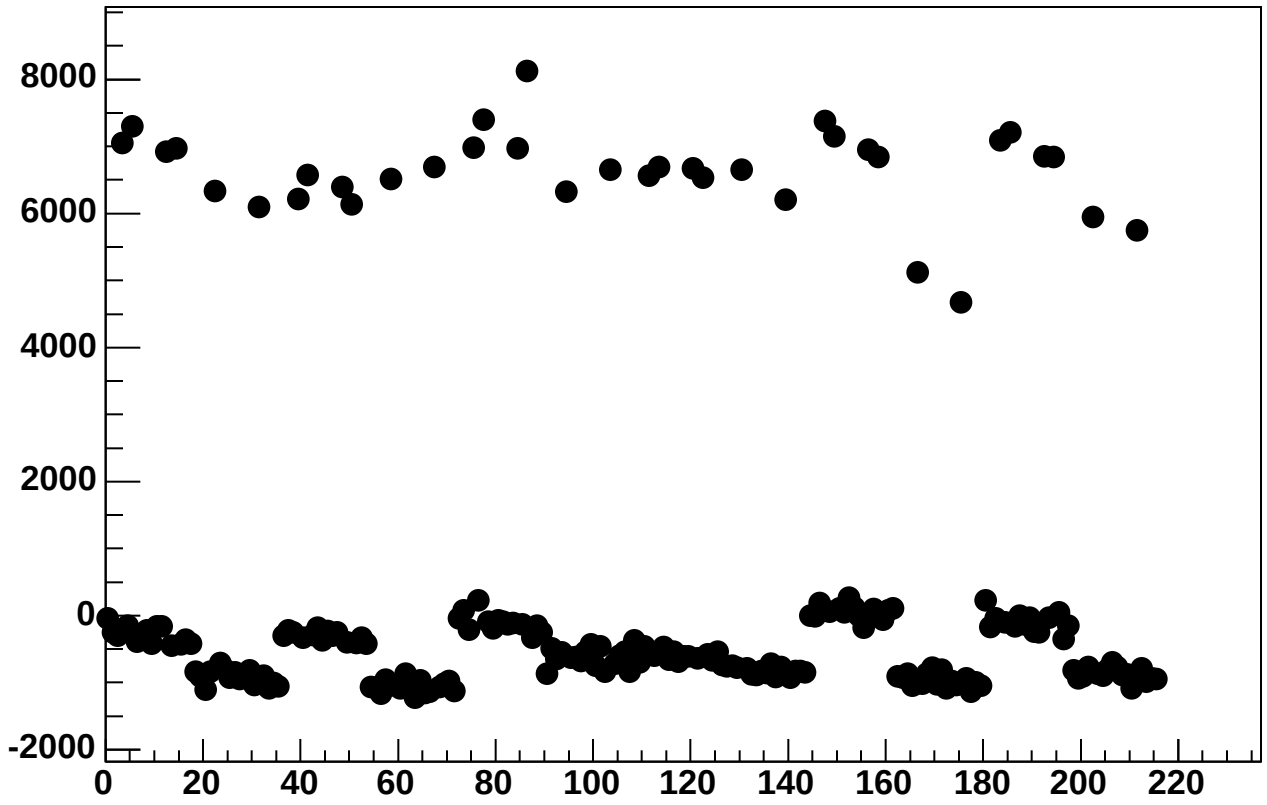
Enable 1, DAC=1600, Hold=90, ADC Mean vs 18*Chip+Chan



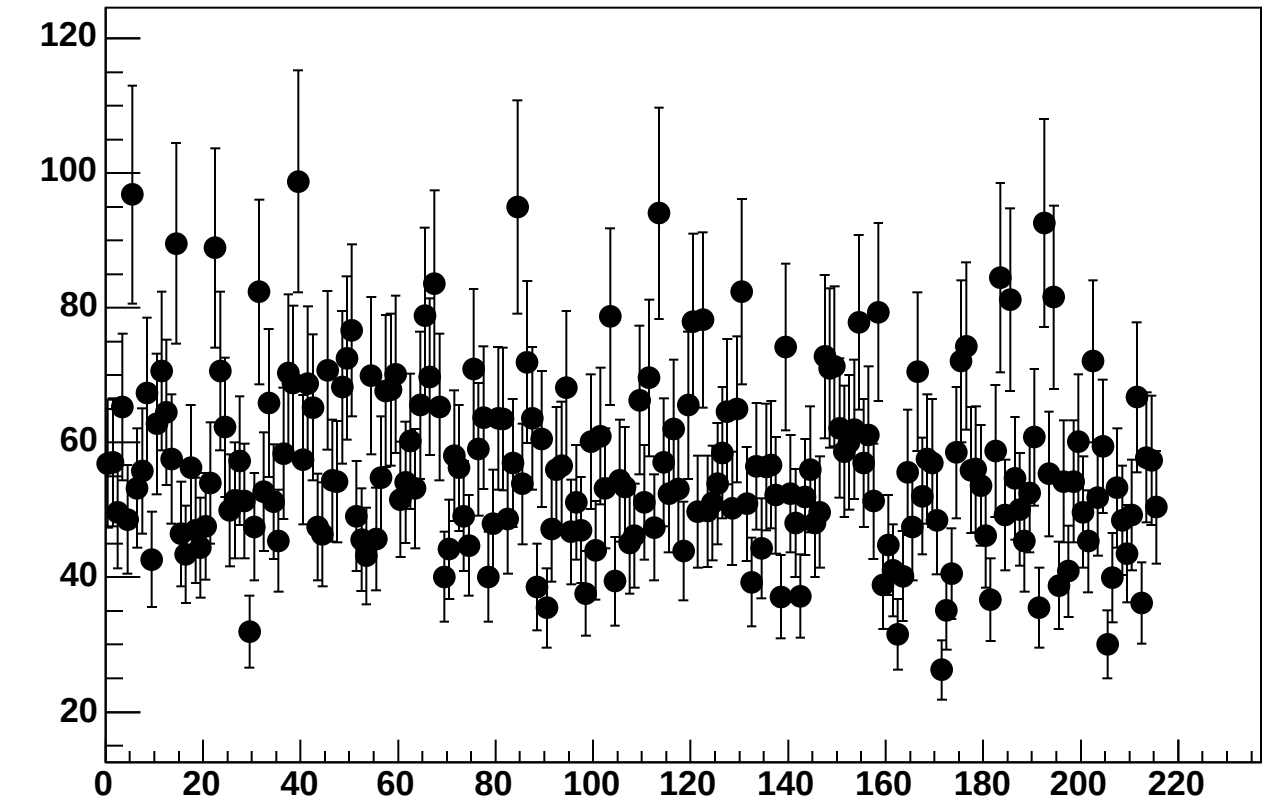
Enable 1, DAC=1600, Hold=90, ADC Noise vs 18*Chip+Chan



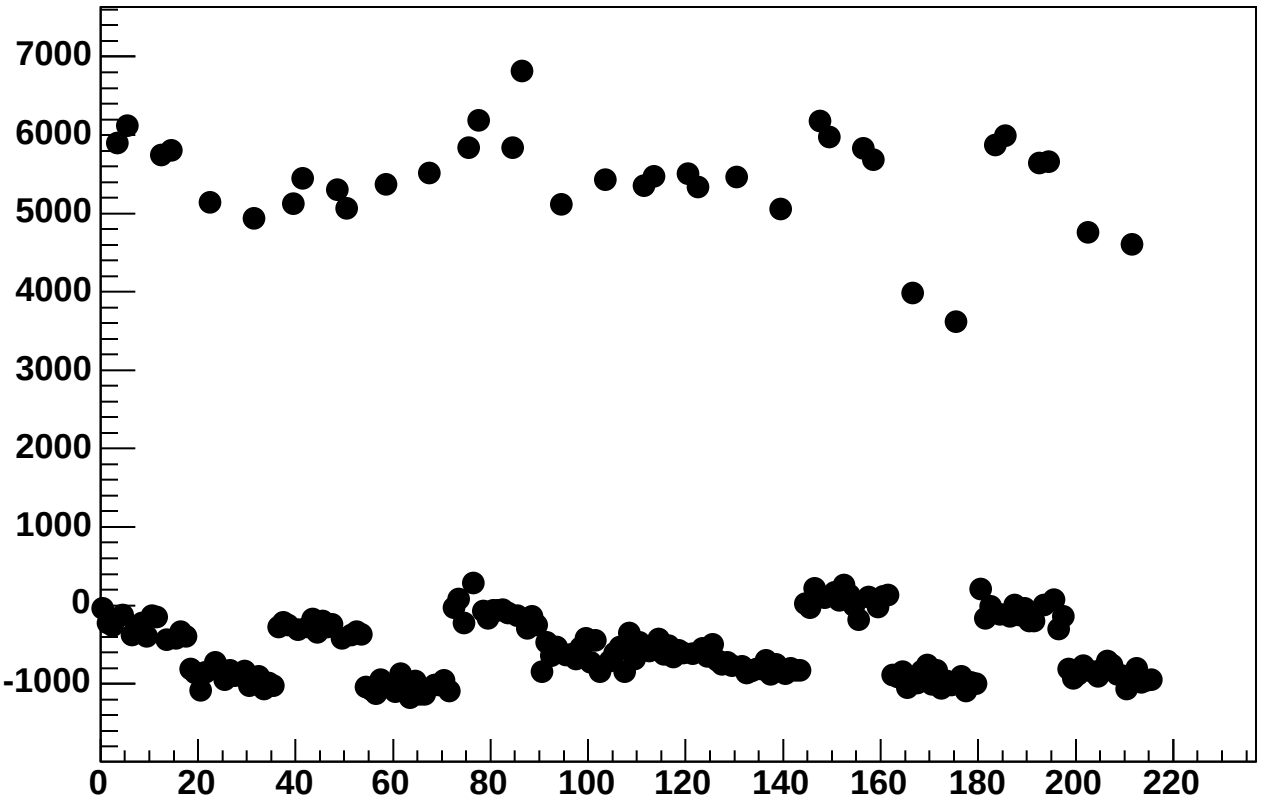
Enable 1, DAC=1600, Hold=95, ADC Mean vs 18*Chip+Chan



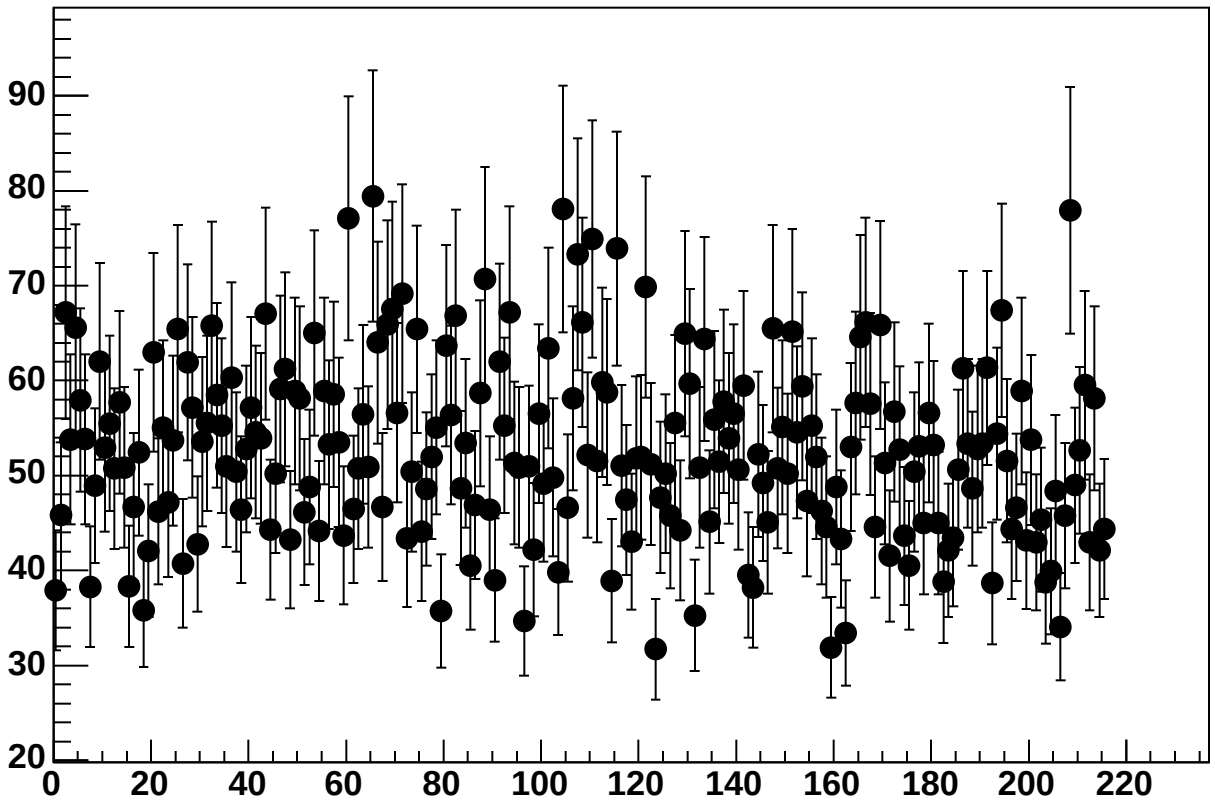
Enable 1, DAC=1600, Hold=95, ADC Noise vs 18*Chip+Chan



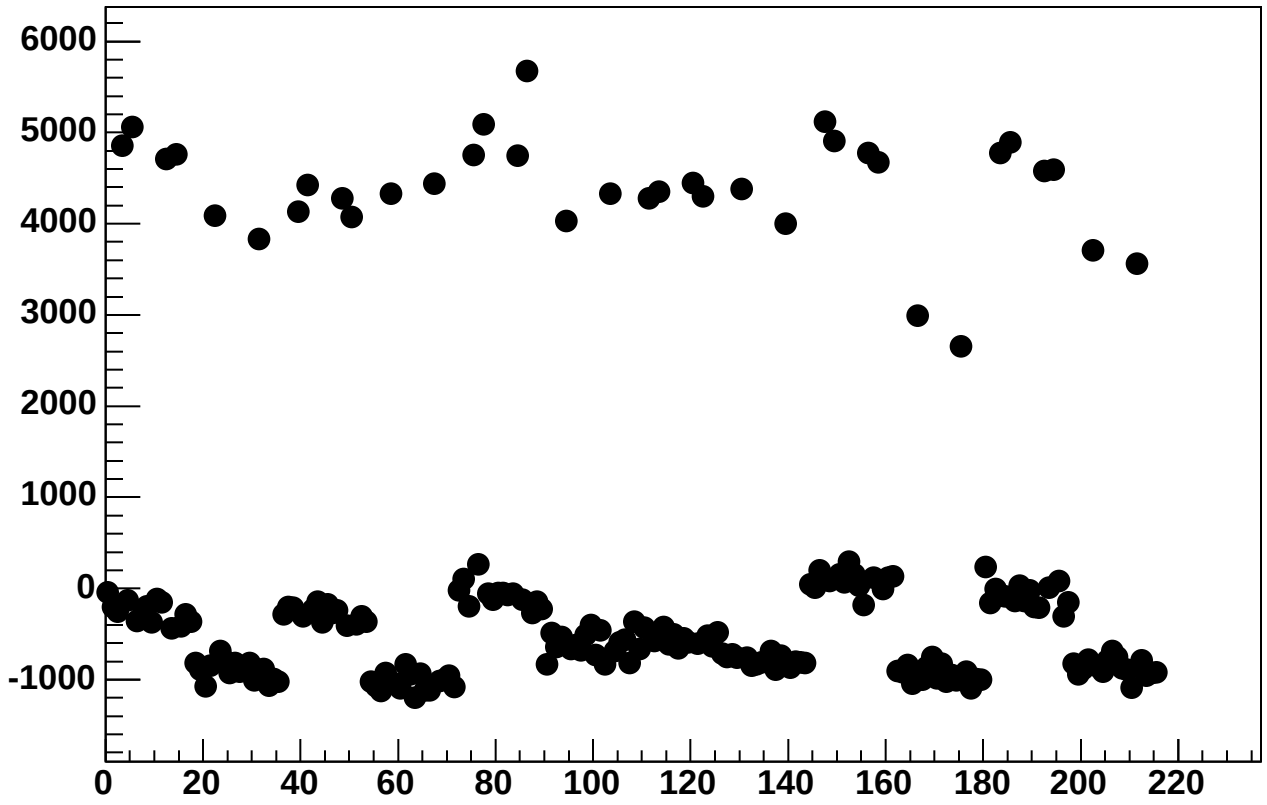
Enable 1, DAC=1600, Hold=100, ADC Mean vs 18*Chip+Chan



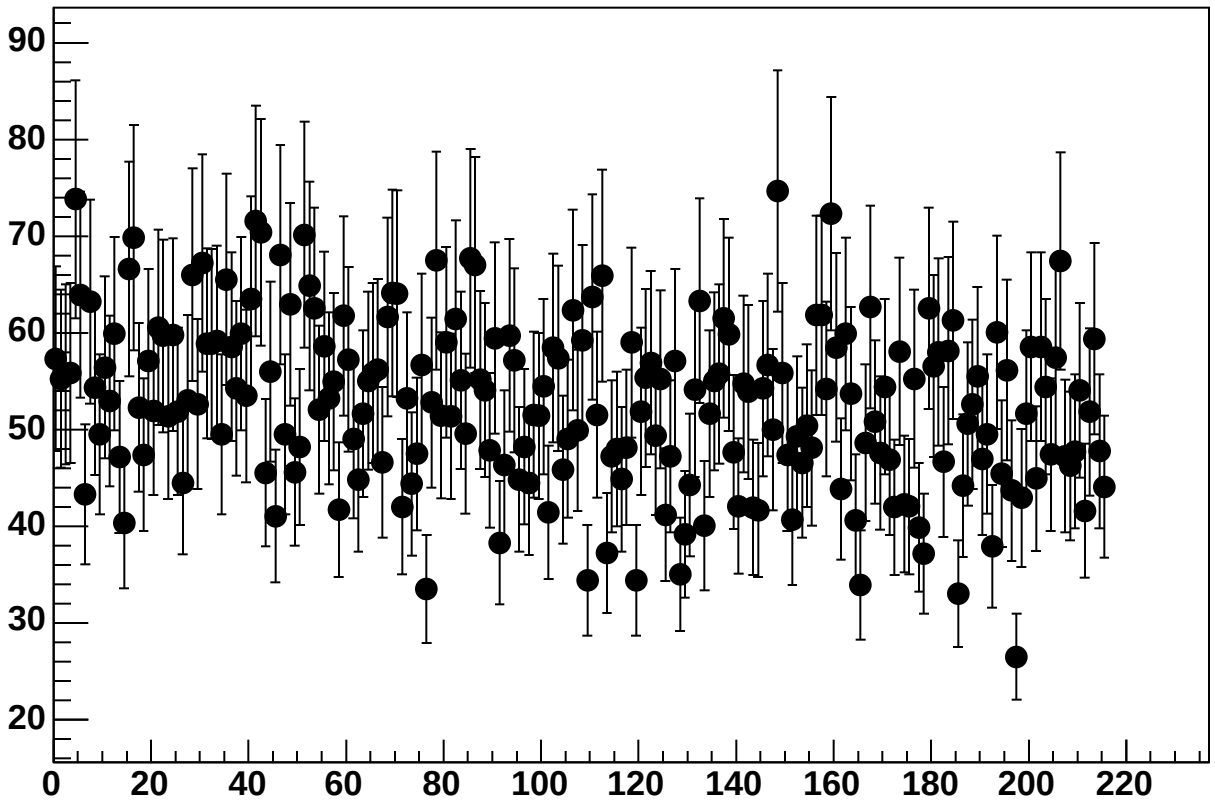
Enable 1, DAC=1600, Hold=100, ADC Noise vs 18*Chip+Chan



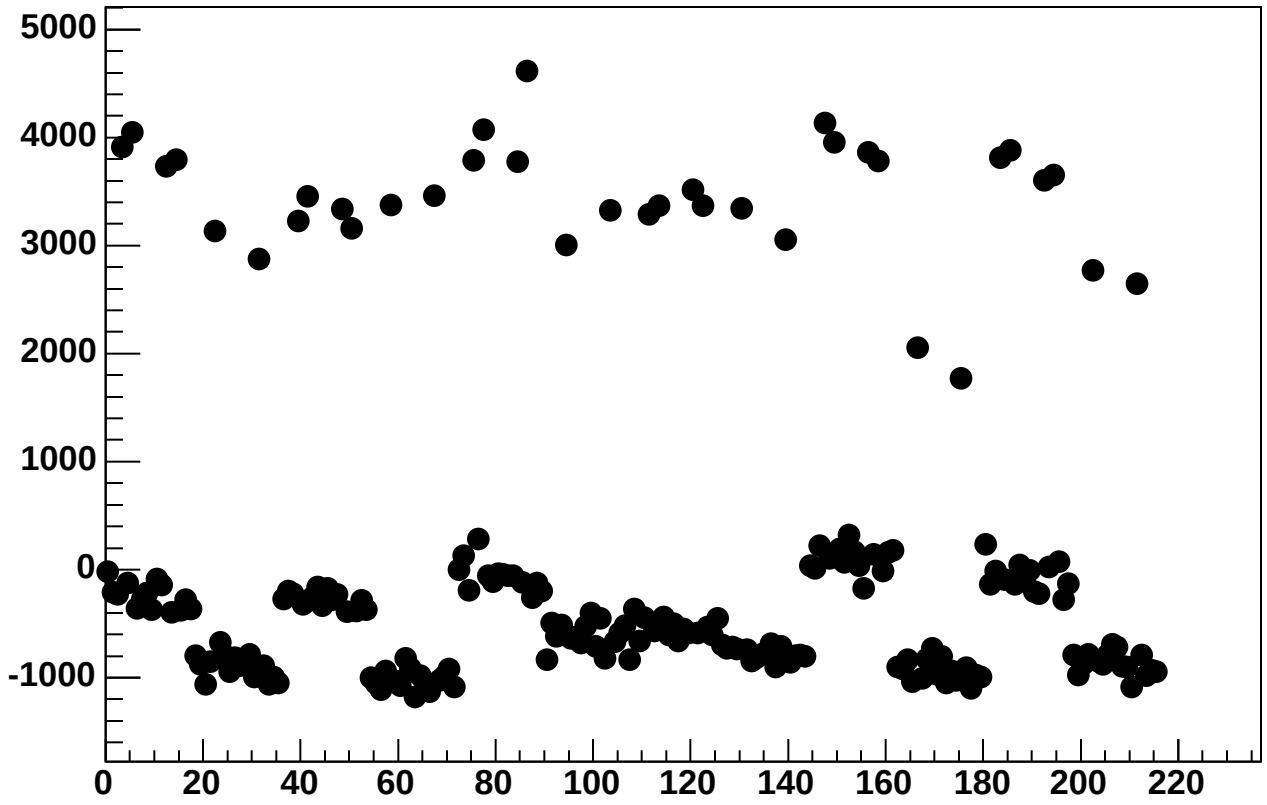
Enable 1, DAC=1600, Hold=105, ADC Mean vs 18*Chip+Chan



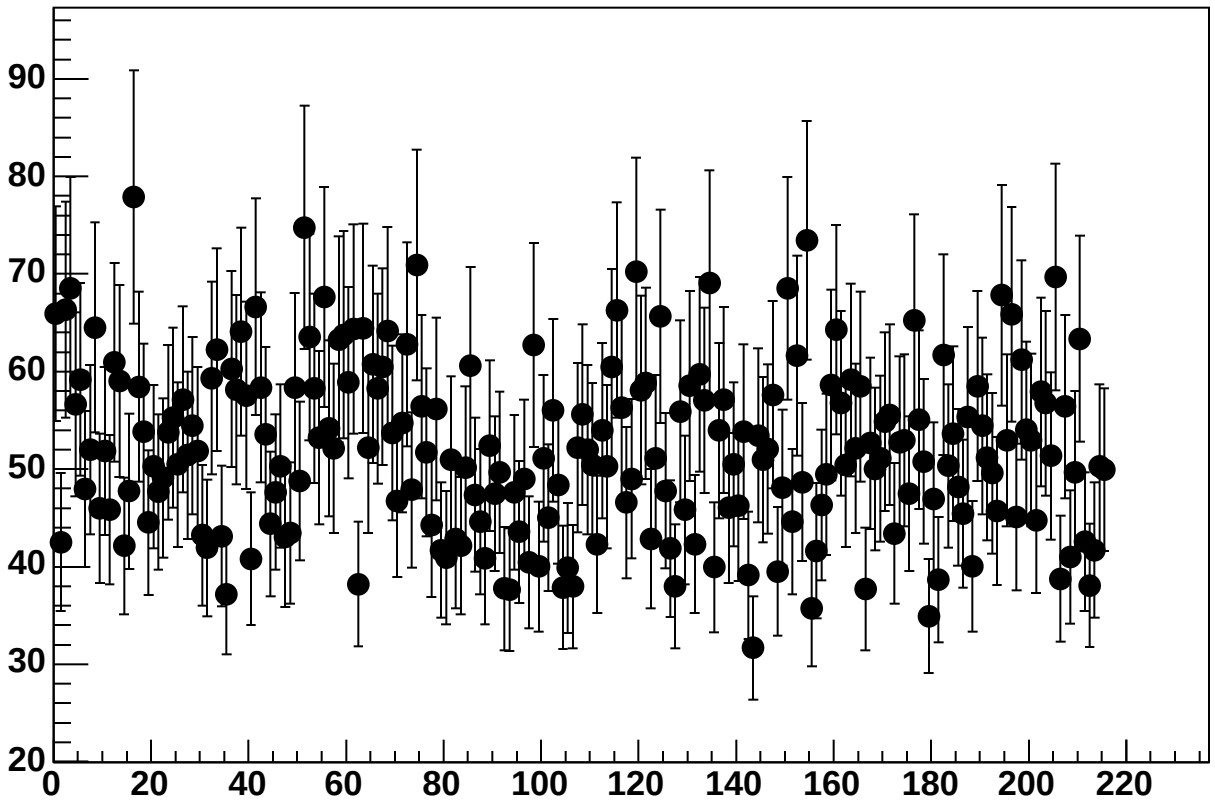
Enable 1, DAC=1600, Hold=105, ADC Noise vs 18*Chip+Chan



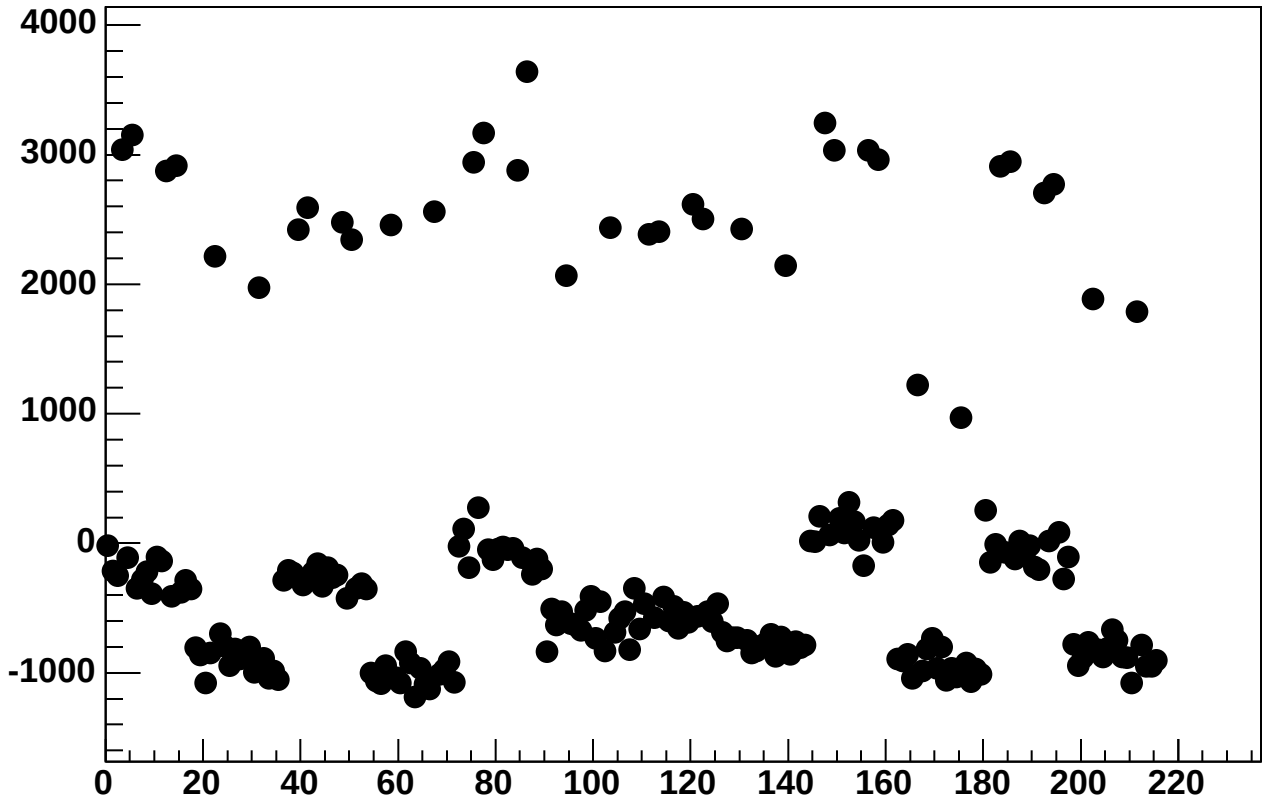
Enable 1, DAC=1600, Hold=110, ADC Mean vs 18*Chip+Chan



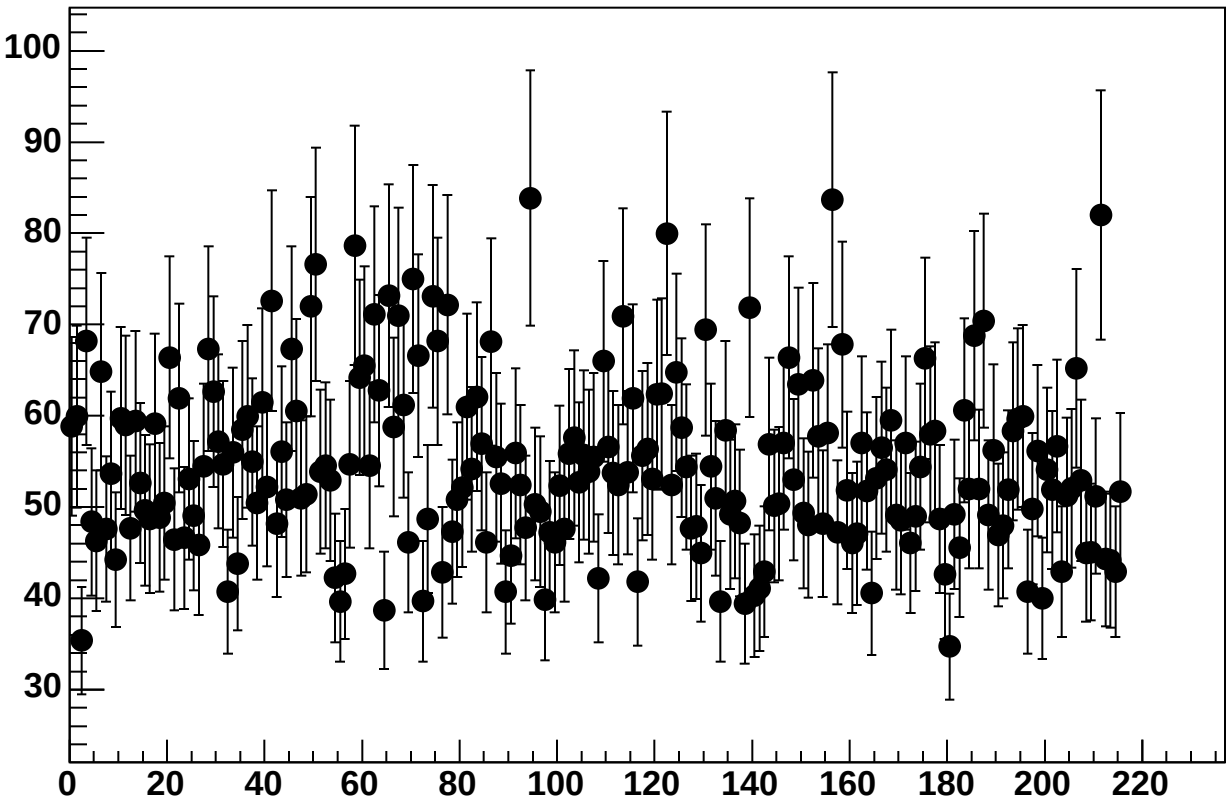
Enable 1, DAC=1600, Hold=110, ADC Noise vs 18*Chip+Chan



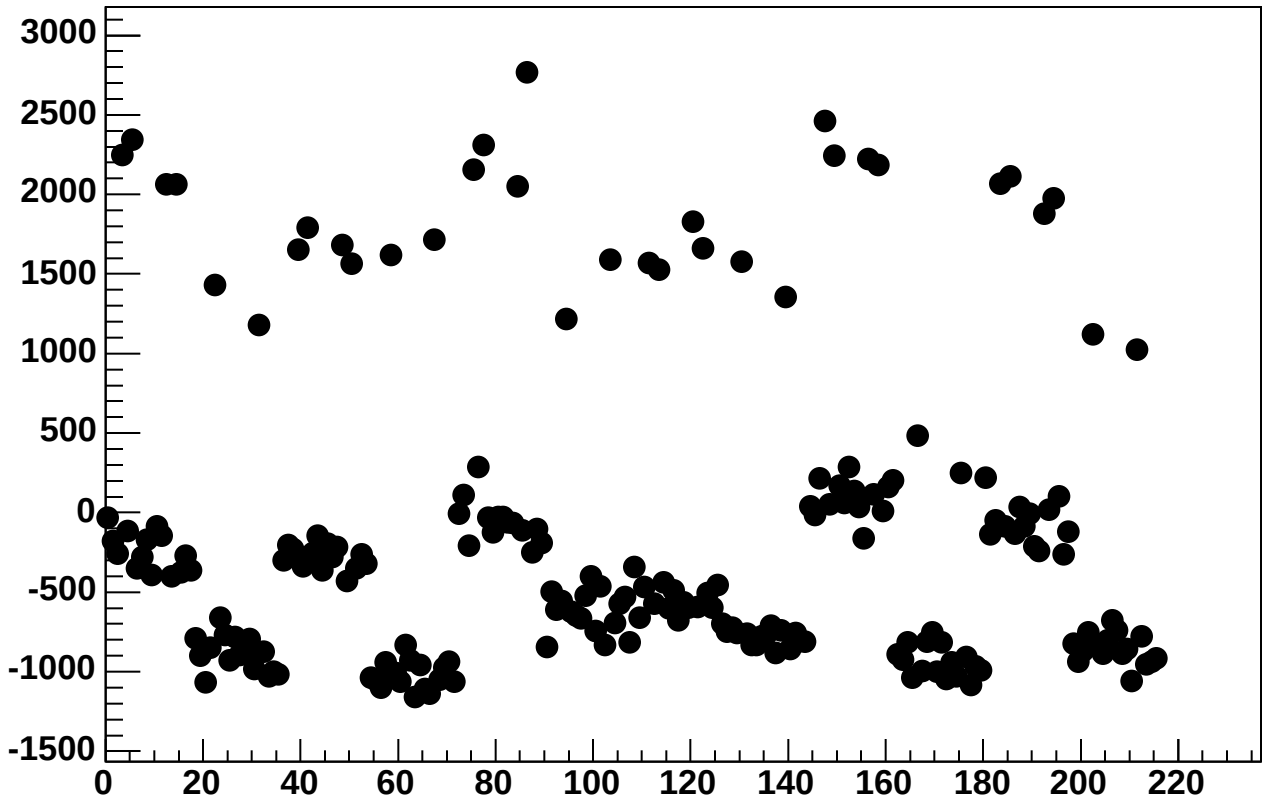
Enable 1, DAC=1600, Hold=115, ADC Mean vs 18*Chip+Chan



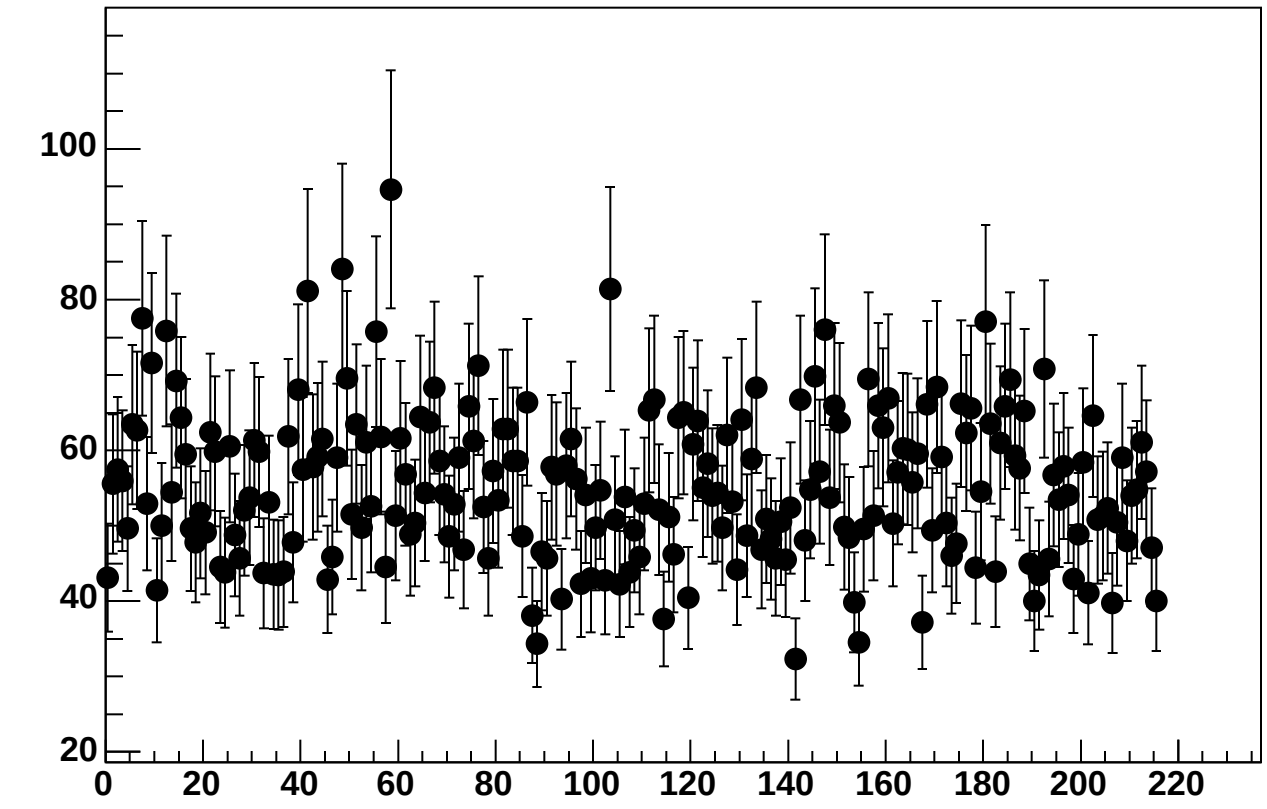
Enable 1, DAC=1600, Hold=115, ADC Noise vs 18*Chip+Chan



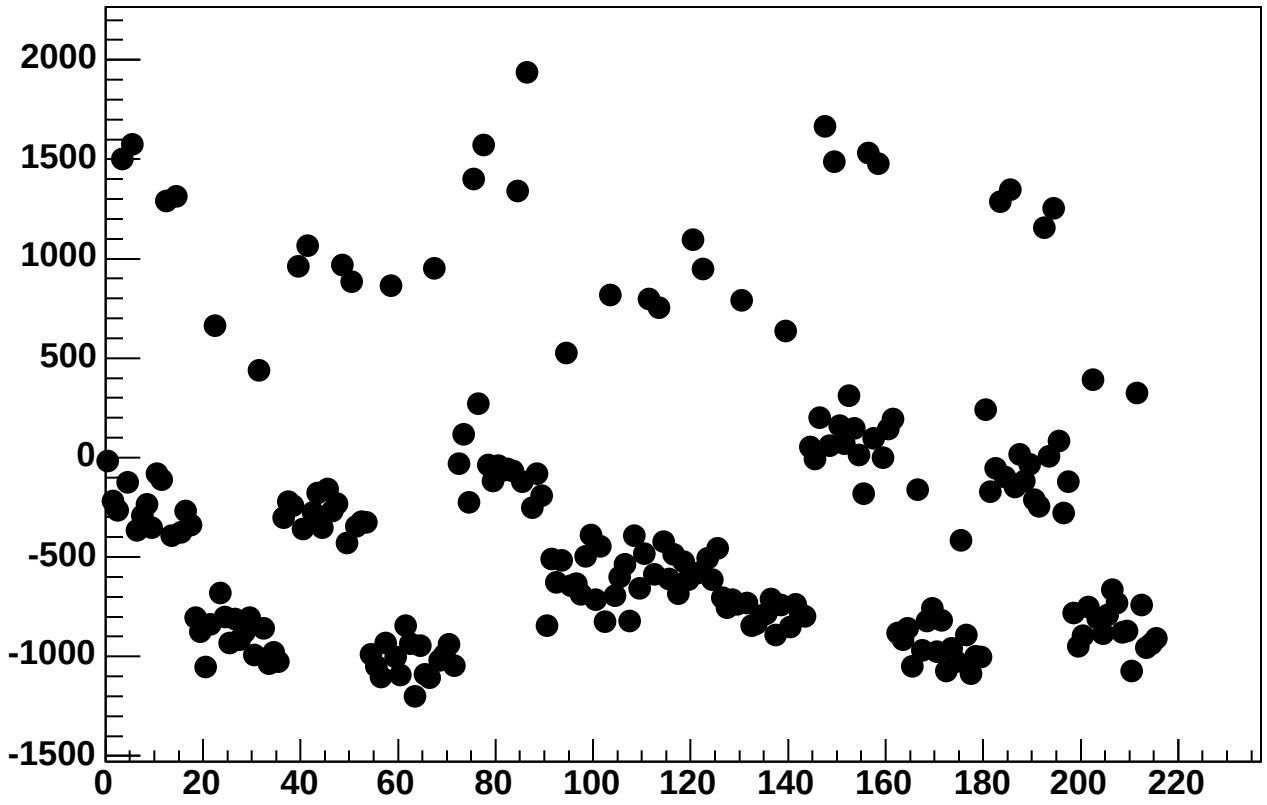
Enable 1, DAC=1600, Hold=120, ADC Mean vs 18*Chip+Chan



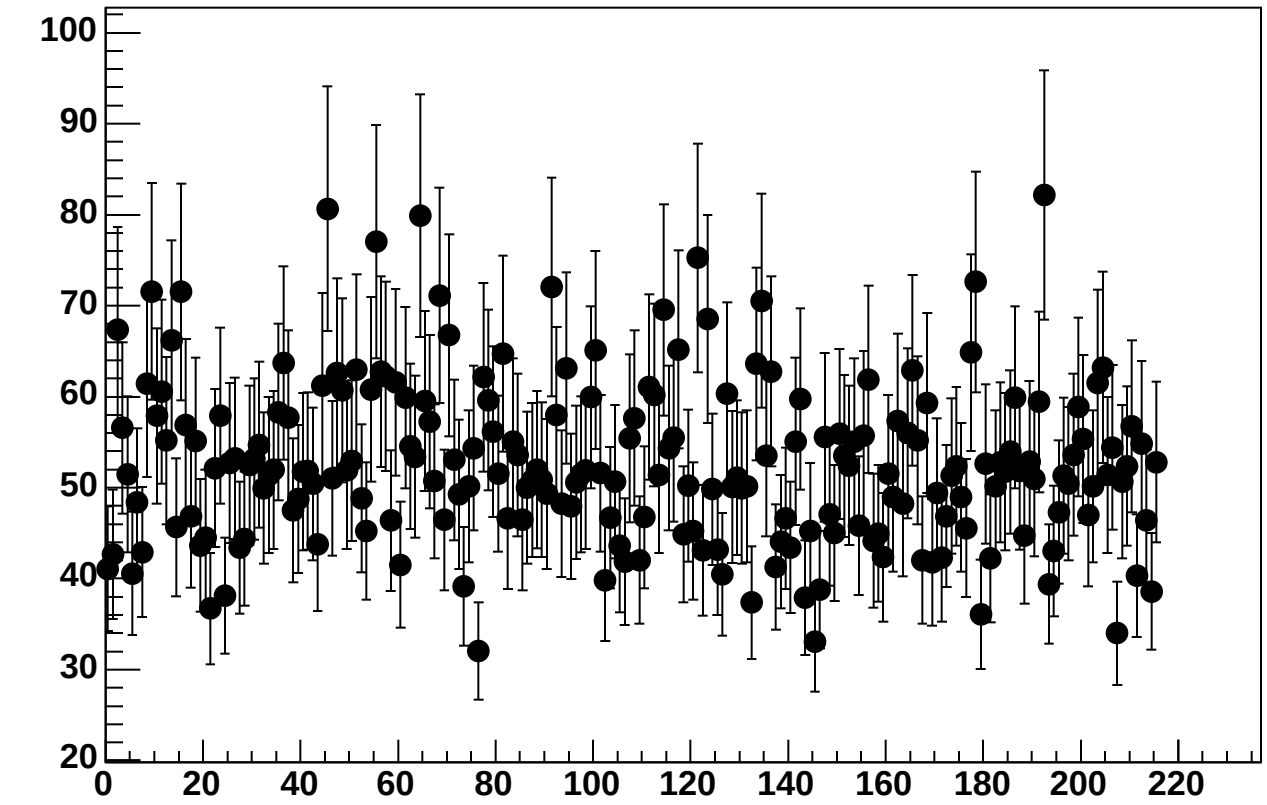
Enable 1, DAC=1600, Hold=120, ADC Noise vs 18*Chip+Chan



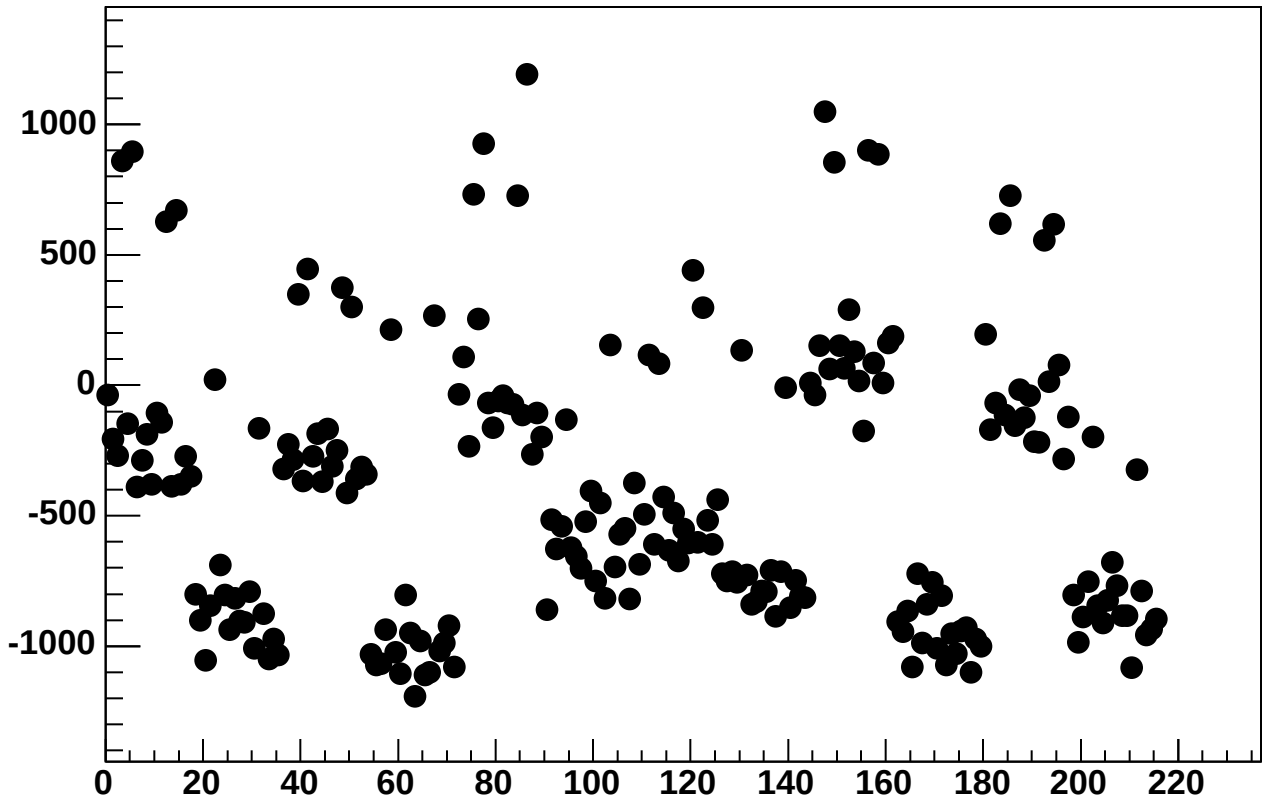
Enable 1, DAC=1600, Hold=125, ADC Mean vs 18*Chip+Chan



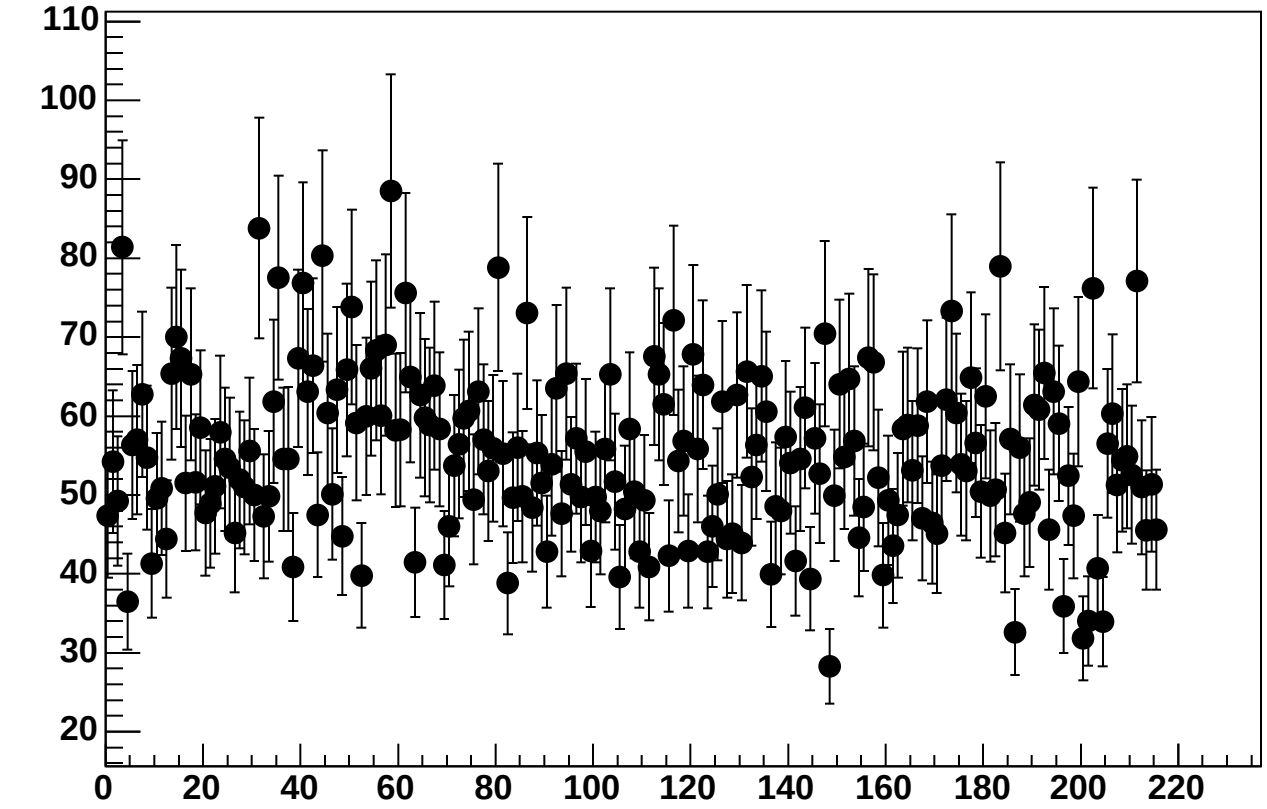
Enable 1, DAC=1600, Hold=125, ADC Noise vs 18*Chip+Chan



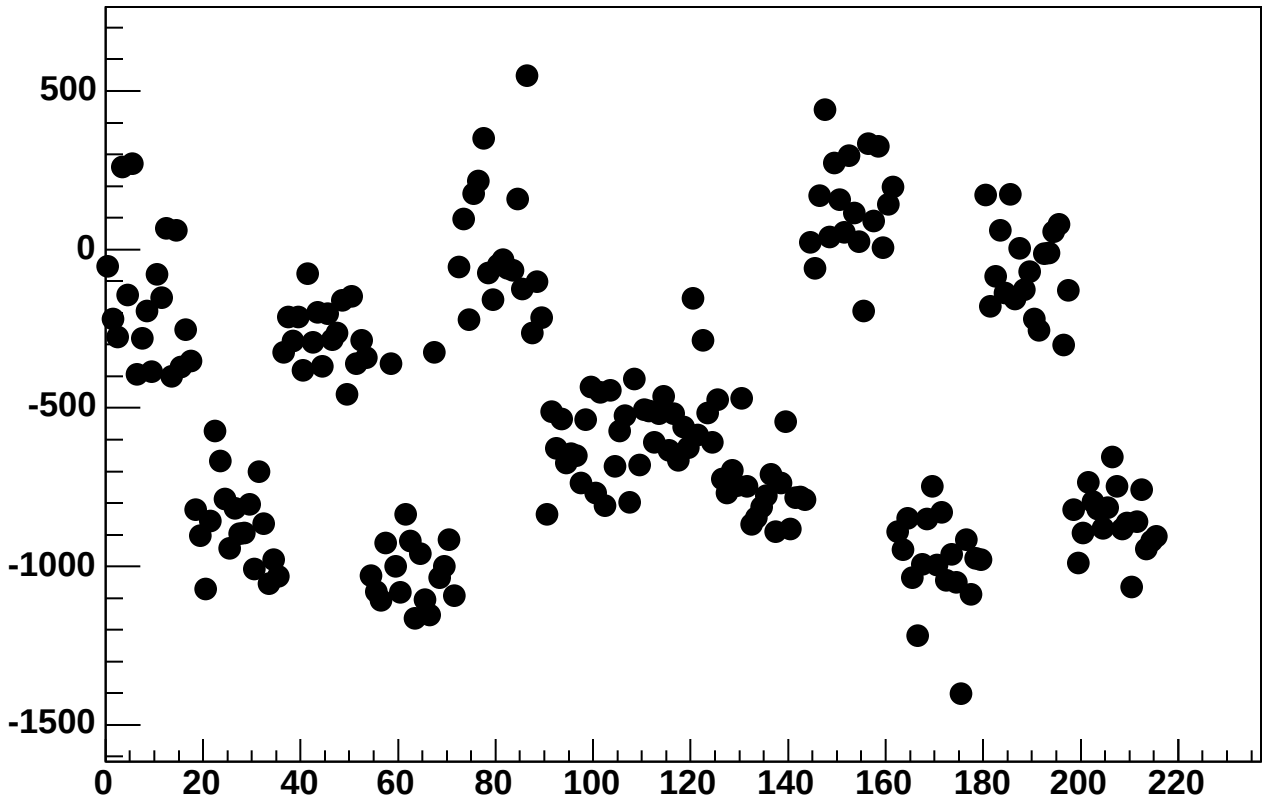
Enable 1, DAC=1600, Hold=130, ADC Mean vs 18*Chip+Chan



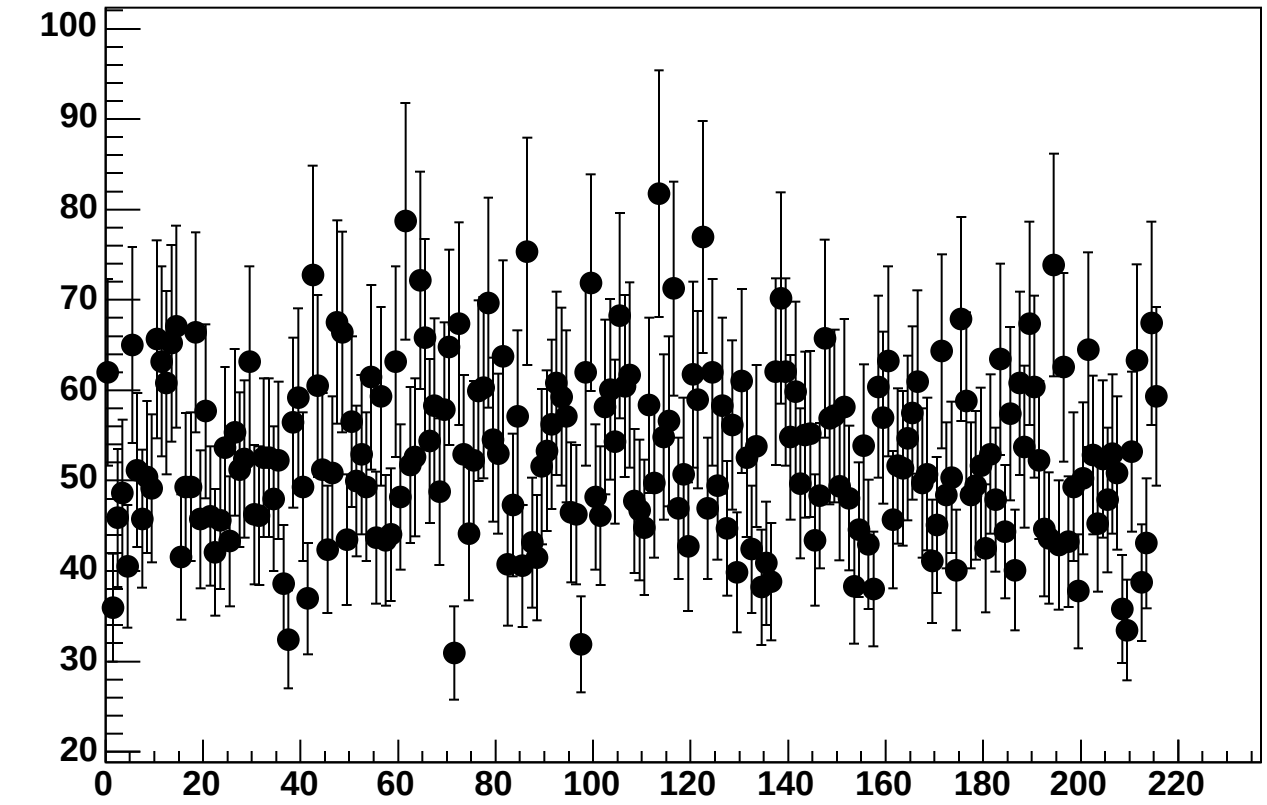
Enable 1, DAC=1600, Hold=130, ADC Noise vs 18*Chip+Chan



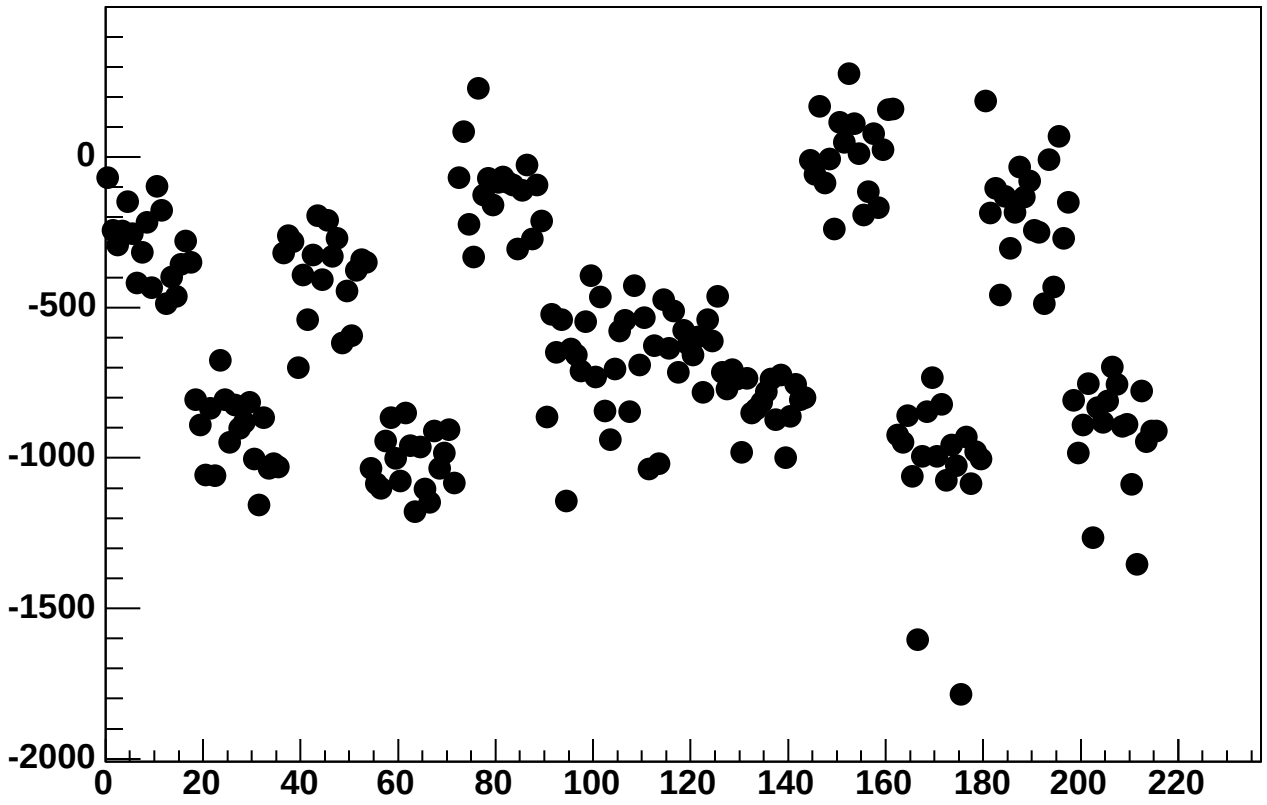
Enable 1, DAC=1600, Hold=135, ADC Mean vs 18*Chip+Chan



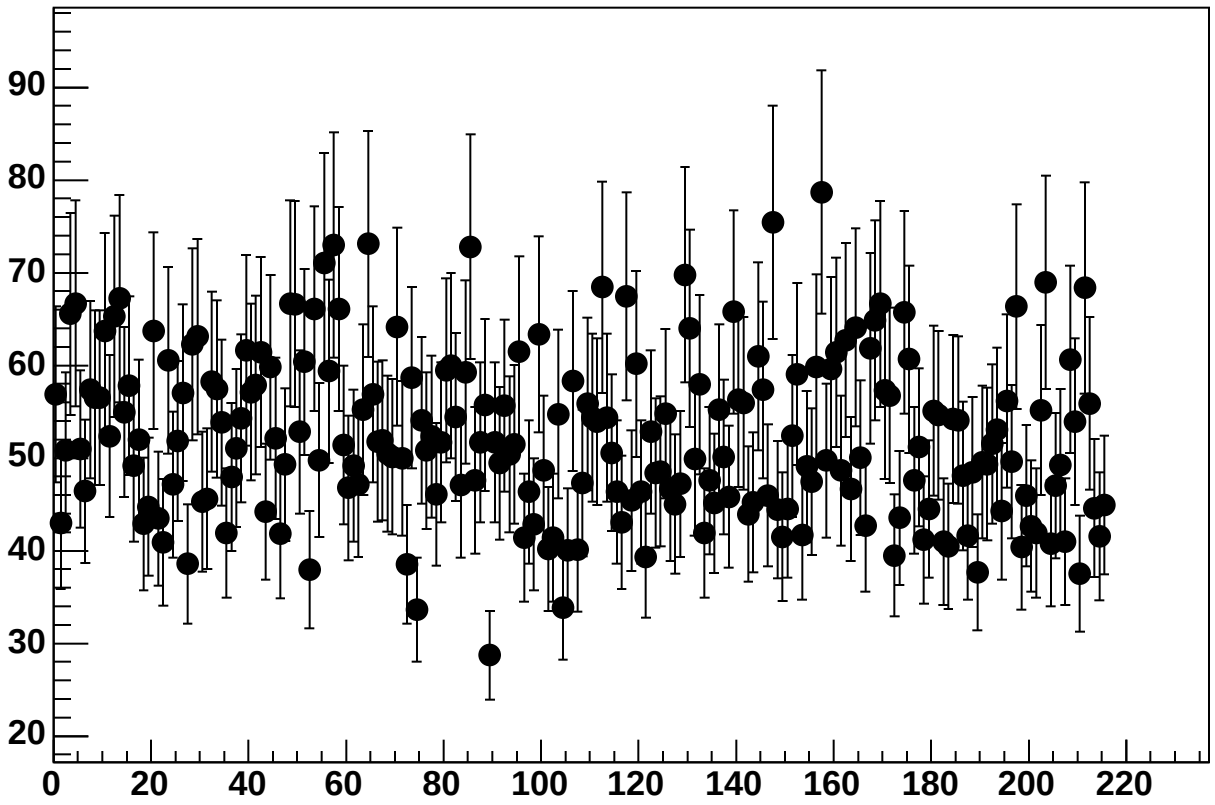
Enable 1, DAC=1600, Hold=135, ADC Noise vs 18*Chip+Chan



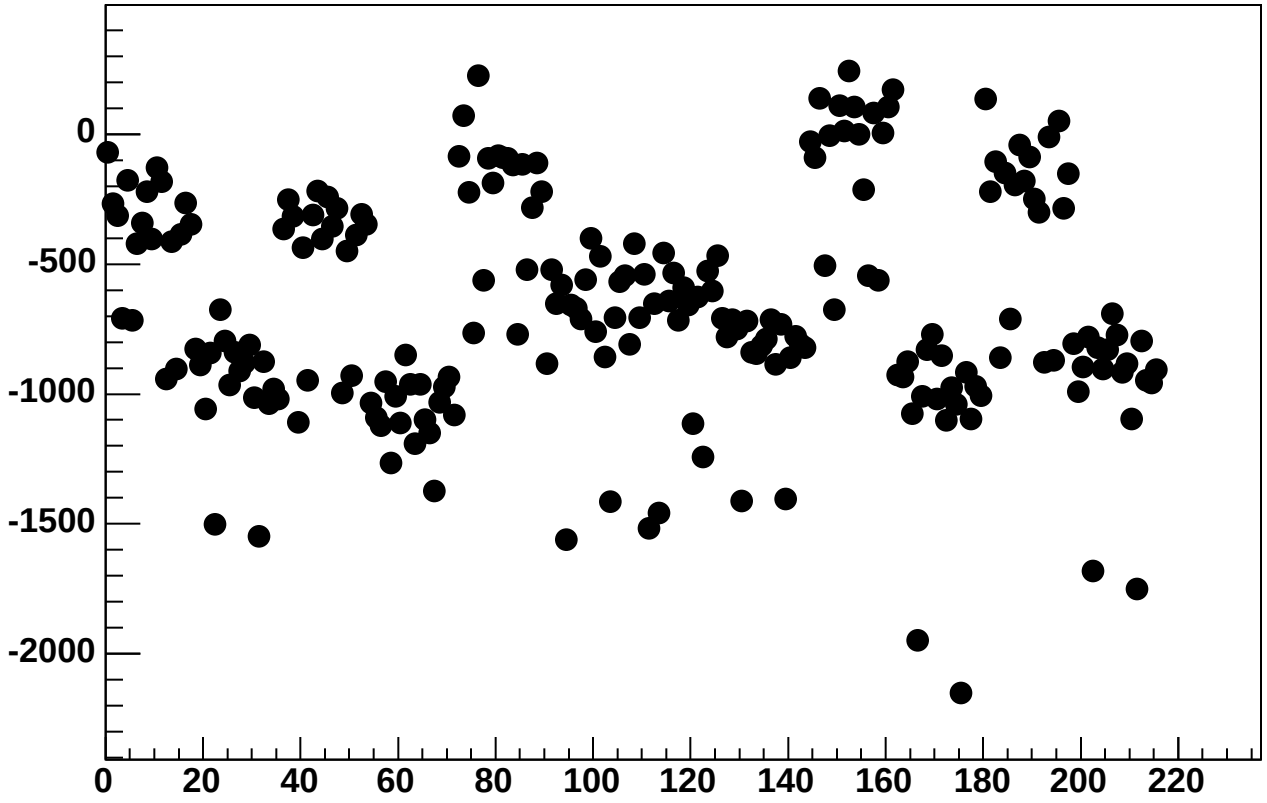
Enable 1, DAC=1600, Hold=140, ADC Mean vs 18*Chip+Chan



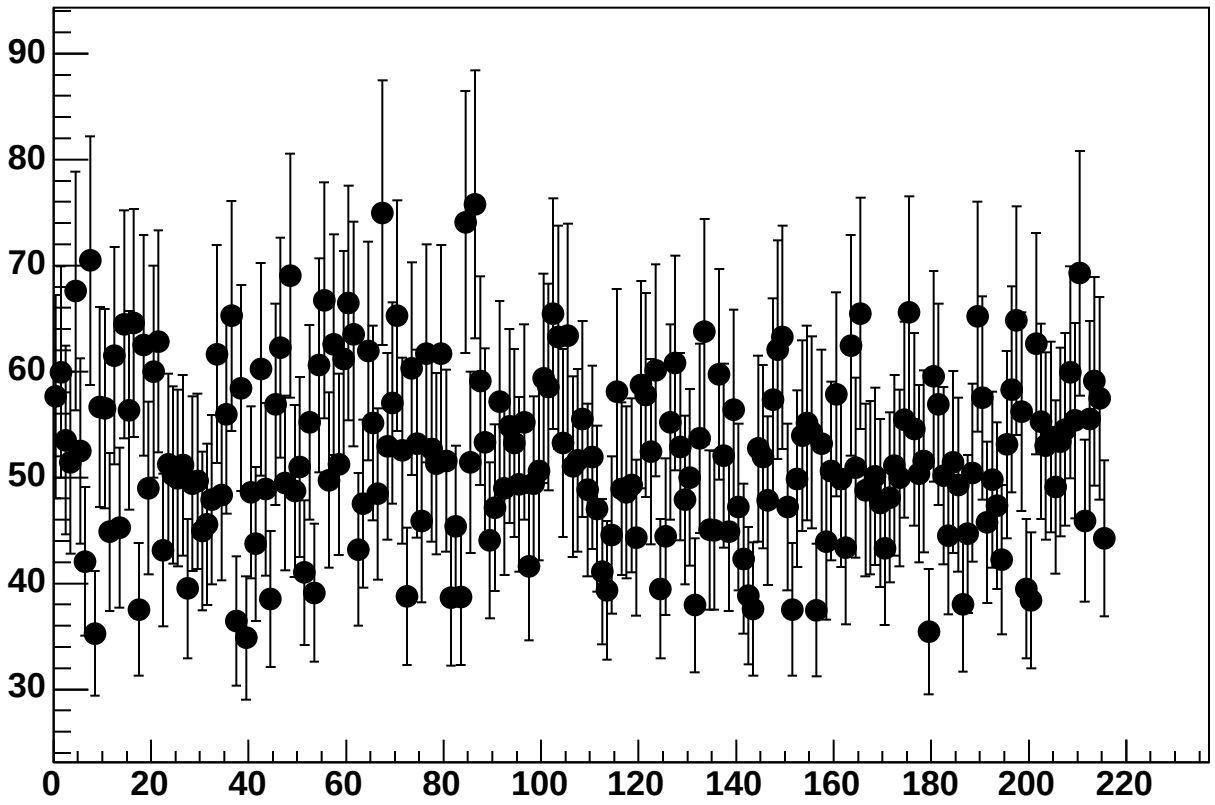
Enable 1, DAC=1600, Hold=140, ADC Noise vs 18*Chip+Chan



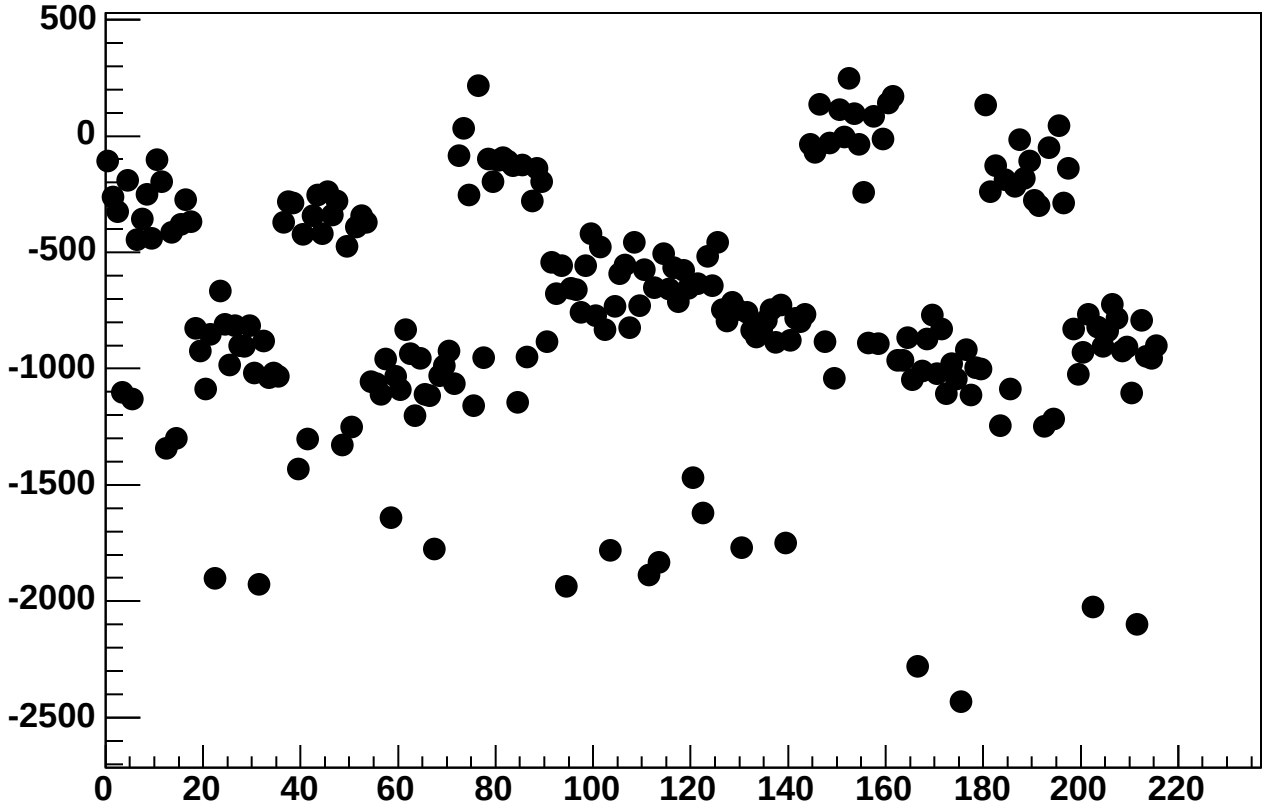
Enable 1, DAC=1600, Hold=145, ADC Mean vs 18*Chip+Chan



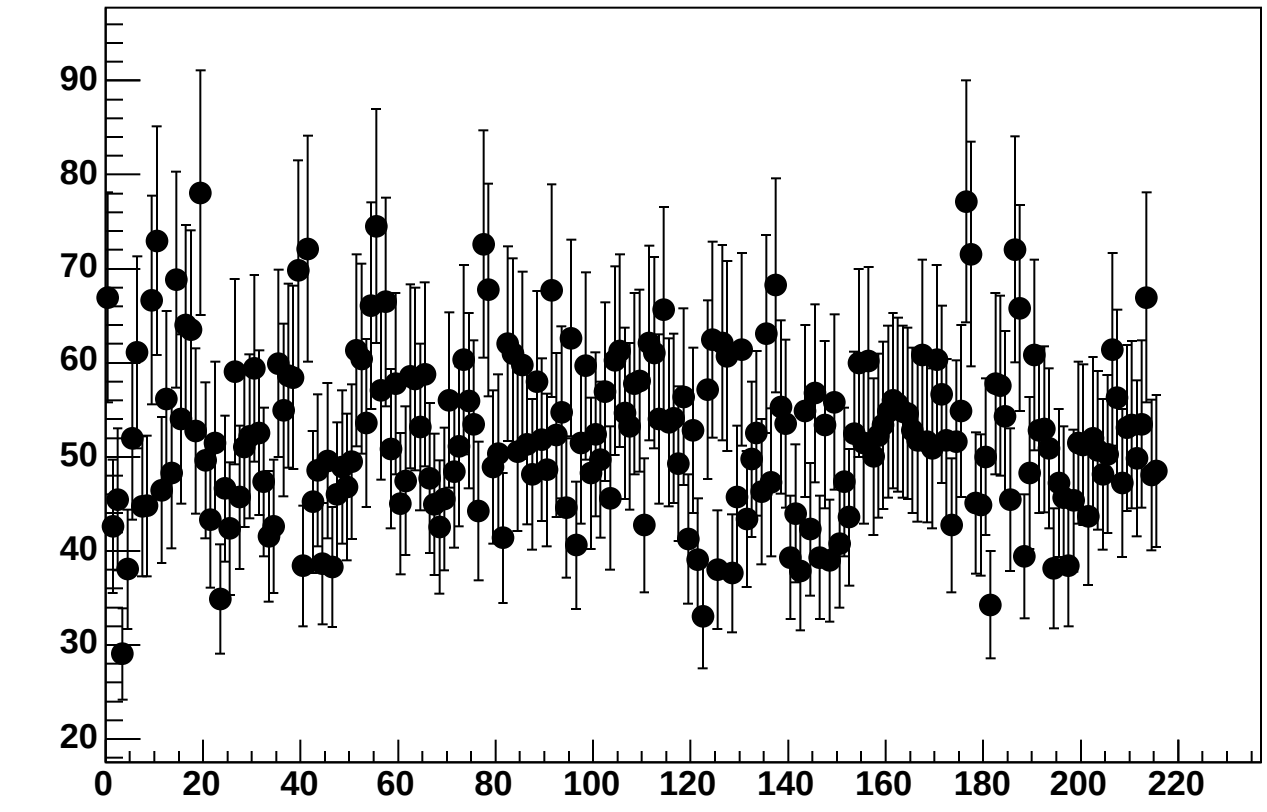
Enable 1, DAC=1600, Hold=145, ADC Noise vs 18*Chip+Chan



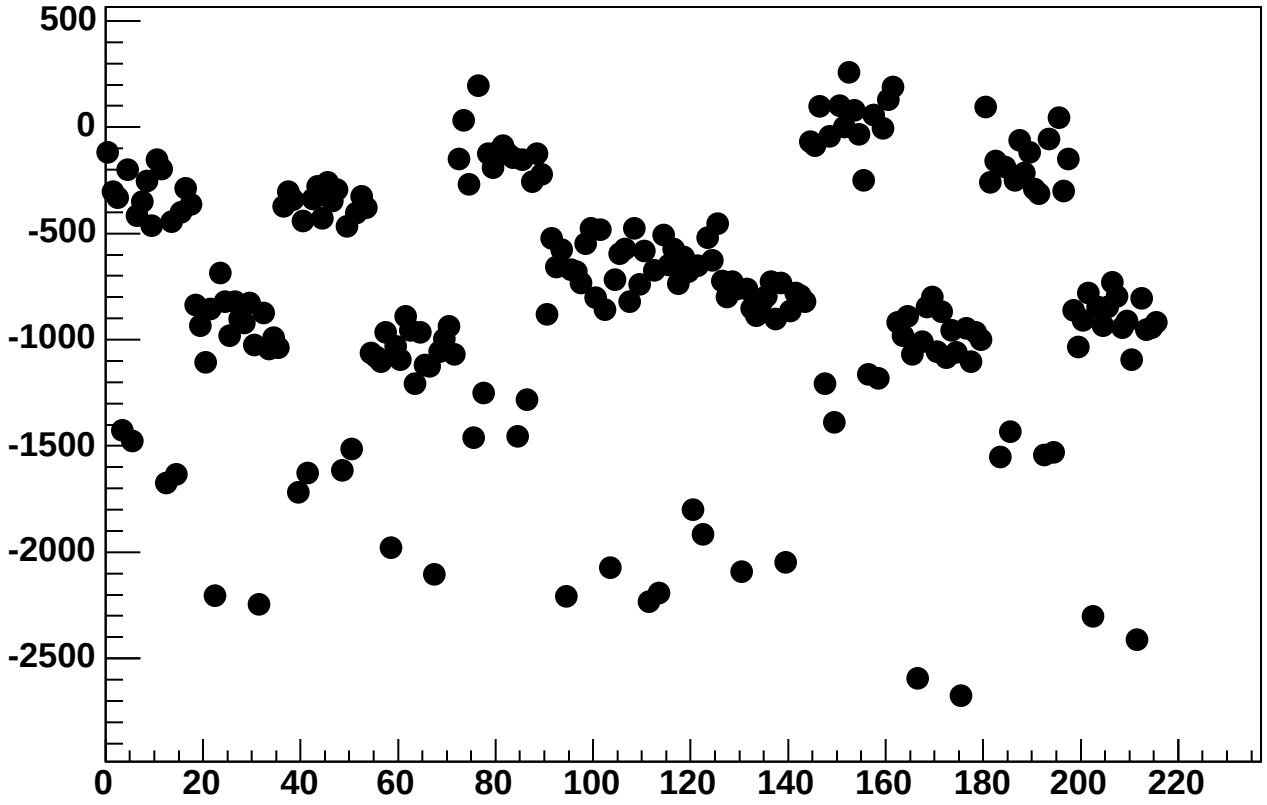
Enable 1, DAC=1600, Hold=150, ADC Mean vs 18*Chip+Chan



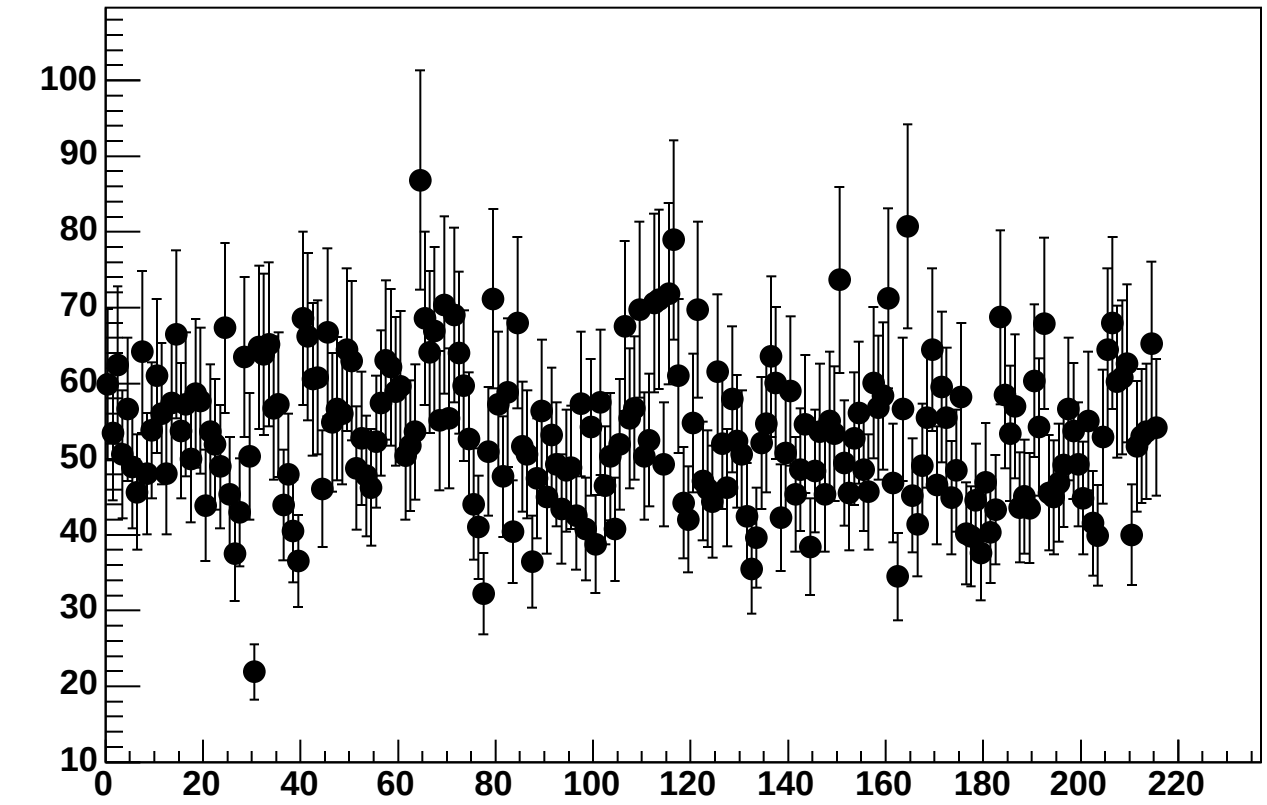
Enable 1, DAC=1600, Hold=150, ADC Noise vs 18*Chip+Chan



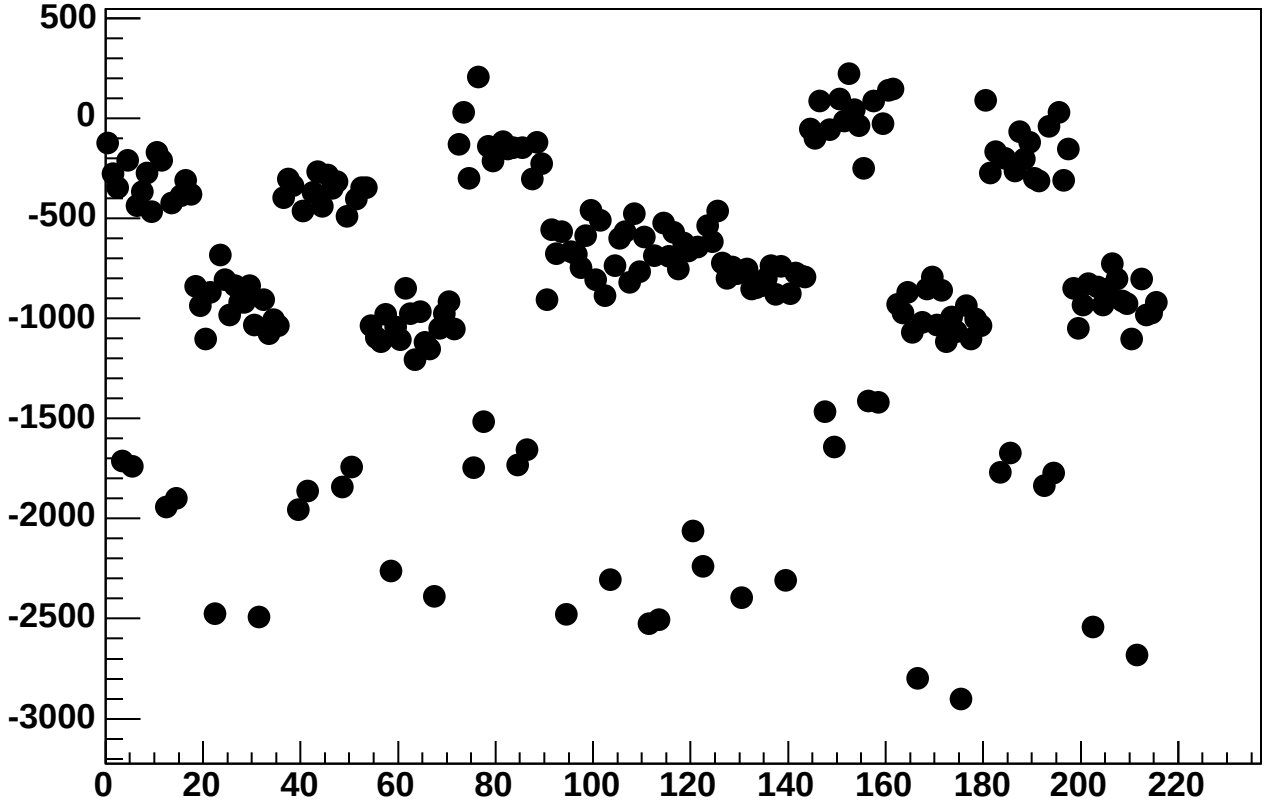
Enable 1, DAC=1600, Hold=155, ADC Mean vs 18*Chip+Chan



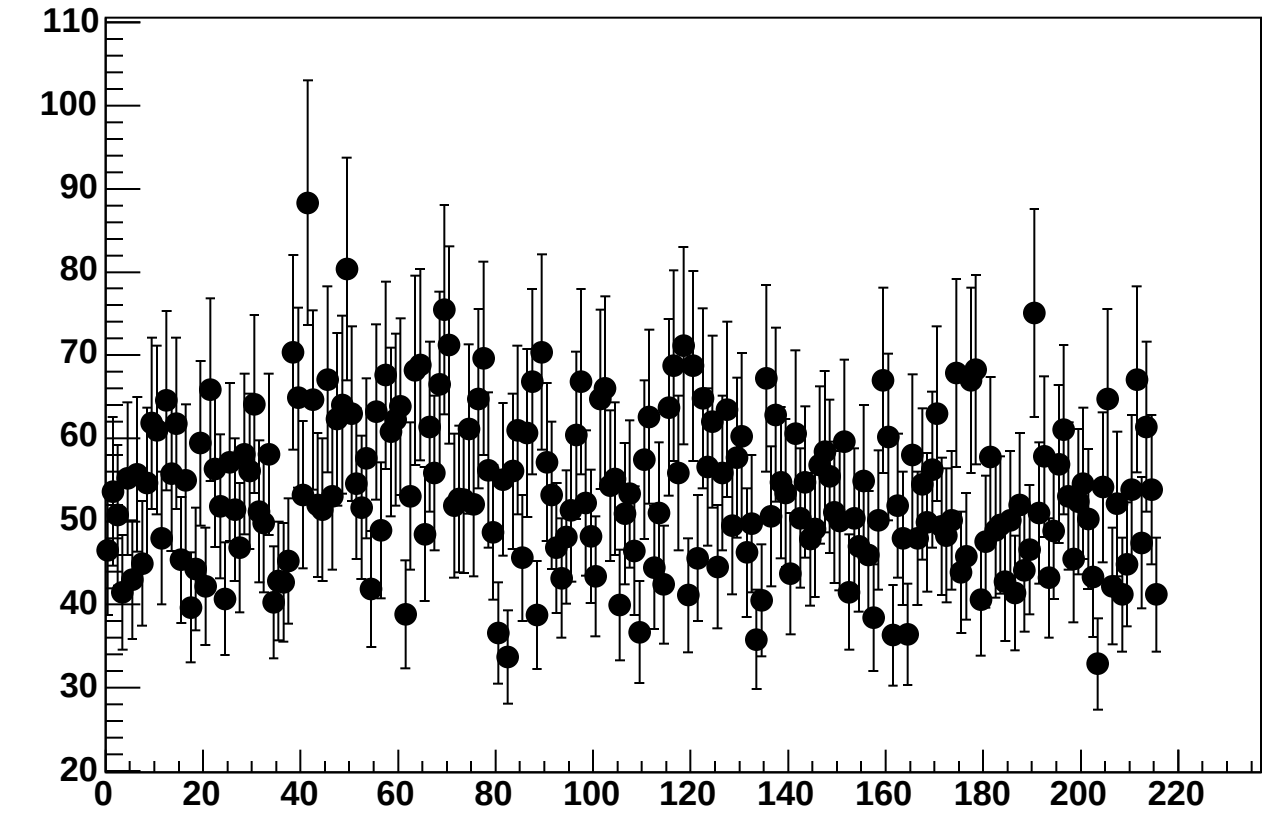
Enable 1, DAC=1600, Hold=155, ADC Noise vs 18*Chip+Chan



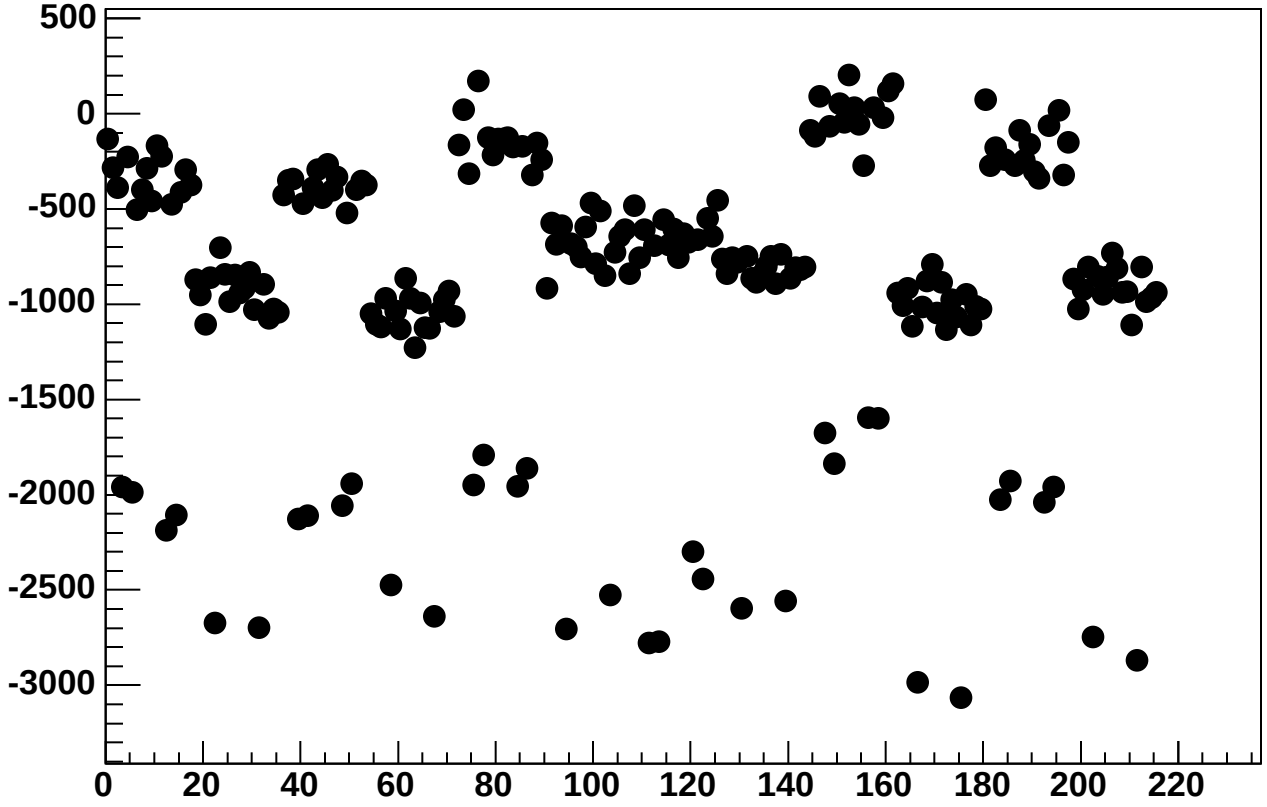
Enable 1, DAC=1600, Hold=160, ADC Mean vs 18*Chip+Chan



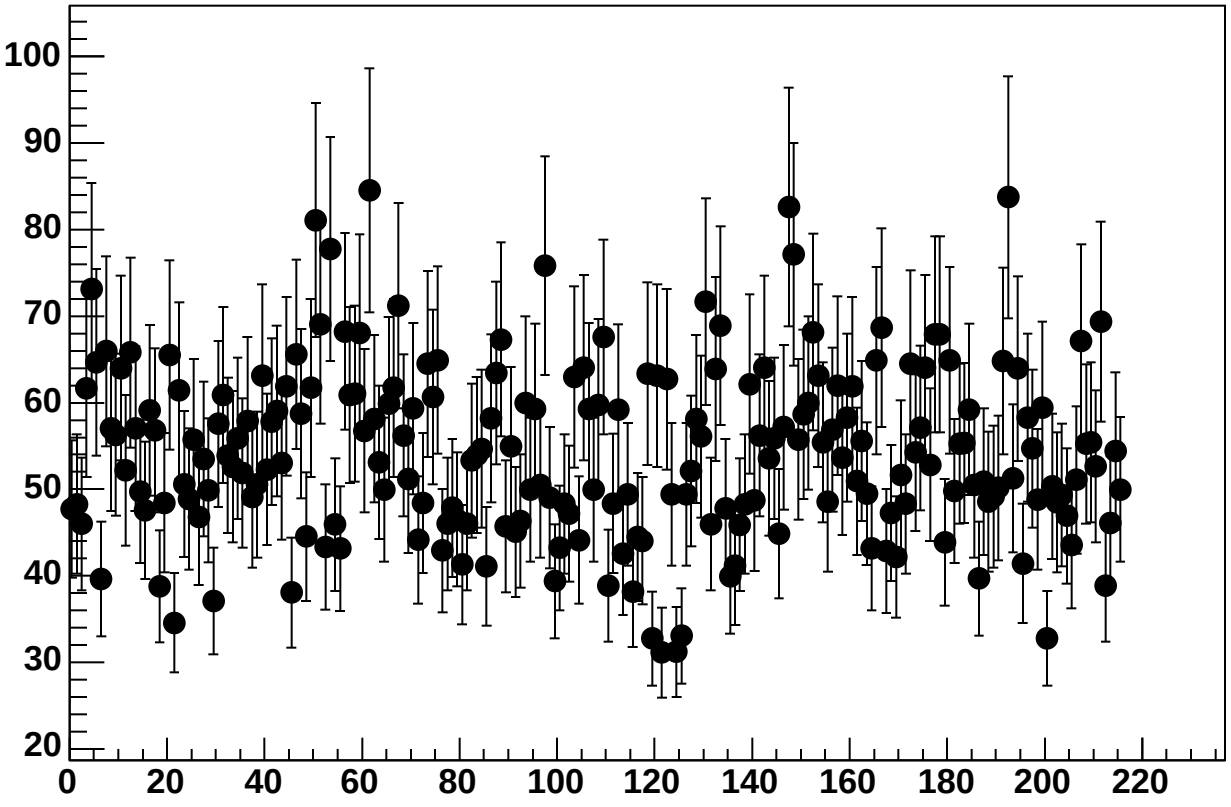
Enable 1, DAC=1600, Hold=160, ADC Noise vs 18*Chip+Chan



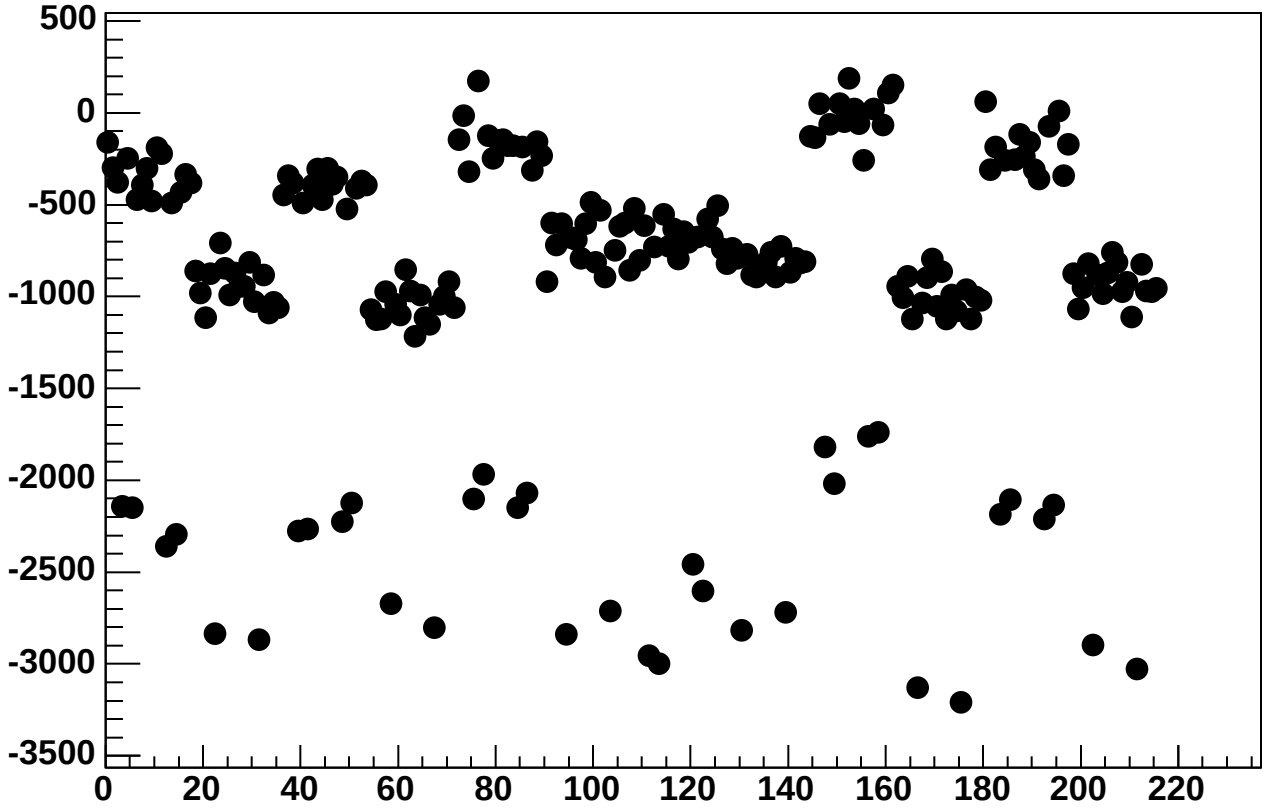
Enable 1, DAC=1600, Hold=165, ADC Mean vs 18*Chip+Chan



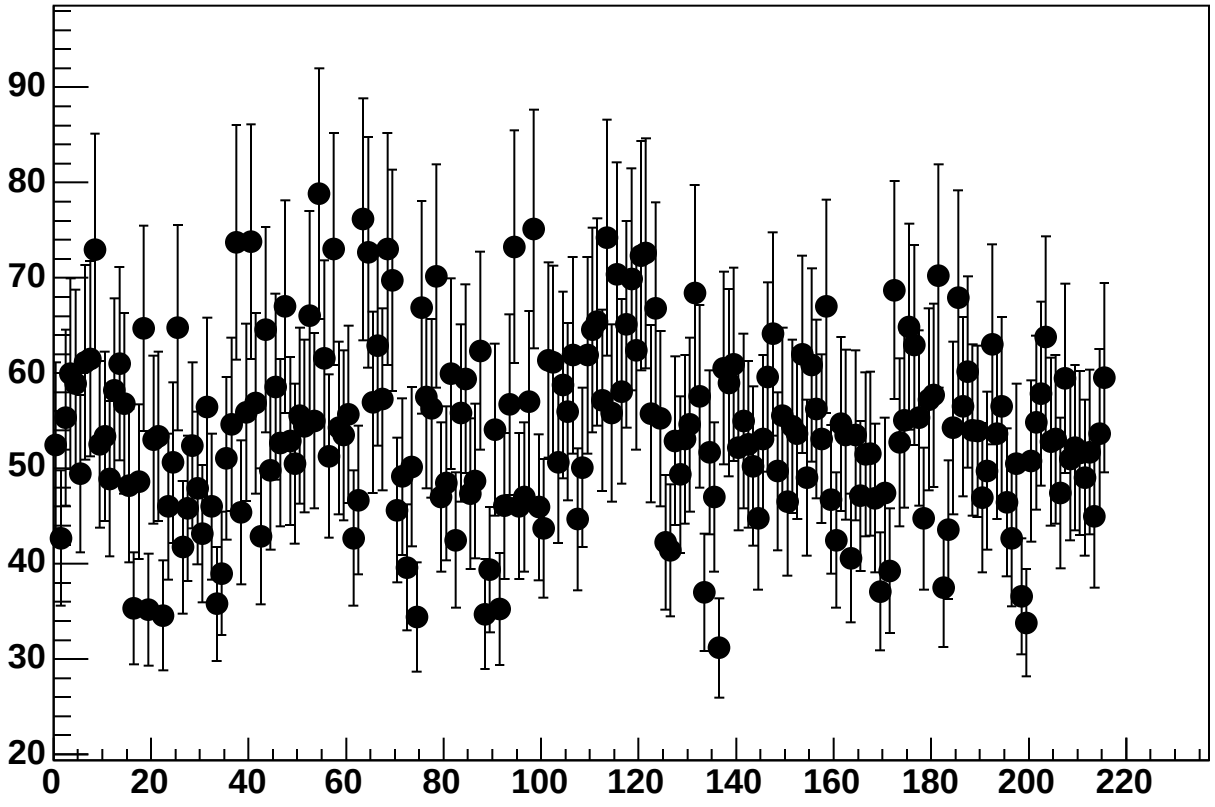
Enable 1, DAC=1600, Hold=165, ADC Noise vs 18*Chip+Chan



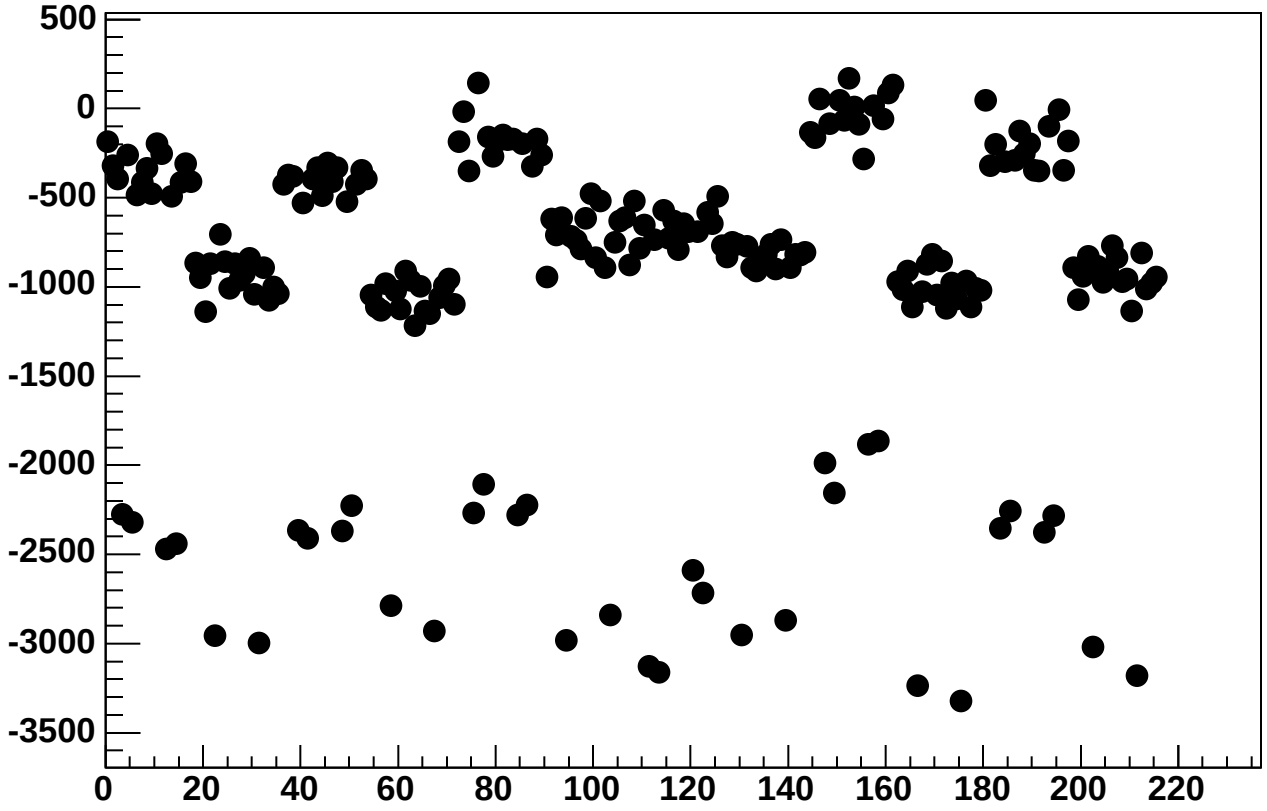
Enable 1, DAC=1600, Hold=170, ADC Mean vs 18*Chip+Chan



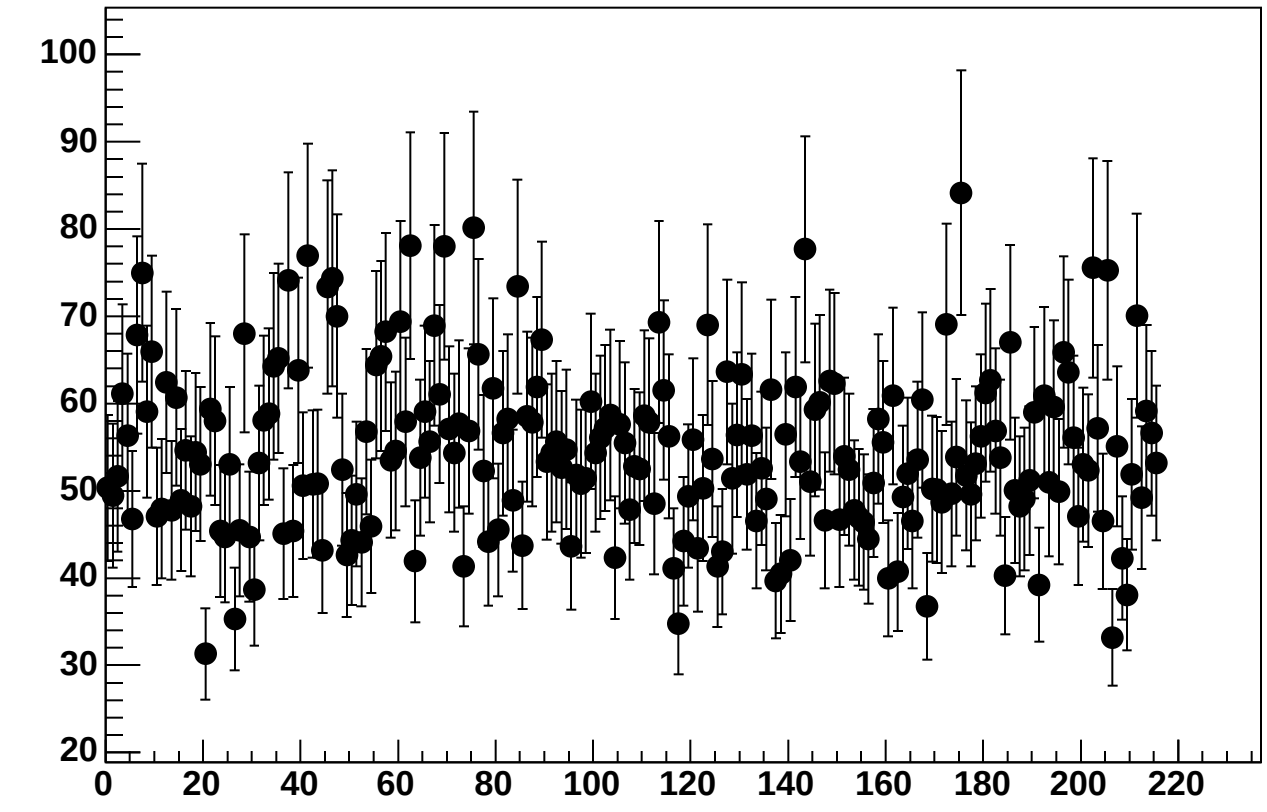
Enable 1, DAC=1600, Hold=170, ADC Noise vs 18*Chip+Chan



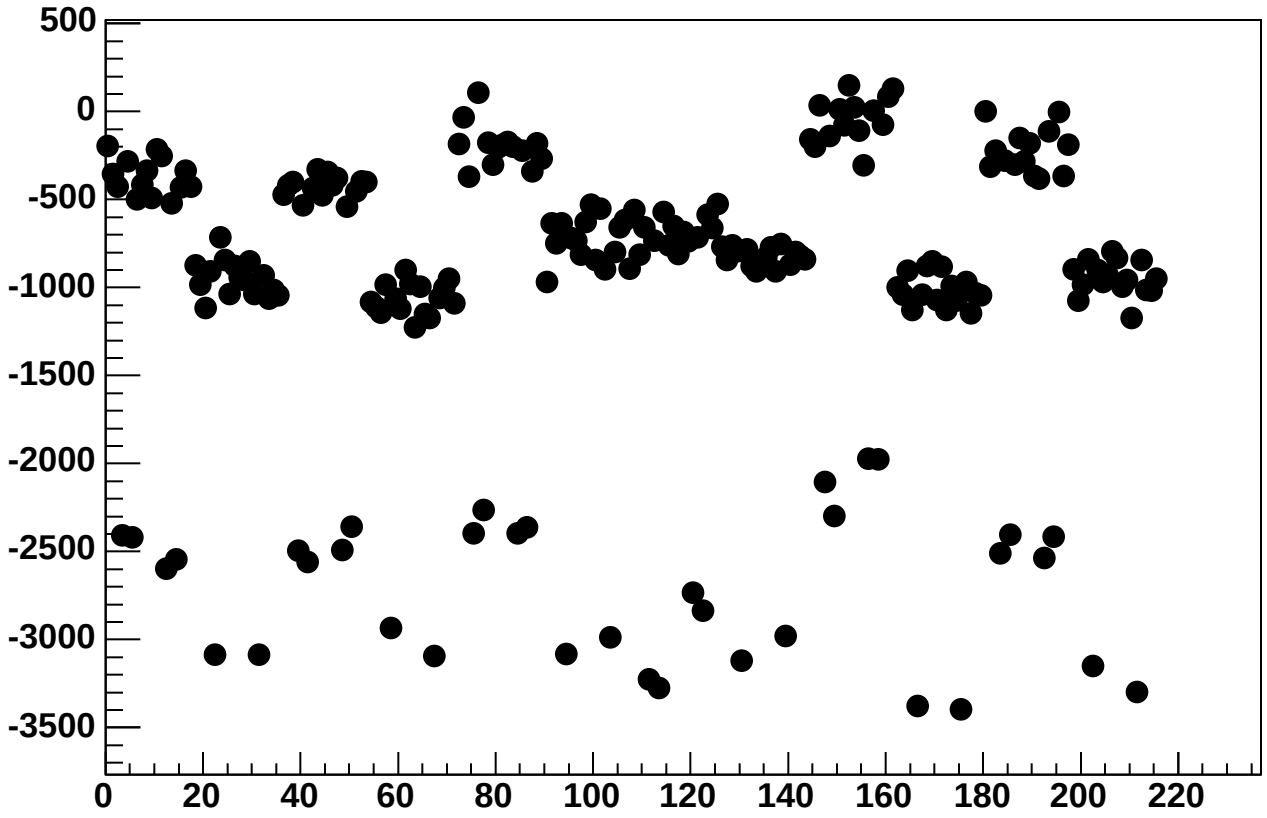
Enable 1, DAC=1600, Hold=175, ADC Mean vs 18*Chip+Chan



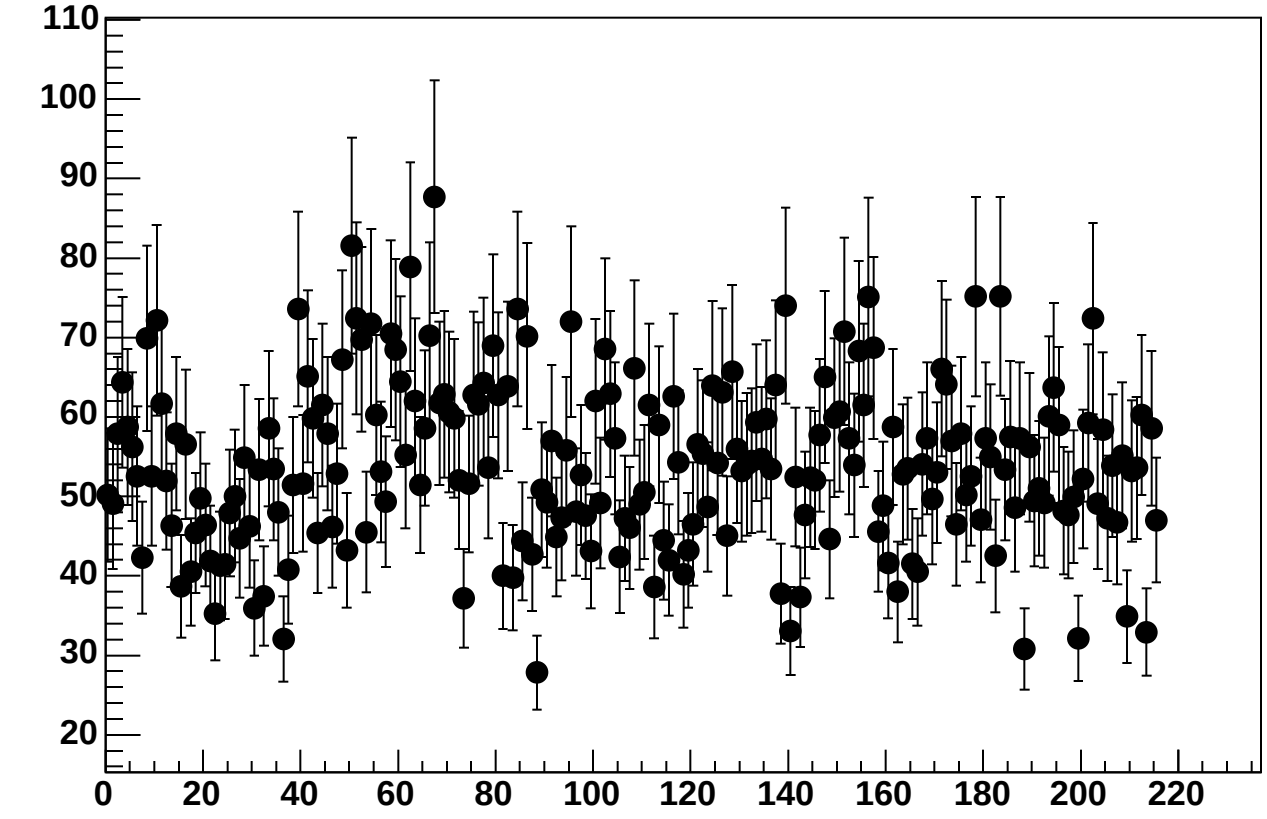
Enable 1, DAC=1600, Hold=175, ADC Noise vs 18*Chip+Chan



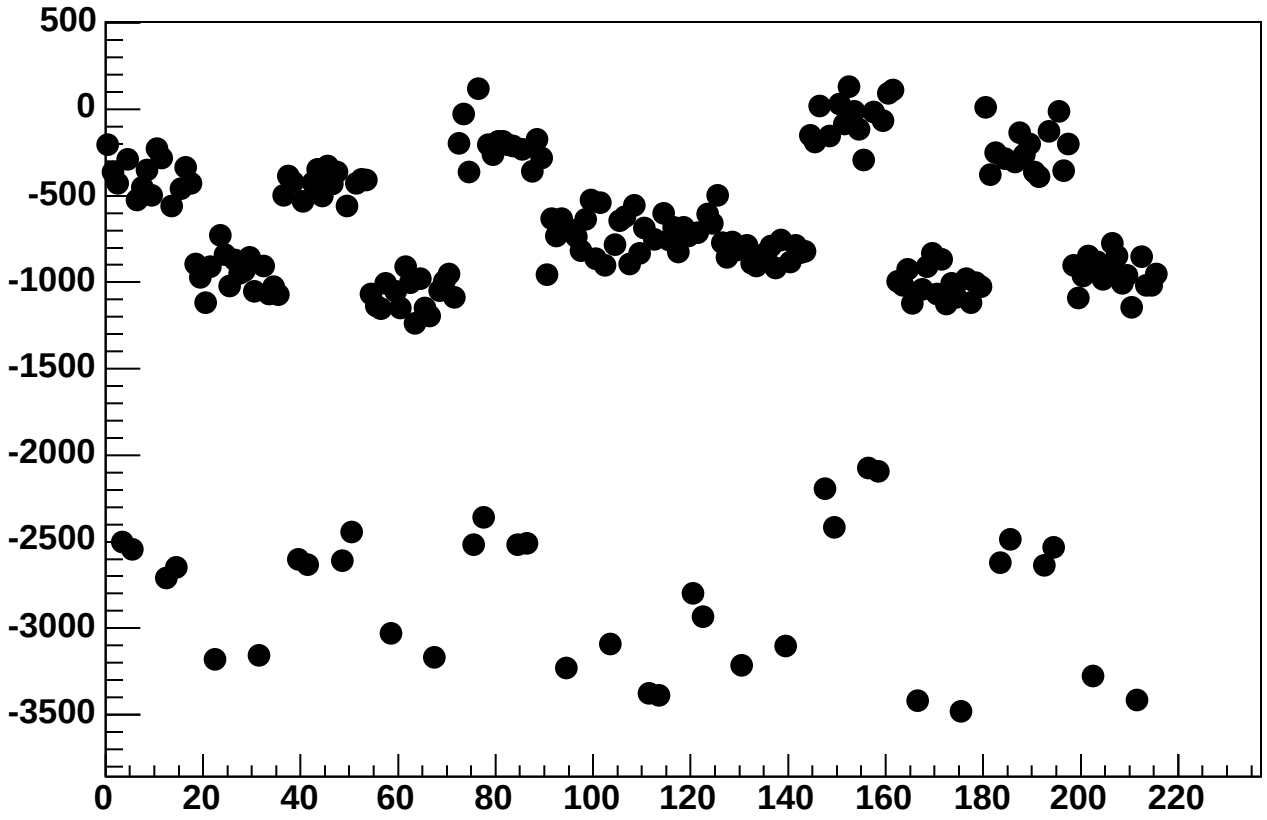
Enable 1, DAC=1600, Hold=180, ADC Mean vs 18*Chip+Chan



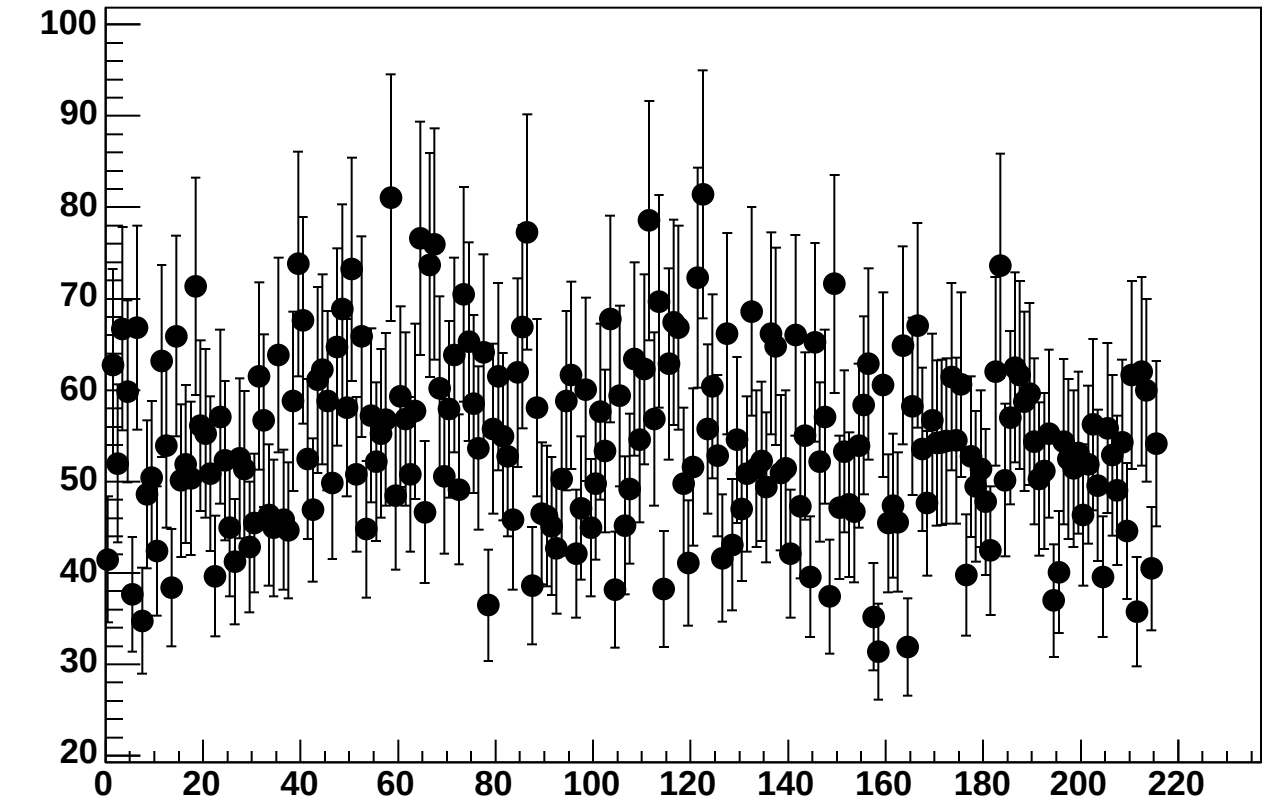
Enable 1, DAC=1600, Hold=180, ADC Noise vs 18*Chip+Chan



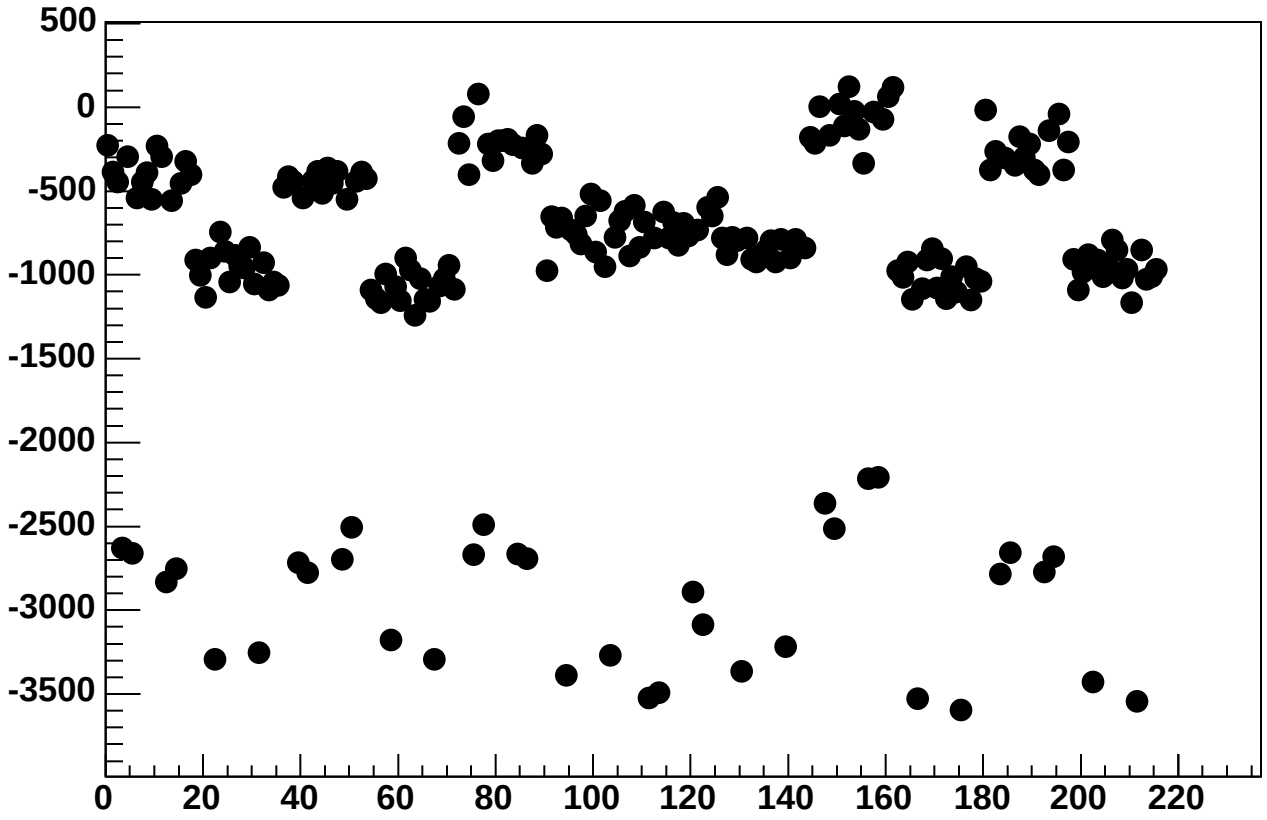
Enable 1, DAC=1600, Hold=185, ADC Mean vs 18*Chip+Chan



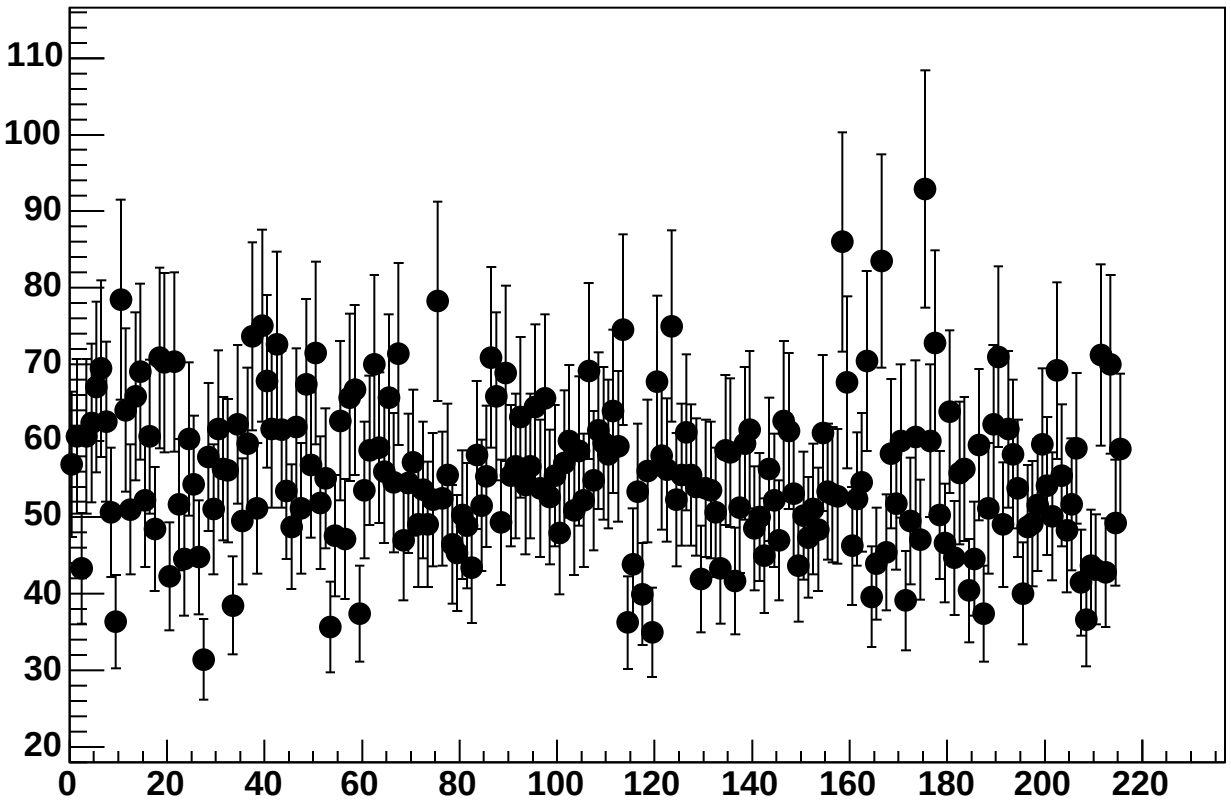
Enable 1, DAC=1600, Hold=185, ADC Noise vs 18*Chip+Chan



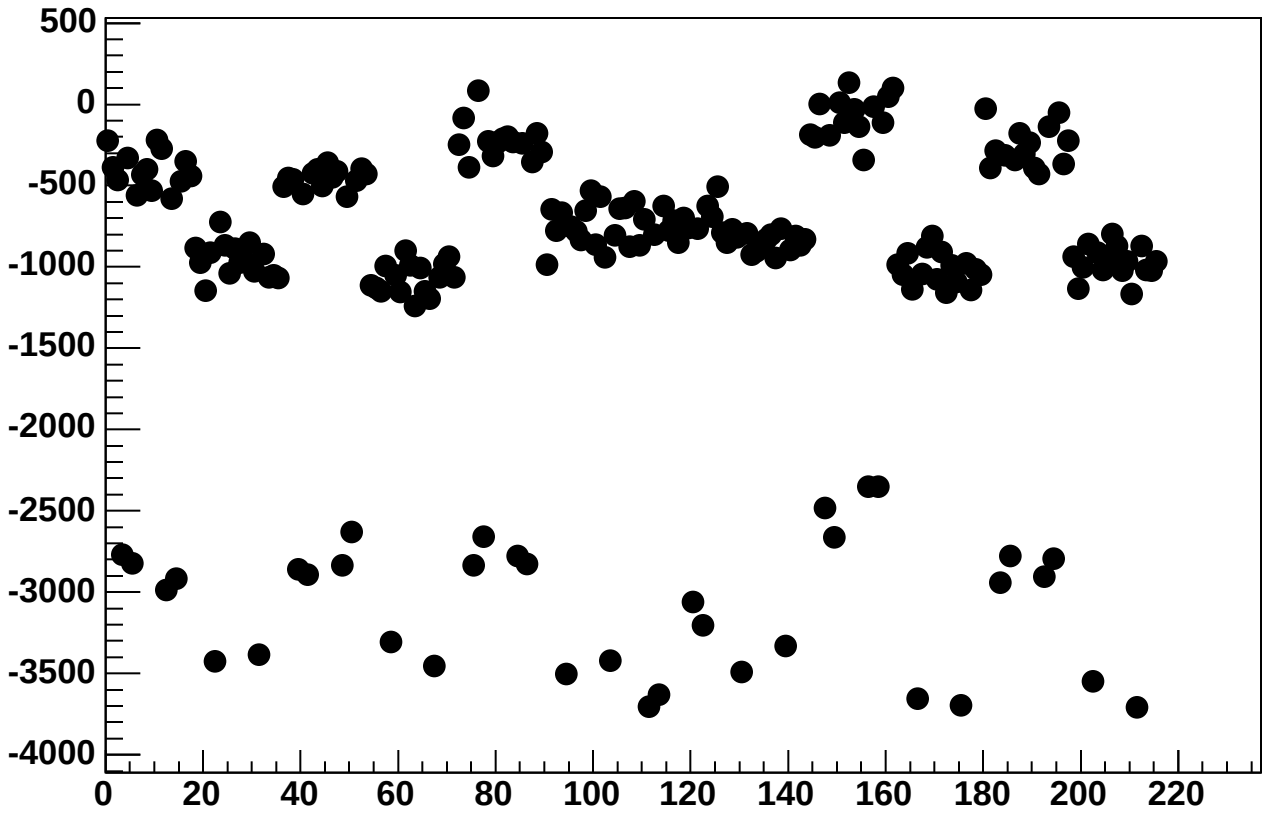
Enable 1, DAC=1600, Hold=190, ADC Mean vs 18*Chip+Chan



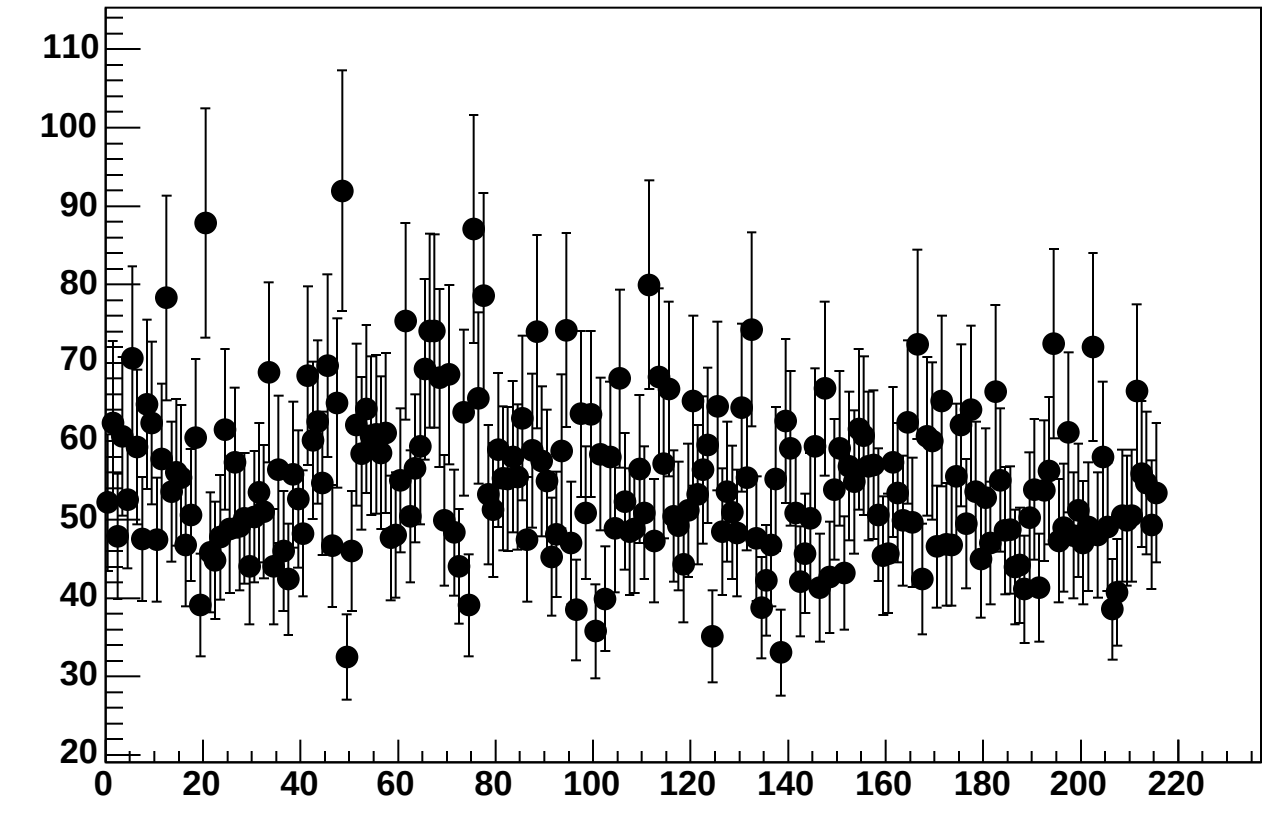
Enable 1, DAC=1600, Hold=190, ADC Noise vs 18*Chip+Chan



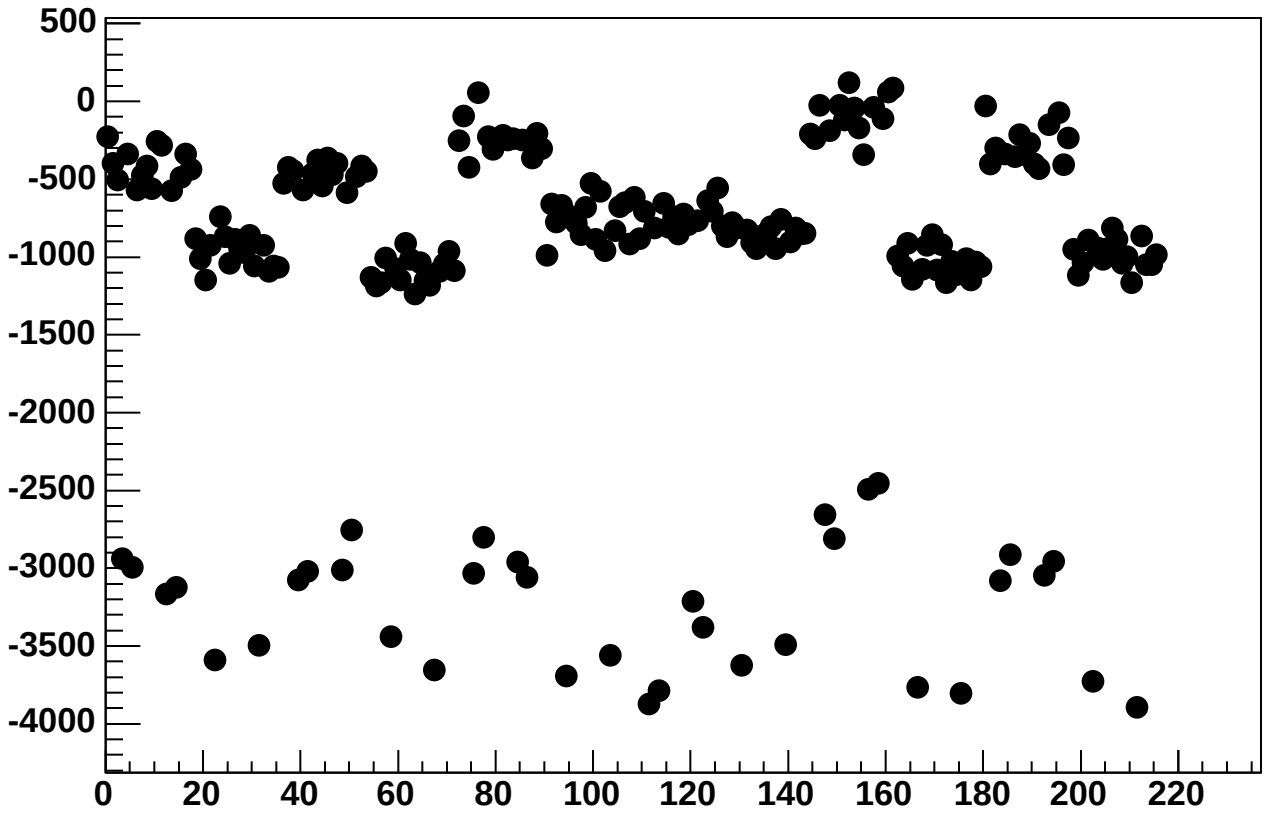
Enable 1, DAC=1600, Hold=195, ADC Mean vs 18*Chip+Chan



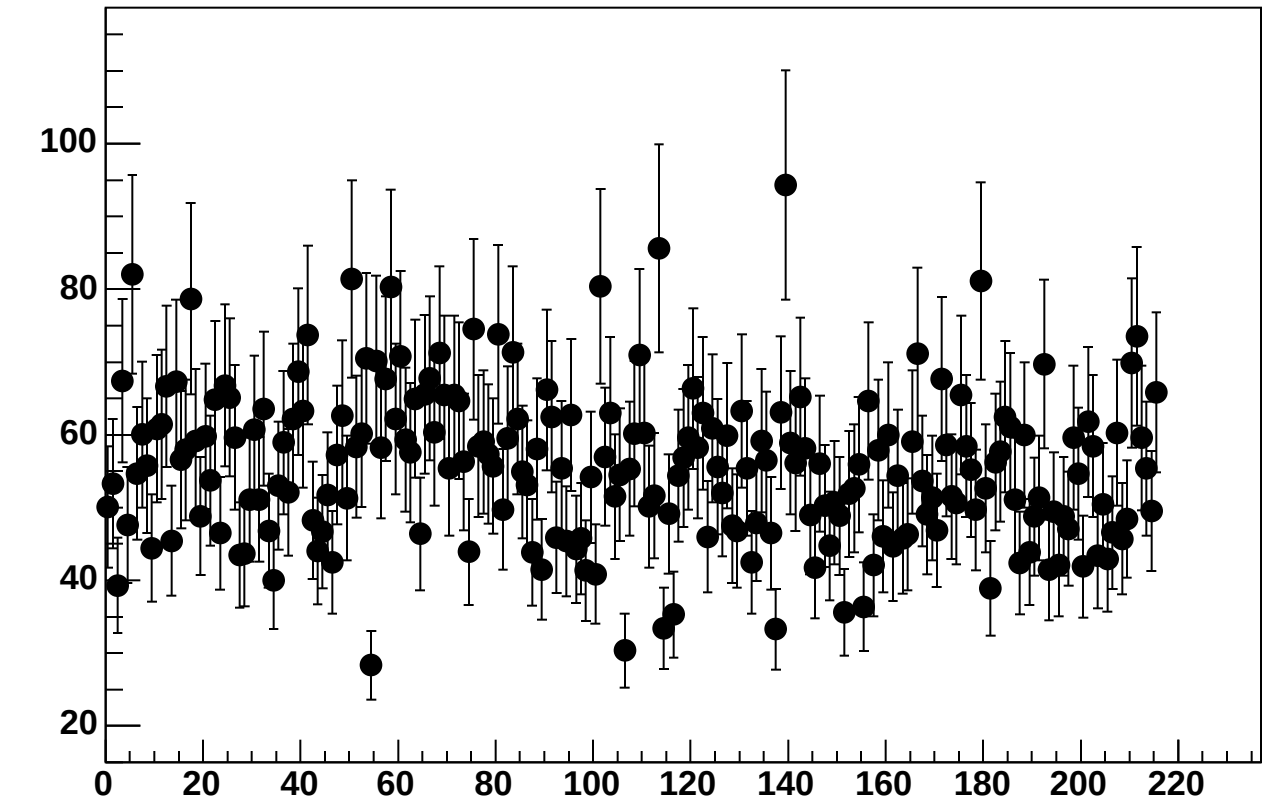
Enable 1, DAC=1600, Hold=195, ADC Noise vs 18*Chip+Chan



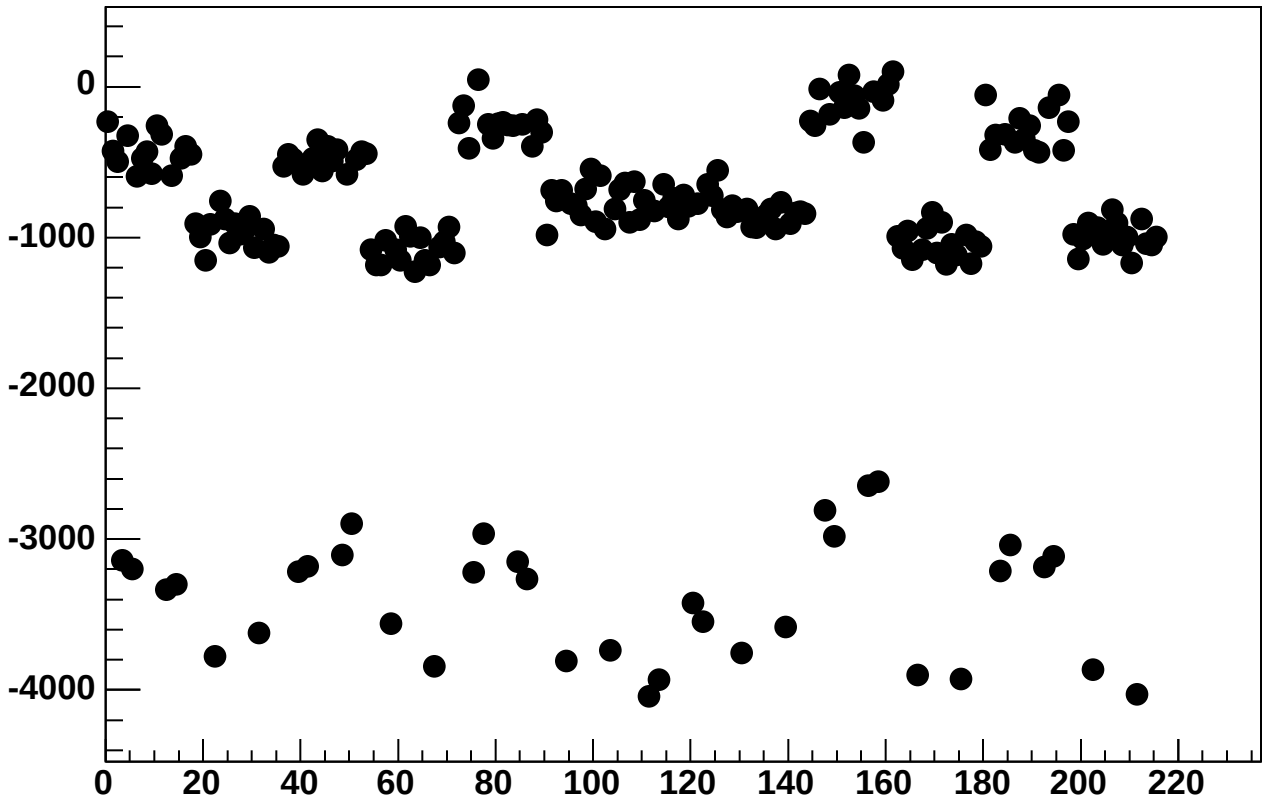
Enable 1, DAC=1600, Hold=200, ADC Mean vs 18*Chip+Chan



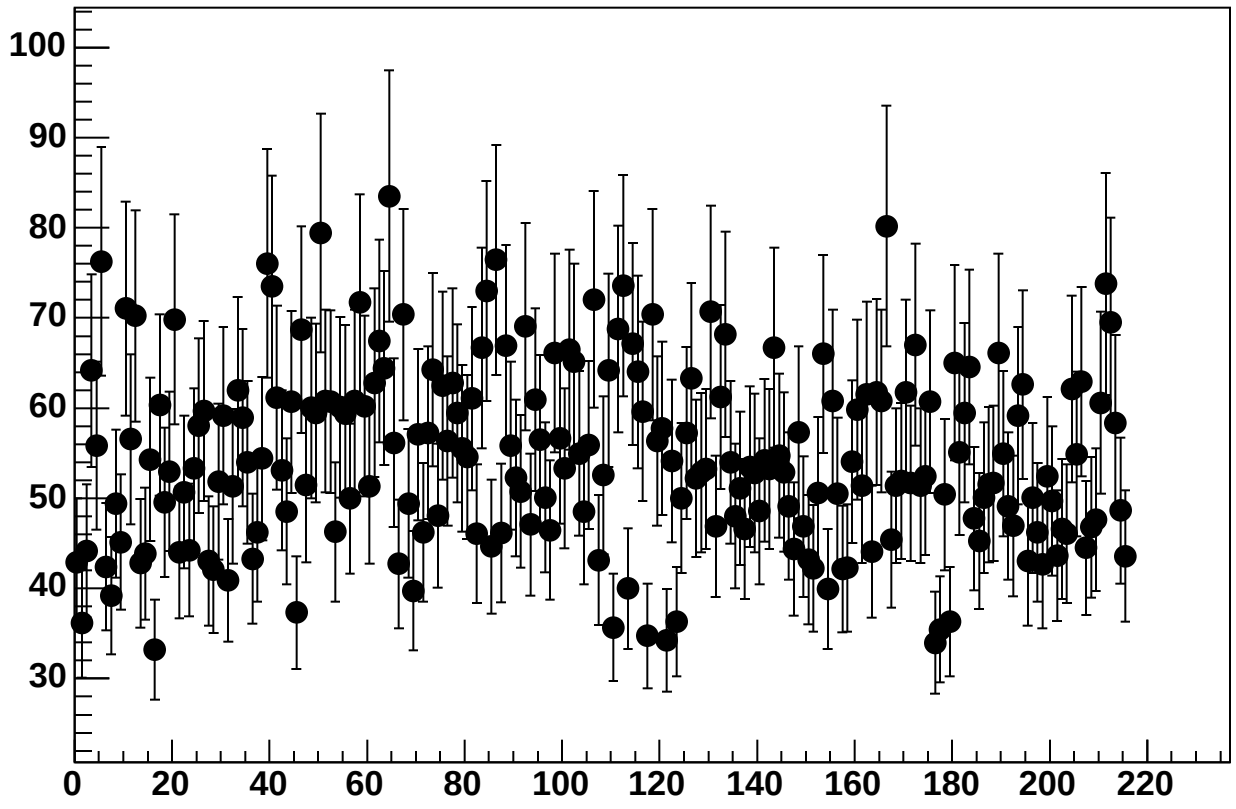
Enable 1, DAC=1600, Hold=200, ADC Noise vs 18*Chip+Chan



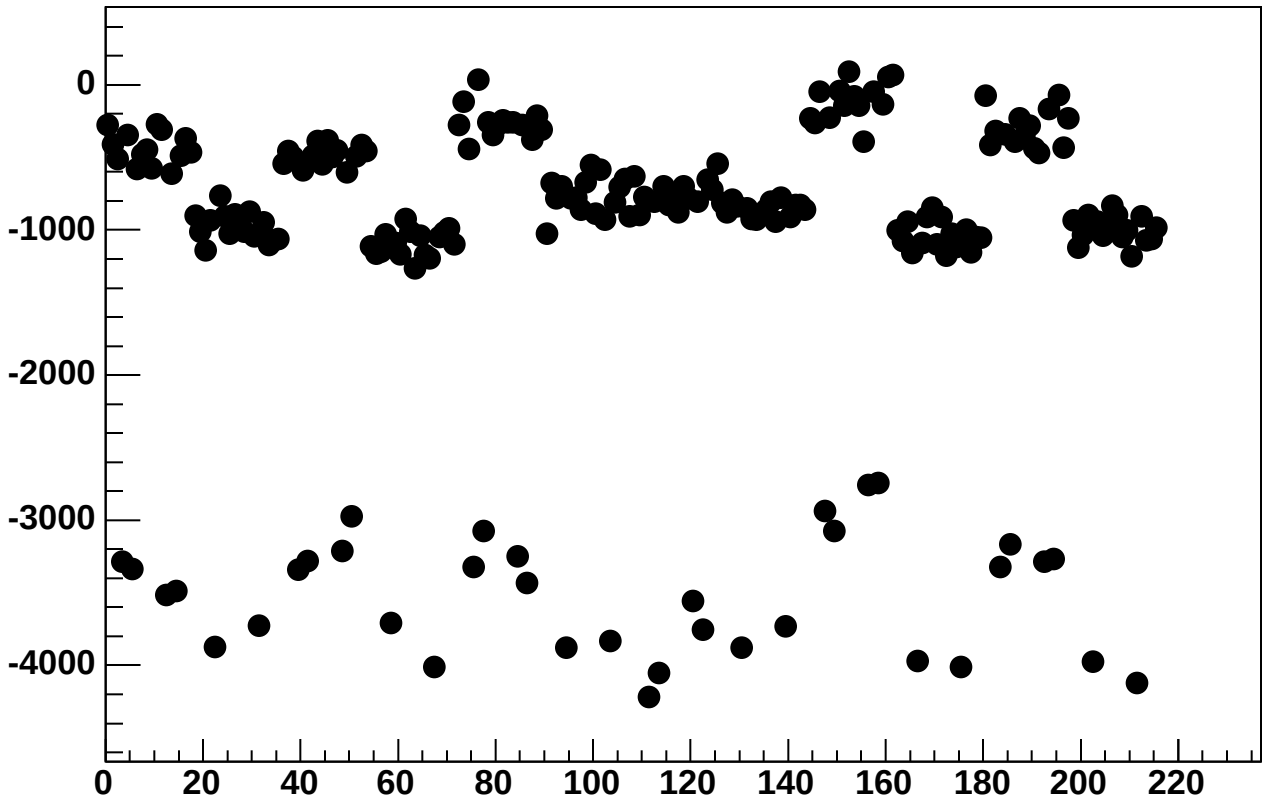
Enable 1, DAC=1600, Hold=205, ADC Mean vs 18*Chip+Chan



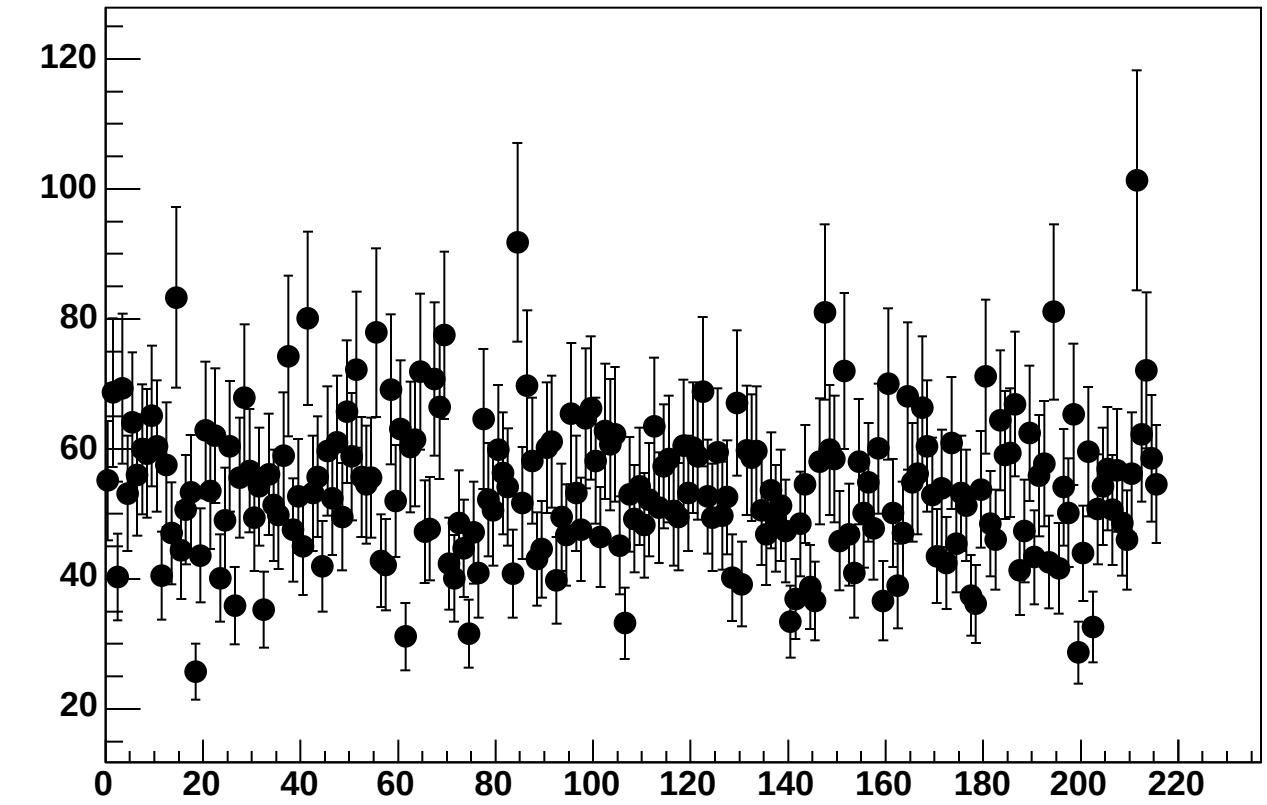
Enable 1, DAC=1600, Hold=205, ADC Noise vs 18*Chip+Chan



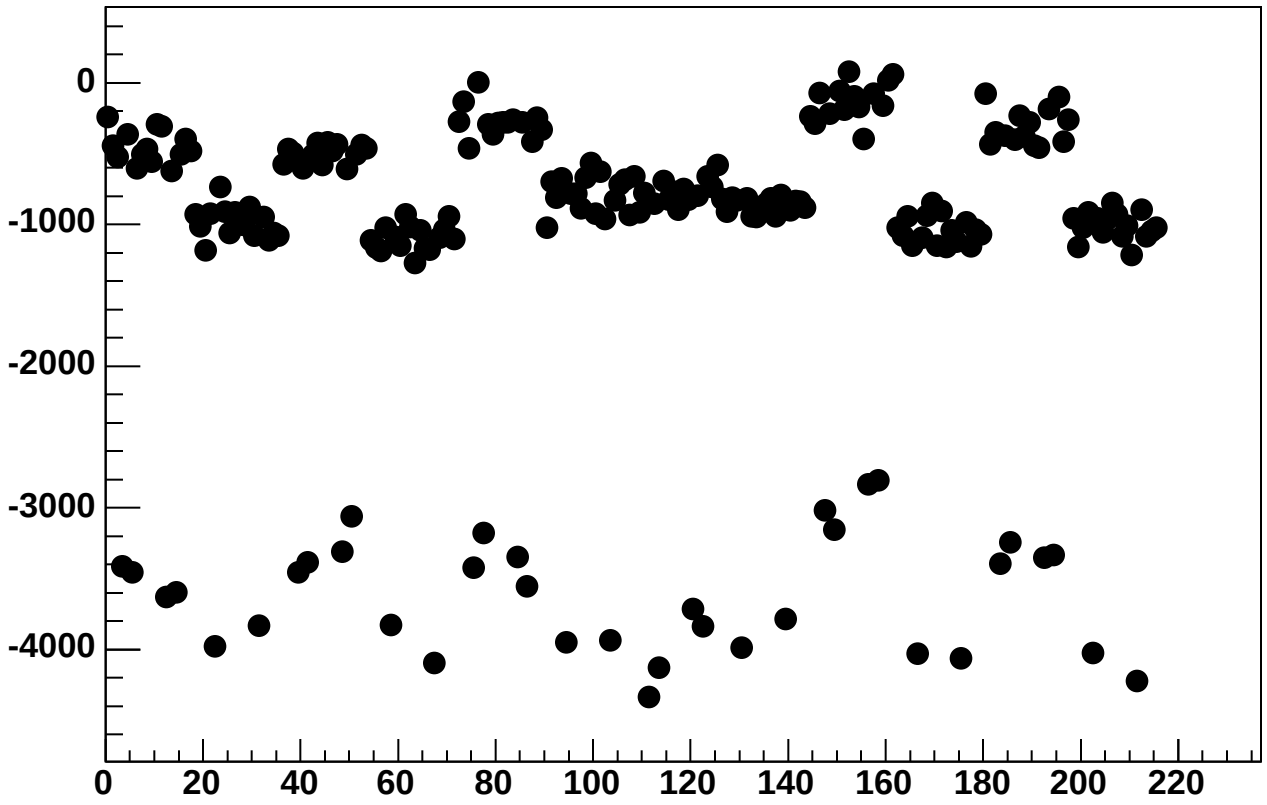
Enable 1, DAC=1600, Hold=210, ADC Mean vs 18*Chip+Chan



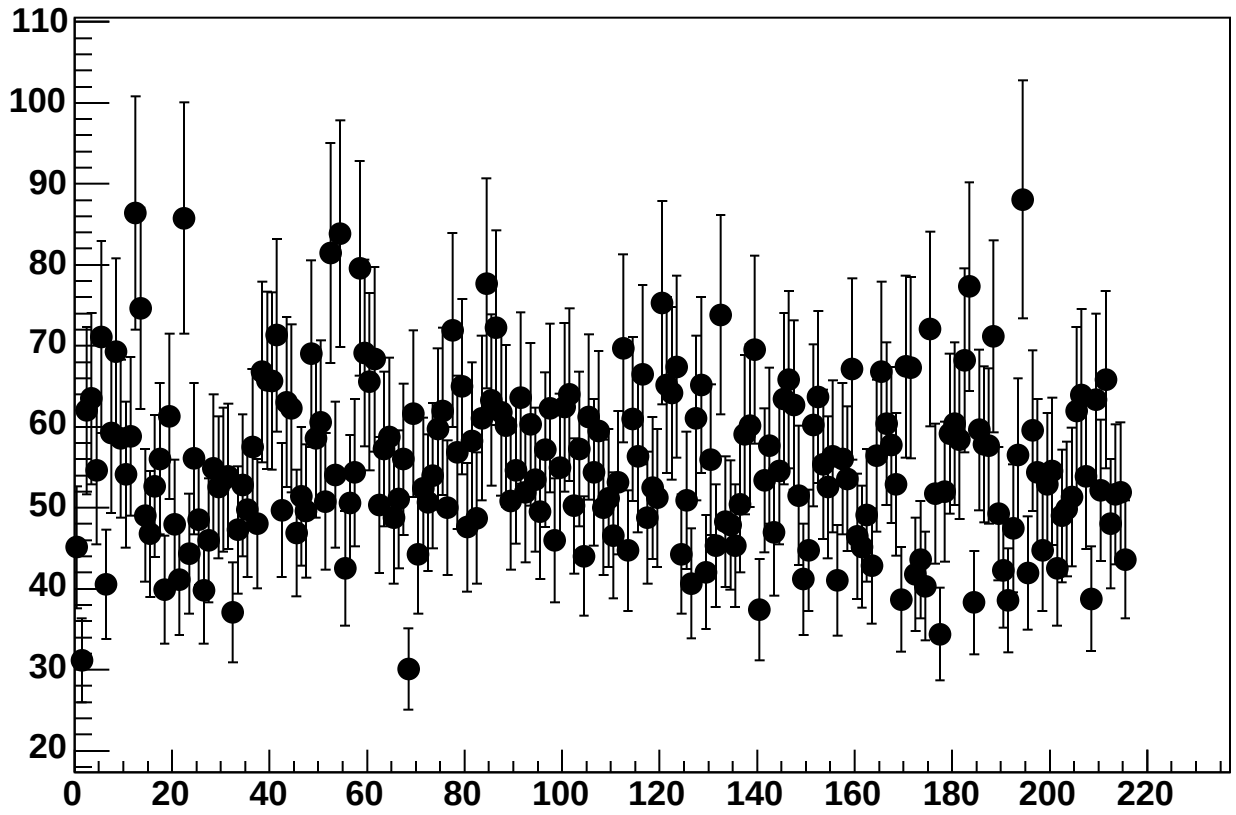
Enable 1, DAC=1600, Hold=210, ADC Noise vs 18*Chip+Chan



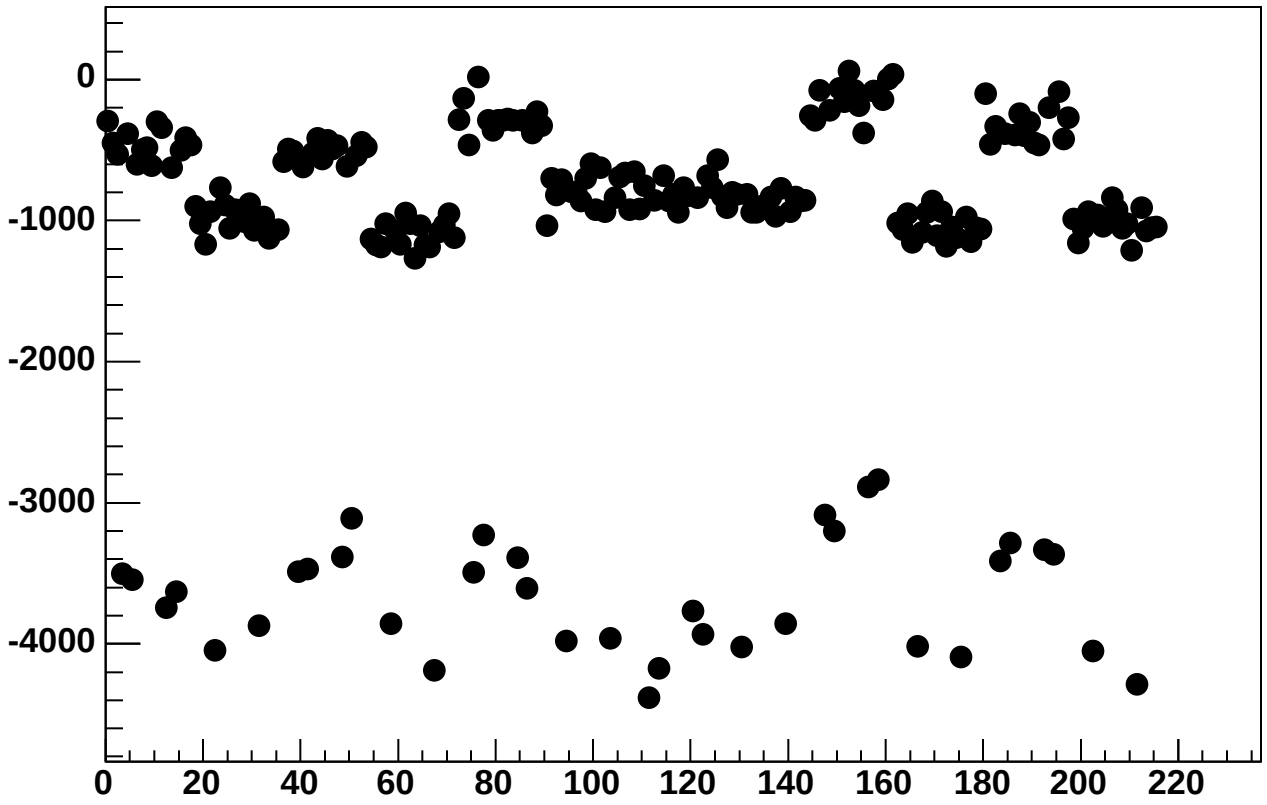
Enable 1, DAC=1600, Hold=215, ADC Mean vs 18*Chip+Chan



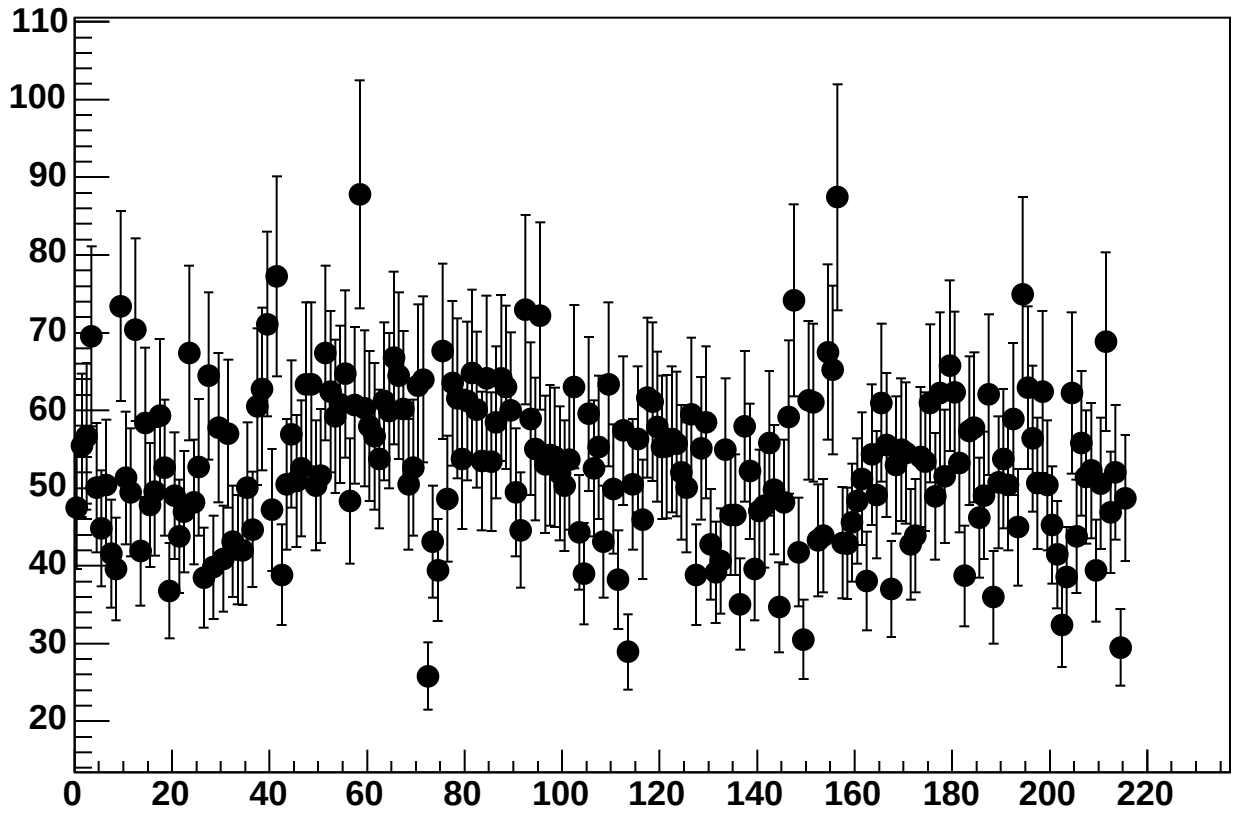
Enable 1, DAC=1600, Hold=215, ADC Noise vs 18*Chip+Chan



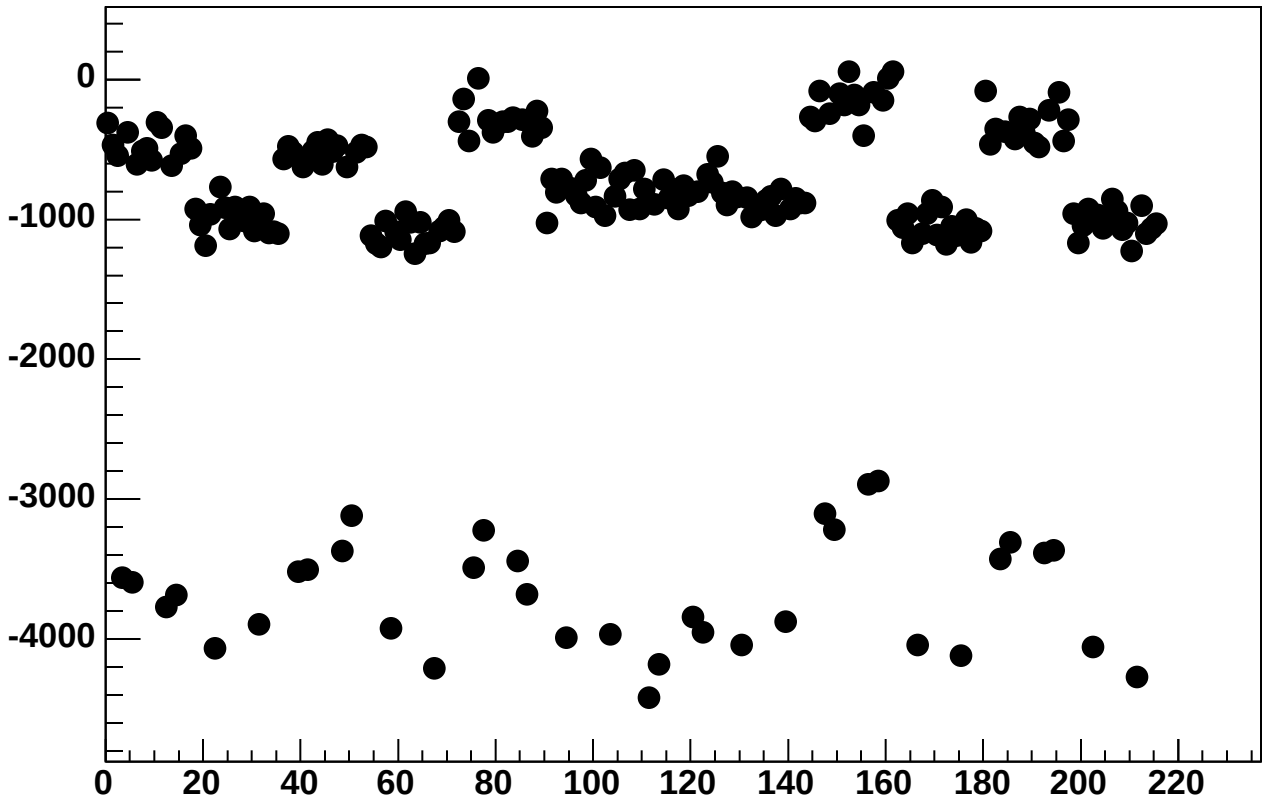
Enable 1, DAC=1600, Hold=220, ADC Mean vs 18*Chip+Chan



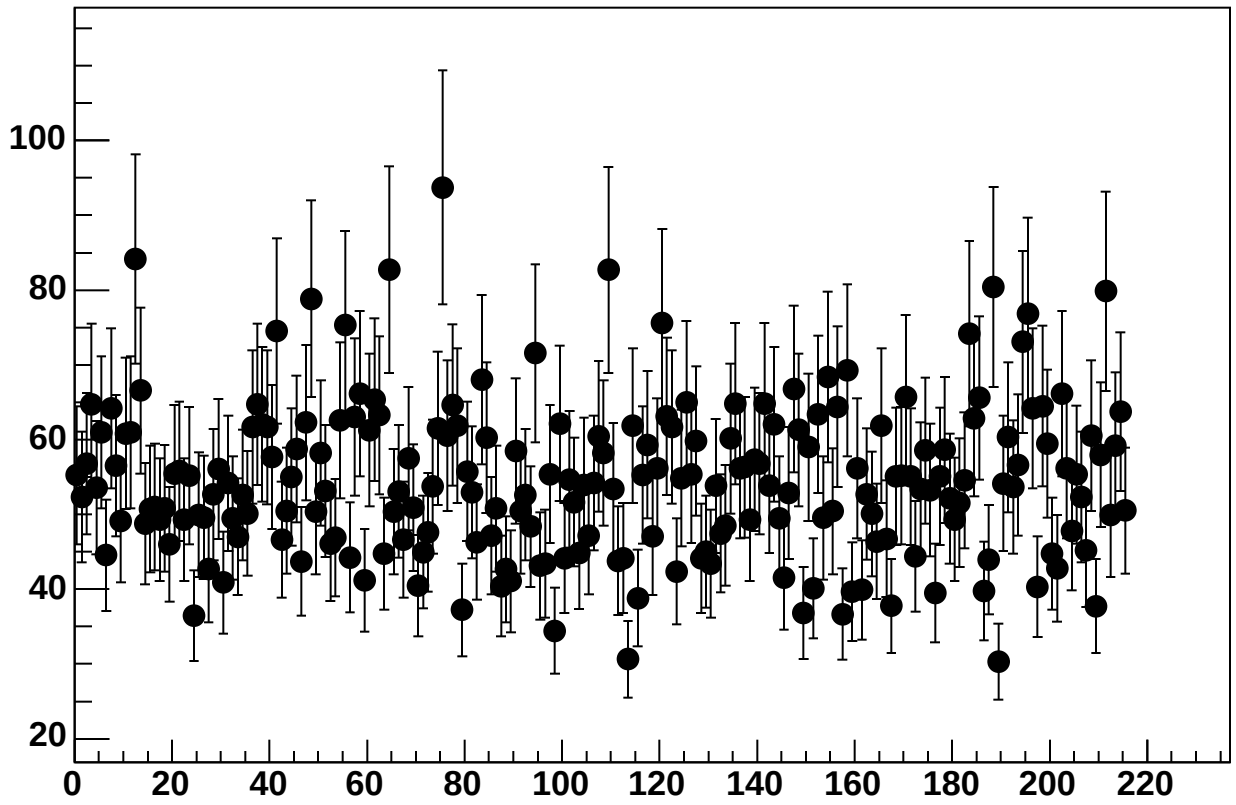
Enable 1, DAC=1600, Hold=220, ADC Noise vs 18*Chip+Chan



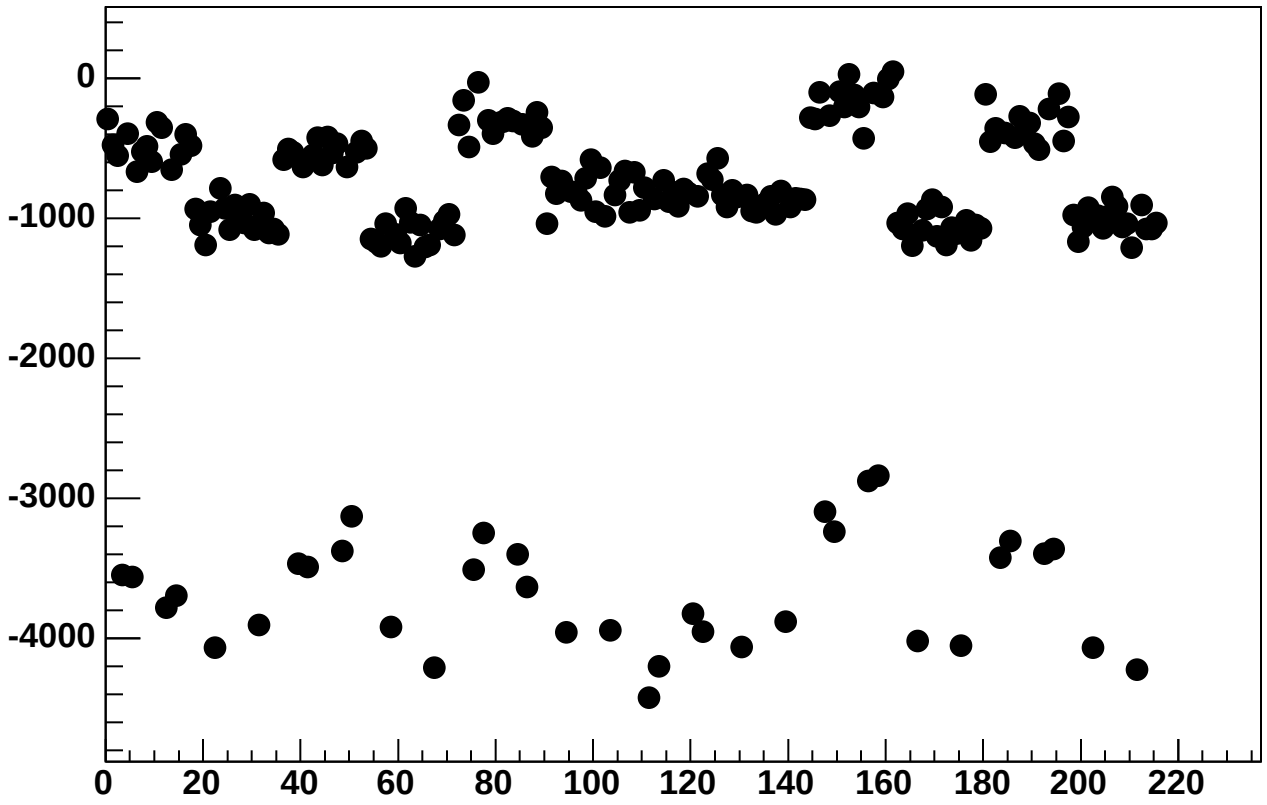
Enable 1, DAC=1600, Hold=225, ADC Mean vs 18*Chip+Chan



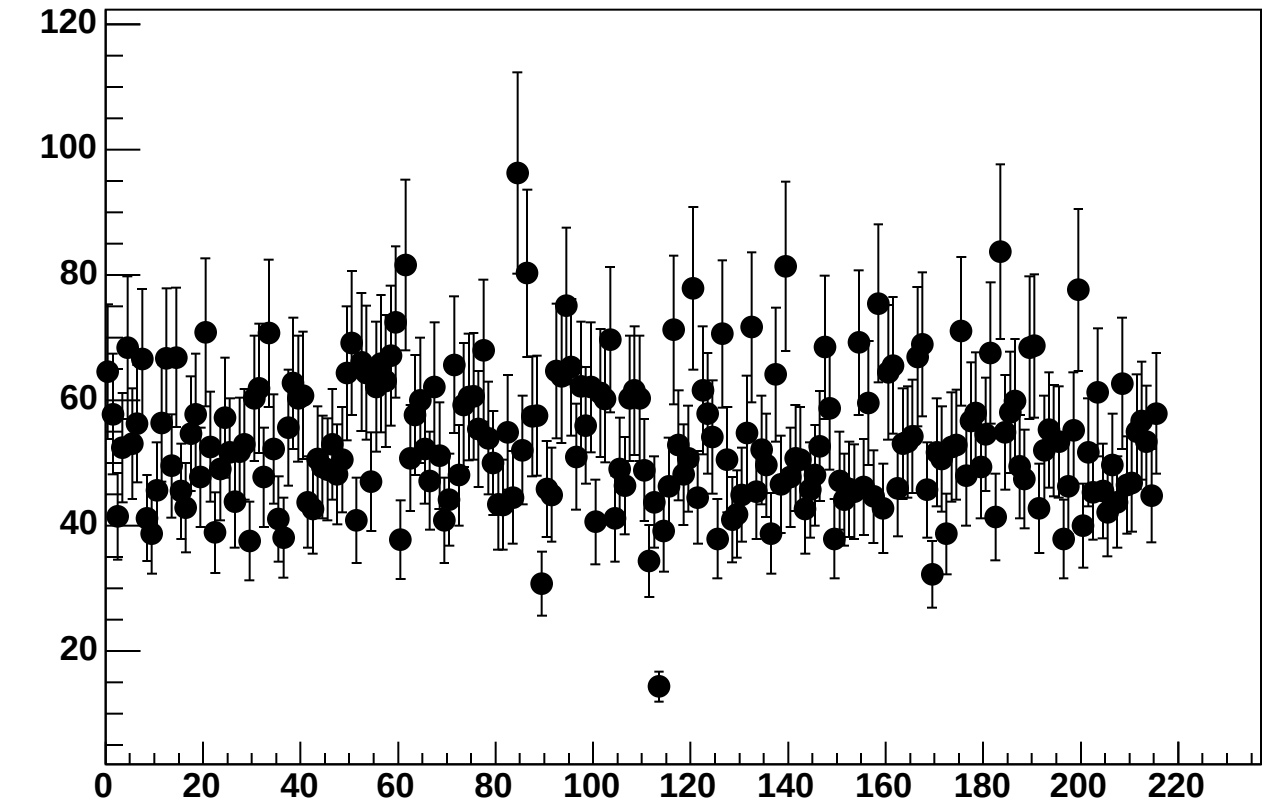
Enable 1, DAC=1600, Hold=225, ADC Noise vs 18*Chip+Chan



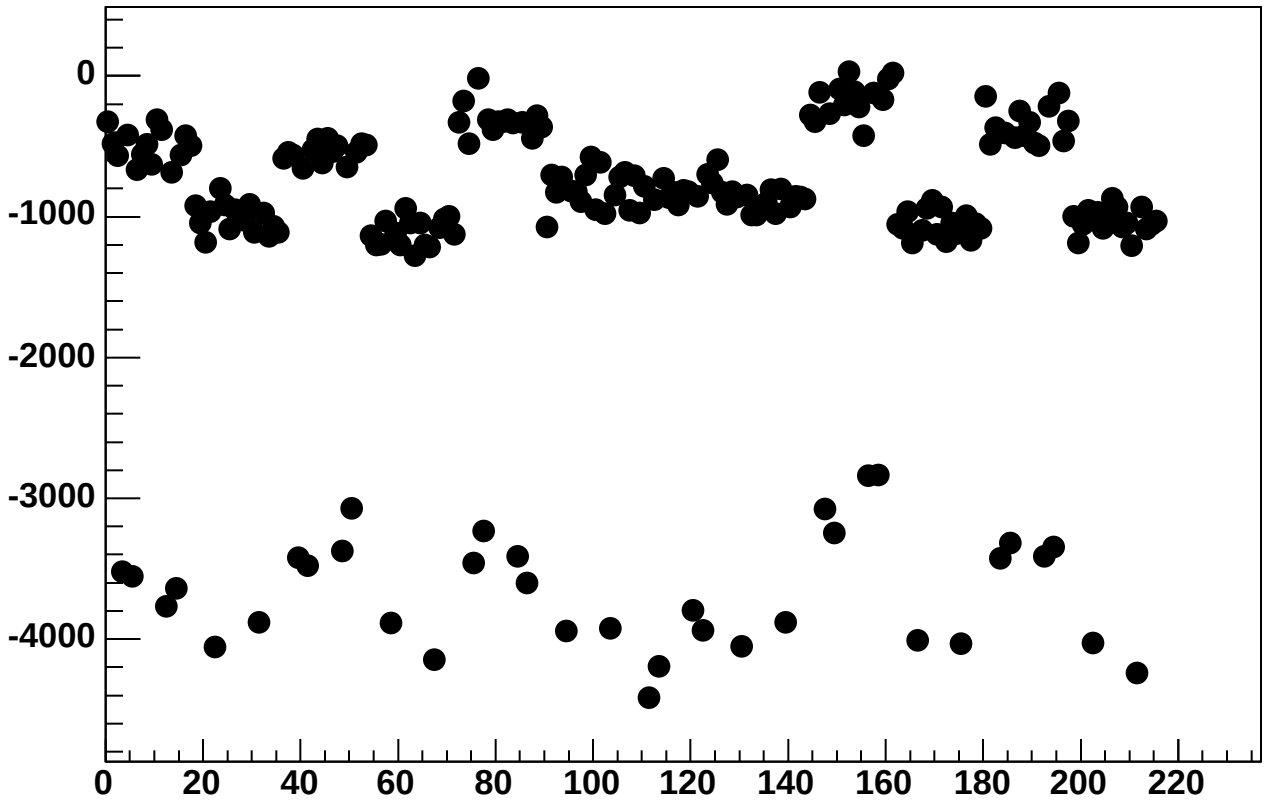
Enable 1, DAC=1600, Hold=230, ADC Mean vs 18*Chip+Chan



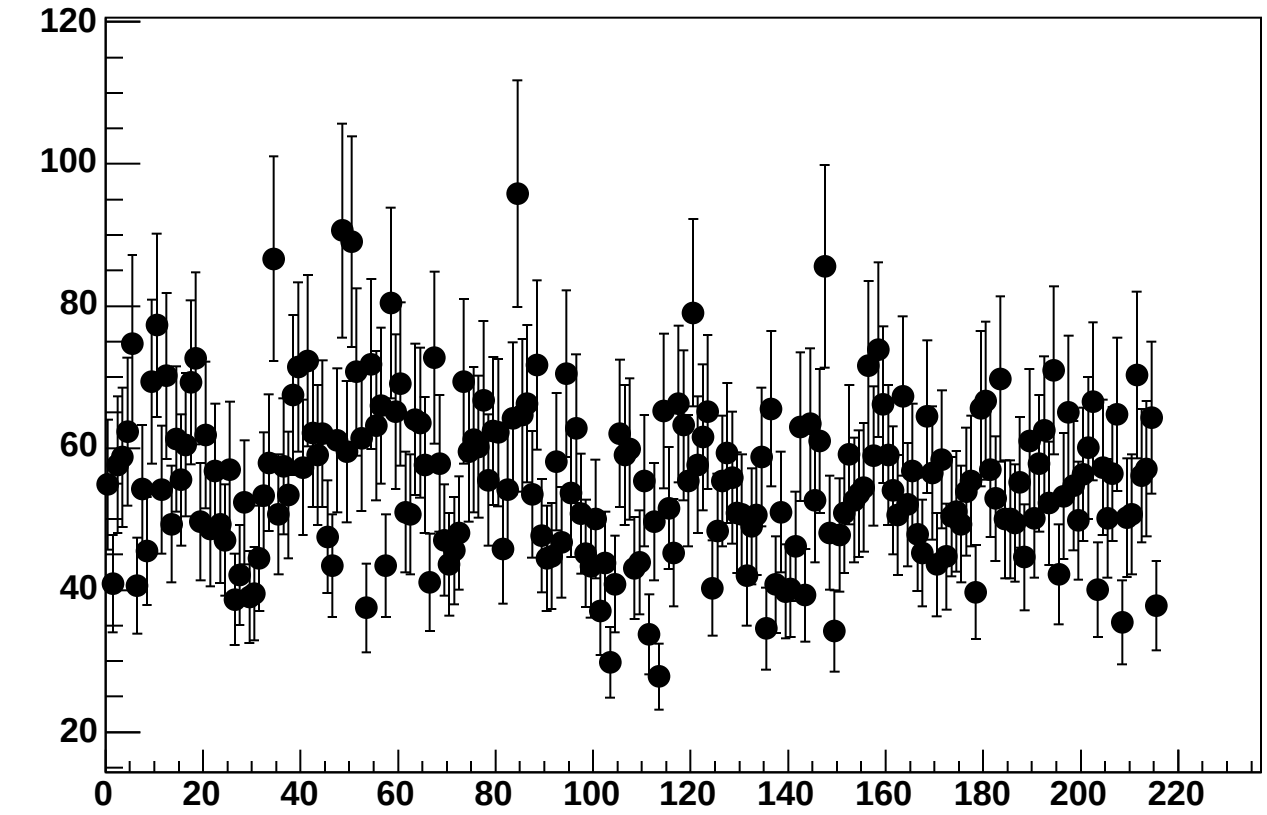
Enable 1, DAC=1600, Hold=230, ADC Noise vs 18*Chip+Chan



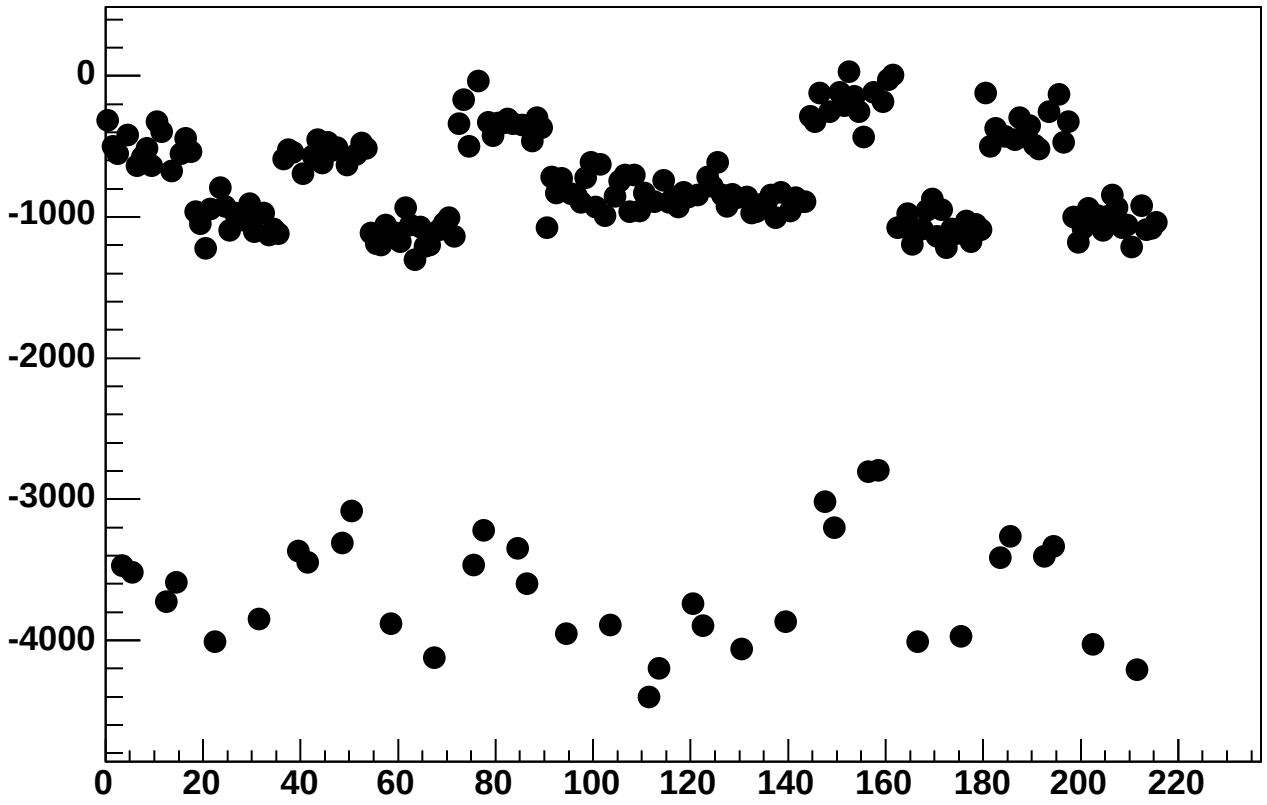
Enable 1, DAC=1600, Hold=235, ADC Mean vs 18*Chip+Chan



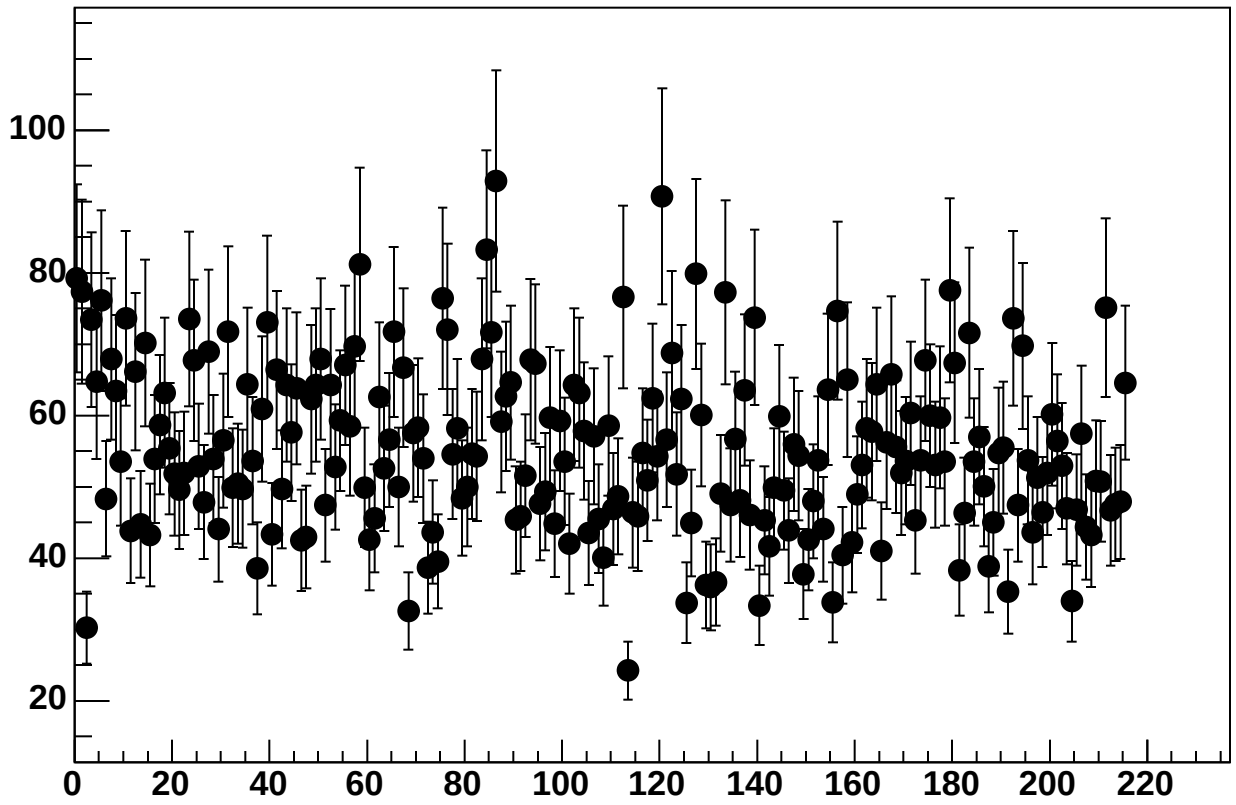
Enable 1, DAC=1600, Hold=235, ADC Noise vs 18*Chip+Chan



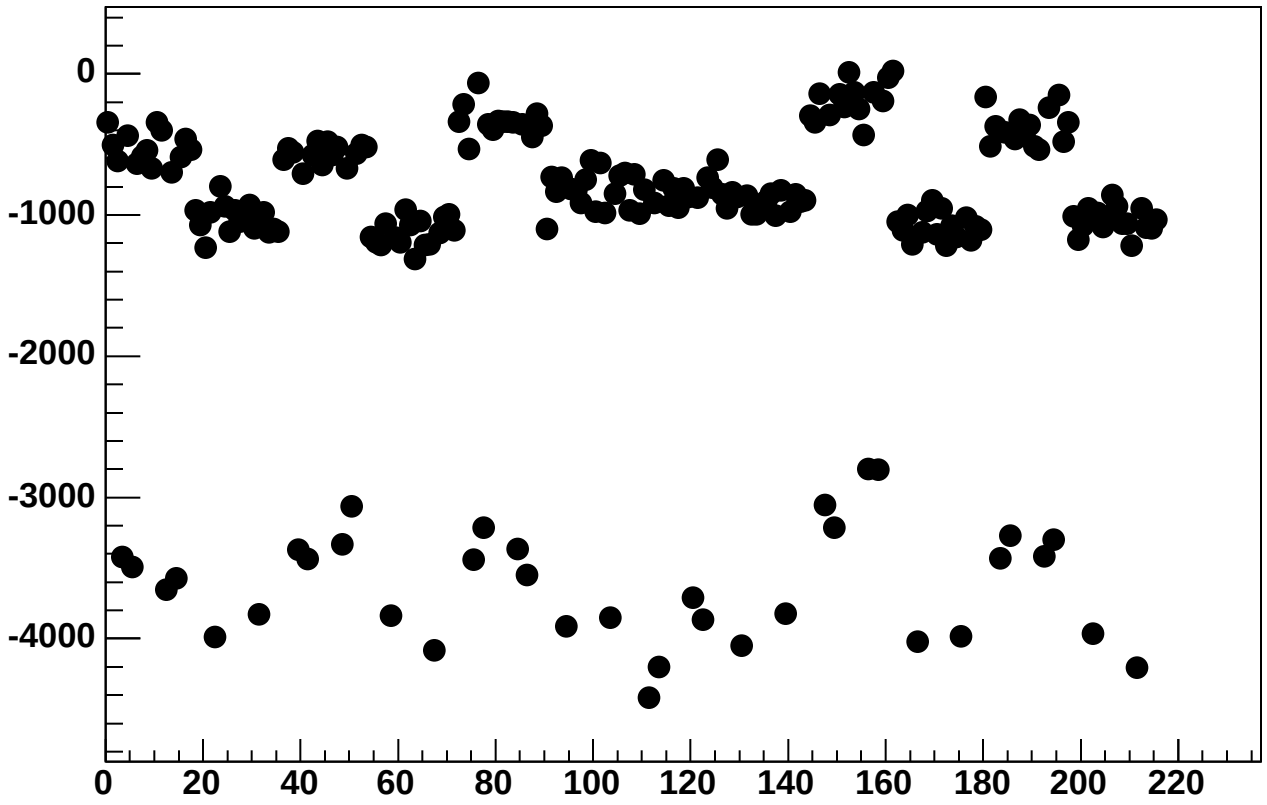
Enable 1, DAC=1600, Hold=240, ADC Mean vs 18*Chip+Chan



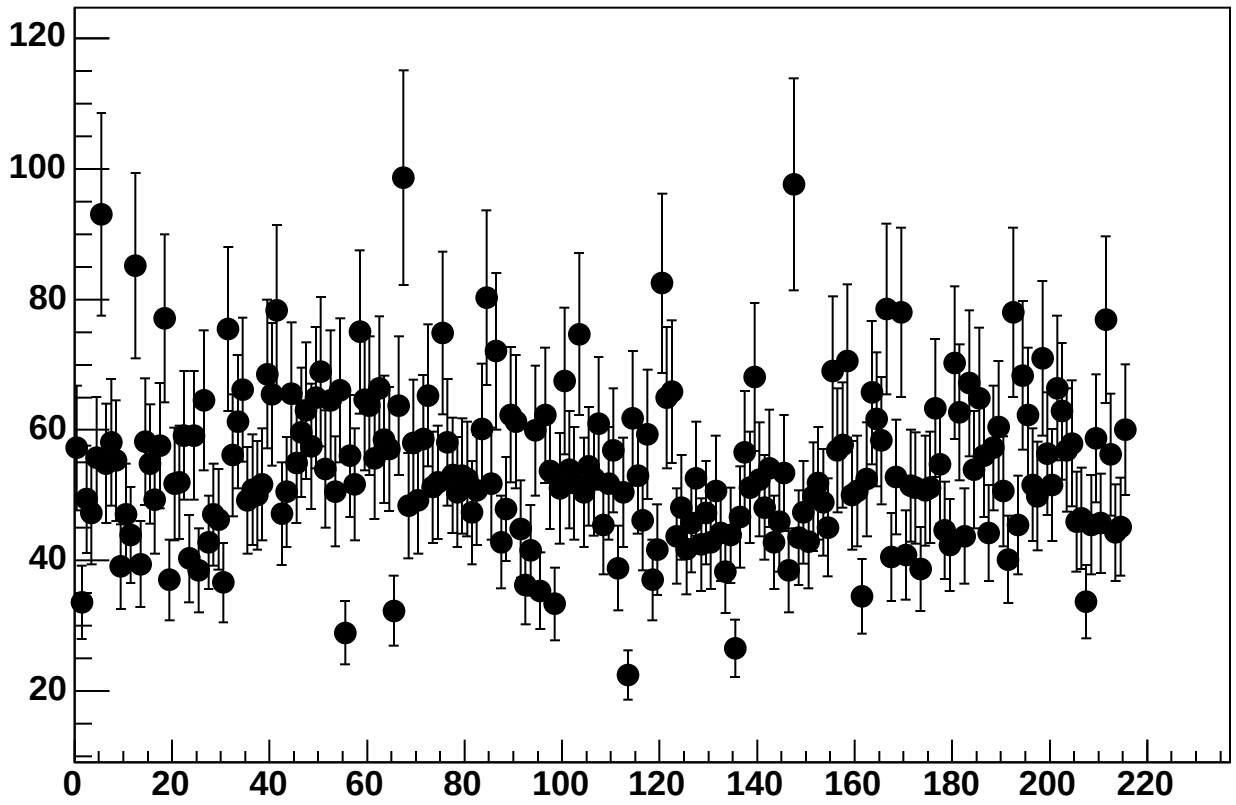
Enable 1, DAC=1600, Hold=240, ADC Noise vs 18*Chip+Chan



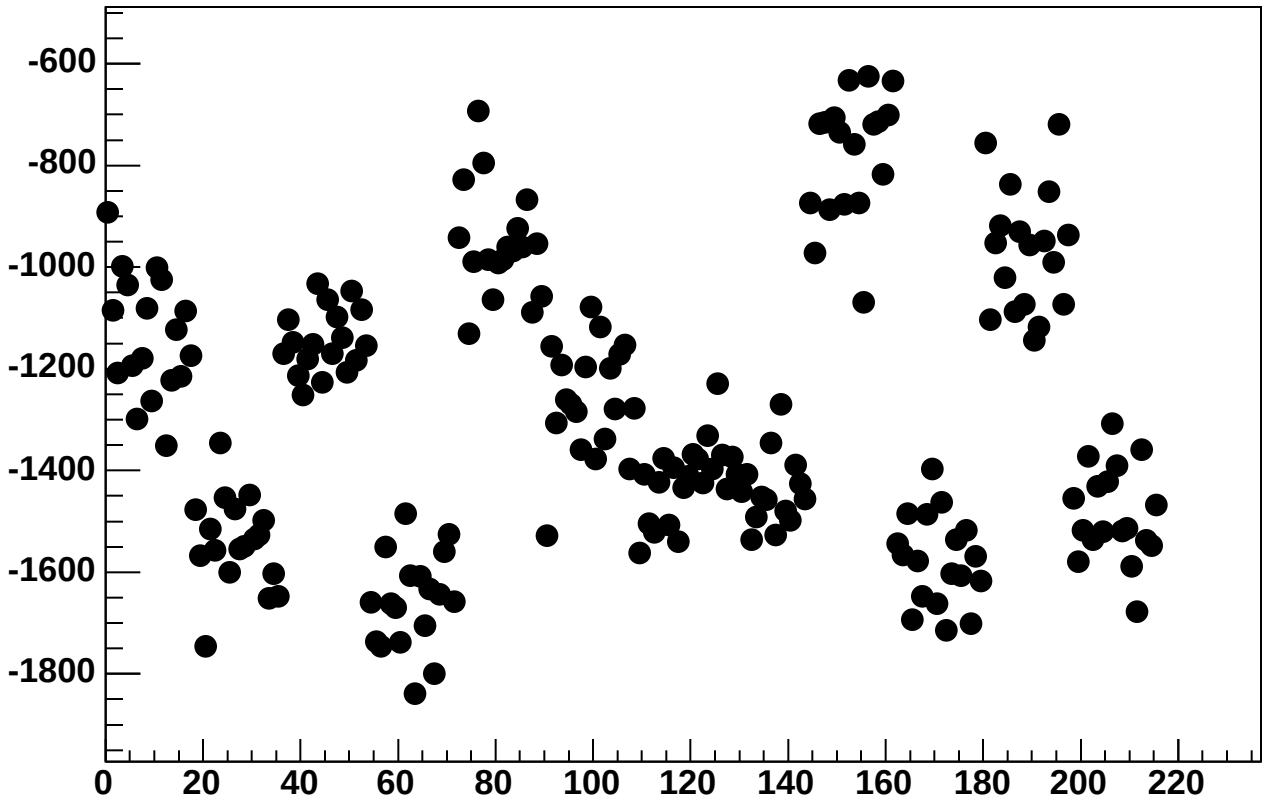
Enable 1, DAC=1600, Hold=245, ADC Mean vs 18*Chip+Chan



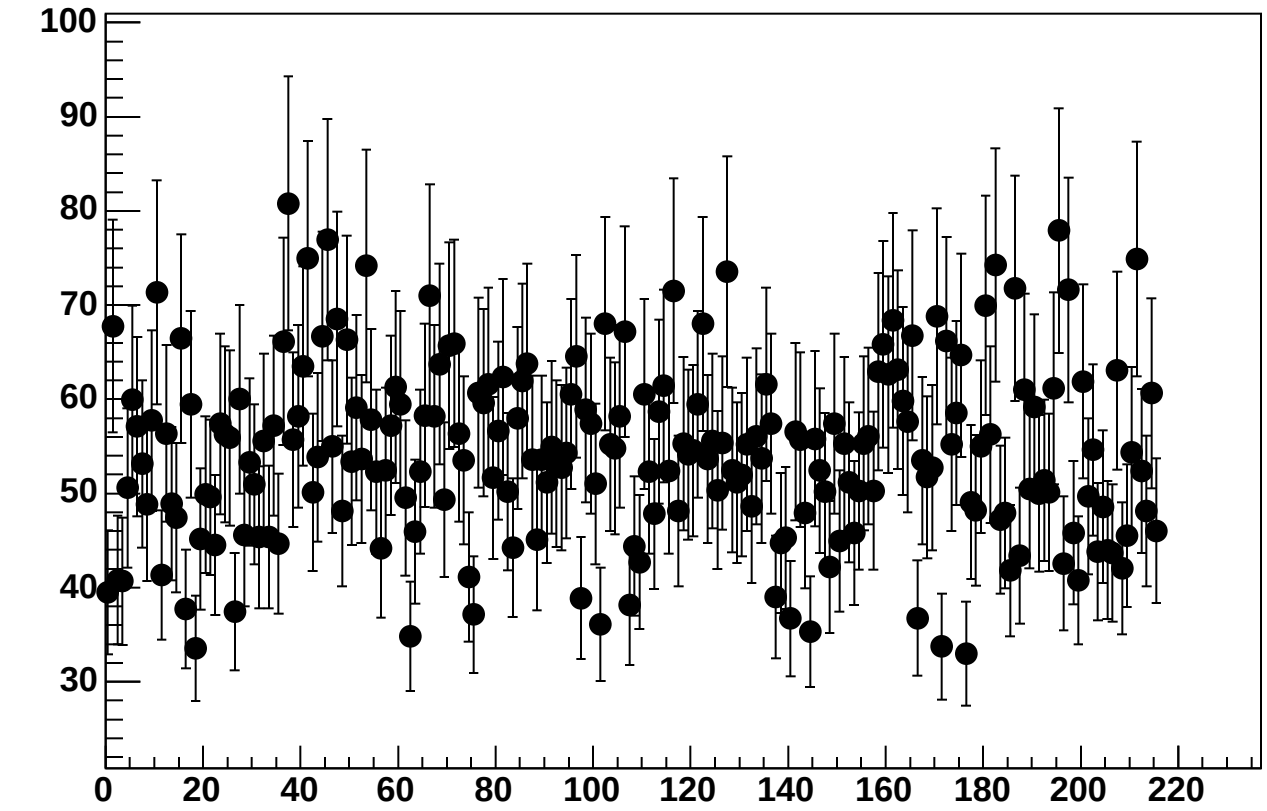
Enable 1, DAC=1600, Hold=245, ADC Noise vs 18*Chip+Chan



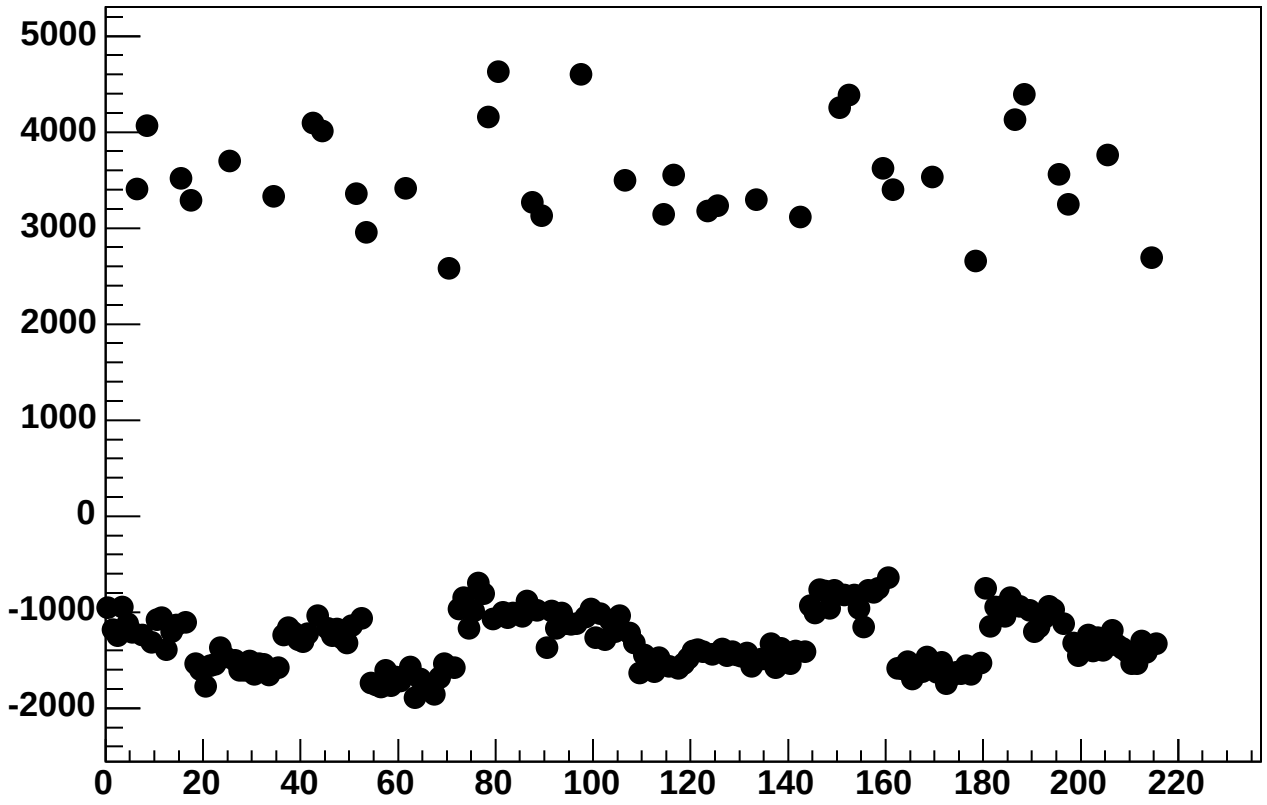
Enable 2, DAC=1600, Hold=0, ADC Mean vs 18*Chip+Chan



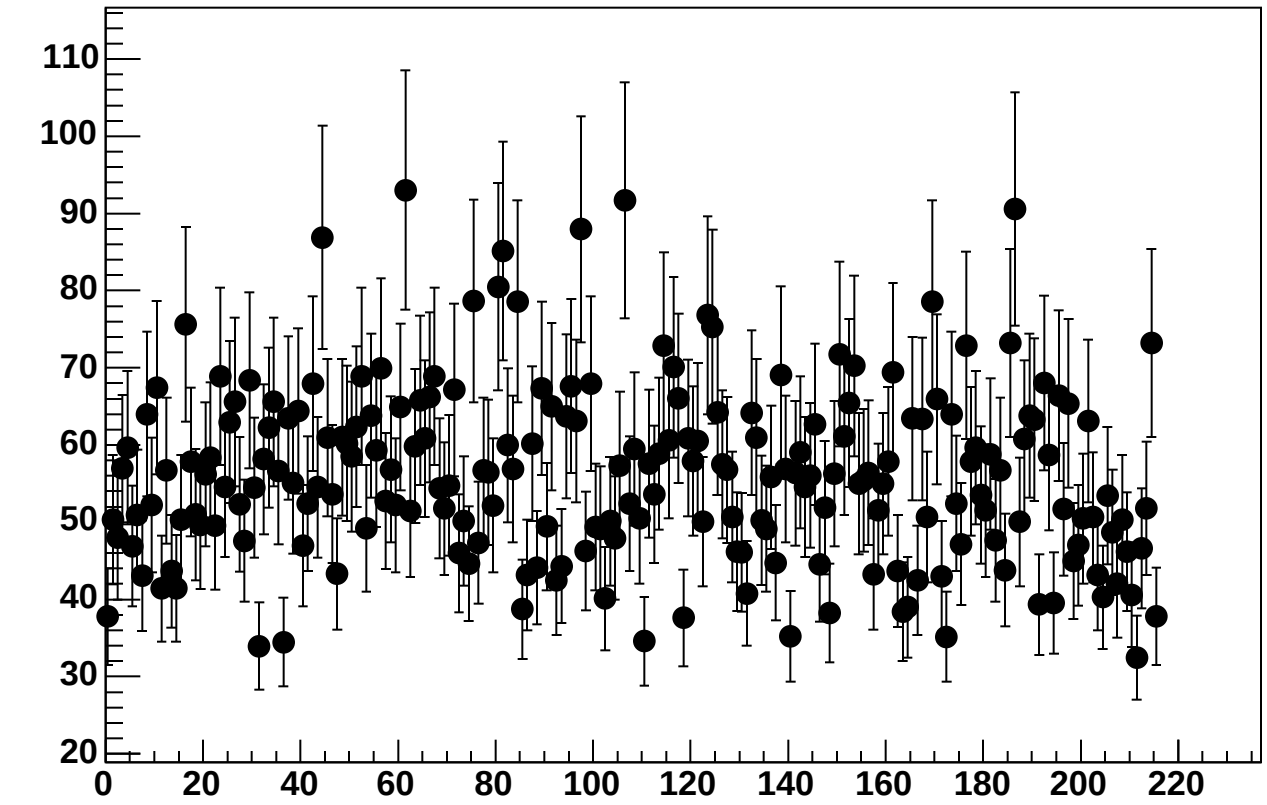
Enable 2, DAC=1600, Hold=0, ADC Noise vs 18*Chip+Chan



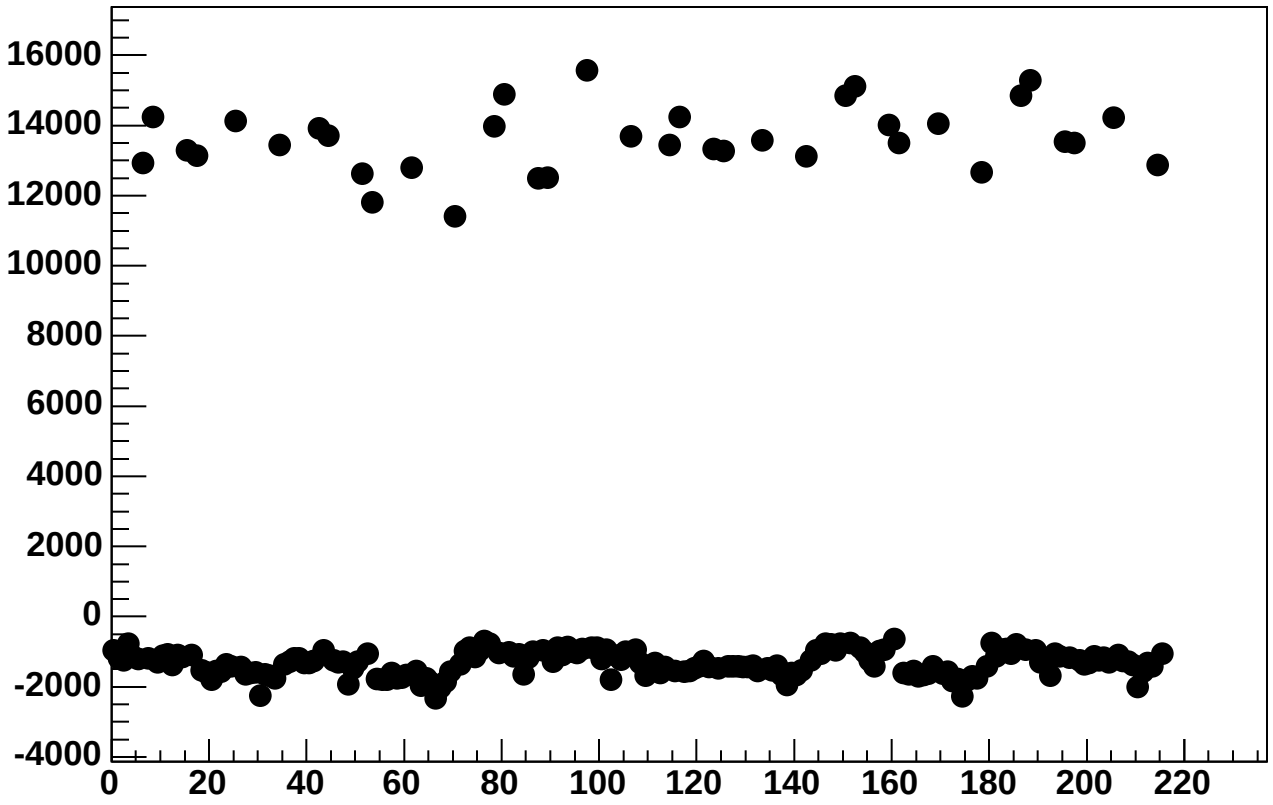
Enable 2, DAC=1600, Hold=5, ADC Mean vs 18*Chip+Chan



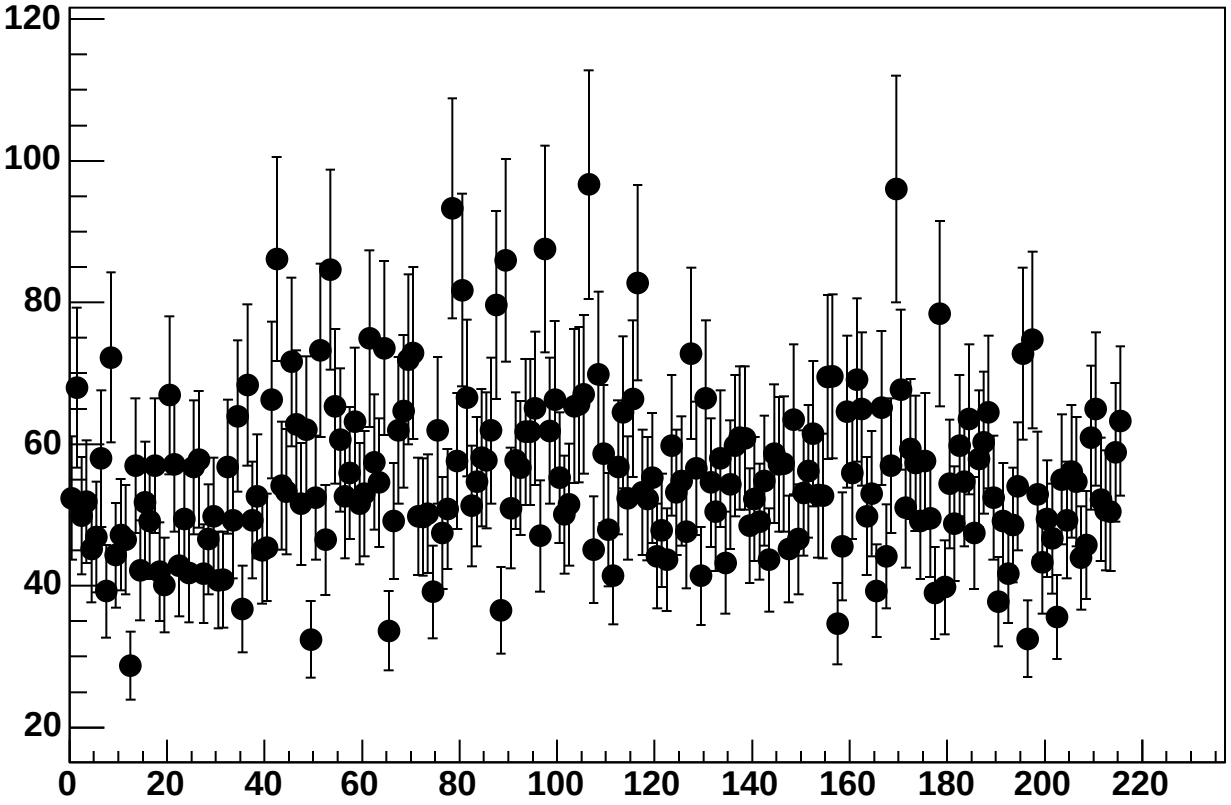
Enable 2, DAC=1600, Hold=5, ADC Noise vs 18*Chip+Chan



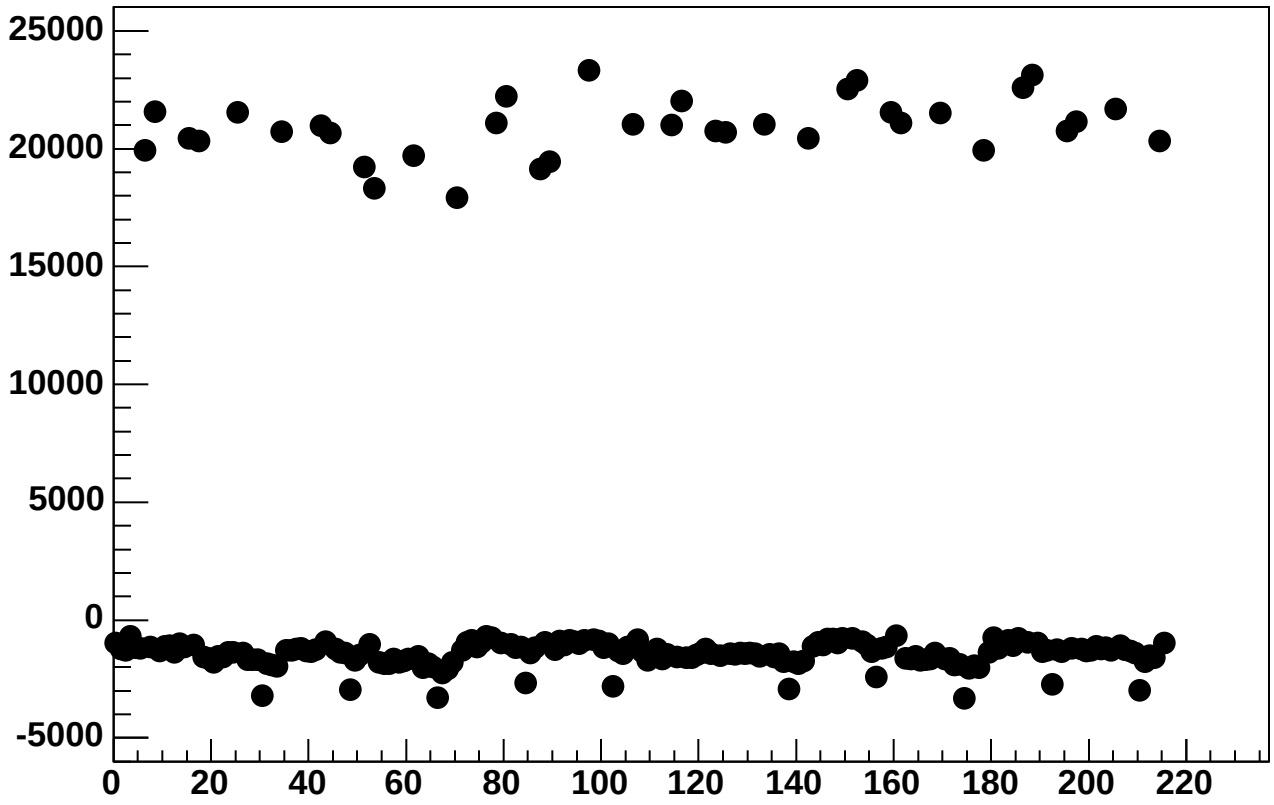
Enable 2, DAC=1600, Hold=10, ADC Mean vs 18*Chip+Chan



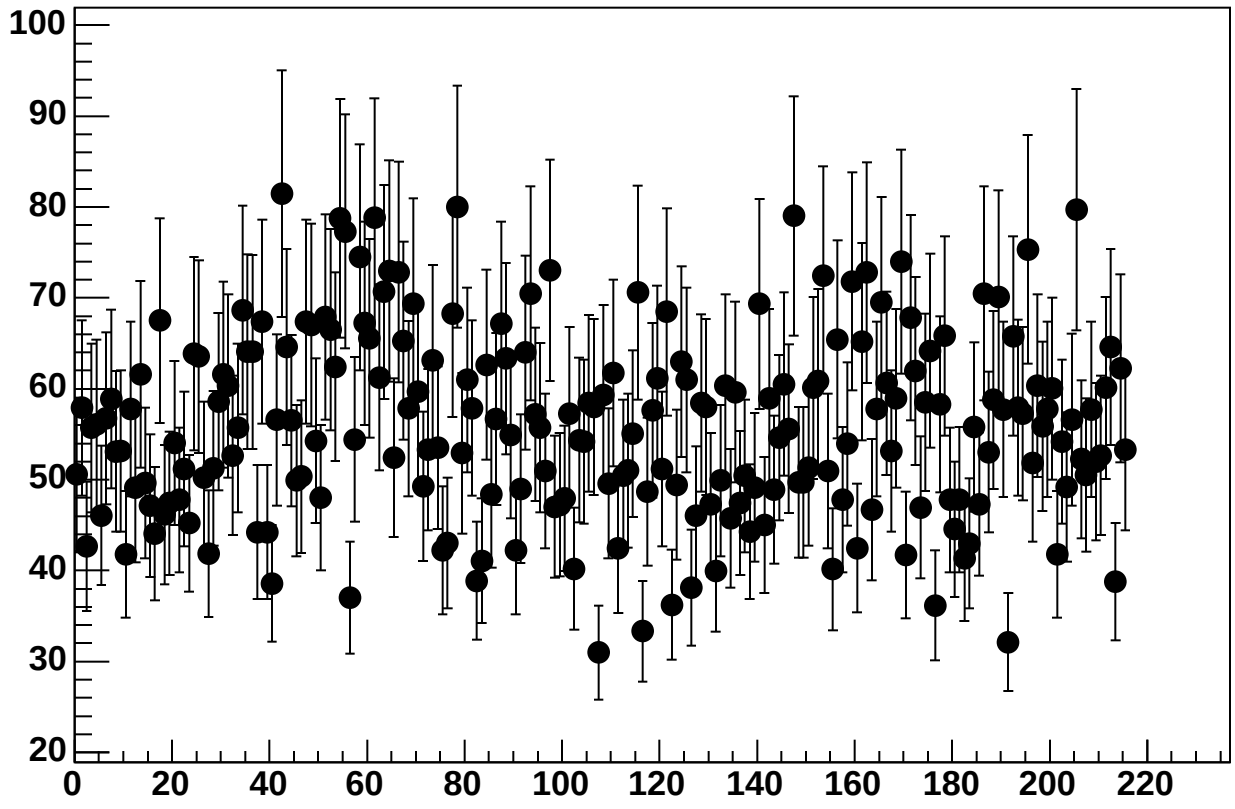
Enable 2, DAC=1600, Hold=10, ADC Noise vs 18*Chip+Chan



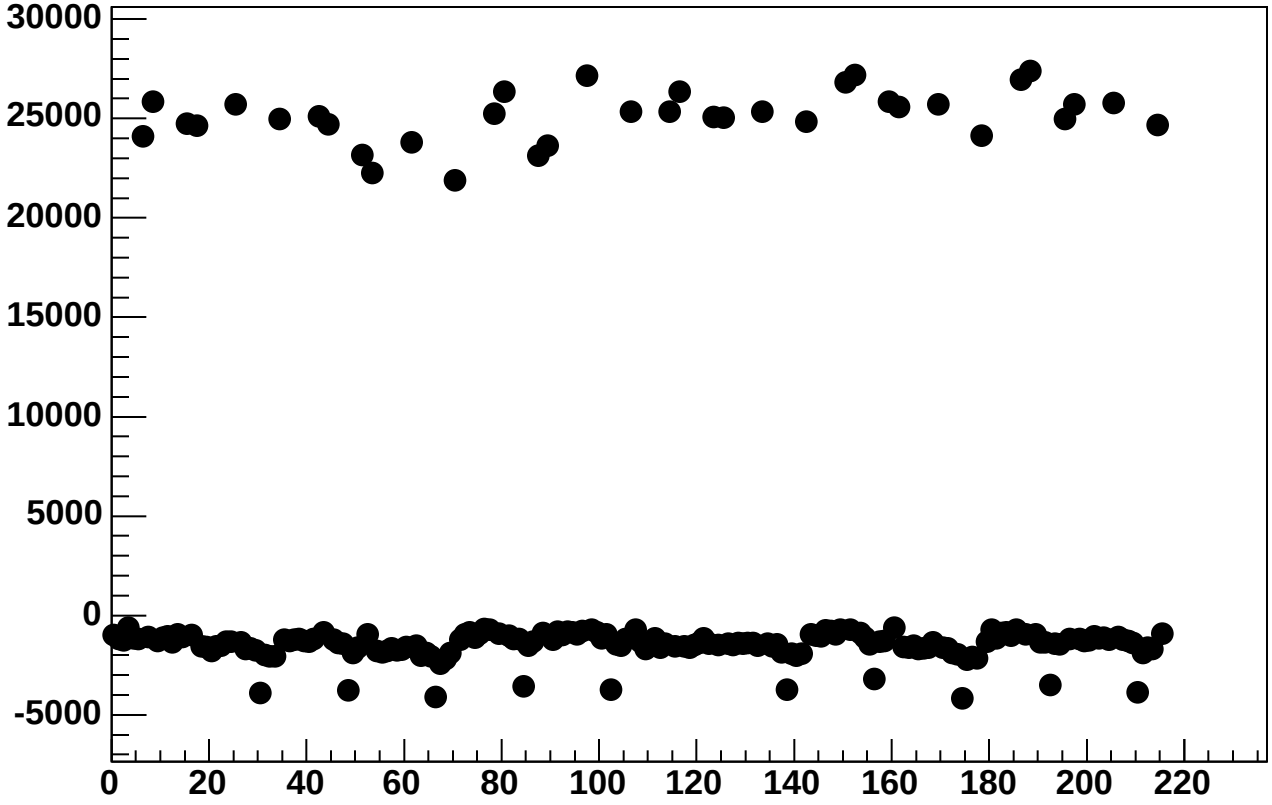
Enable 2, DAC=1600, Hold=15, ADC Mean vs 18*Chip+Chan



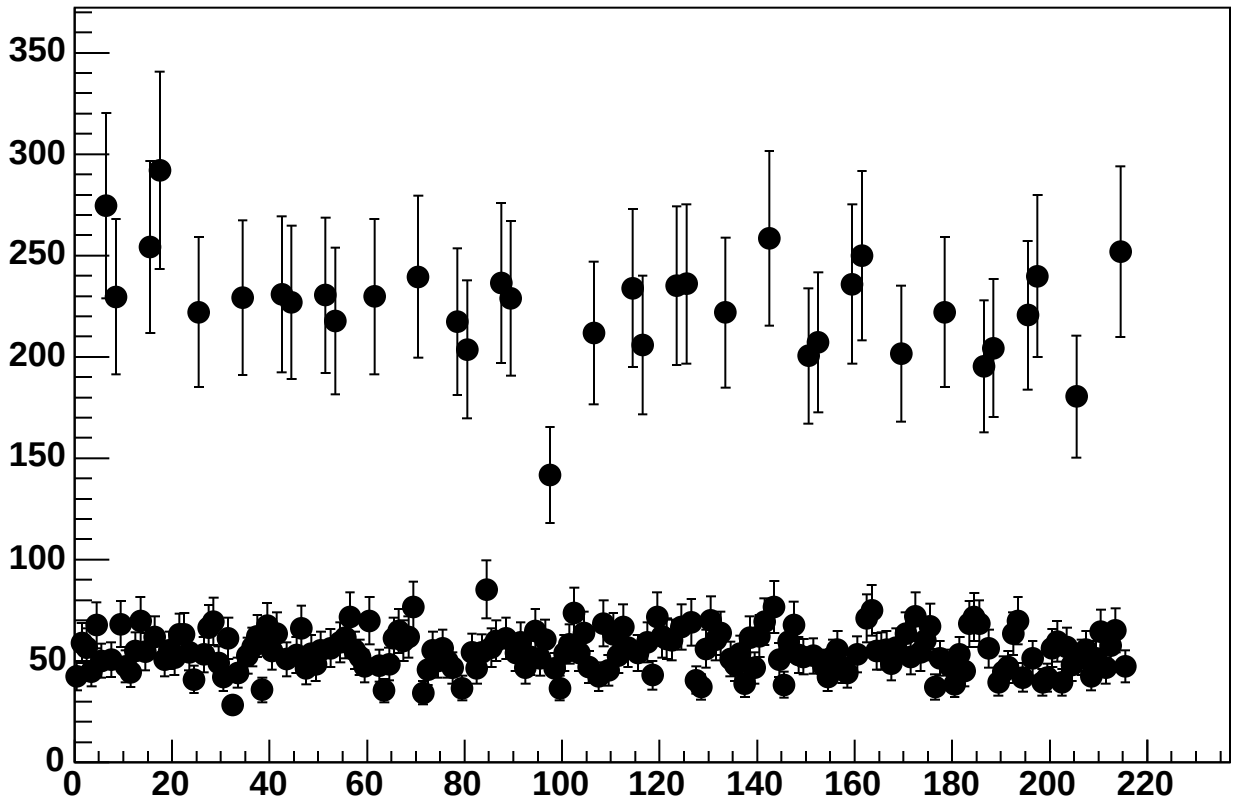
Enable 2, DAC=1600, Hold=15, ADC Noise vs 18*Chip+Chan



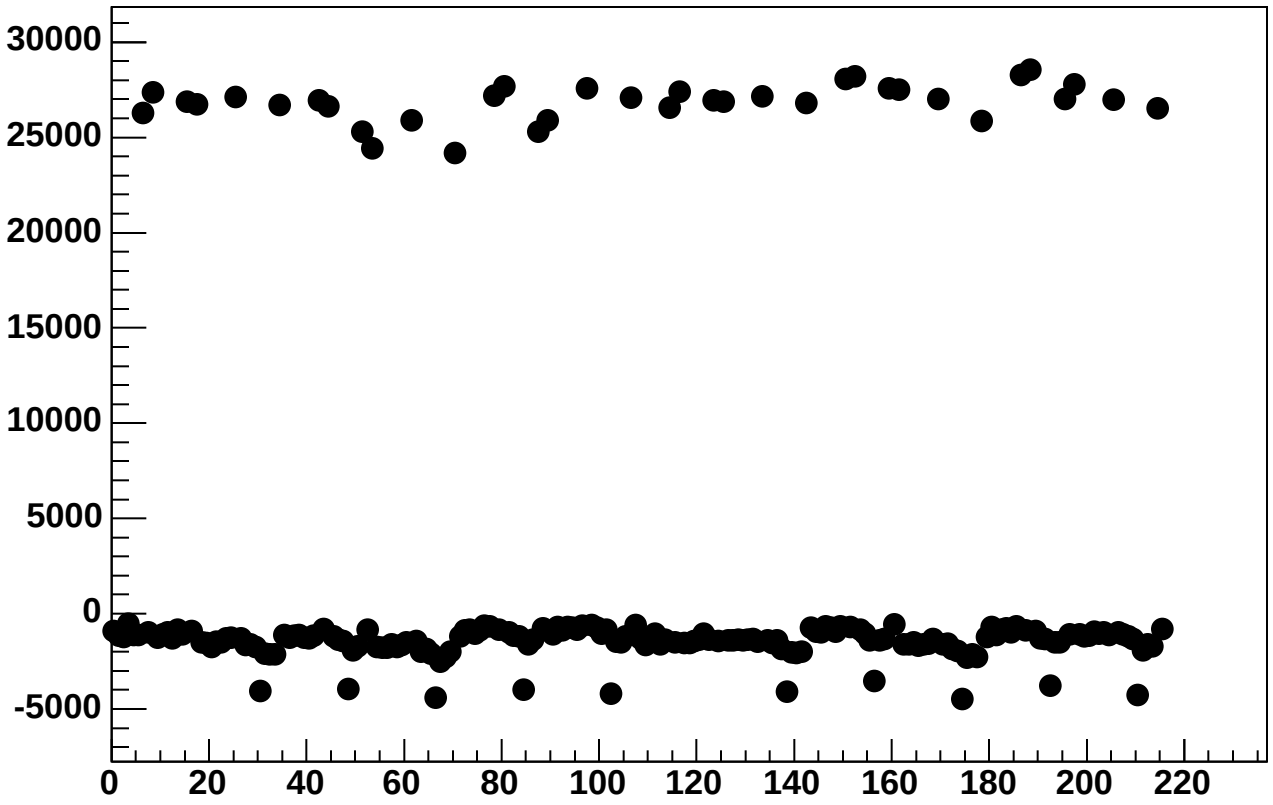
Enable 2, DAC=1600, Hold=20, ADC Mean vs 18*Chip+Chan



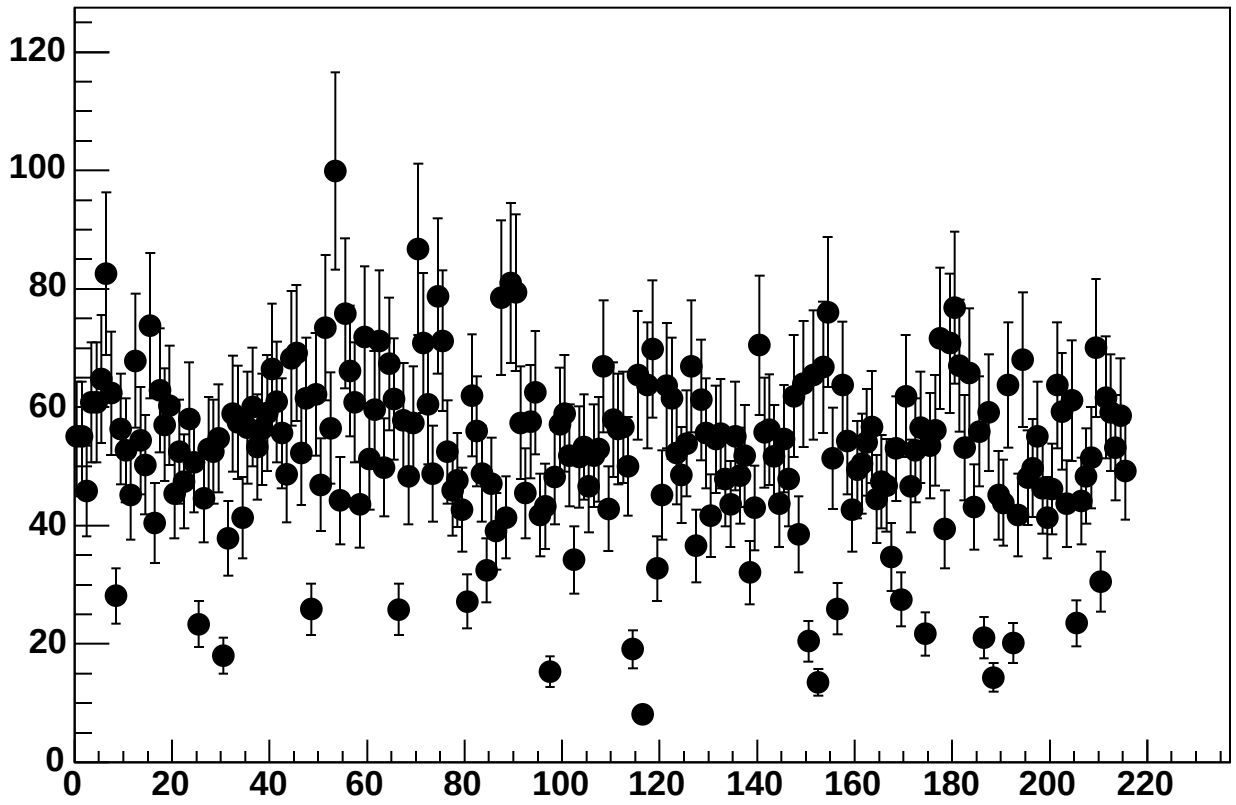
Enable 2, DAC=1600, Hold=20, ADC Noise vs 18*Chip+Chan



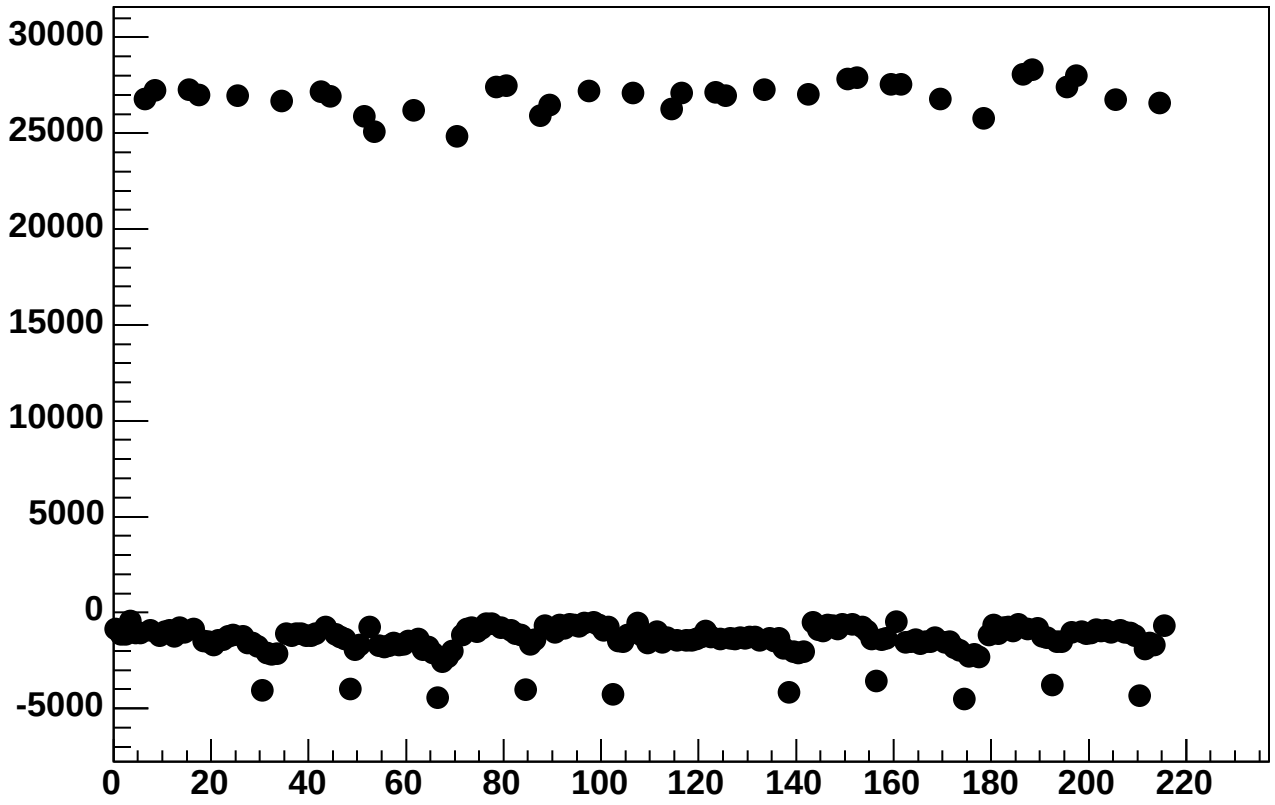
Enable 2, DAC=1600, Hold=25, ADC Mean vs 18*Chip+Chan



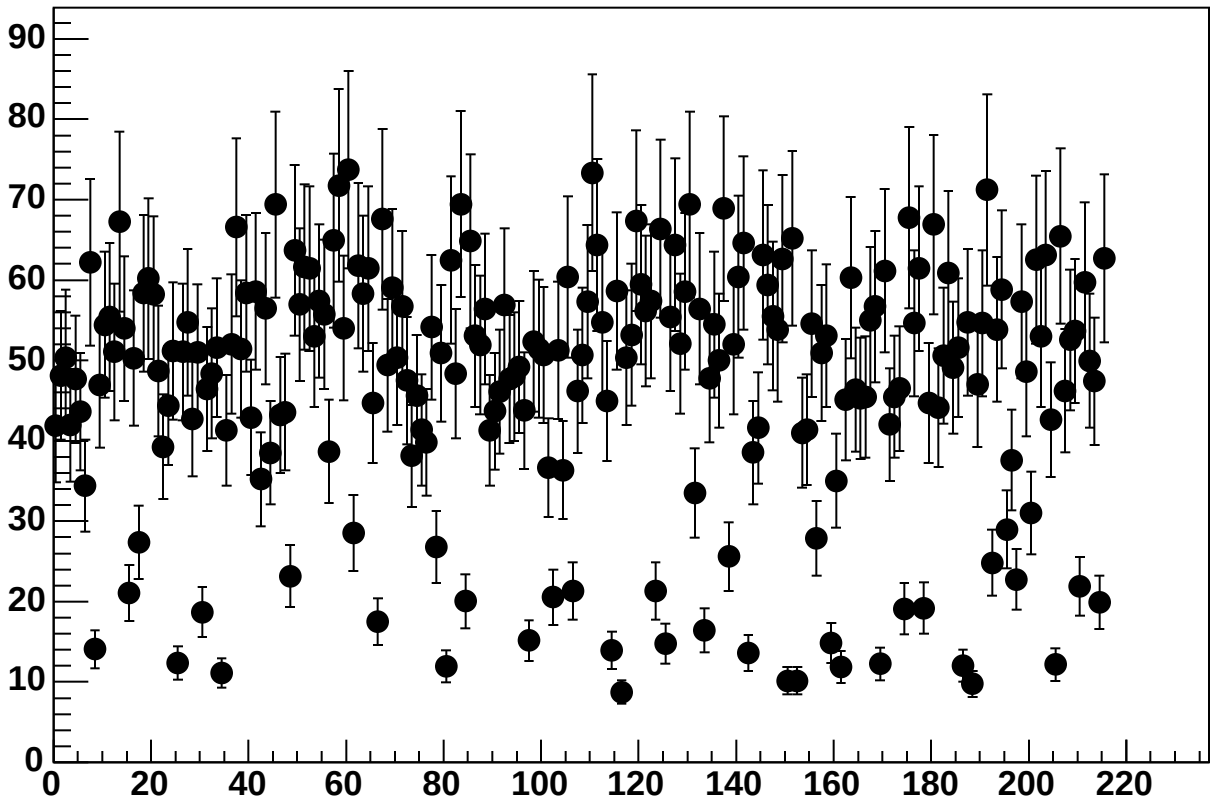
Enable 2, DAC=1600, Hold=25, ADC Noise vs 18*Chip+Chan



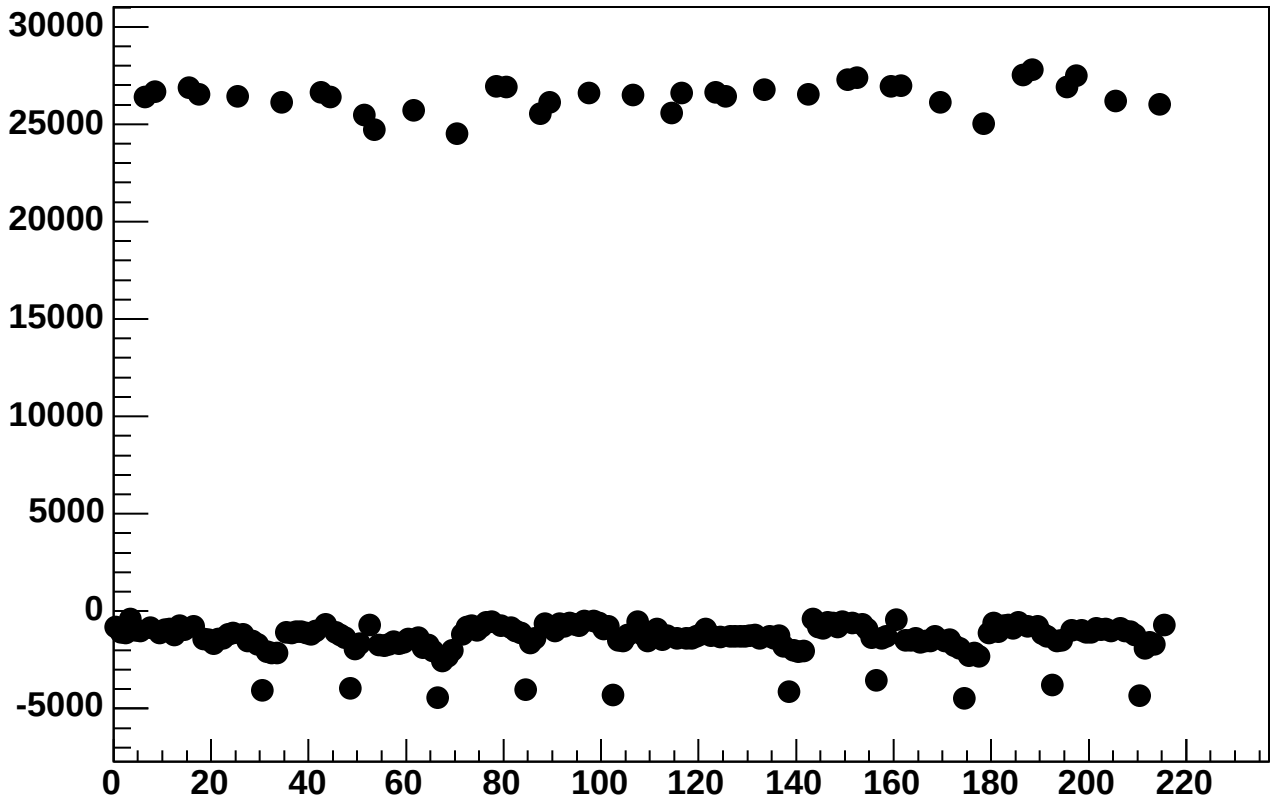
Enable 2, DAC=1600, Hold=30, ADC Mean vs 18*Chip+Chan



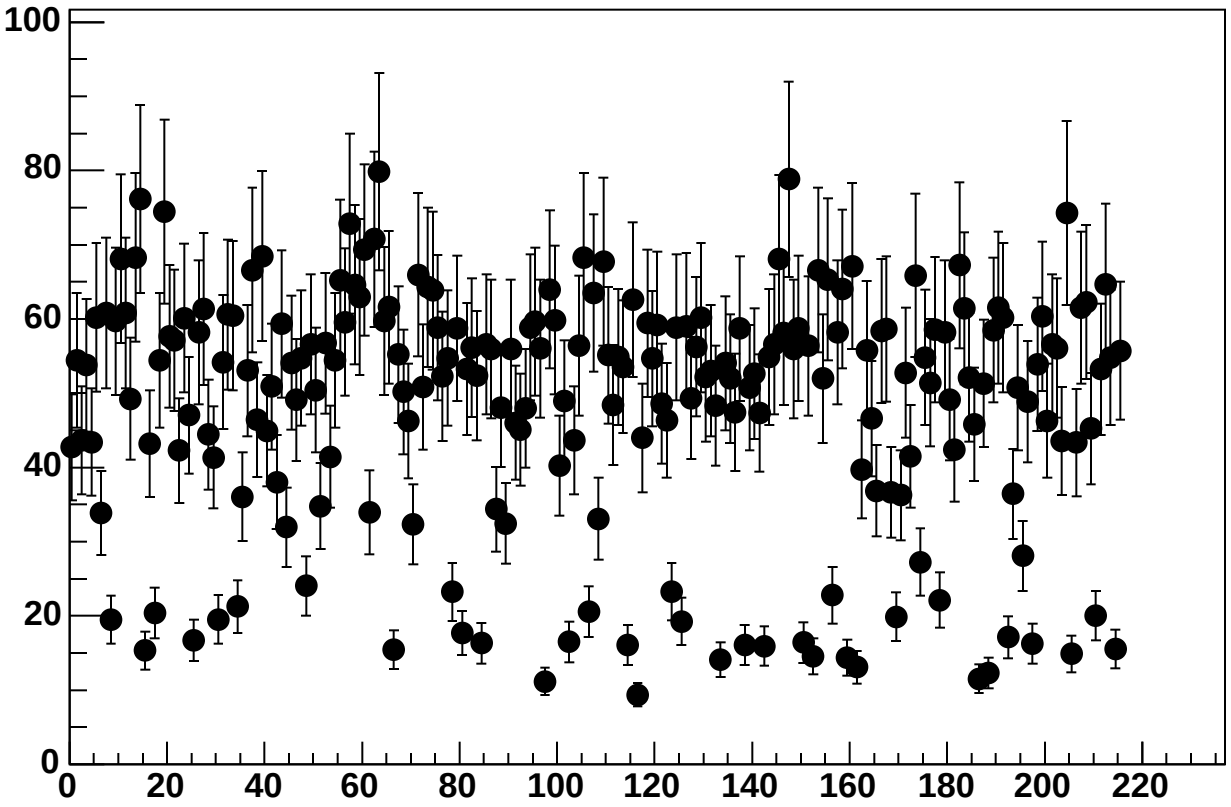
Enable 2, DAC=1600, Hold=30, ADC Noise vs 18*Chip+Chan



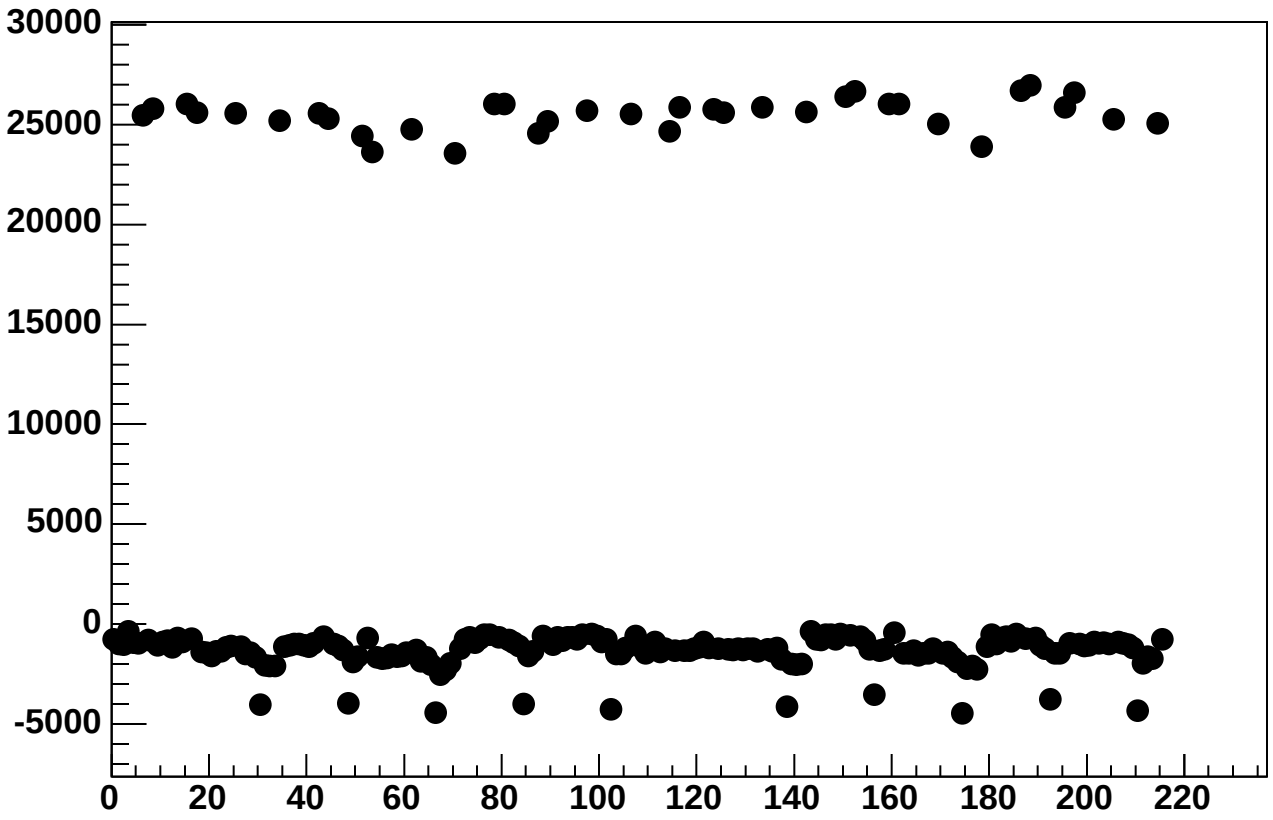
Enable 2, DAC=1600, Hold=35, ADC Mean vs 18*Chip+Chan



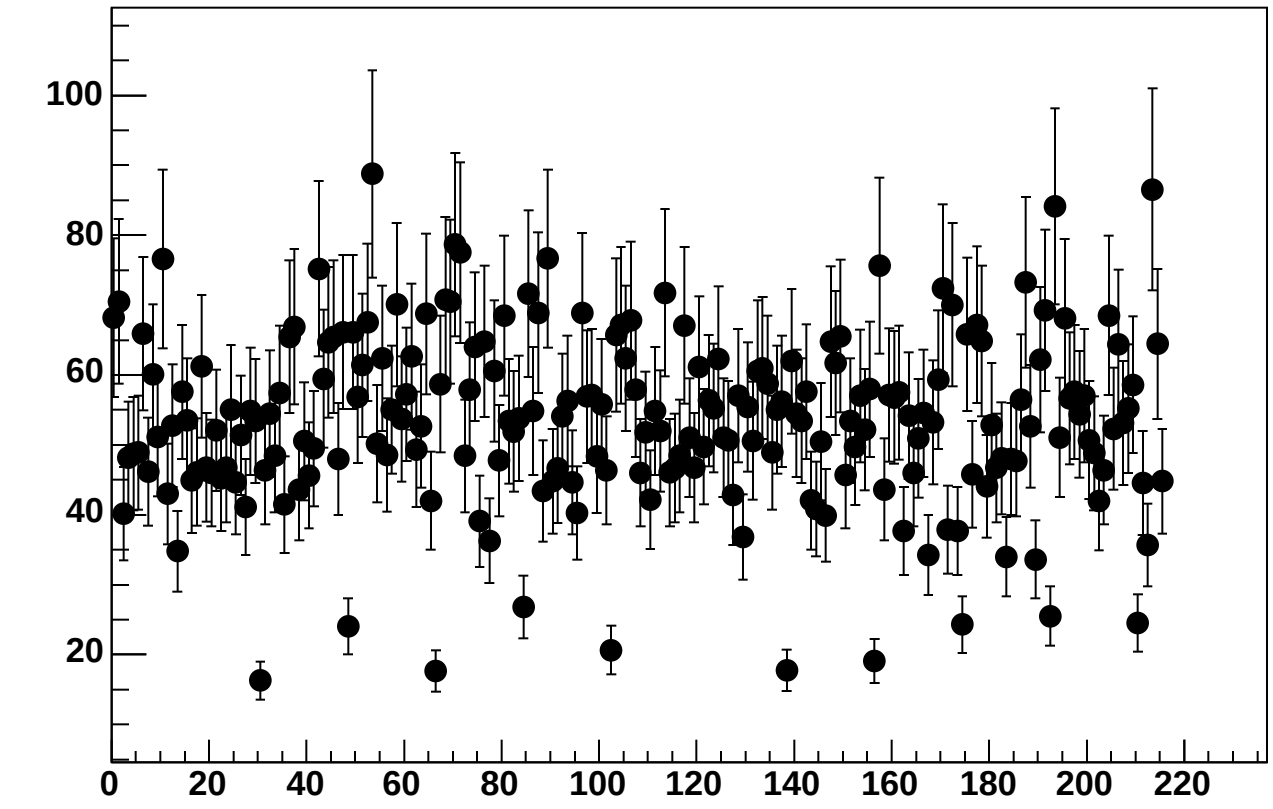
Enable 2, DAC=1600, Hold=35, ADC Noise vs 18*Chip+Chan



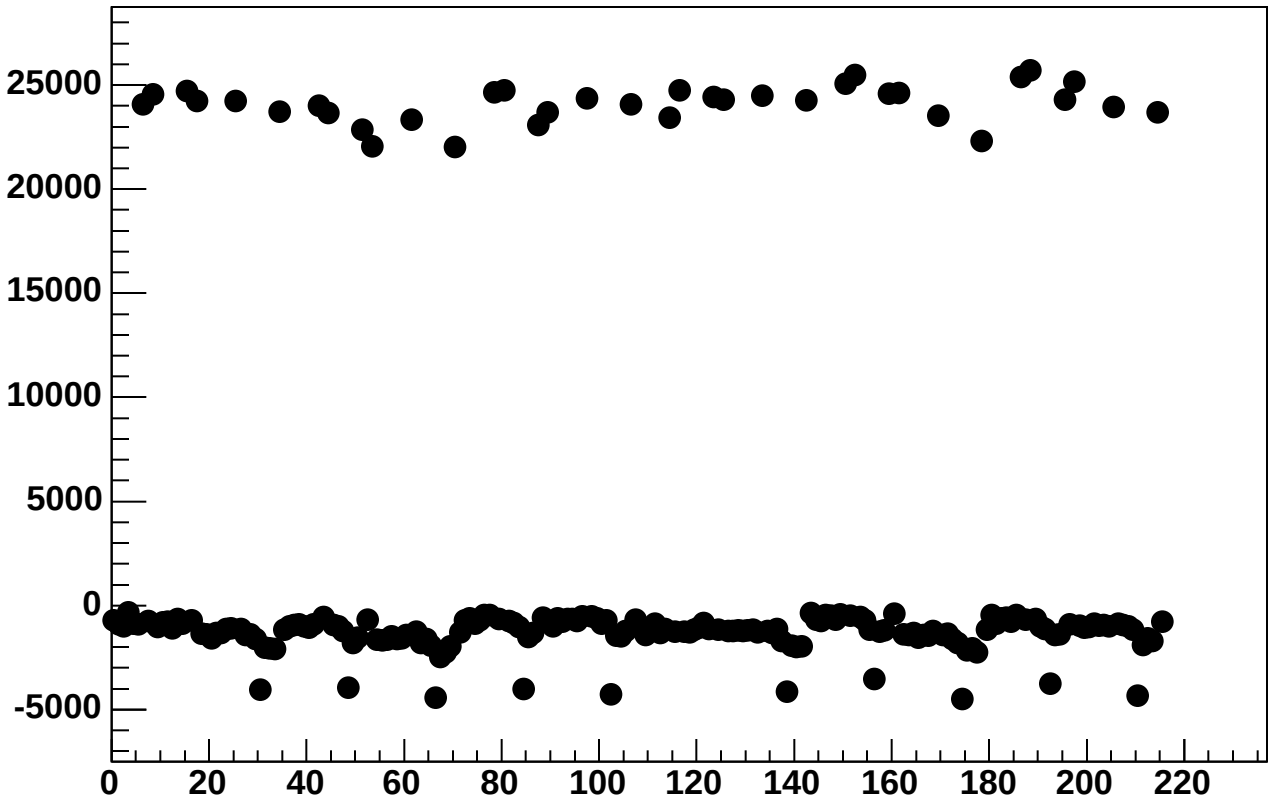
Enable 2, DAC=1600, Hold=40, ADC Mean vs 18*Chip+Chan



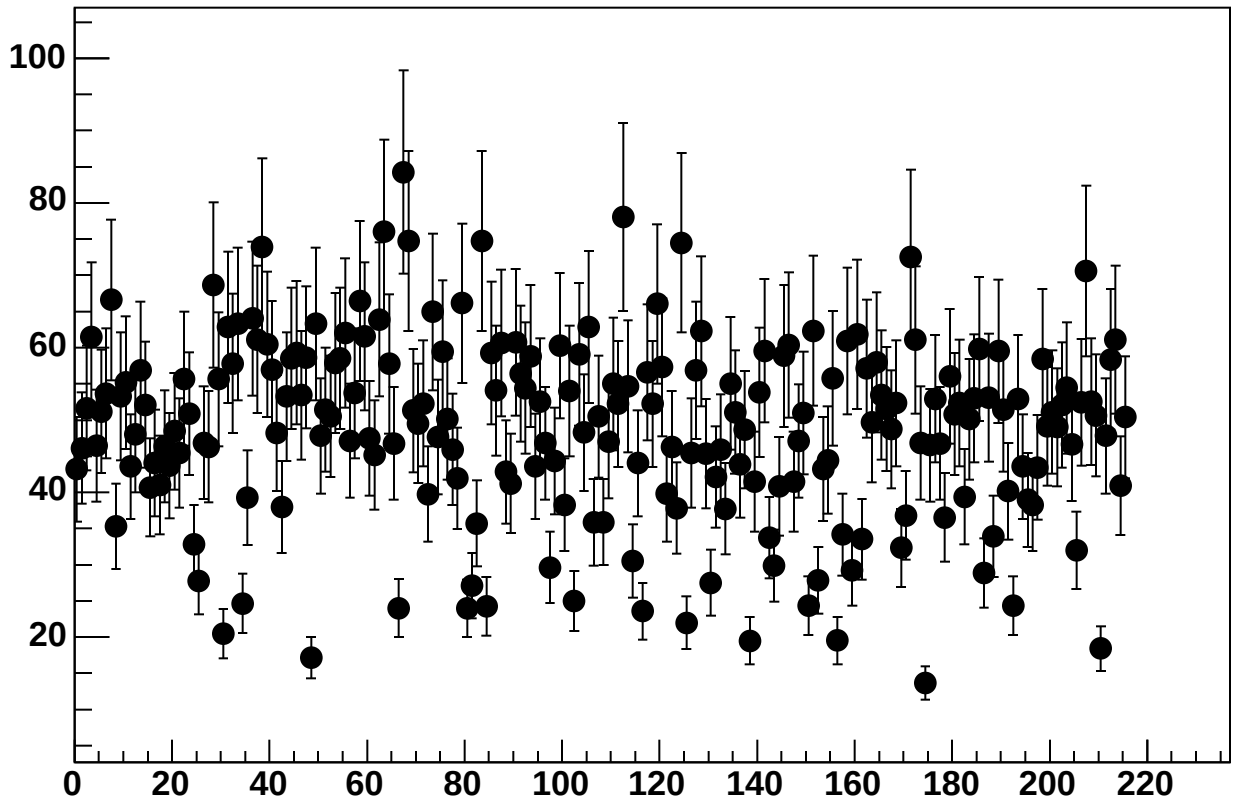
Enable 2, DAC=1600, Hold=40, ADC Noise vs 18*Chip+Chan



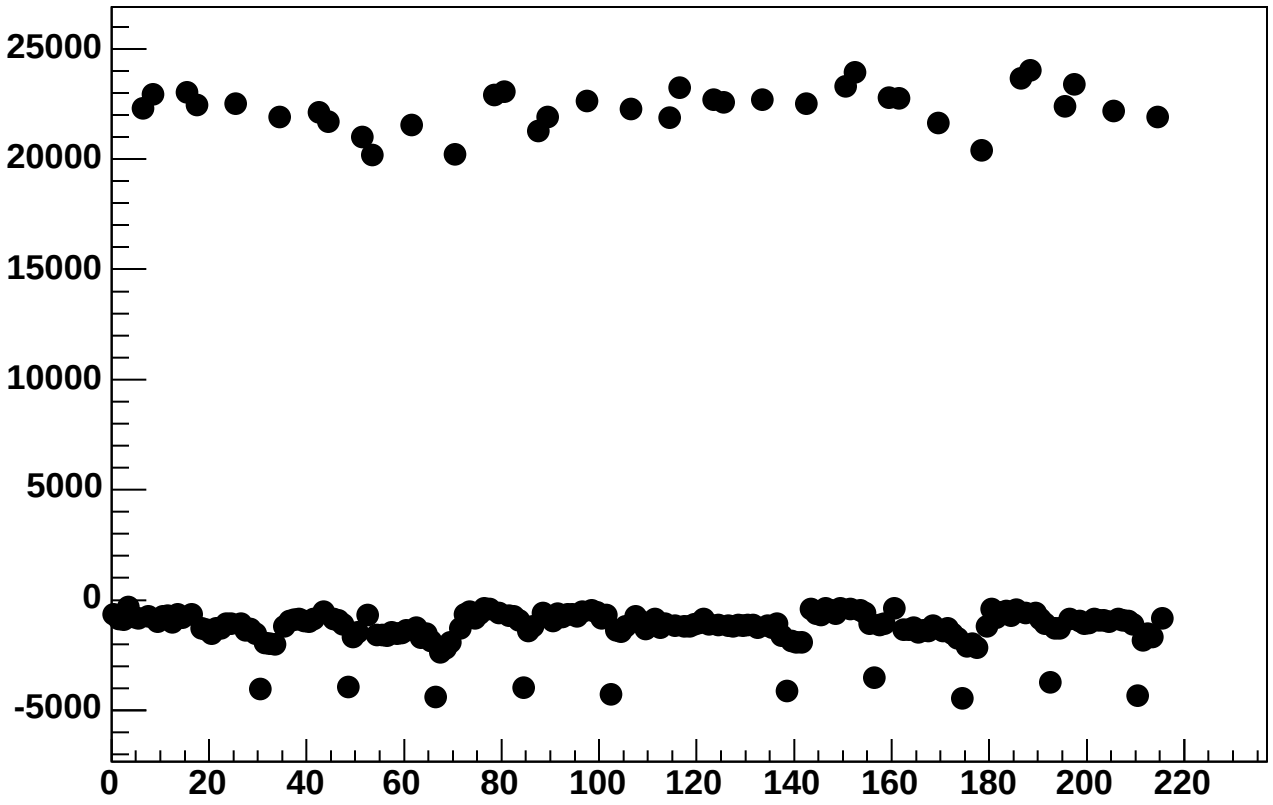
Enable 2, DAC=1600, Hold=45, ADC Mean vs 18*Chip+Chan



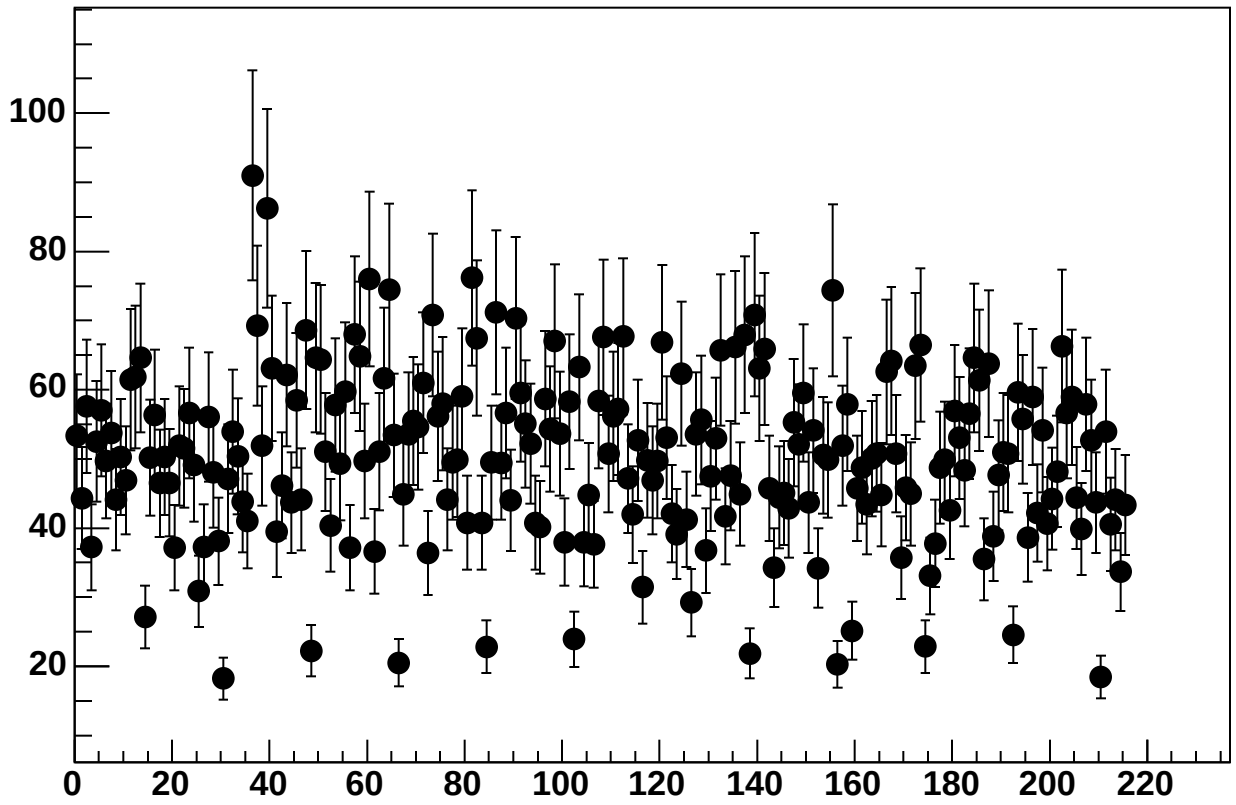
Enable 2, DAC=1600, Hold=45, ADC Noise vs 18*Chip+Chan



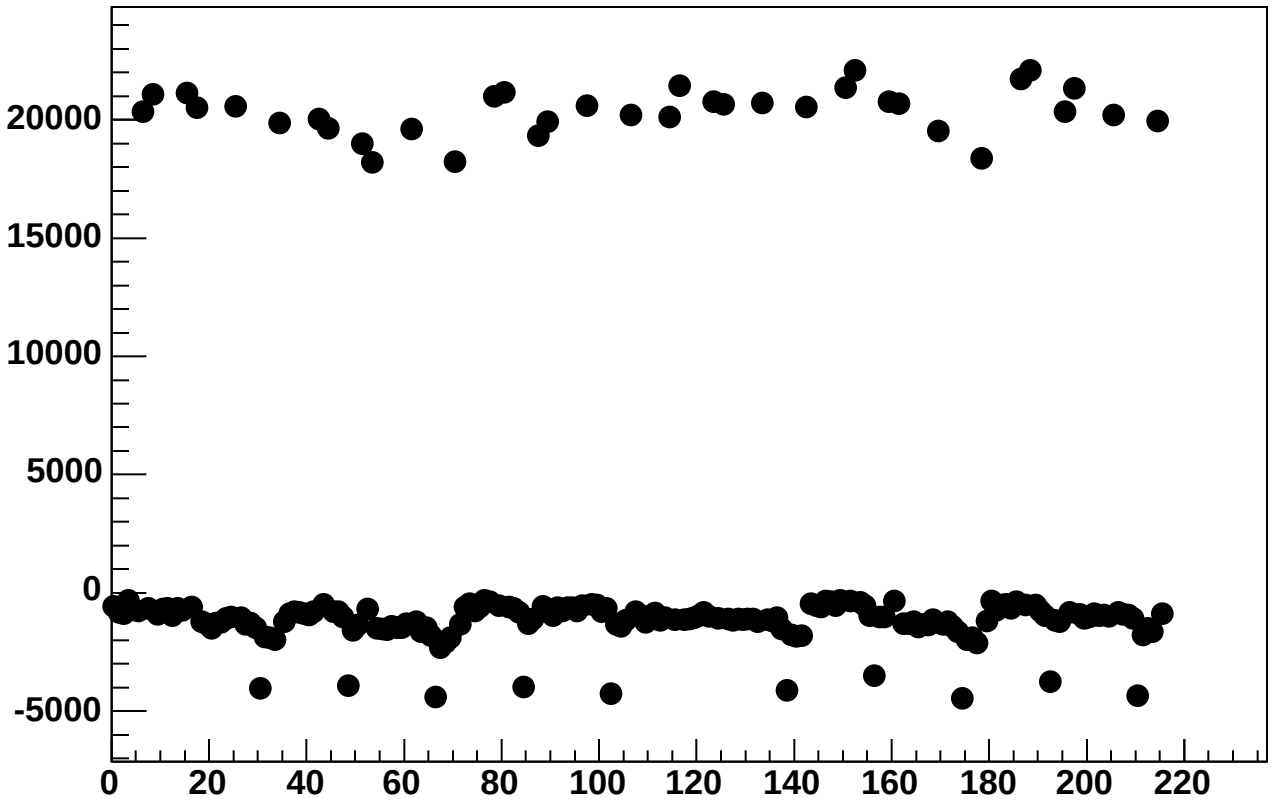
Enable 2, DAC=1600, Hold=50, ADC Mean vs 18*Chip+Chan



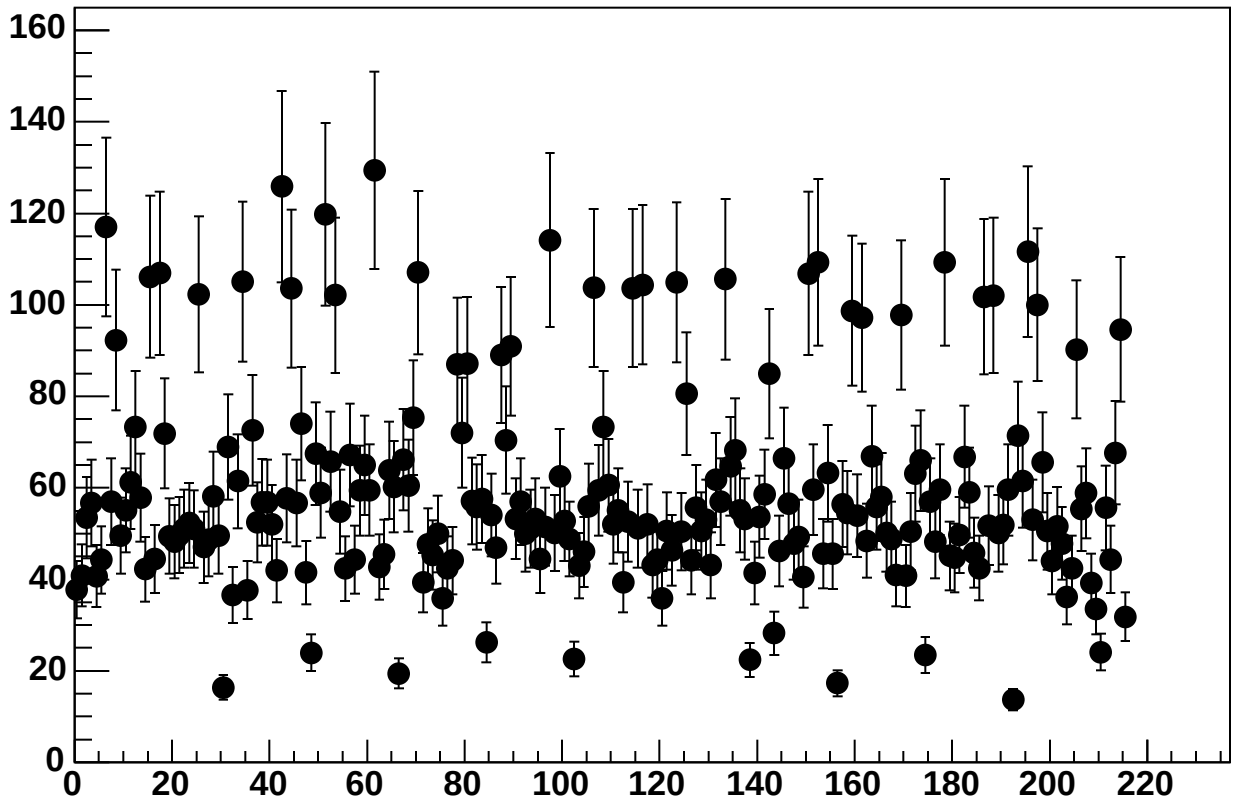
Enable 2, DAC=1600, Hold=50, ADC Noise vs 18*Chip+Chan



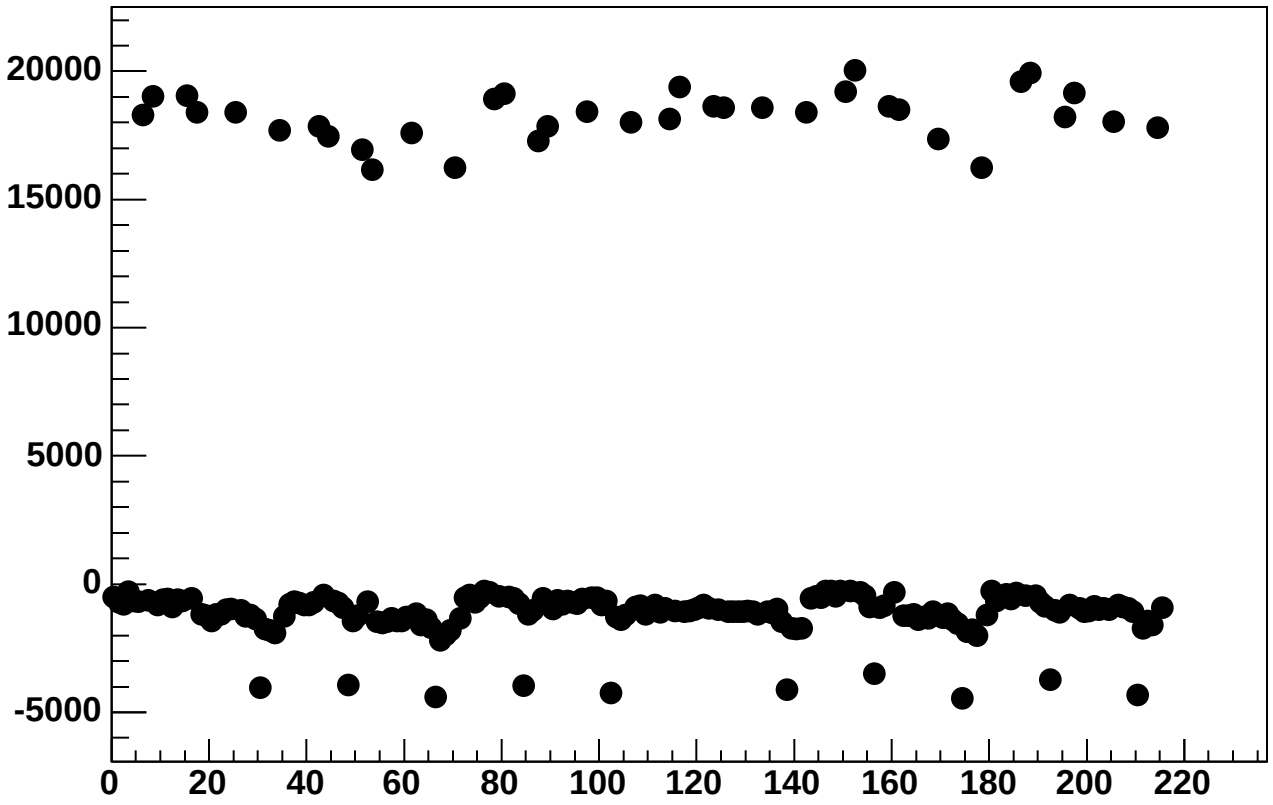
Enable 2, DAC=1600, Hold=55, ADC Mean vs 18*Chip+Chan



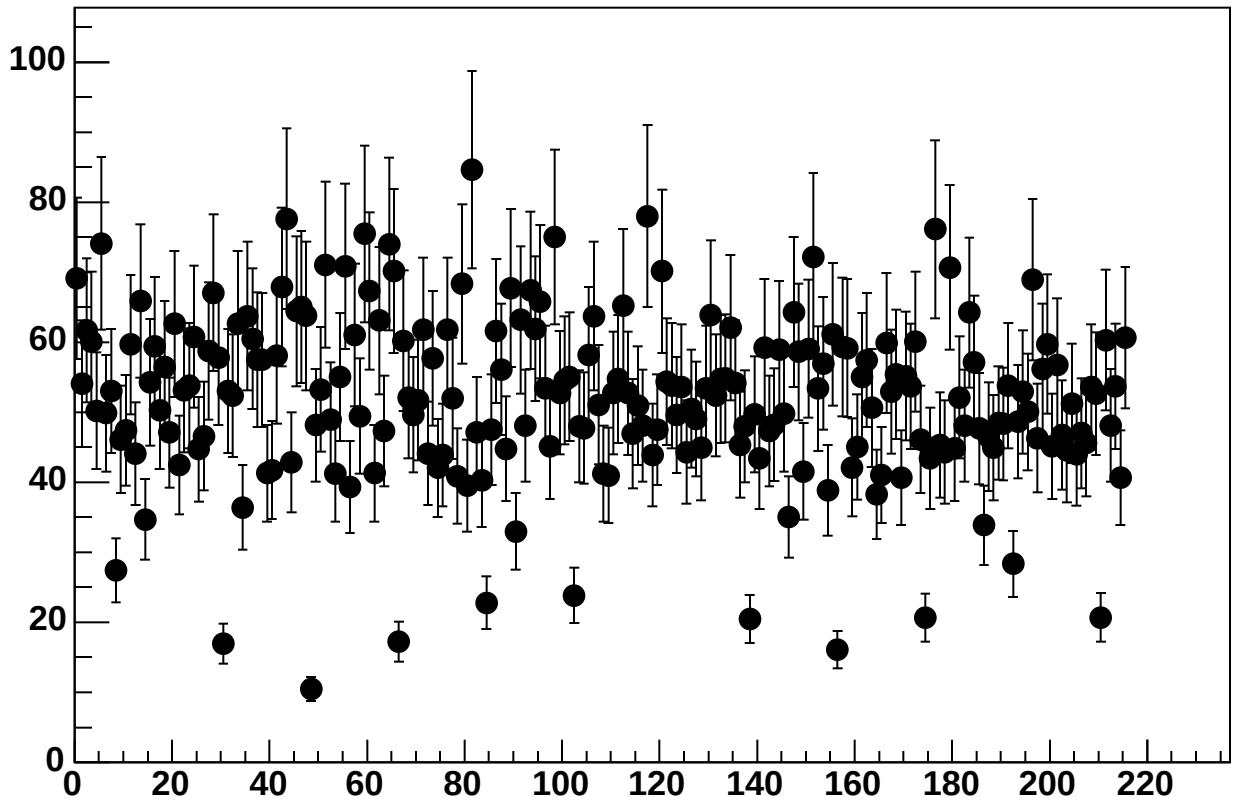
Enable 2, DAC=1600, Hold=55, ADC Noise vs 18*Chip+Chan



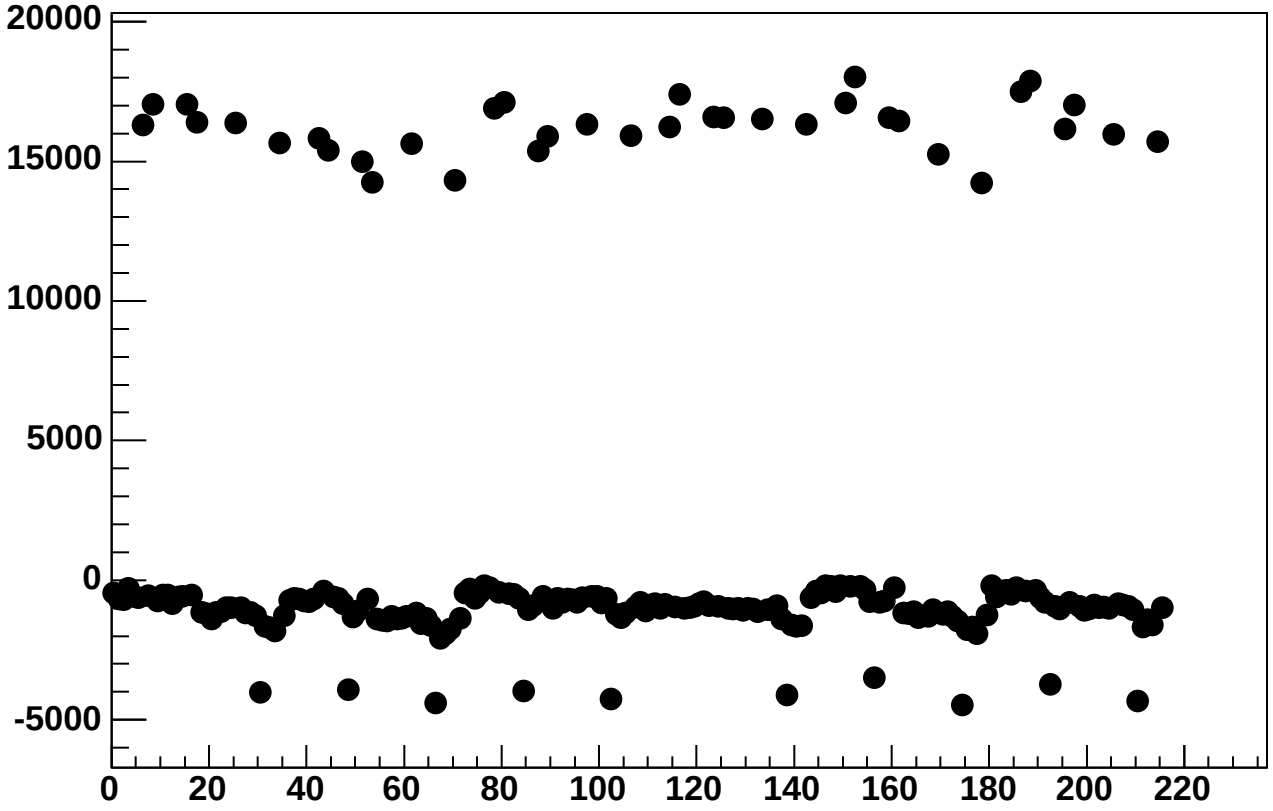
Enable 2, DAC=1600, Hold=60, ADC Mean vs 18*Chip+Chan



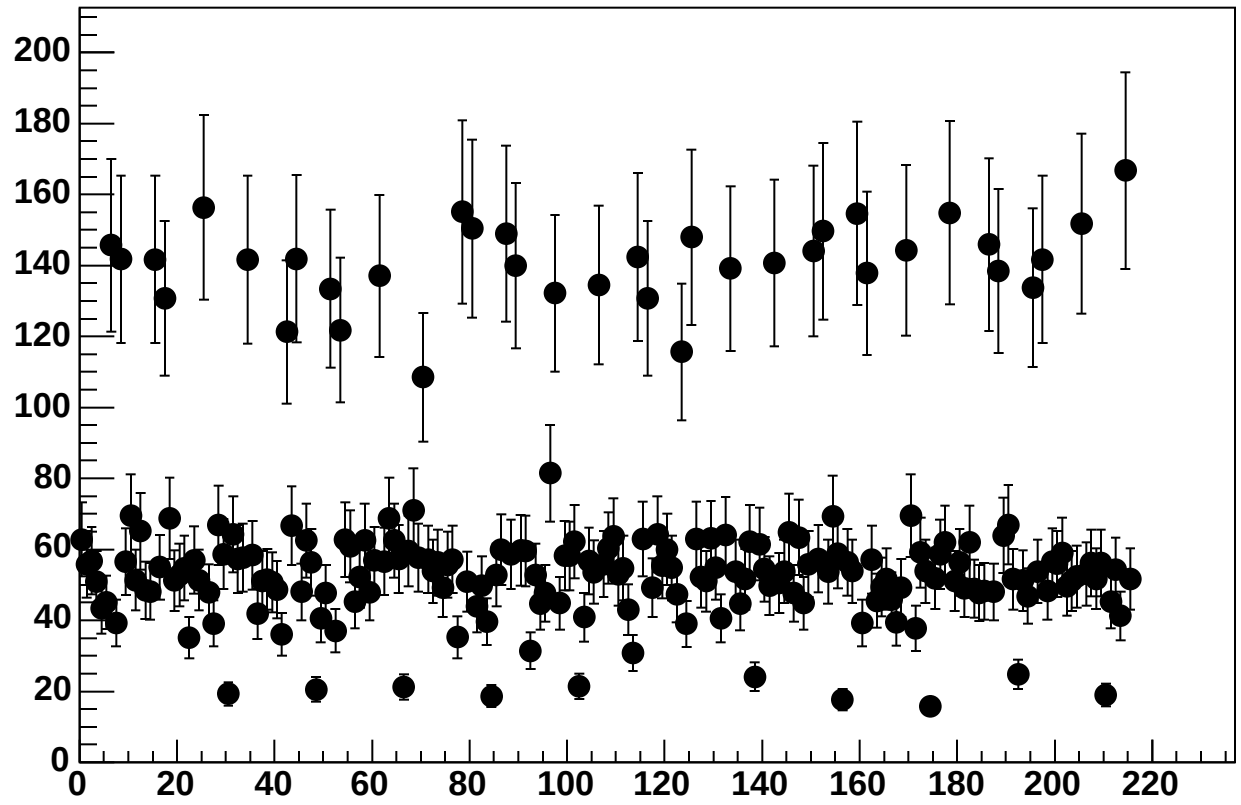
Enable 2, DAC=1600, Hold=60, ADC Noise vs 18*Chip+Chan



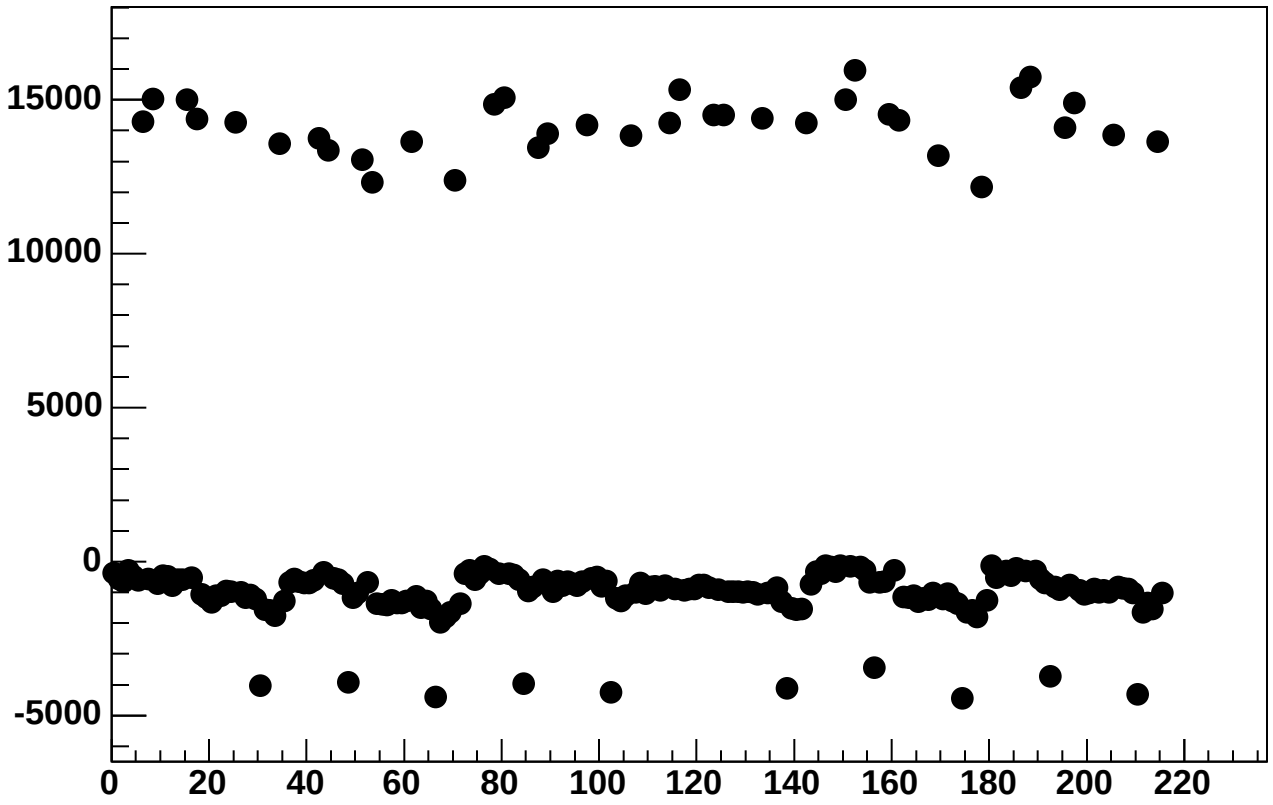
Enable 2, DAC=1600, Hold=65, ADC Mean vs 18*Chip+Chan



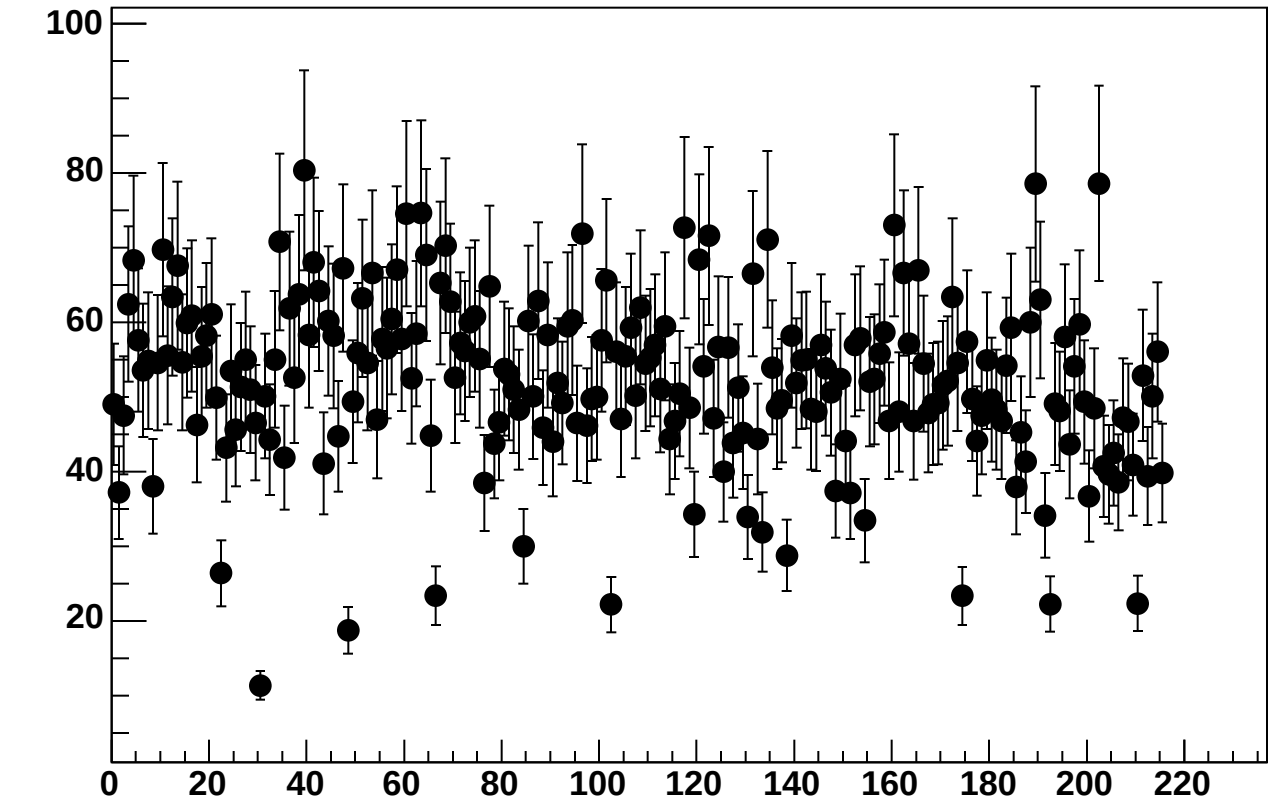
Enable 2, DAC=1600, Hold=65, ADC Noise vs 18*Chip+Chan



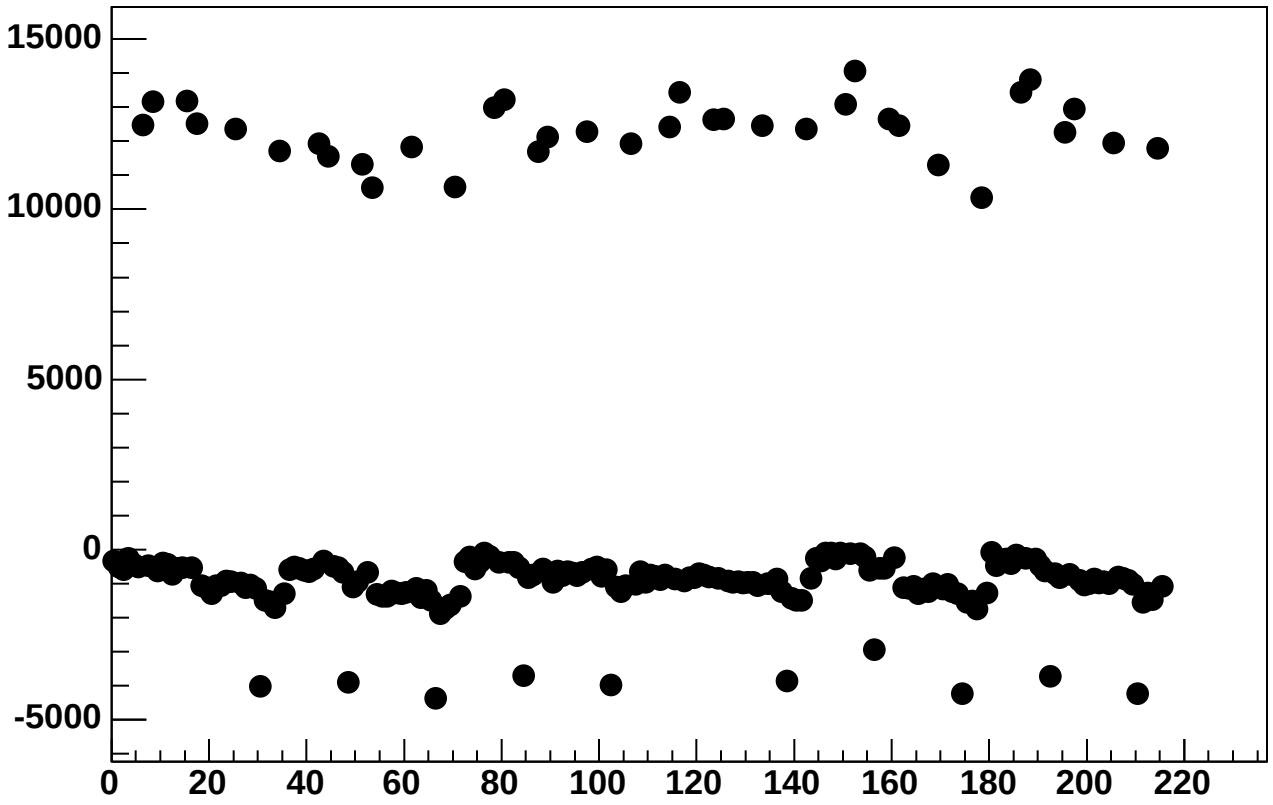
Enable 2, DAC=1600, Hold=70, ADC Mean vs 18*Chip+Chan



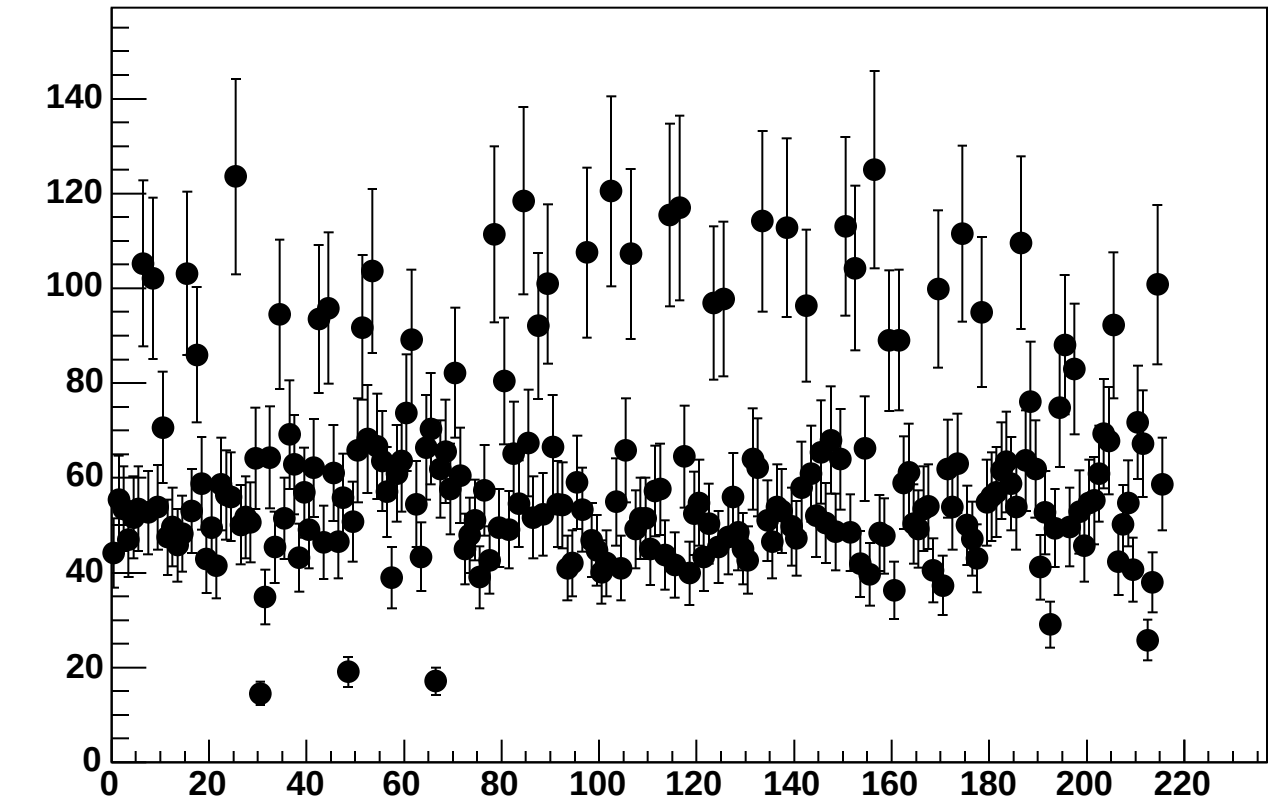
Enable 2, DAC=1600, Hold=70, ADC Noise vs 18*Chip+Chan



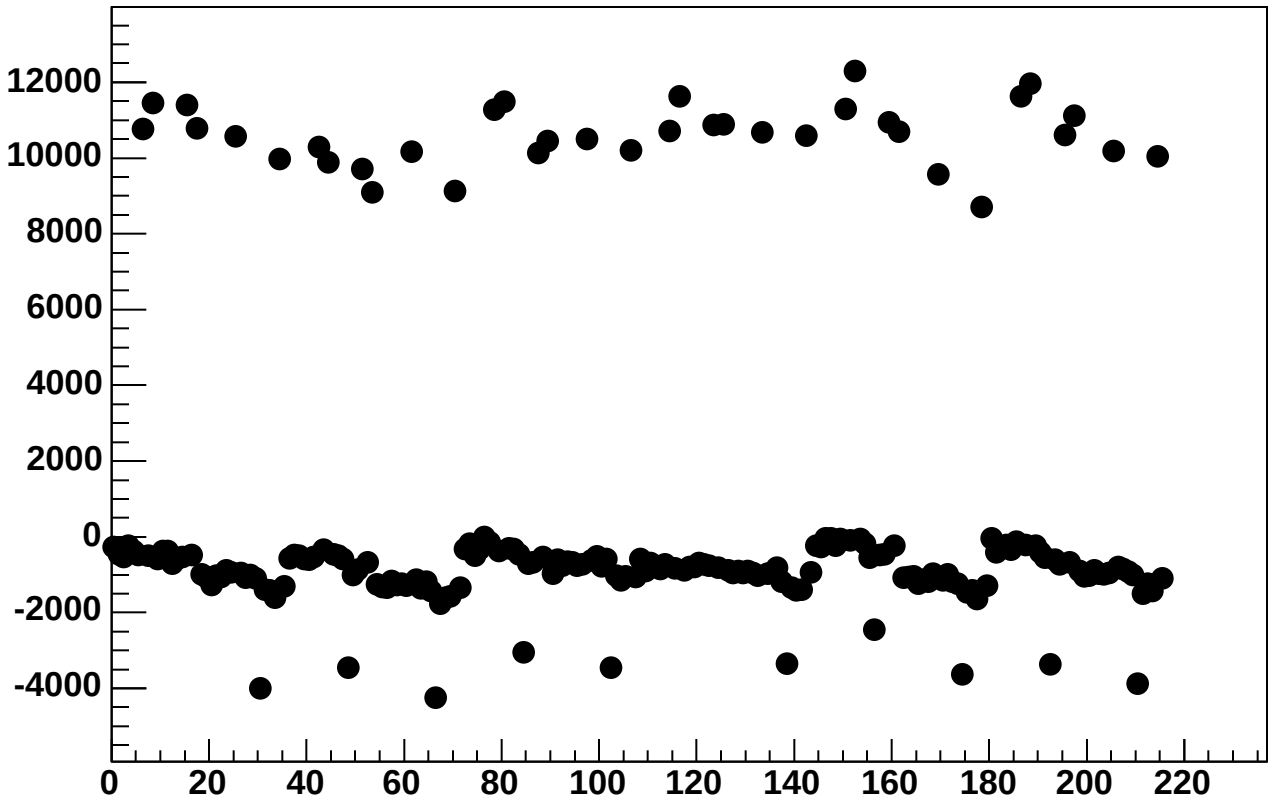
Enable 2, DAC=1600, Hold=75, ADC Mean vs 18*Chip+Chan



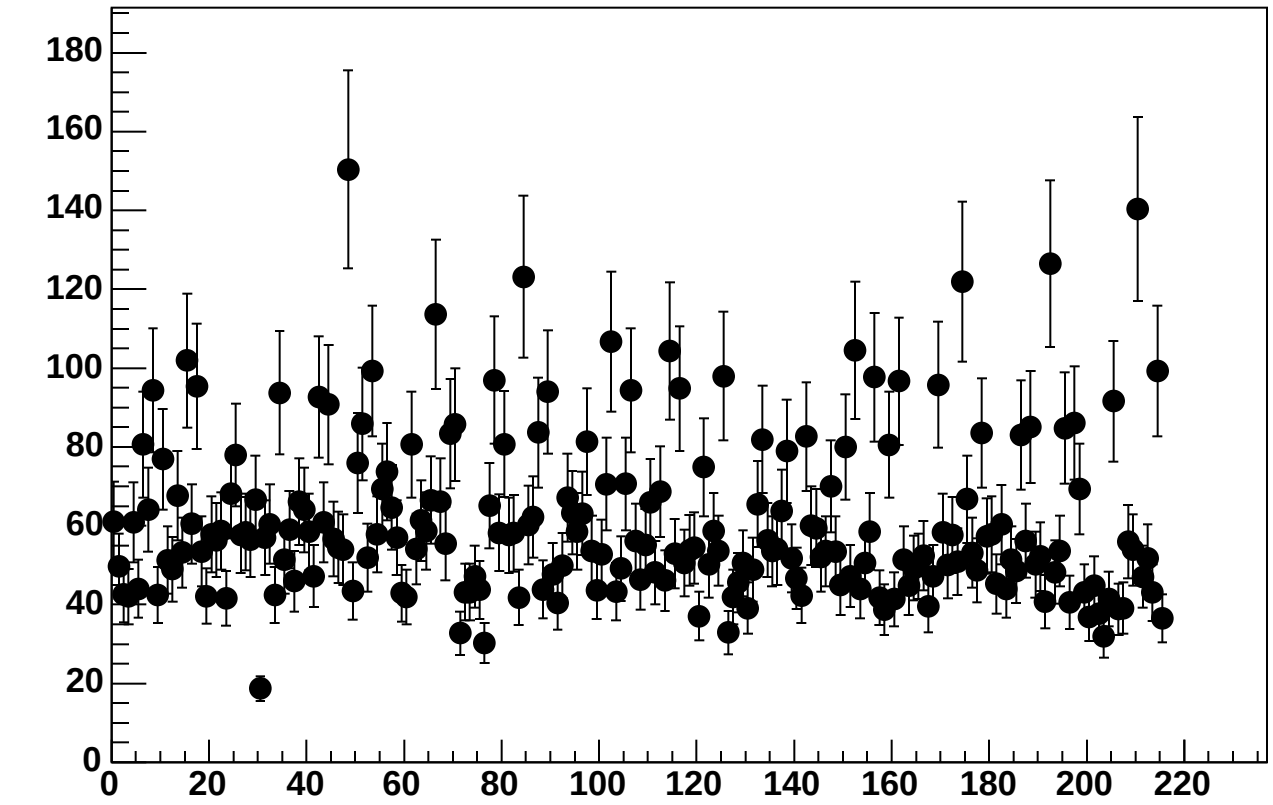
Enable 2, DAC=1600, Hold=75, ADC Noise vs 18*Chip+Chan



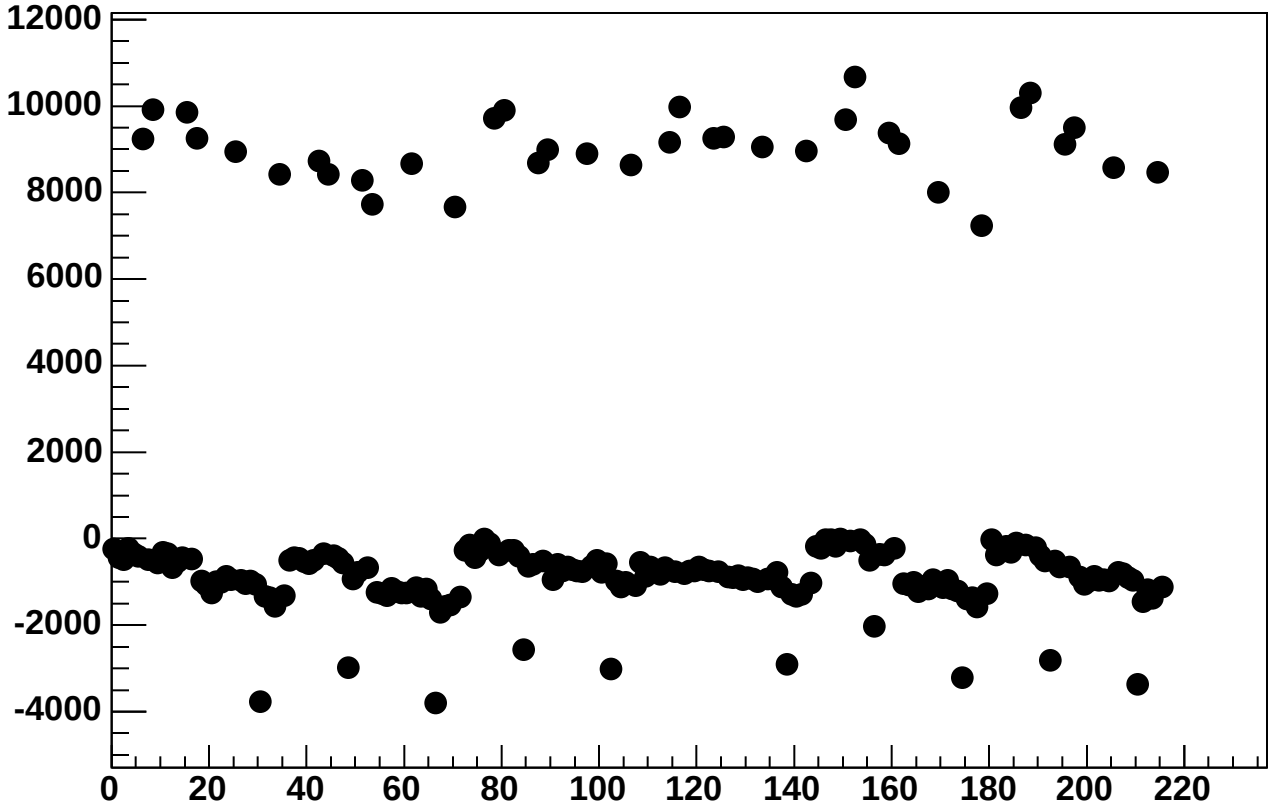
Enable 2, DAC=1600, Hold=80, ADC Mean vs 18*Chip+Chan



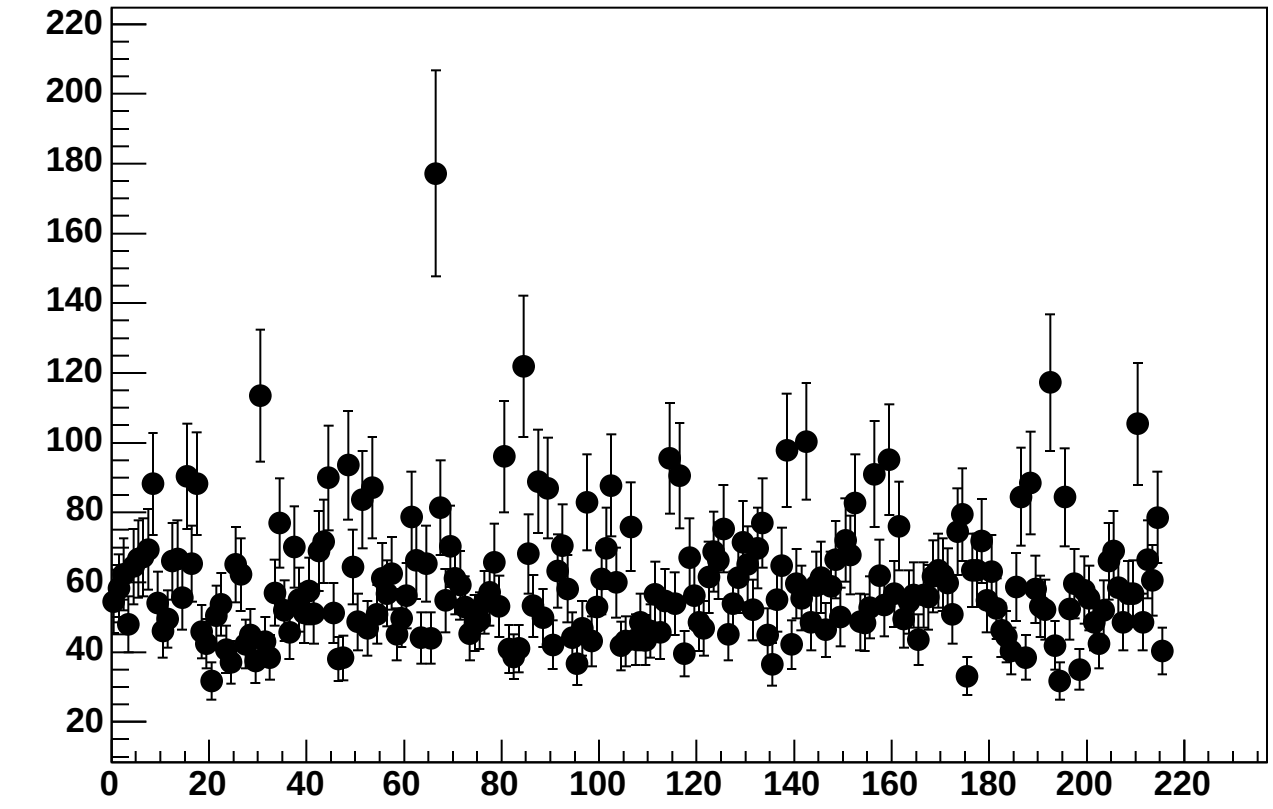
Enable 2, DAC=1600, Hold=80, ADC Noise vs 18*Chip+Chan



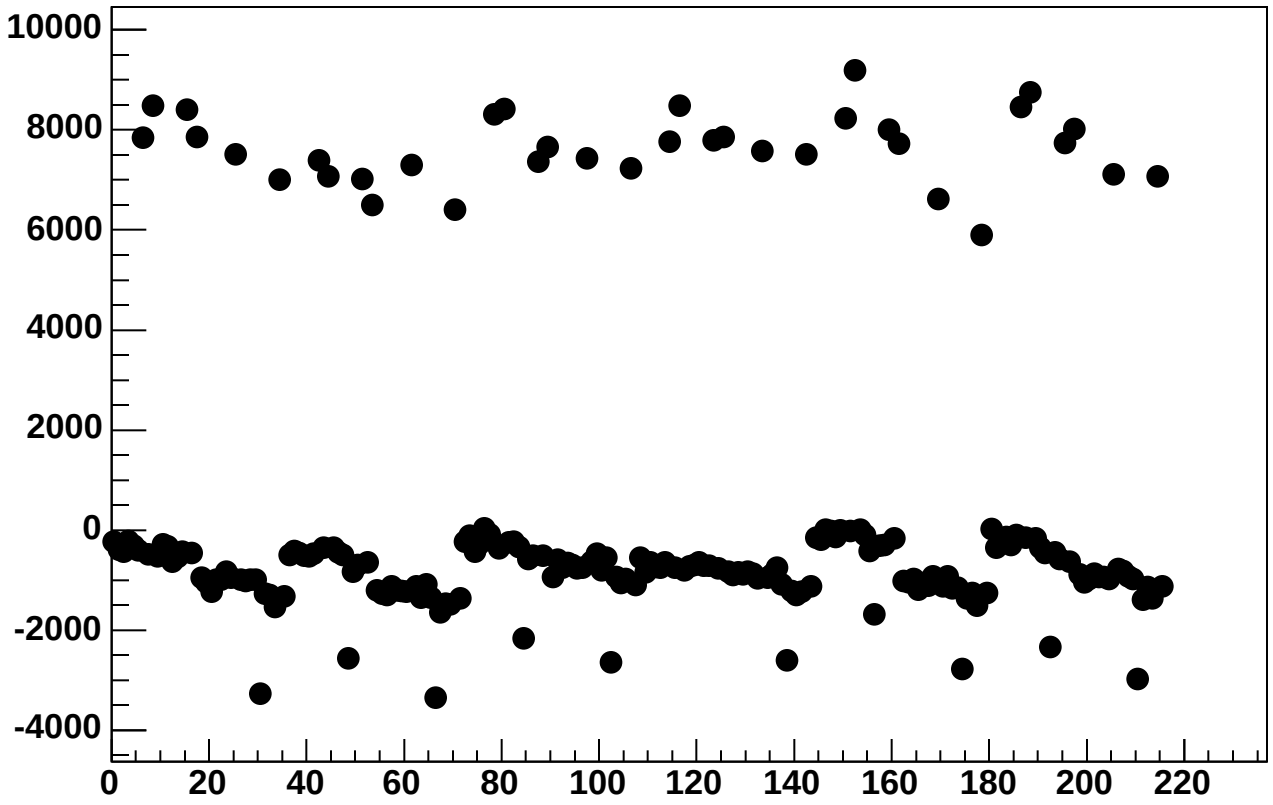
Enable 2, DAC=1600, Hold=85, ADC Mean vs 18*Chip+Chan



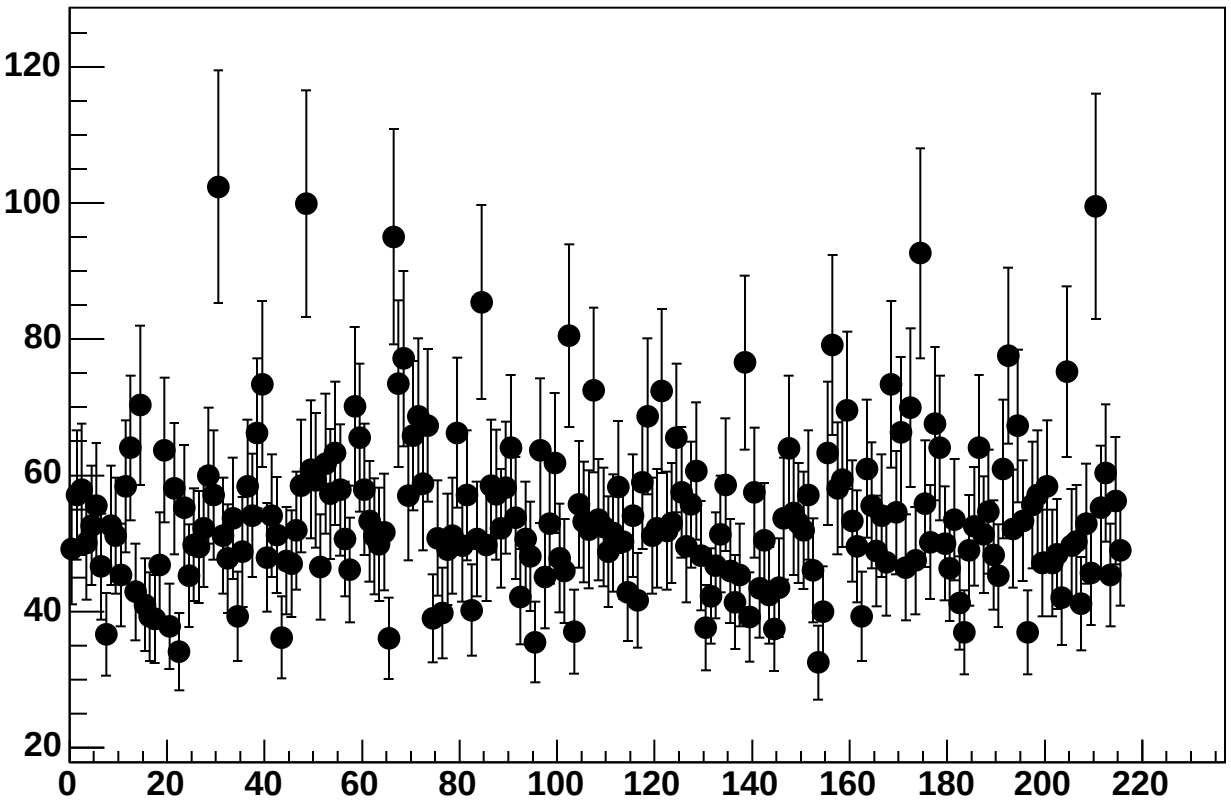
Enable 2, DAC=1600, Hold=85, ADC Noise vs 18*Chip+Chan



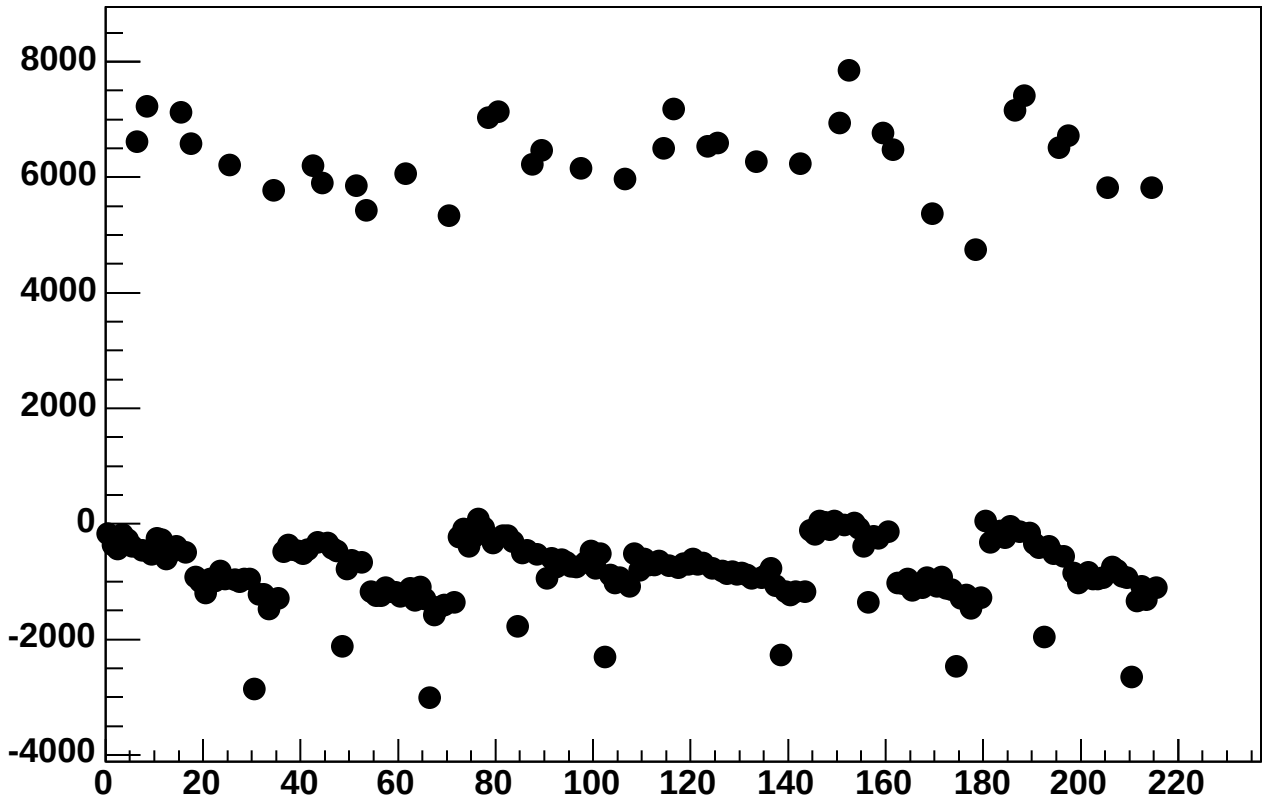
Enable 2, DAC=1600, Hold=90, ADC Mean vs 18*Chip+Chan



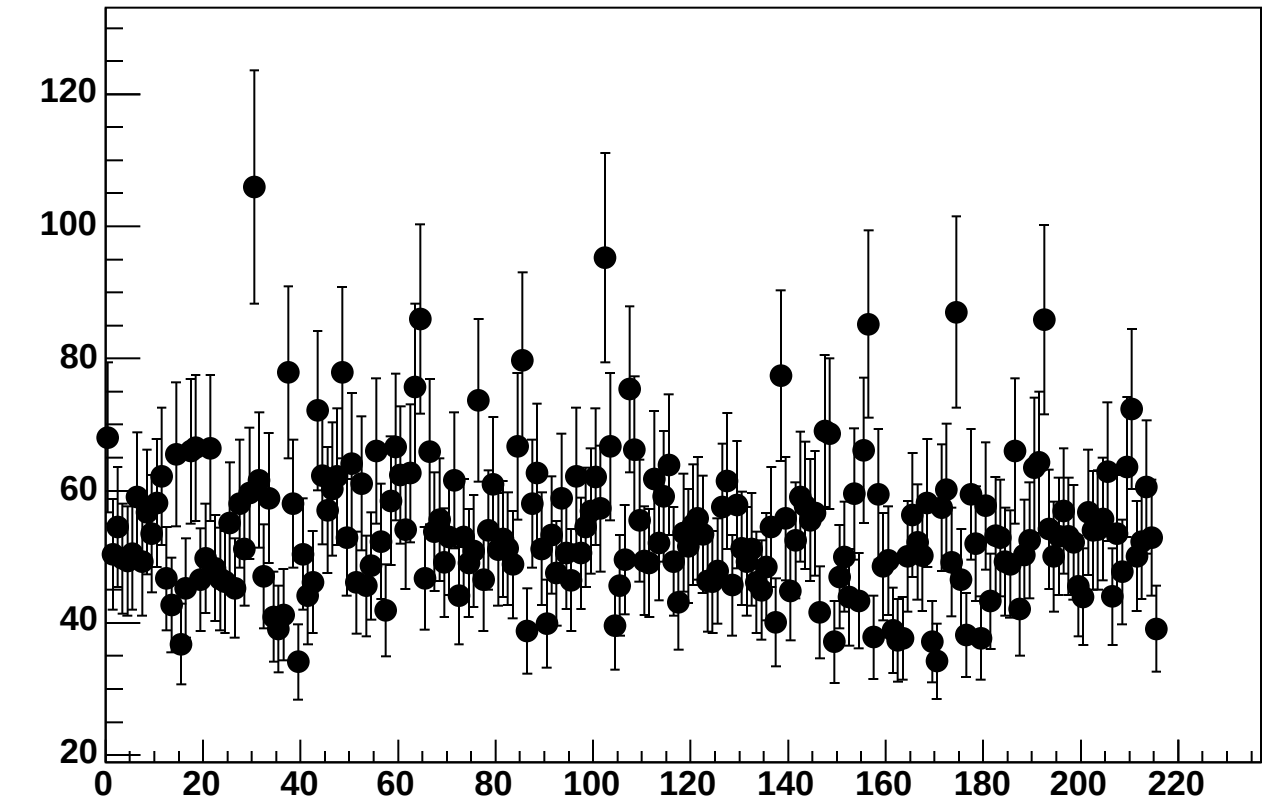
Enable 2, DAC=1600, Hold=90, ADC Noise vs 18*Chip+Chan



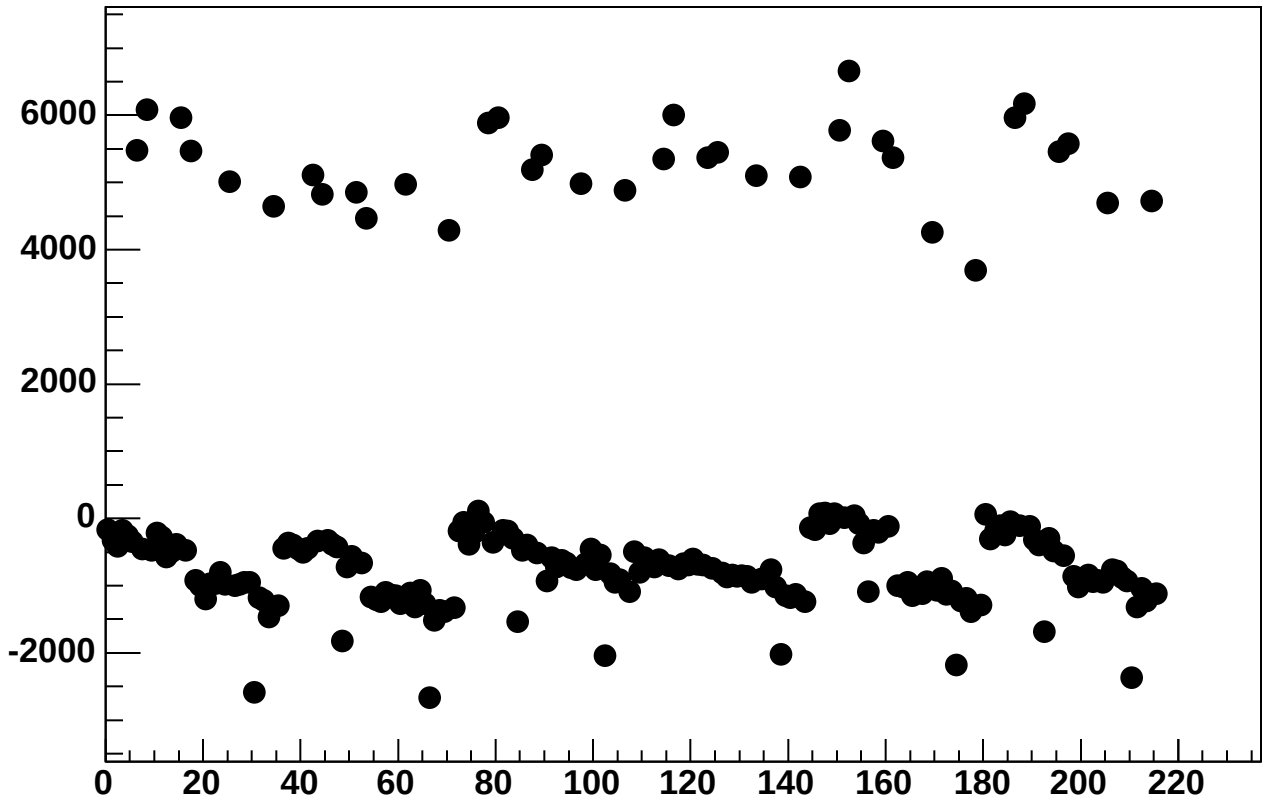
Enable 2, DAC=1600, Hold=95, ADC Mean vs 18*Chip+Chan



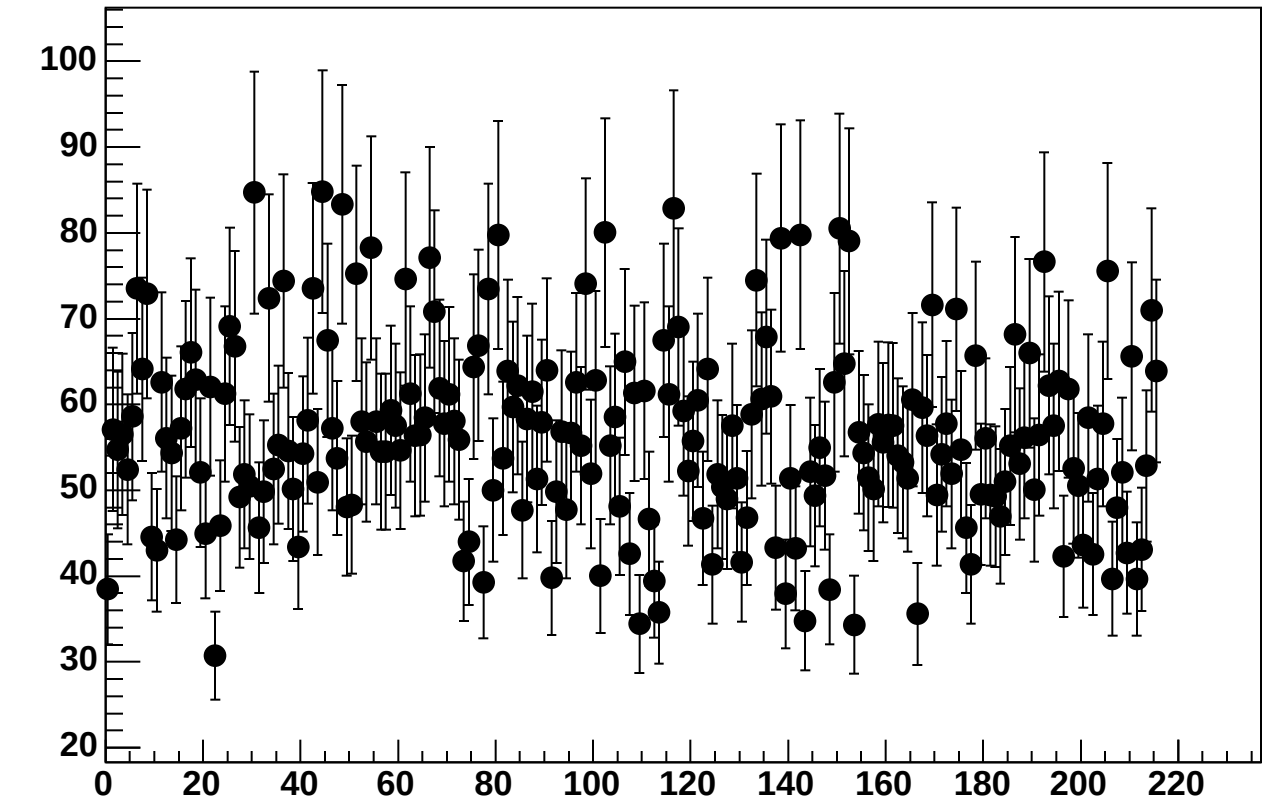
Enable 2, DAC=1600, Hold=95, ADC Noise vs 18*Chip+Chan



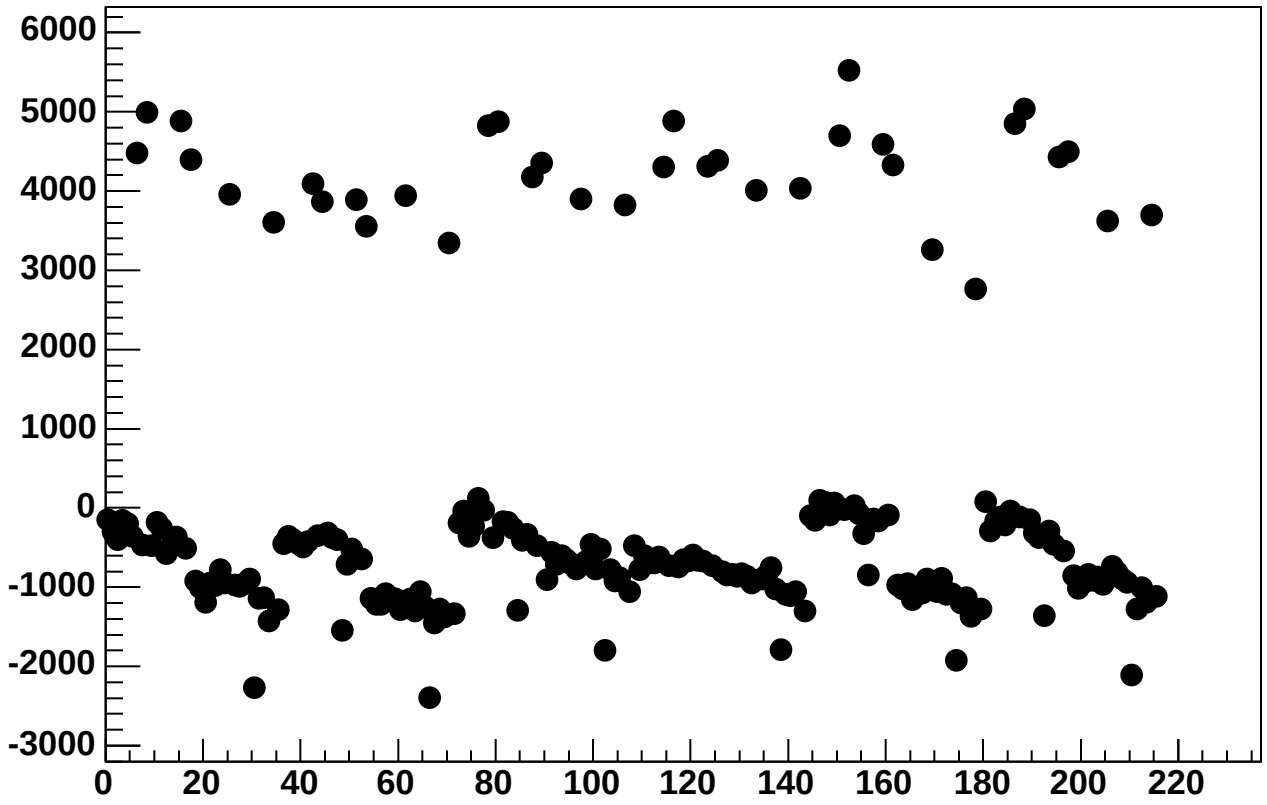
Enable 2, DAC=1600, Hold=100, ADC Mean vs 18*Chip+Chan



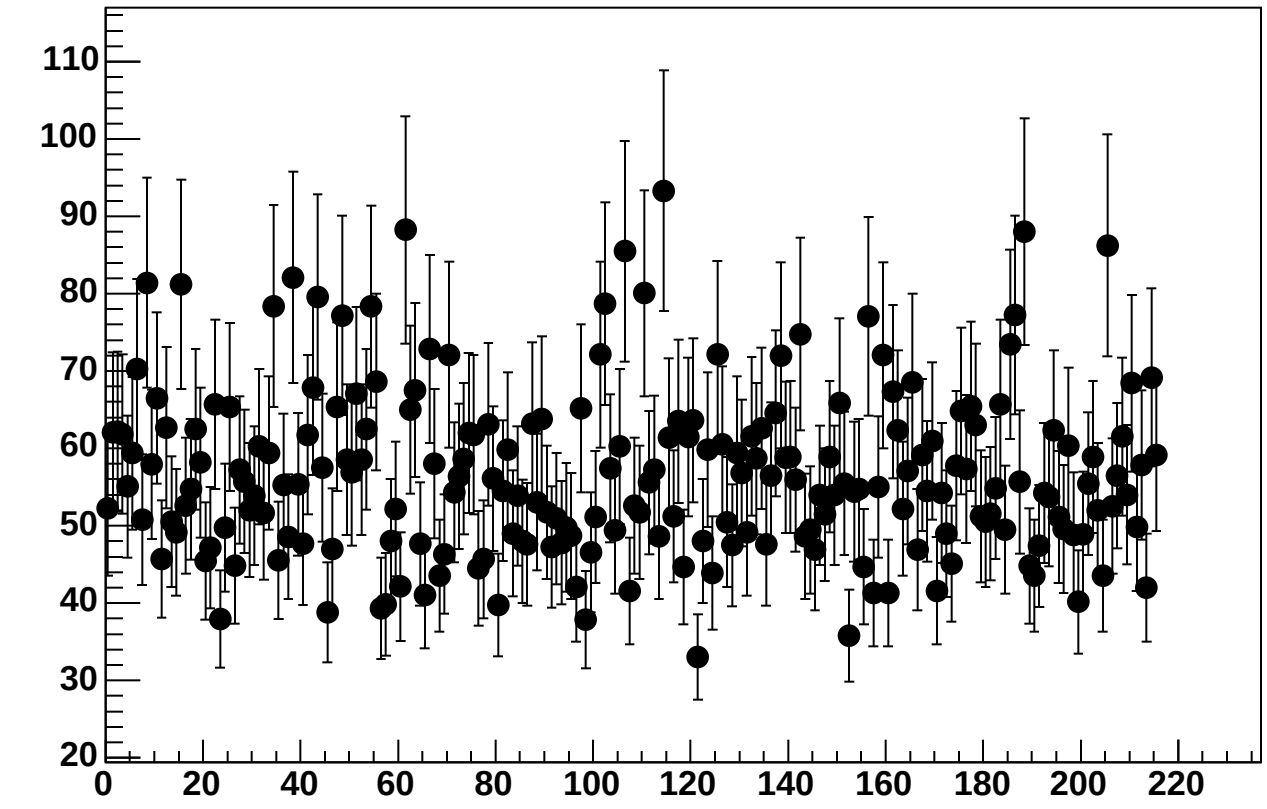
Enable 2, DAC=1600, Hold=100, ADC Noise vs 18*Chip+Chan



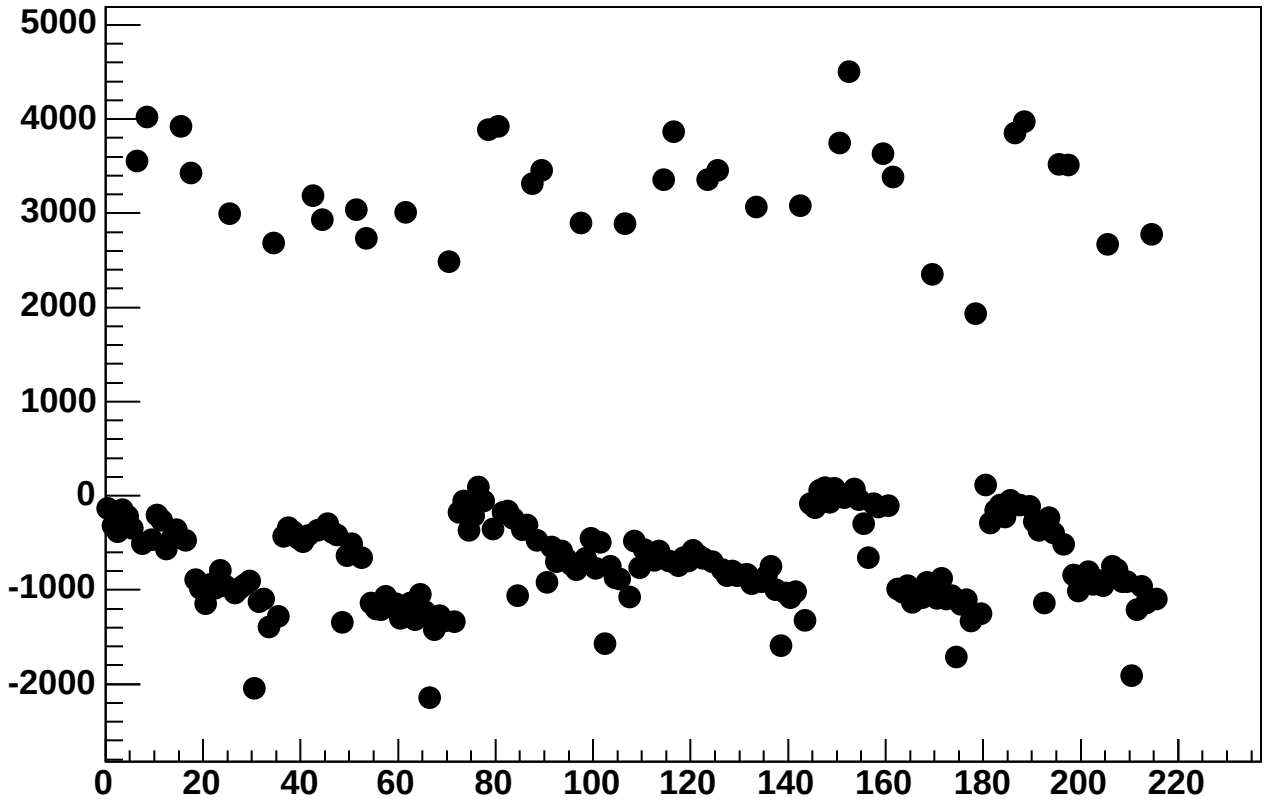
Enable 2, DAC=1600, Hold=105, ADC Mean vs 18*Chip+Chan



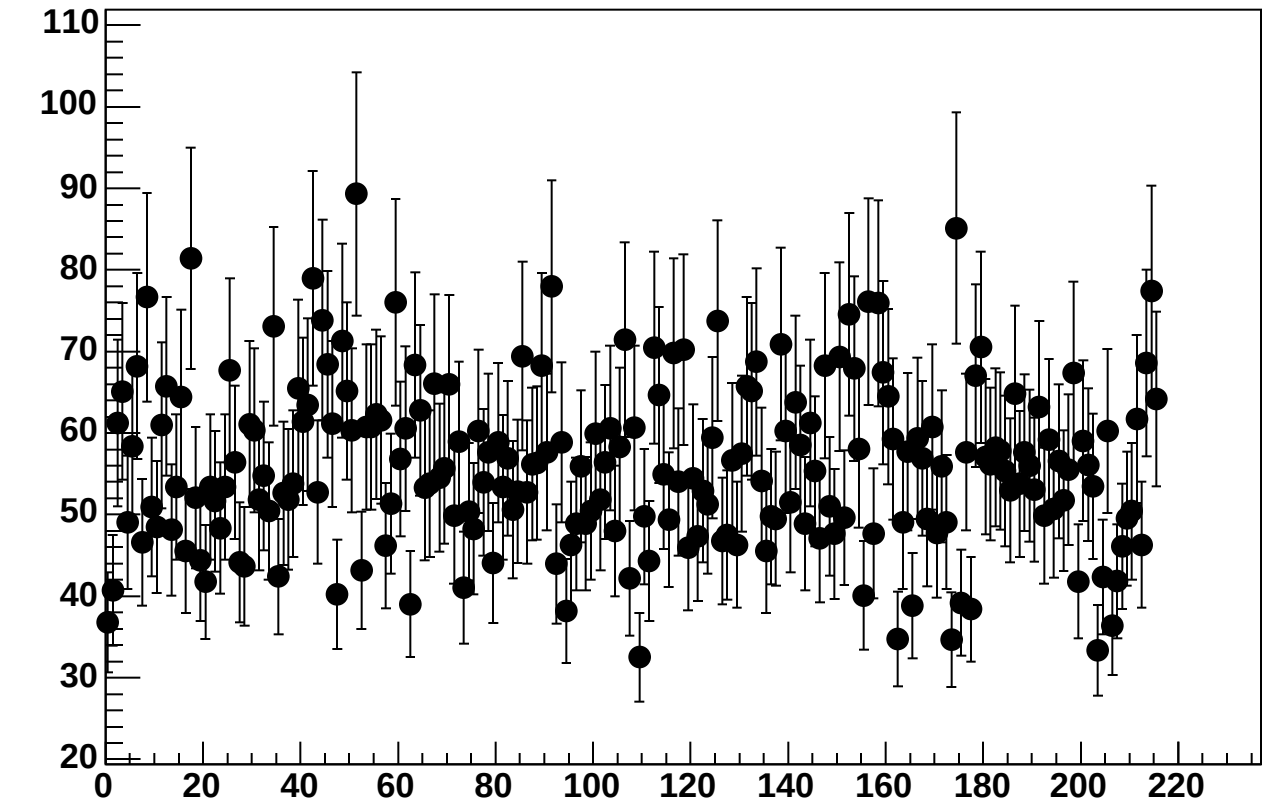
Enable 2, DAC=1600, Hold=105, ADC Noise vs 18*Chip+Chan



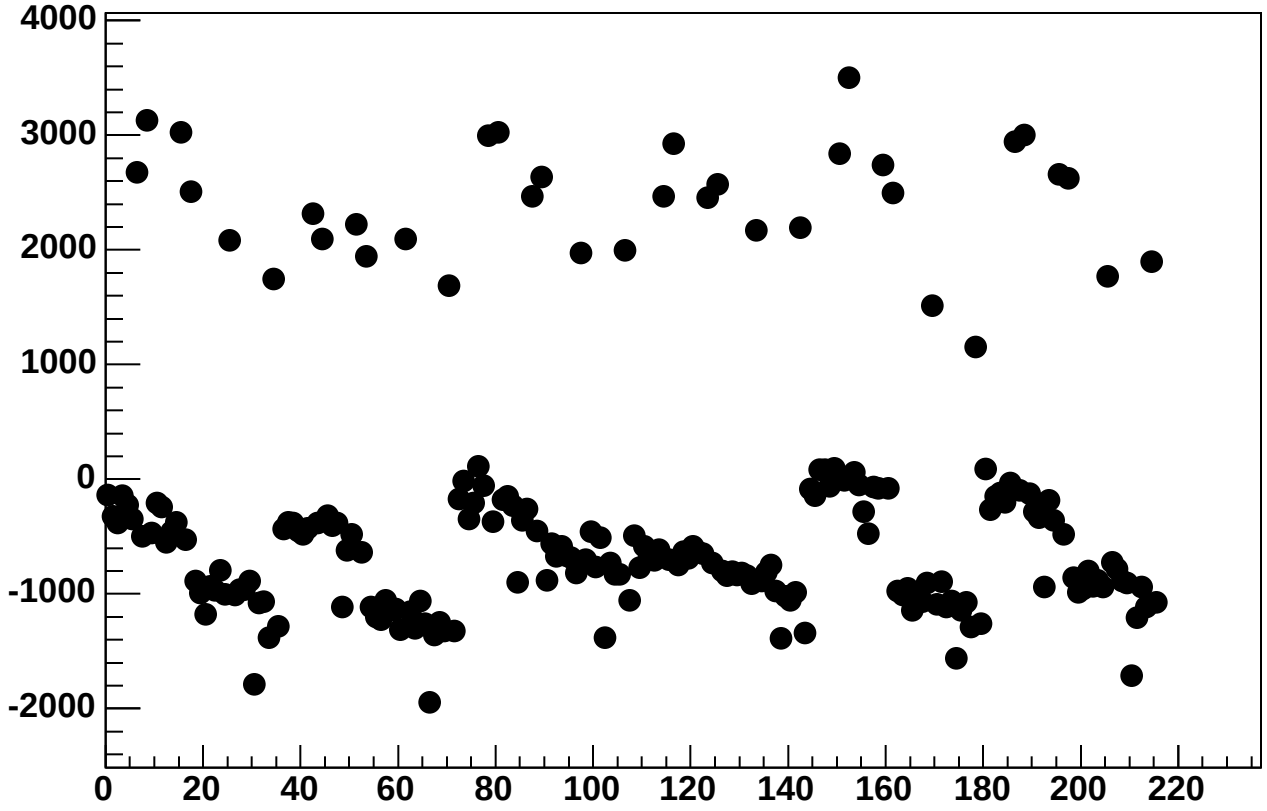
Enable 2, DAC=1600, Hold=110, ADC Mean vs 18*Chip+Chan



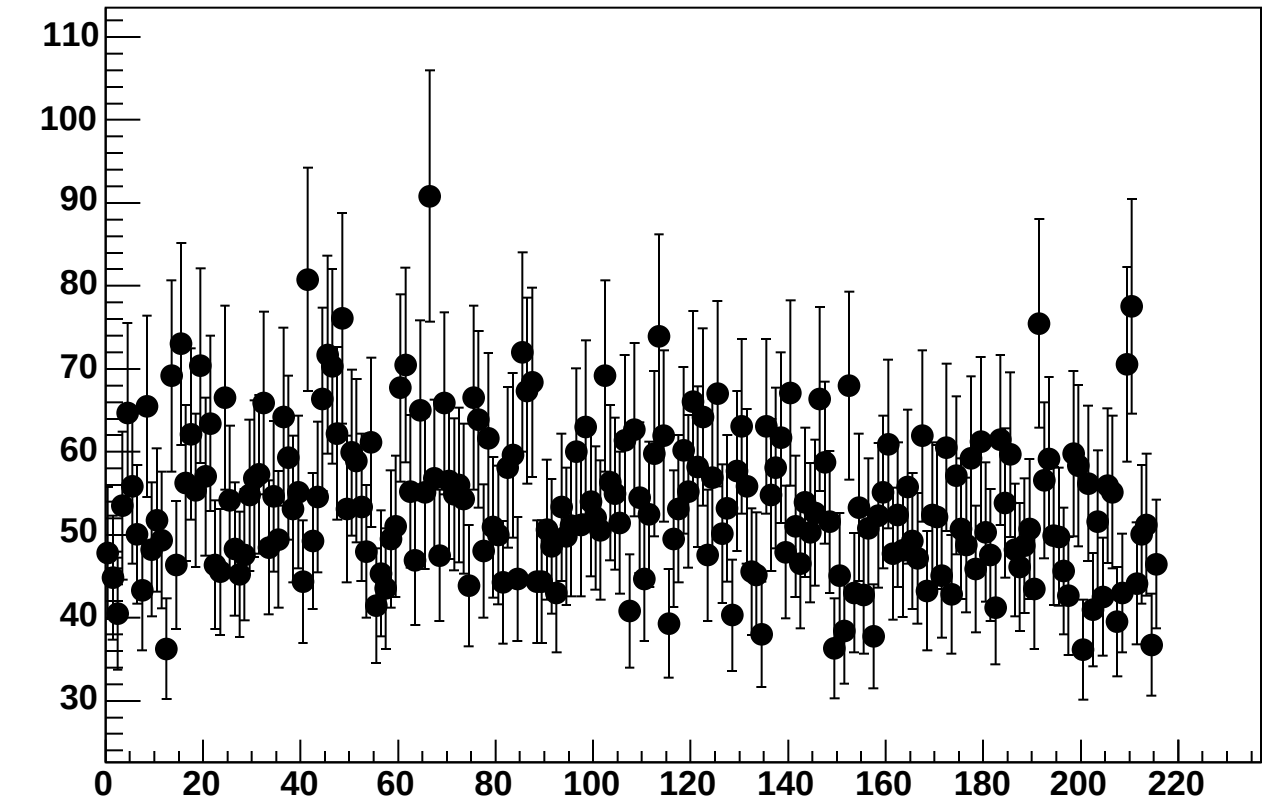
Enable 2, DAC=1600, Hold=110, ADC Noise vs 18*Chip+Chan



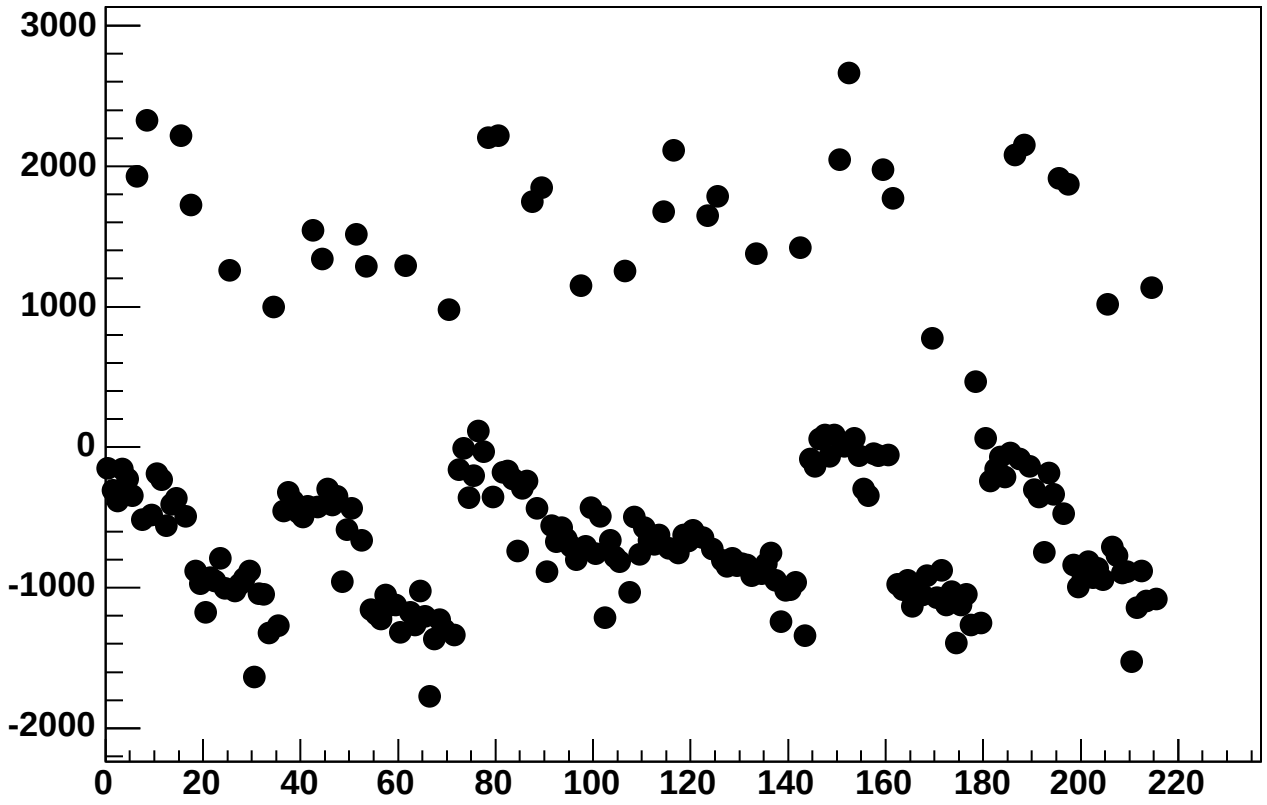
Enable 2, DAC=1600, Hold=115, ADC Mean vs 18*Chip+Chan



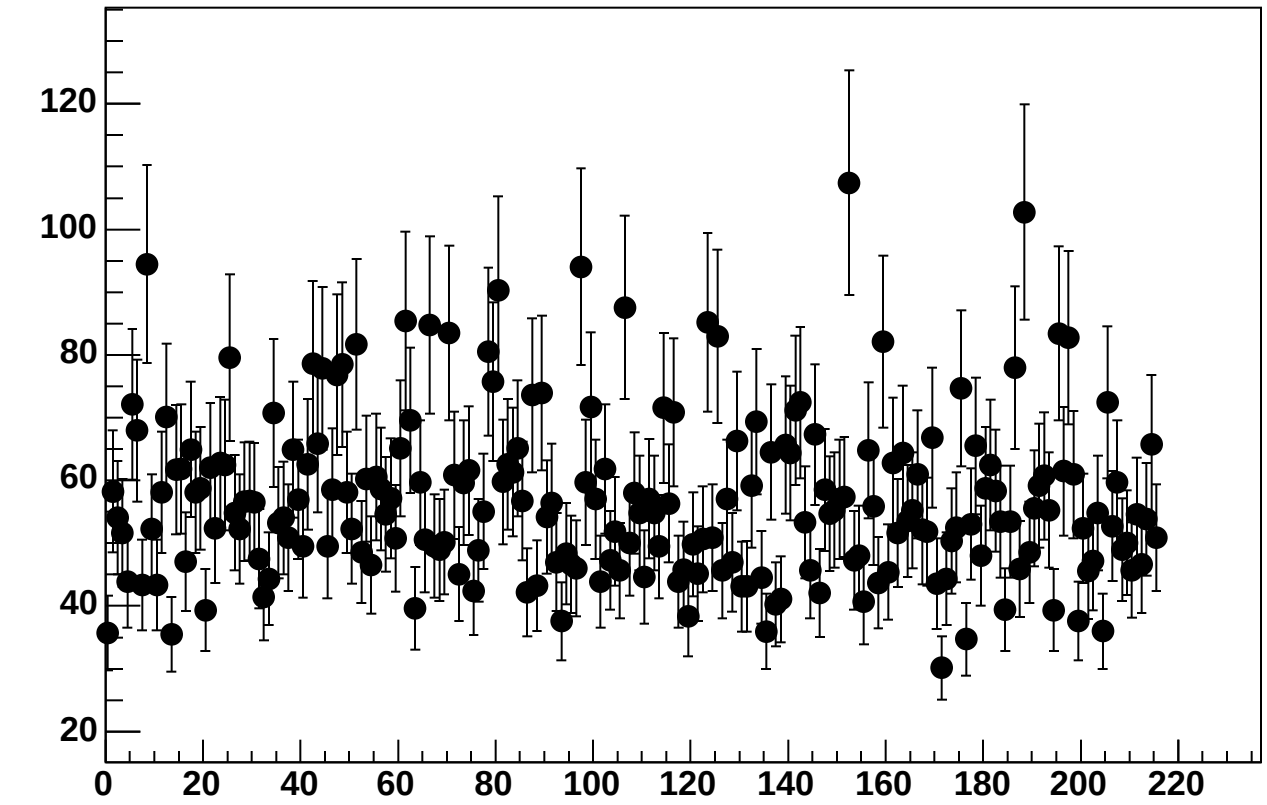
Enable 2, DAC=1600, Hold=115, ADC Noise vs 18*Chip+Chan



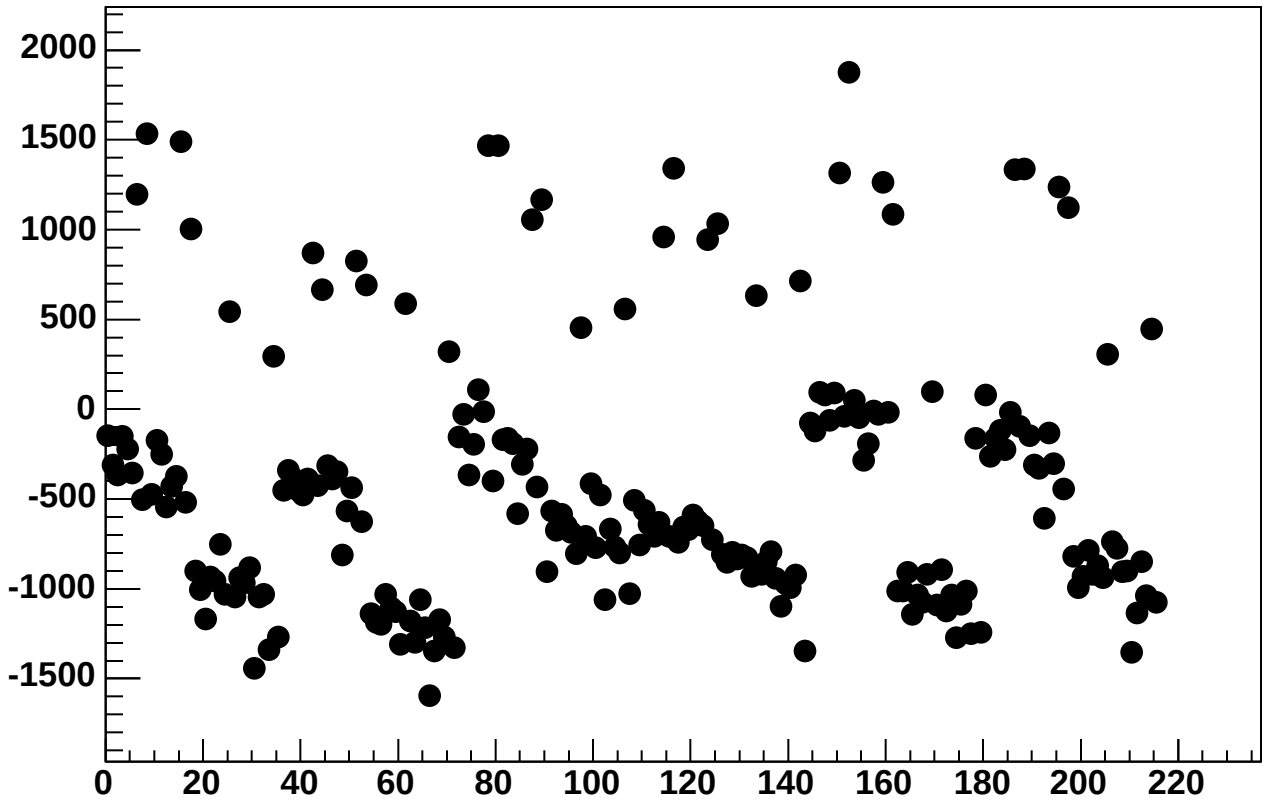
Enable 2, DAC=1600, Hold=120, ADC Mean vs 18*Chip+Chan



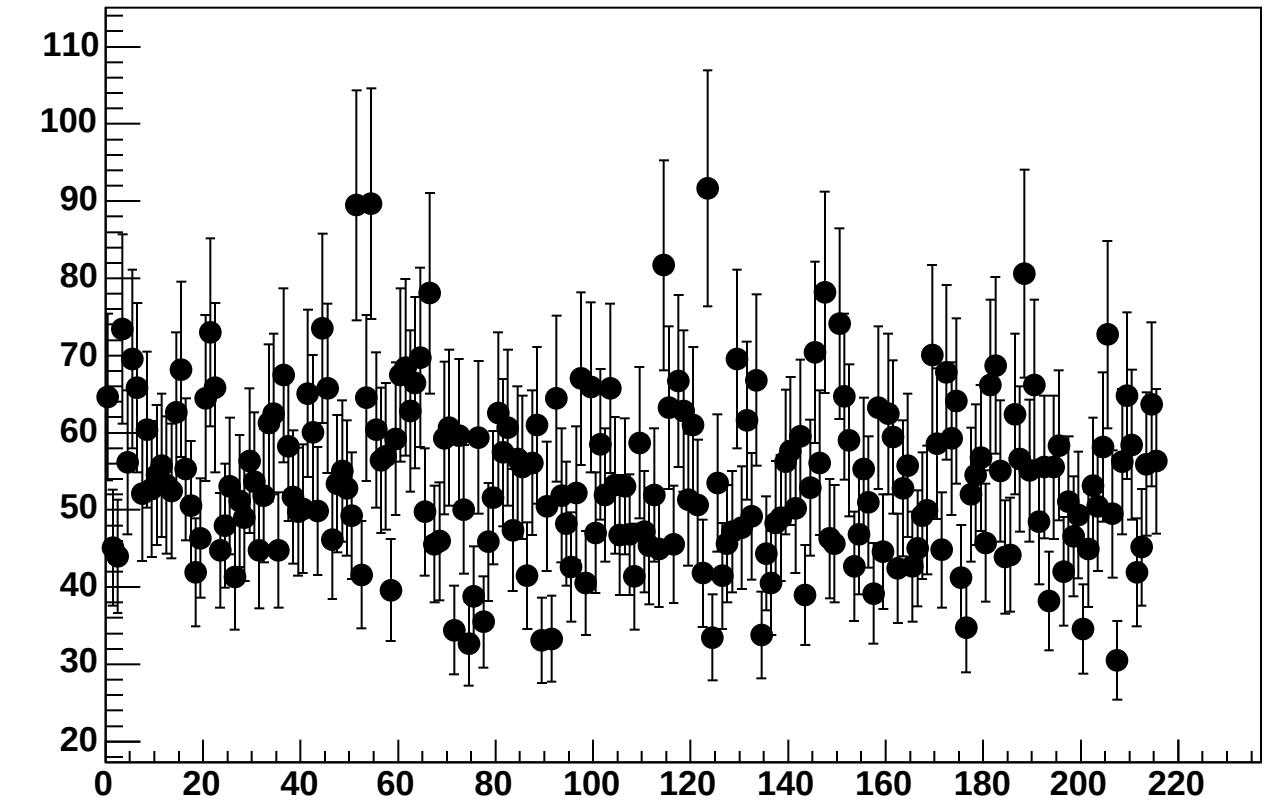
Enable 2, DAC=1600, Hold=120, ADC Noise vs 18*Chip+Chan



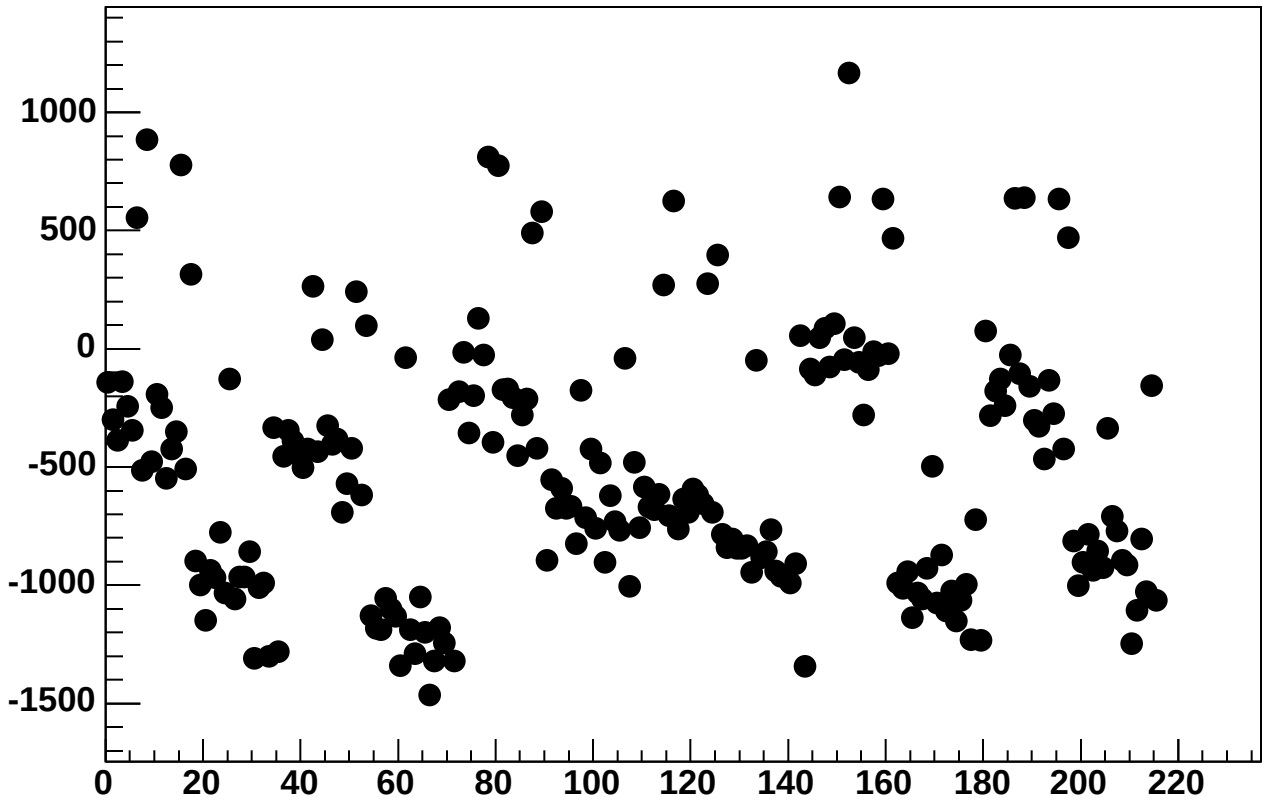
Enable 2, DAC=1600, Hold=125, ADC Mean vs 18*Chip+Chan



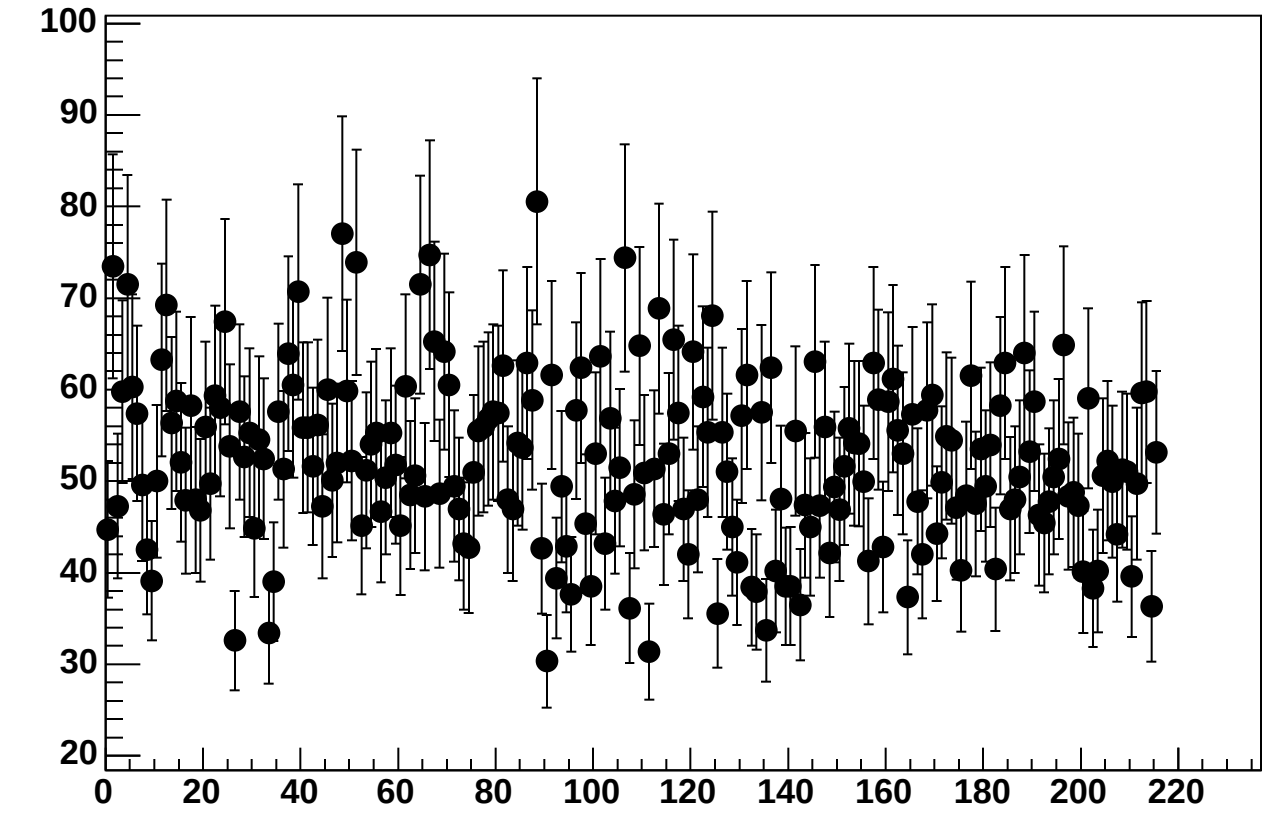
Enable 2, DAC=1600, Hold=125, ADC Noise vs 18*Chip+Chan



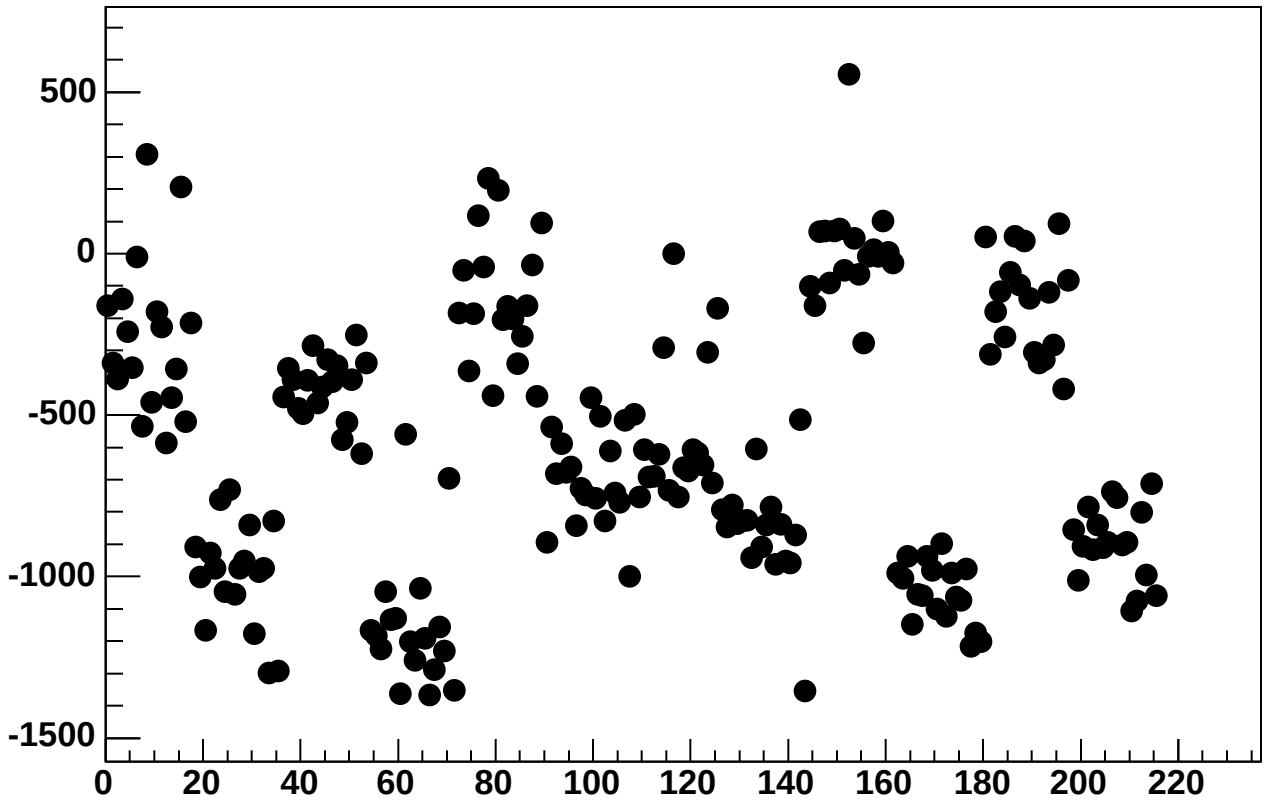
Enable 2, DAC=1600, Hold=130, ADC Mean vs 18*Chip+Chan



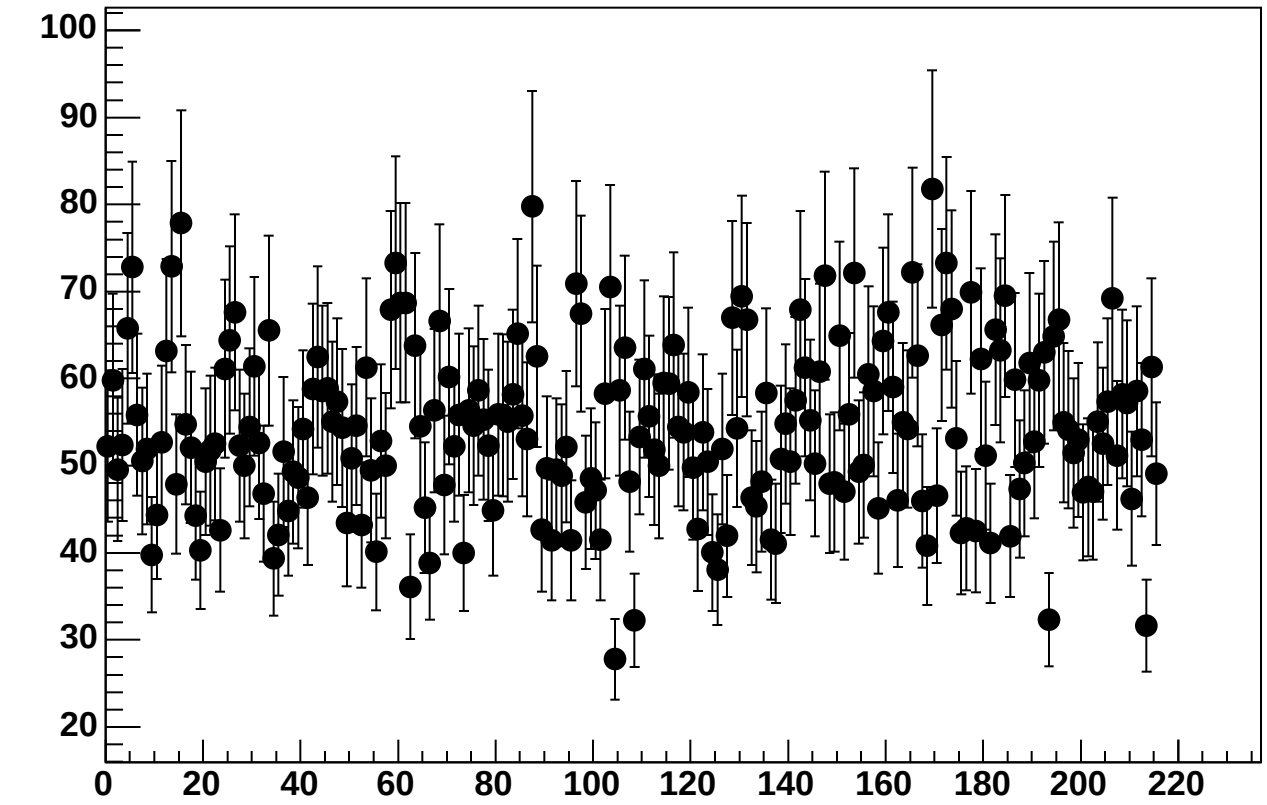
Enable 2, DAC=1600, Hold=130, ADC Noise vs 18*Chip+Chan



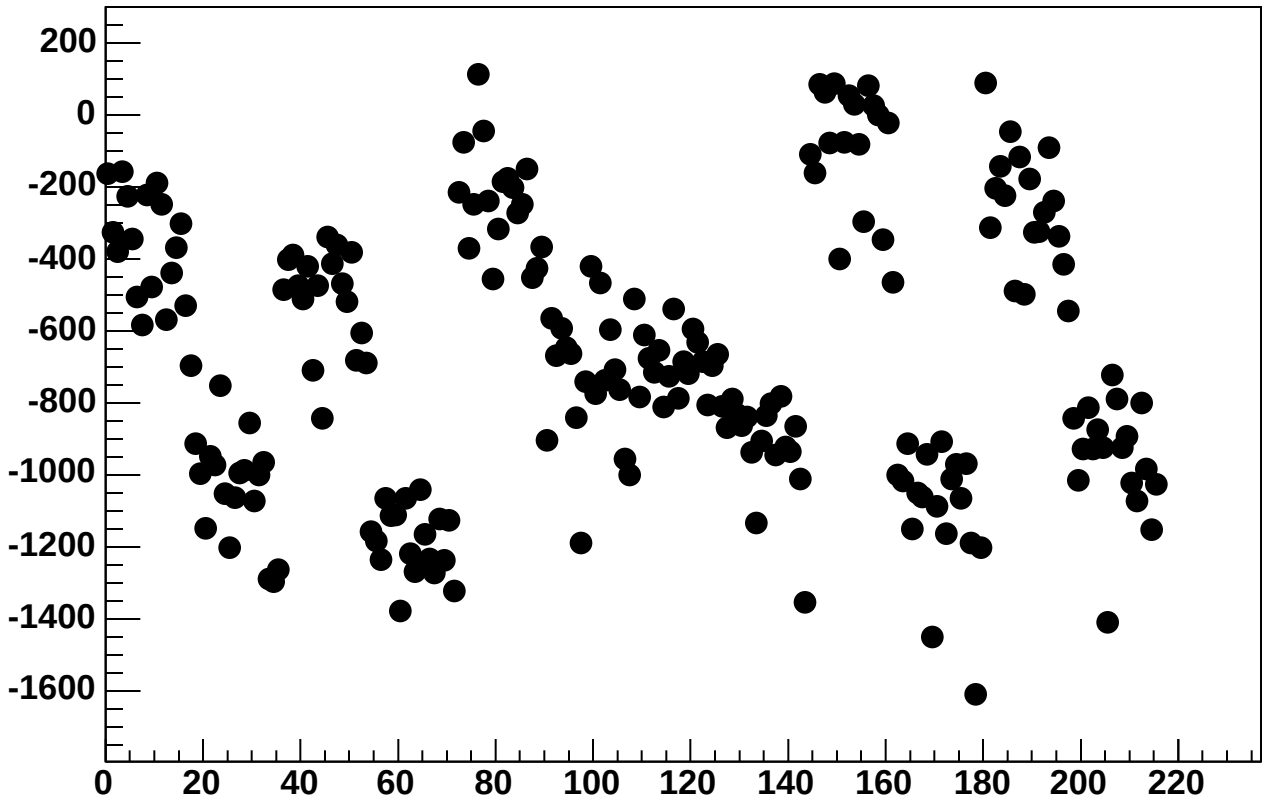
Enable 2, DAC=1600, Hold=135, ADC Mean vs 18*Chip+Chan



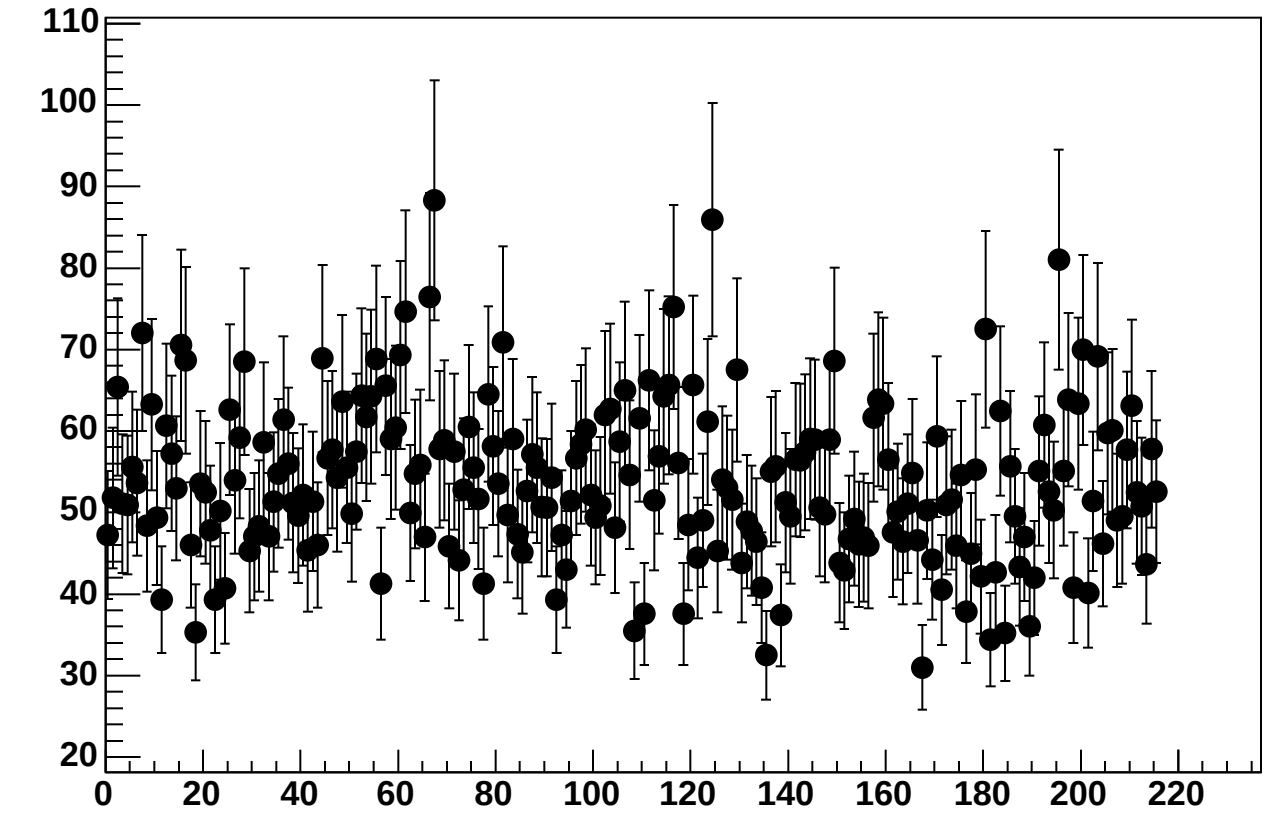
Enable 2, DAC=1600, Hold=135, ADC Noise vs 18*Chip+Chan



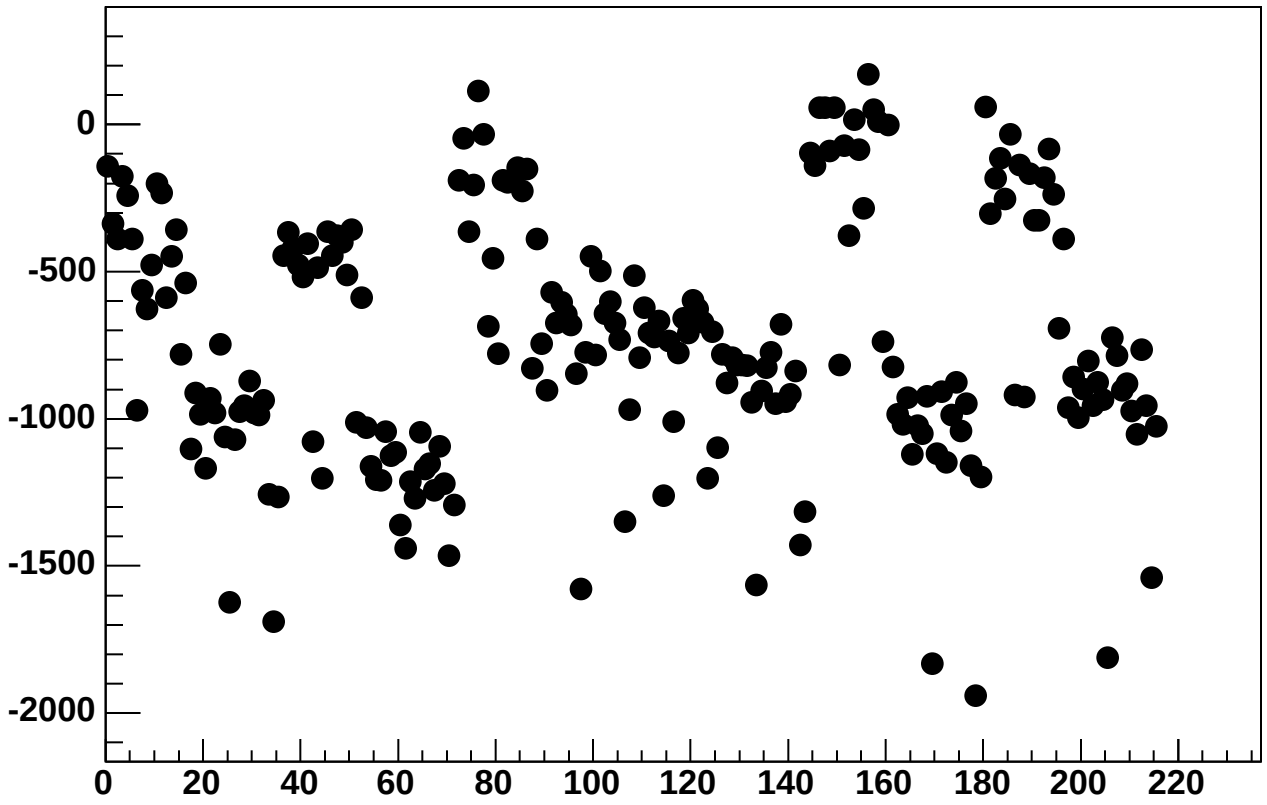
Enable 2, DAC=1600, Hold=140, ADC Mean vs 18*Chip+Chan



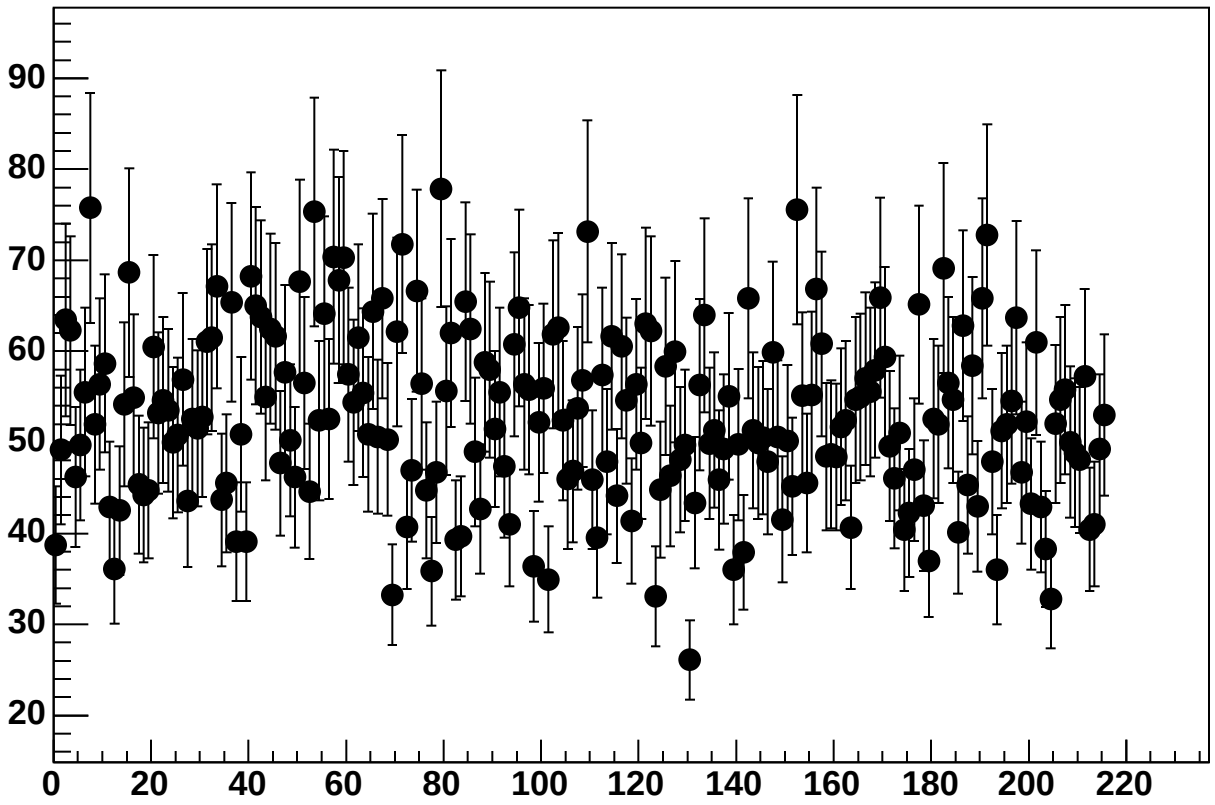
Enable 2, DAC=1600, Hold=140, ADC Noise vs 18*Chip+Chan



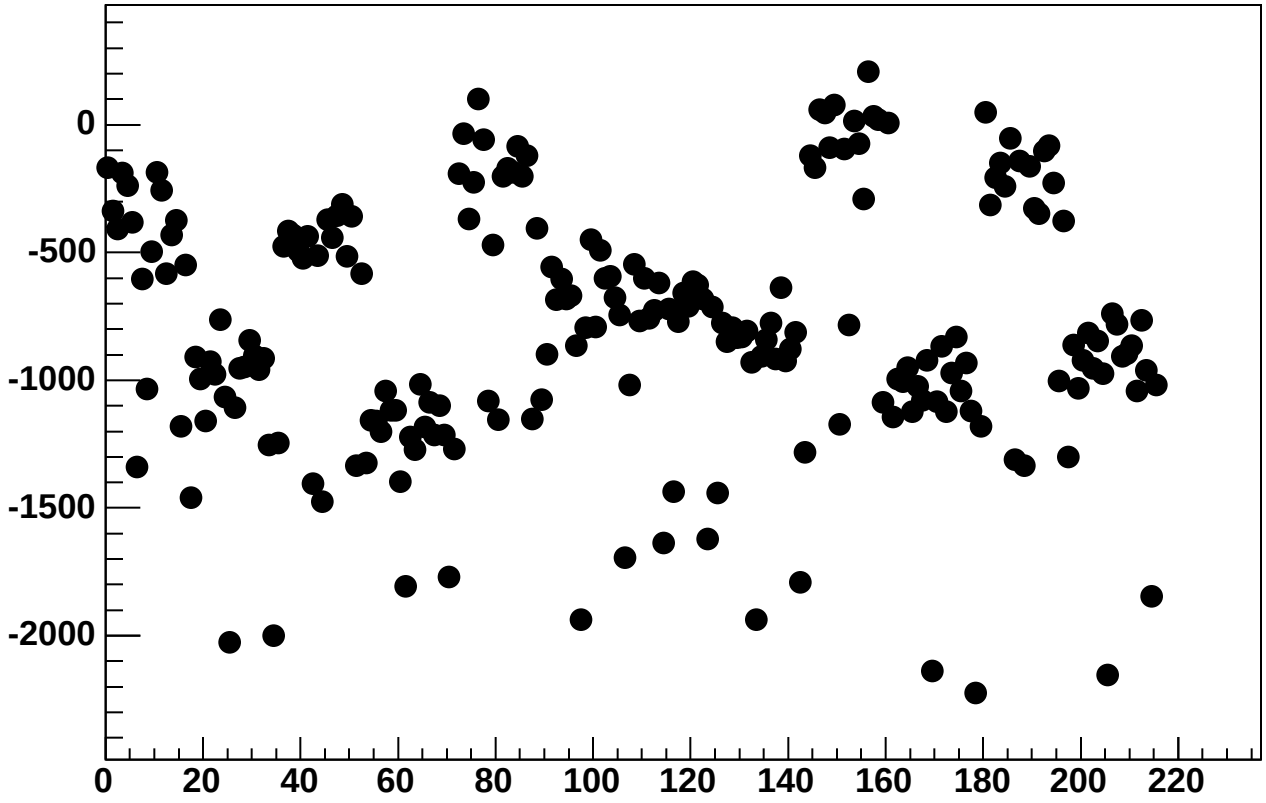
Enable 2, DAC=1600, Hold=145, ADC Mean vs 18*Chip+Chan



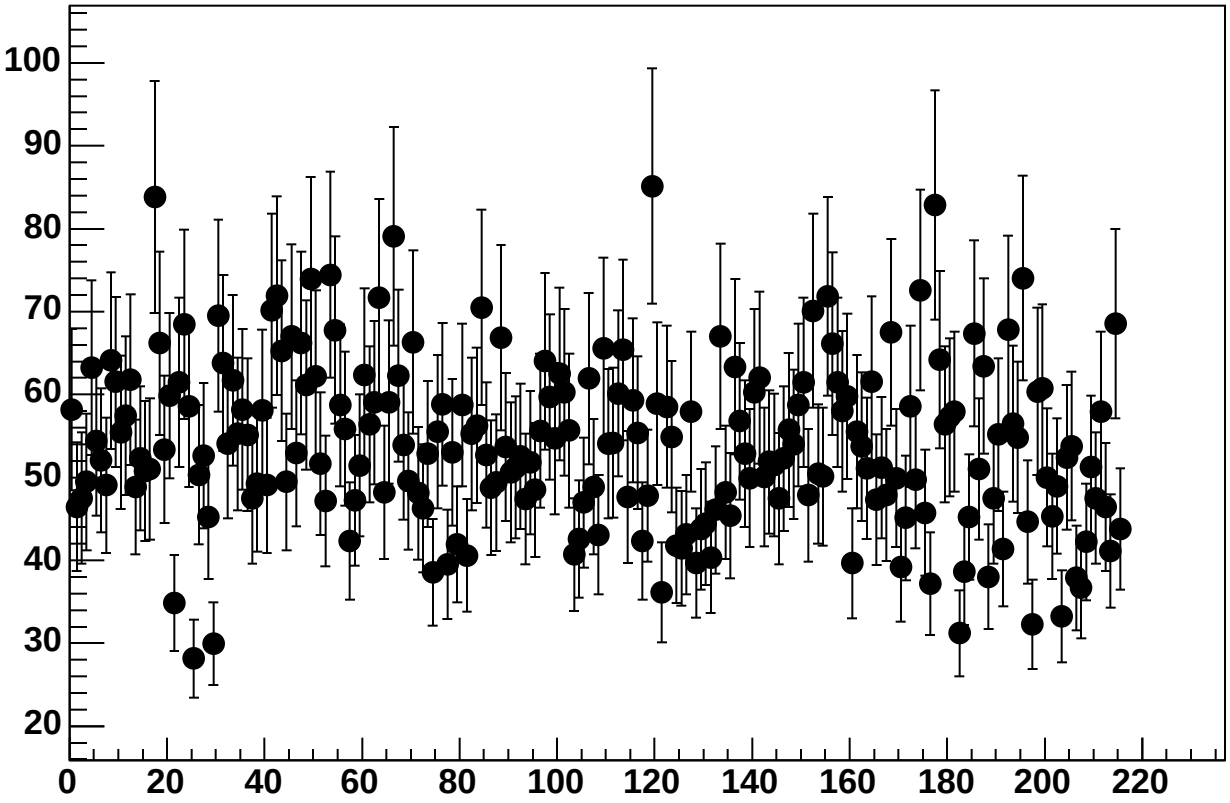
Enable 2, DAC=1600, Hold=145, ADC Noise vs 18*Chip+Chan



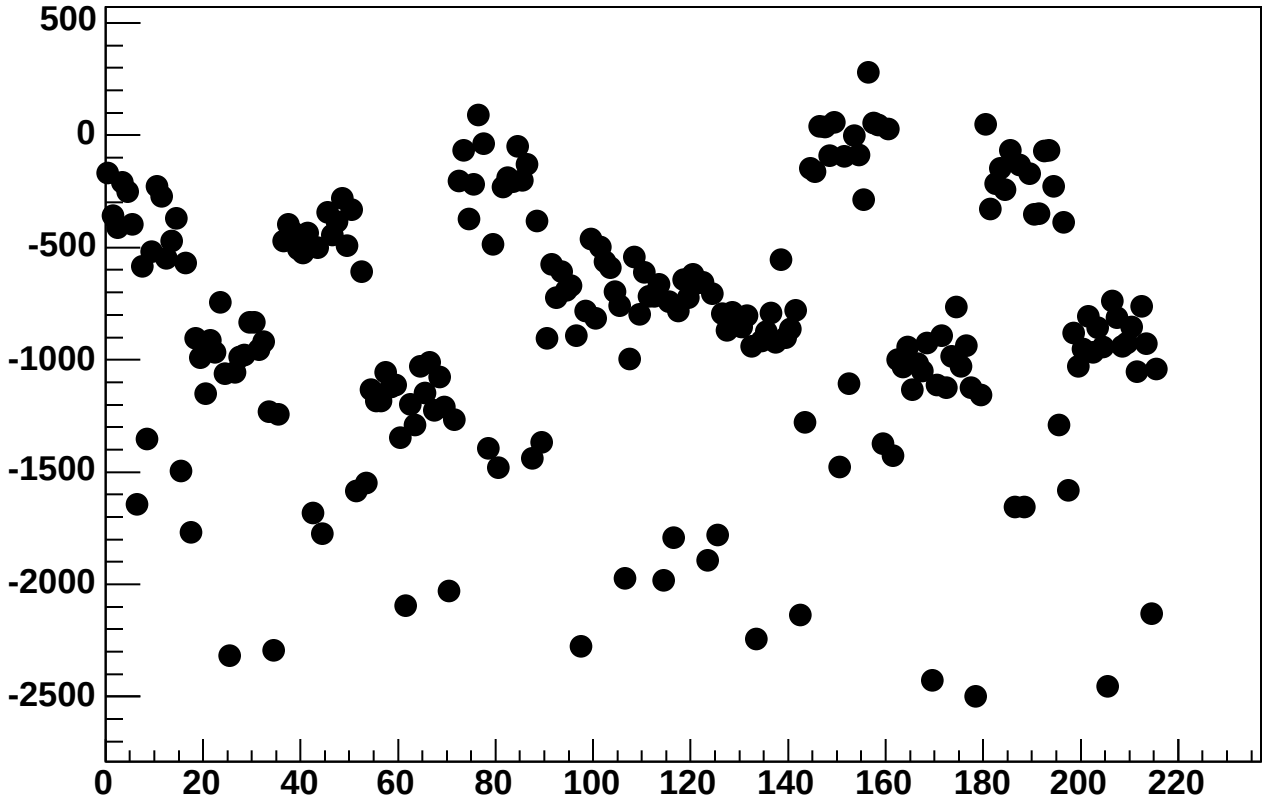
Enable 2, DAC=1600, Hold=150, ADC Mean vs 18*Chip+Chan



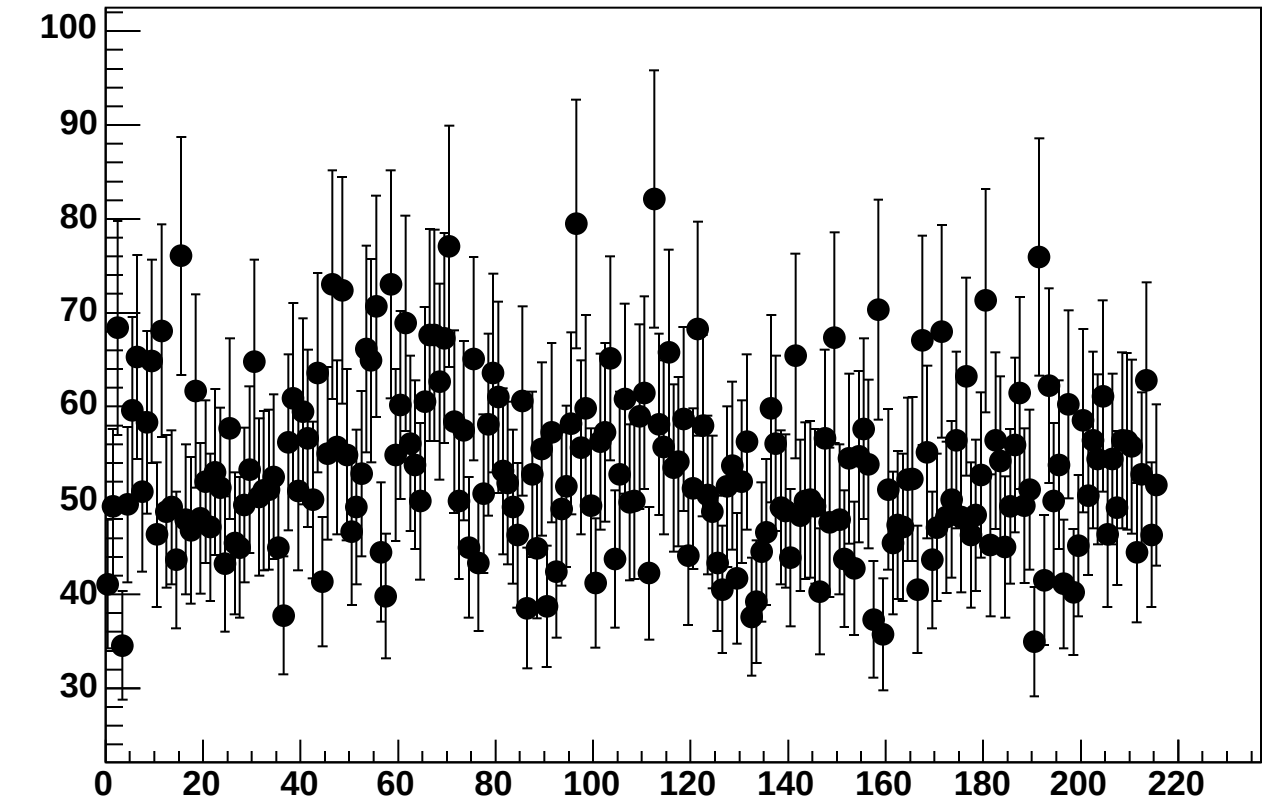
Enable 2, DAC=1600, Hold=150, ADC Noise vs 18*Chip+Chan



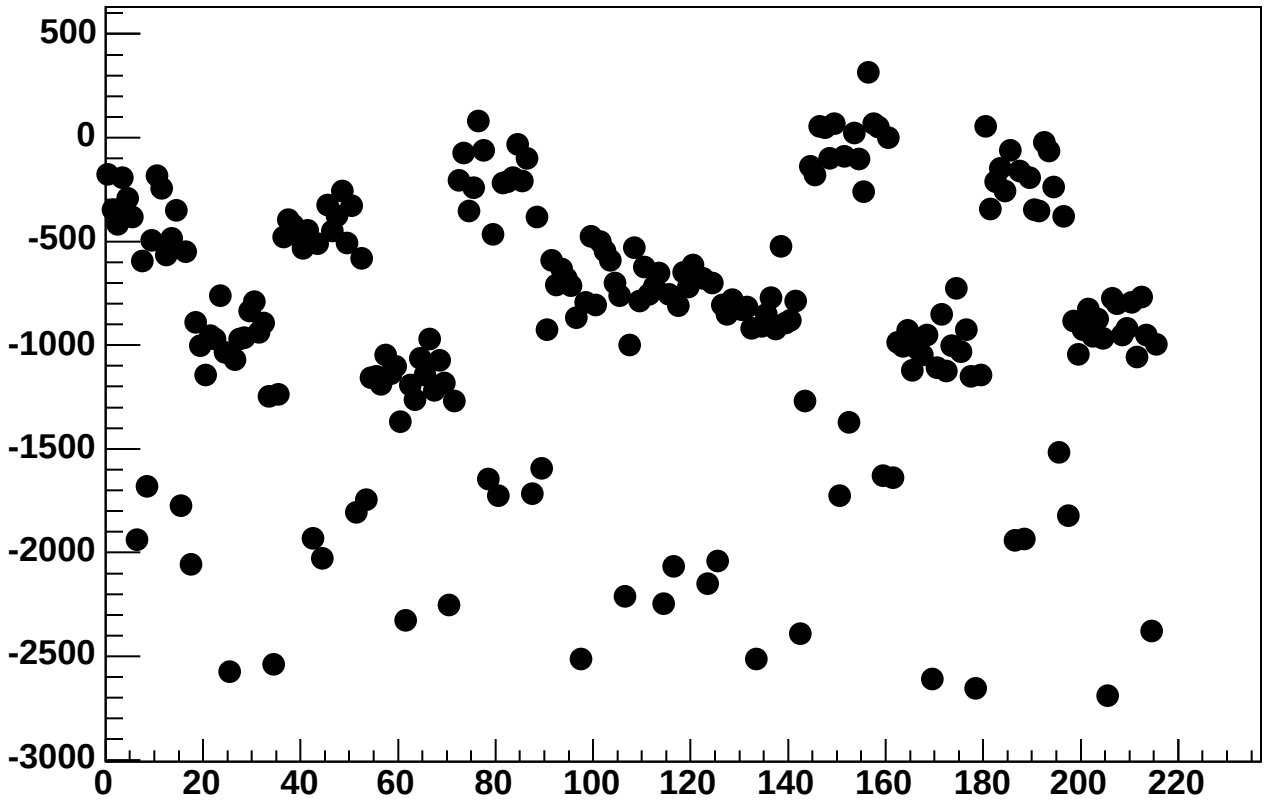
Enable 2, DAC=1600, Hold=155, ADC Mean vs 18*Chip+Chan



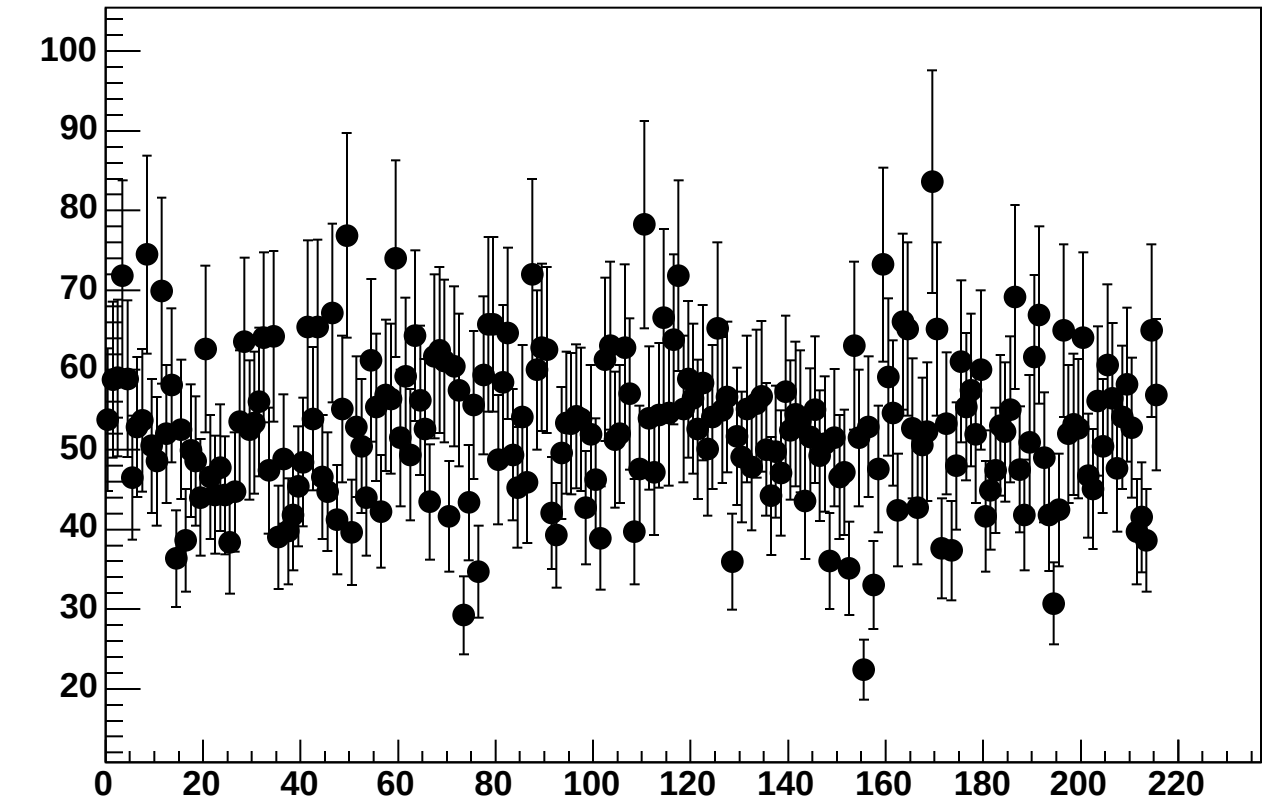
Enable 2, DAC=1600, Hold=155, ADC Noise vs 18*Chip+Chan



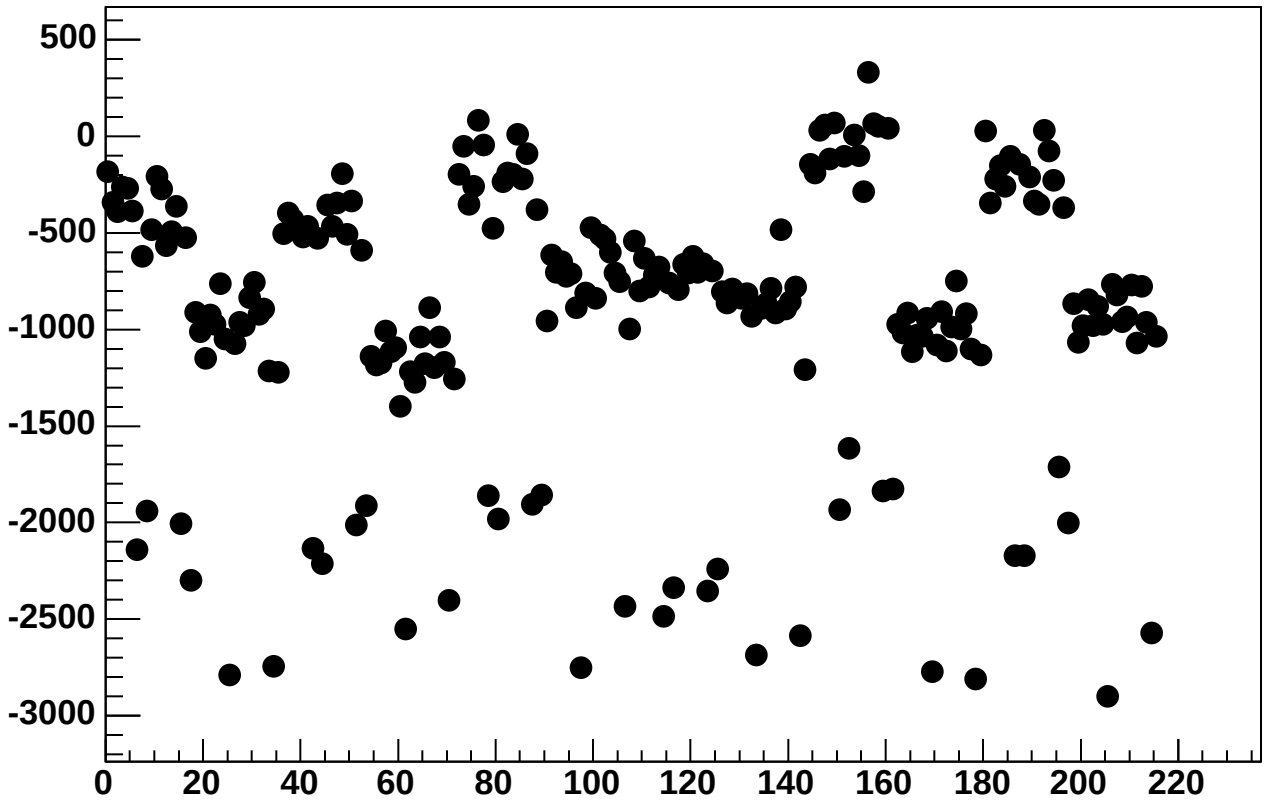
Enable 2, DAC=1600, Hold=160, ADC Mean vs 18*Chip+Chan



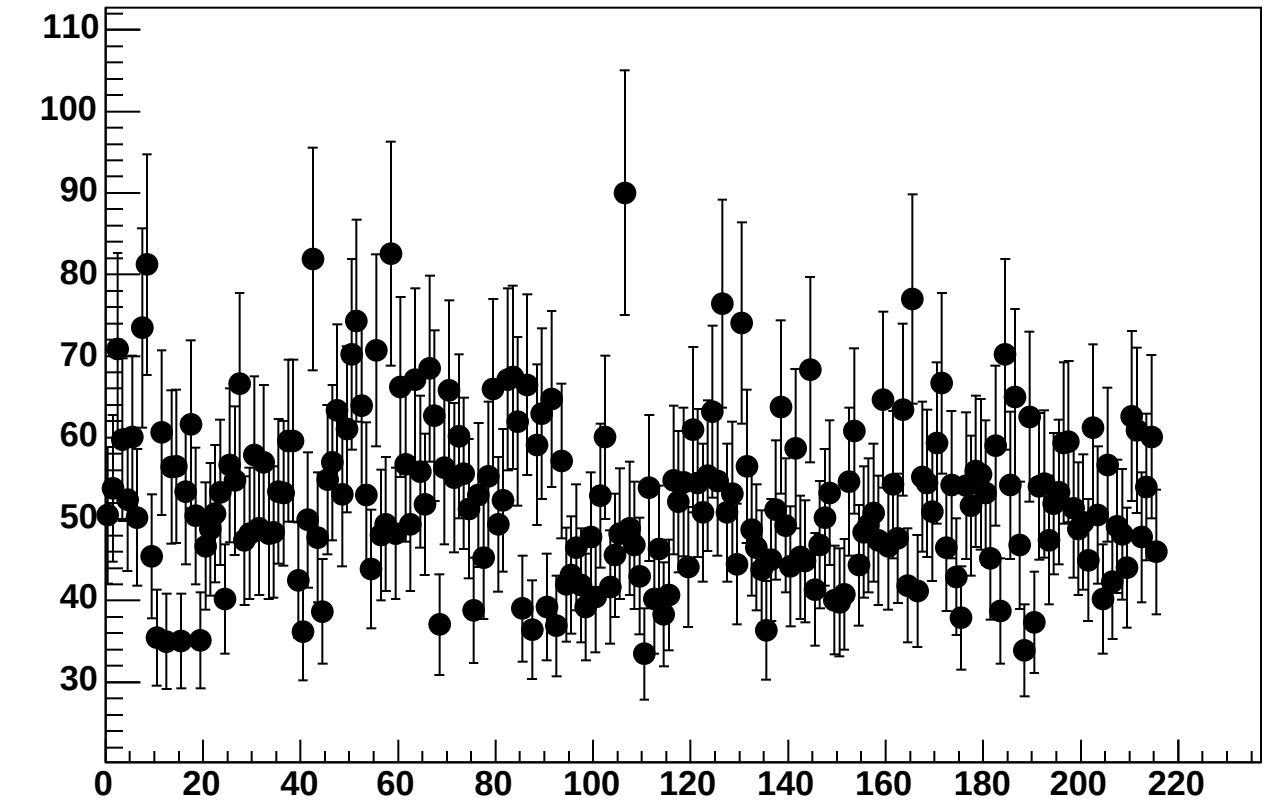
Enable 2, DAC=1600, Hold=160, ADC Noise vs 18*Chip+Chan



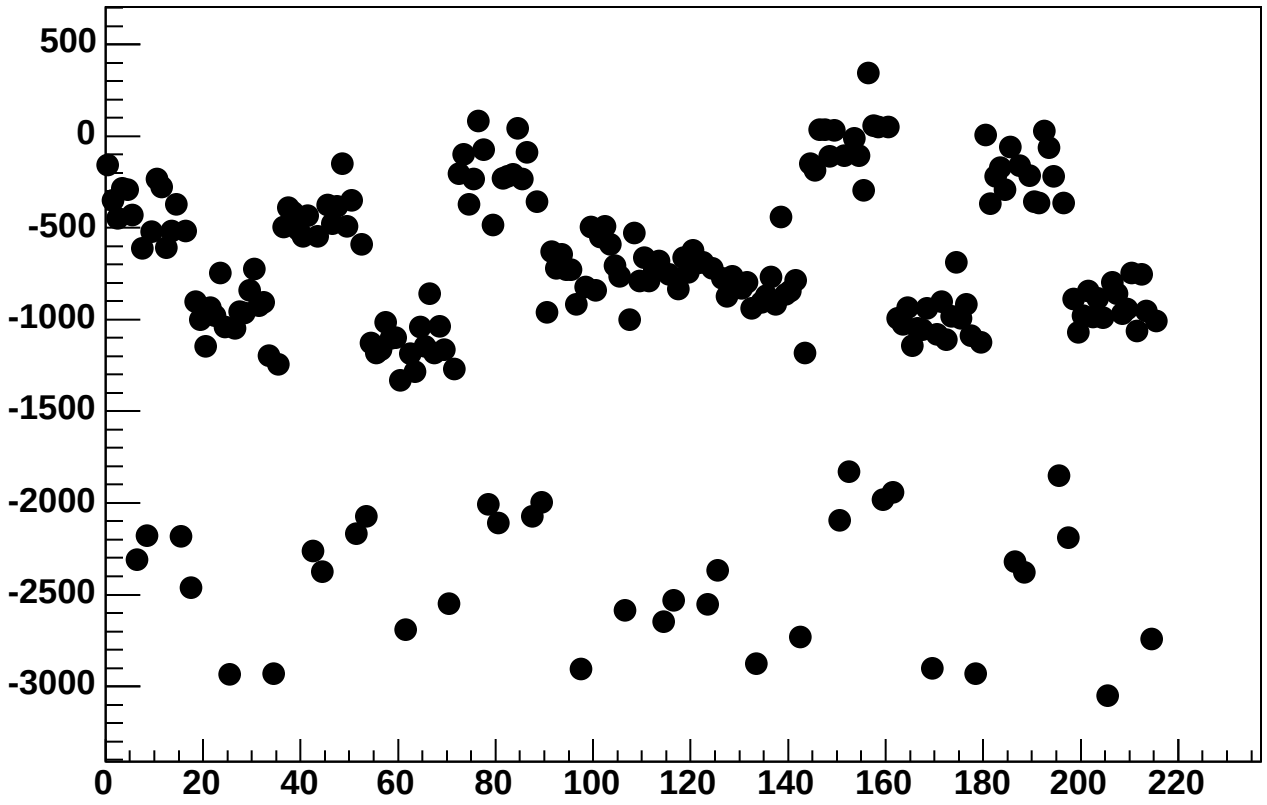
Enable 2, DAC=1600, Hold=165, ADC Mean vs 18*Chip+Chan



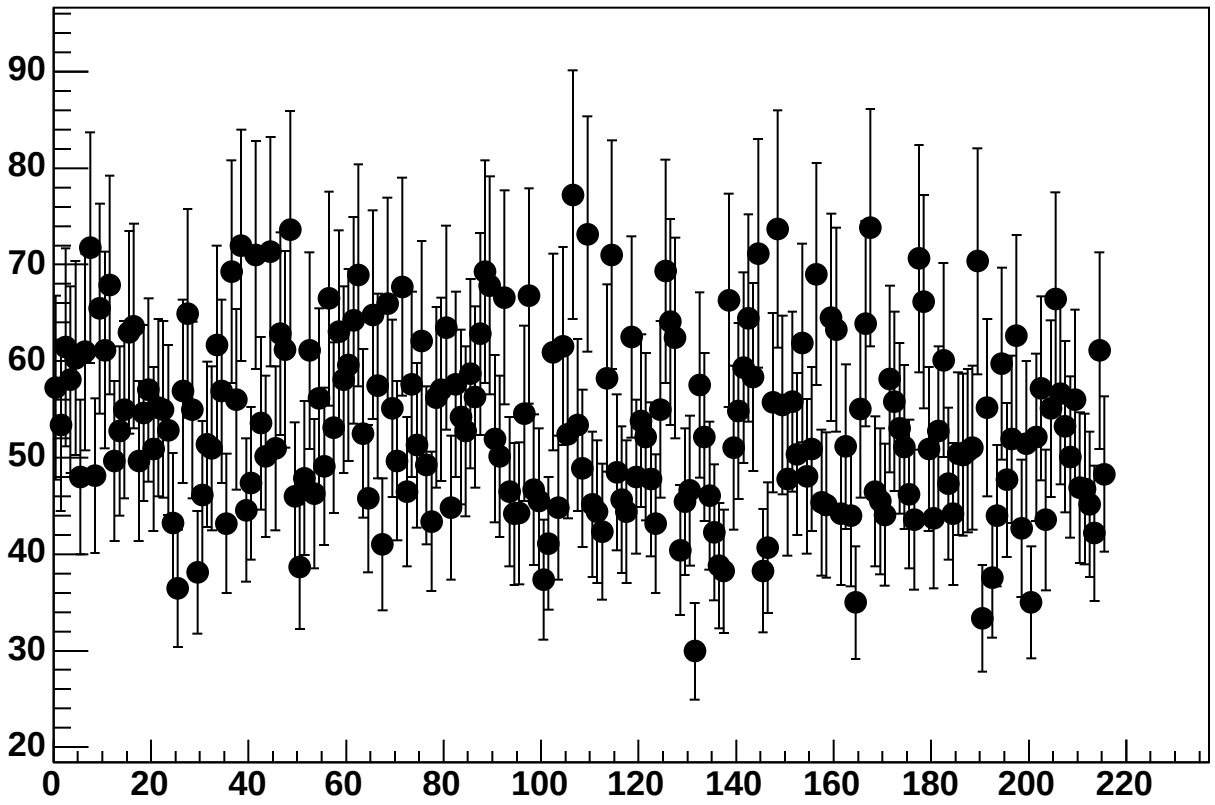
Enable 2, DAC=1600, Hold=165, ADC Noise vs 18*Chip+Chan



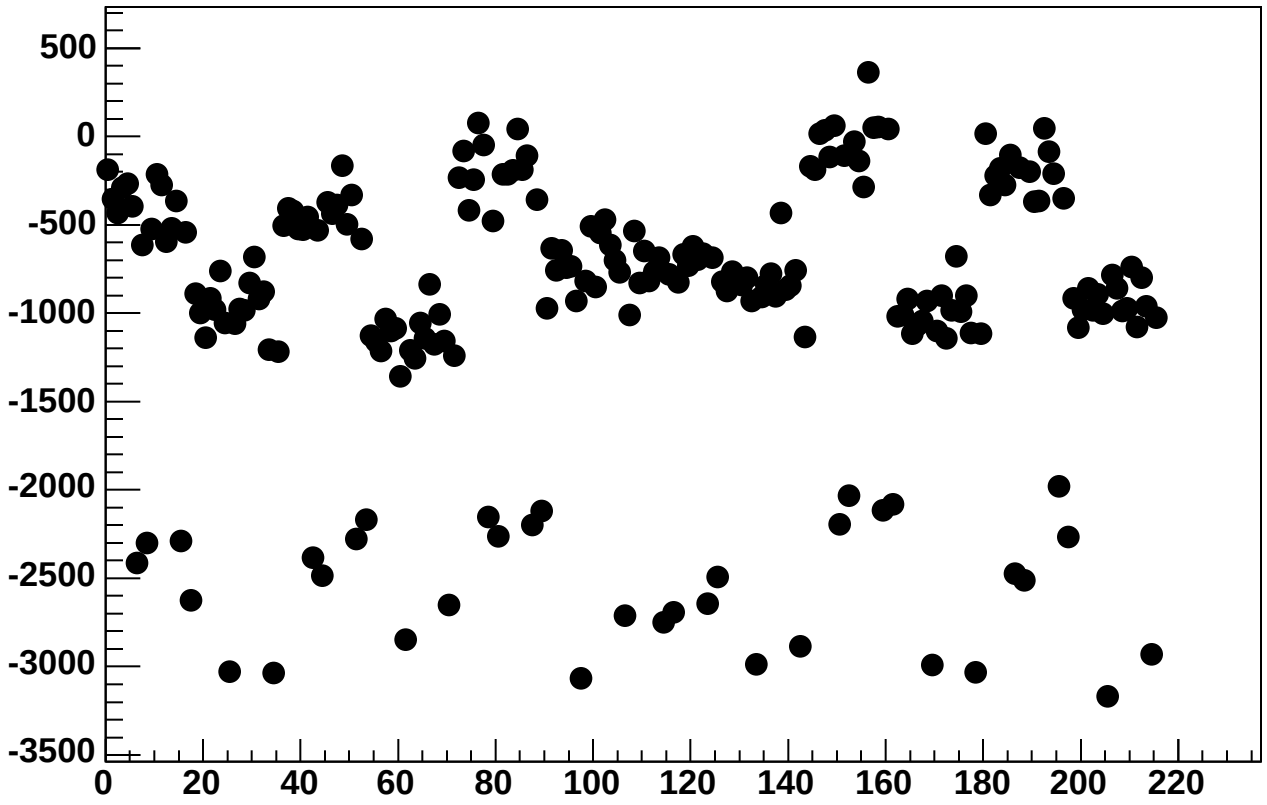
Enable 2, DAC=1600, Hold=170, ADC Mean vs 18*Chip+Chan



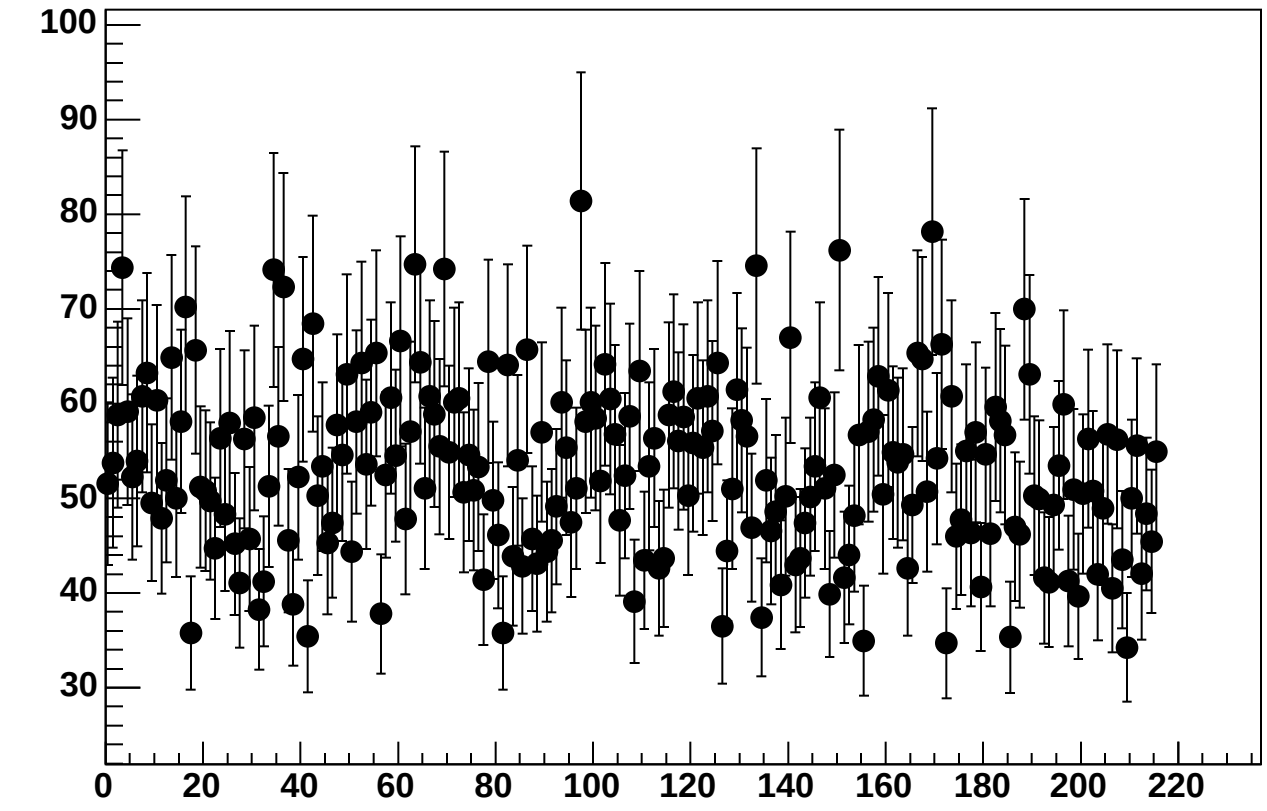
Enable 2, DAC=1600, Hold=170, ADC Noise vs 18*Chip+Chan



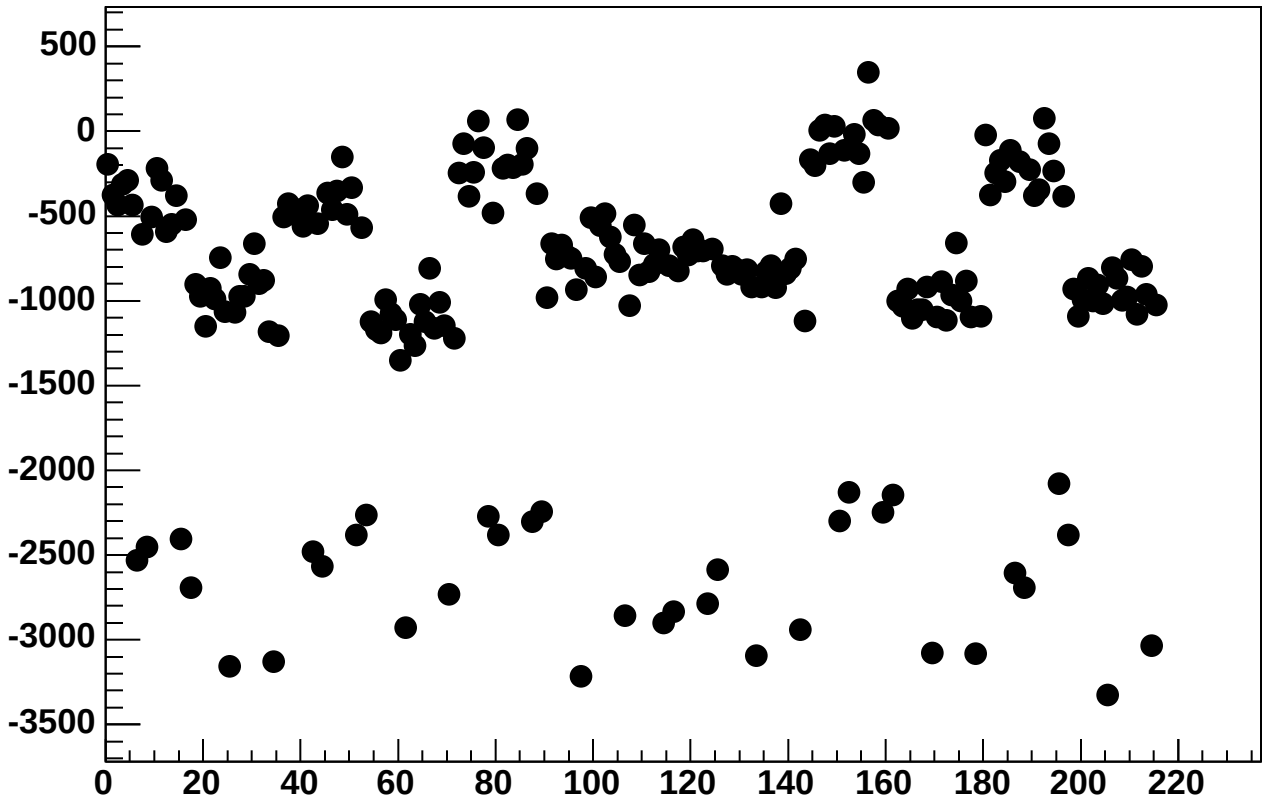
Enable 2, DAC=1600, Hold=175, ADC Mean vs 18*Chip+Chan



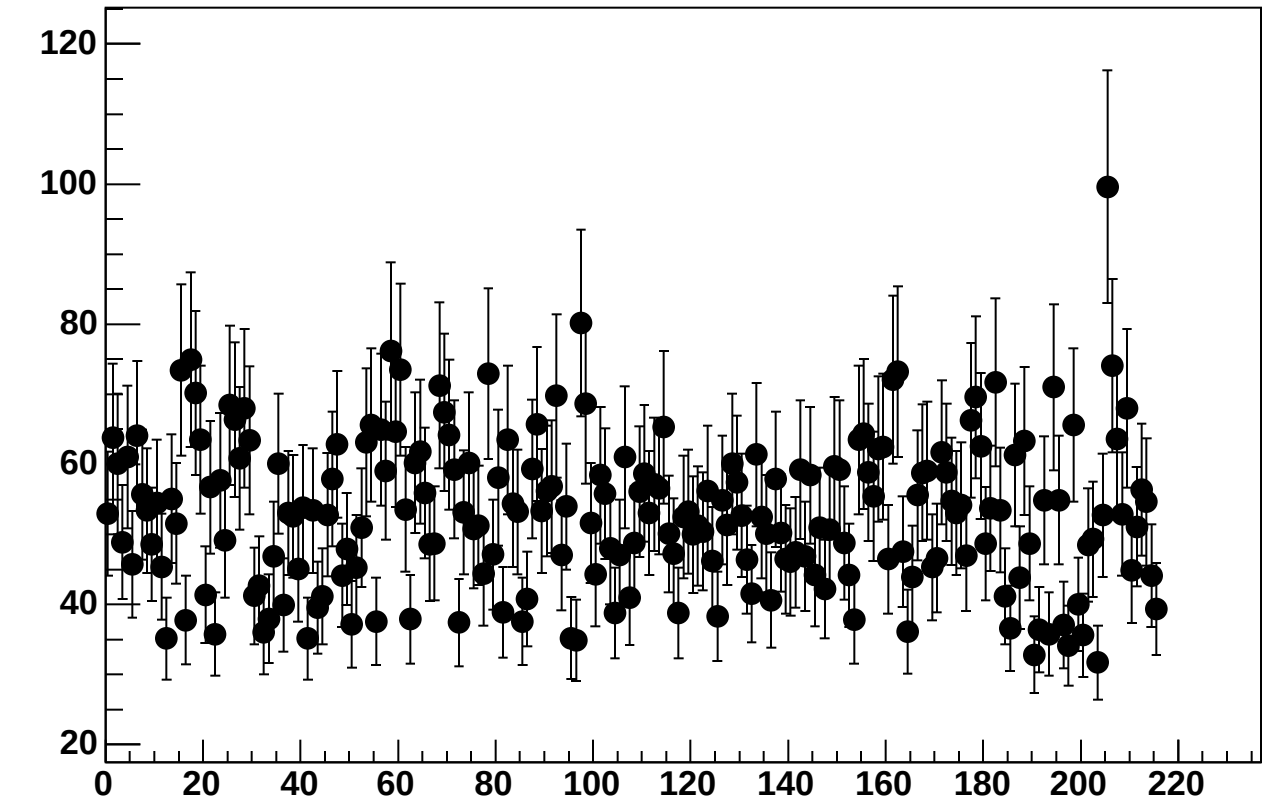
Enable 2, DAC=1600, Hold=175, ADC Noise vs 18*Chip+Chan



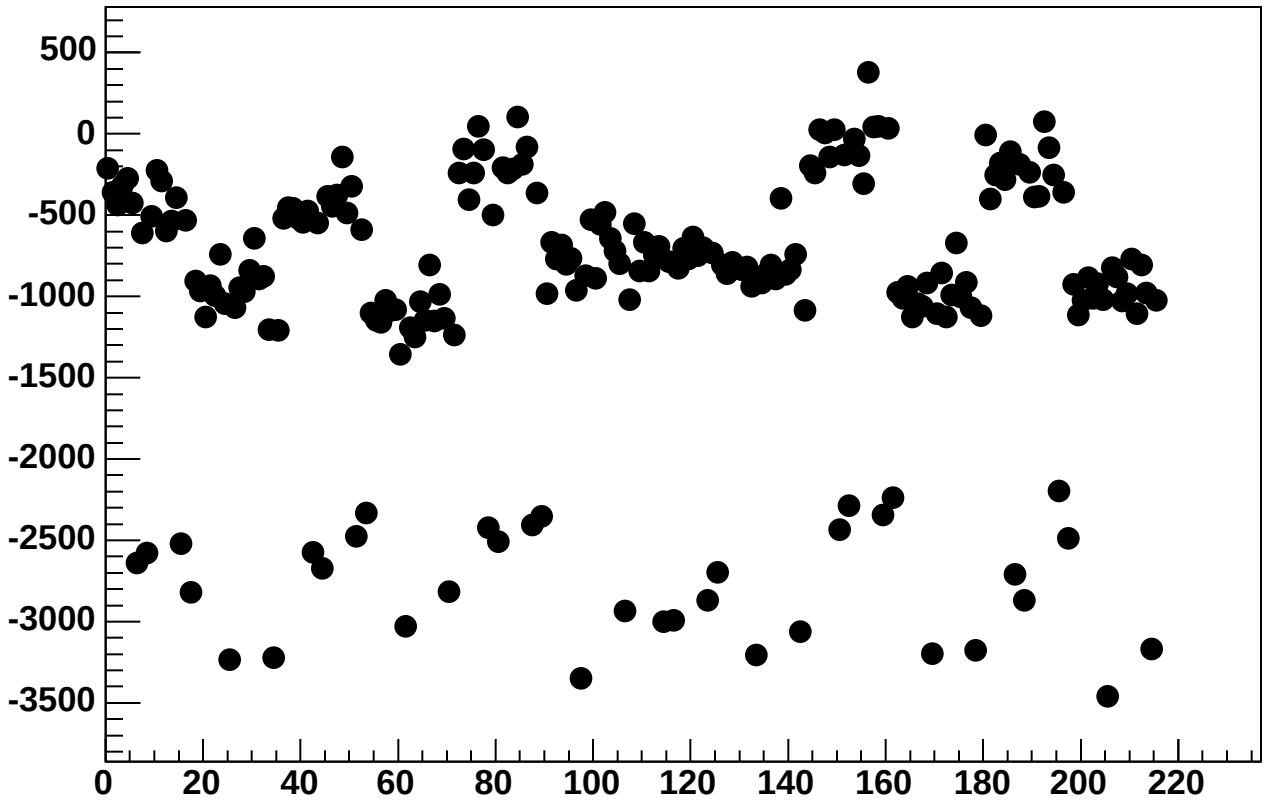
Enable 2, DAC=1600, Hold=180, ADC Mean vs 18*Chip+Chan



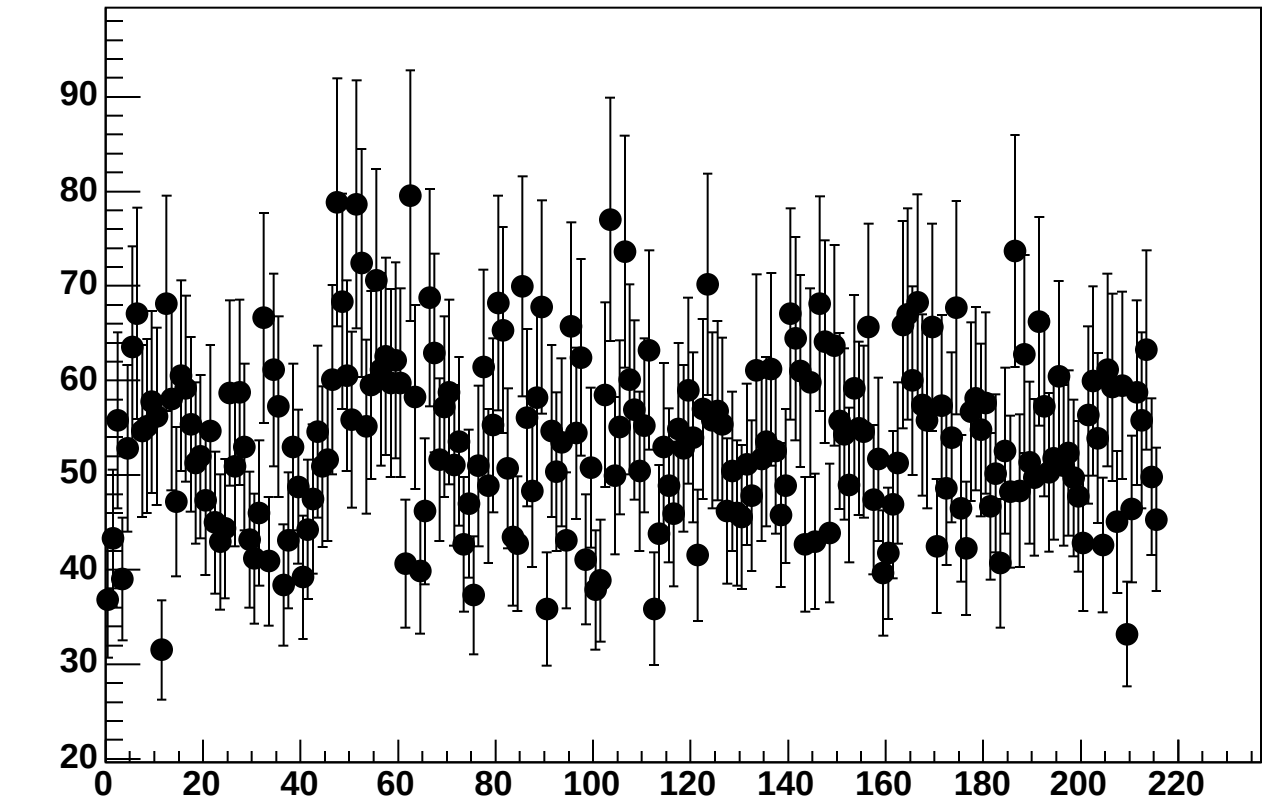
Enable 2, DAC=1600, Hold=180, ADC Noise vs 18*Chip+Chan



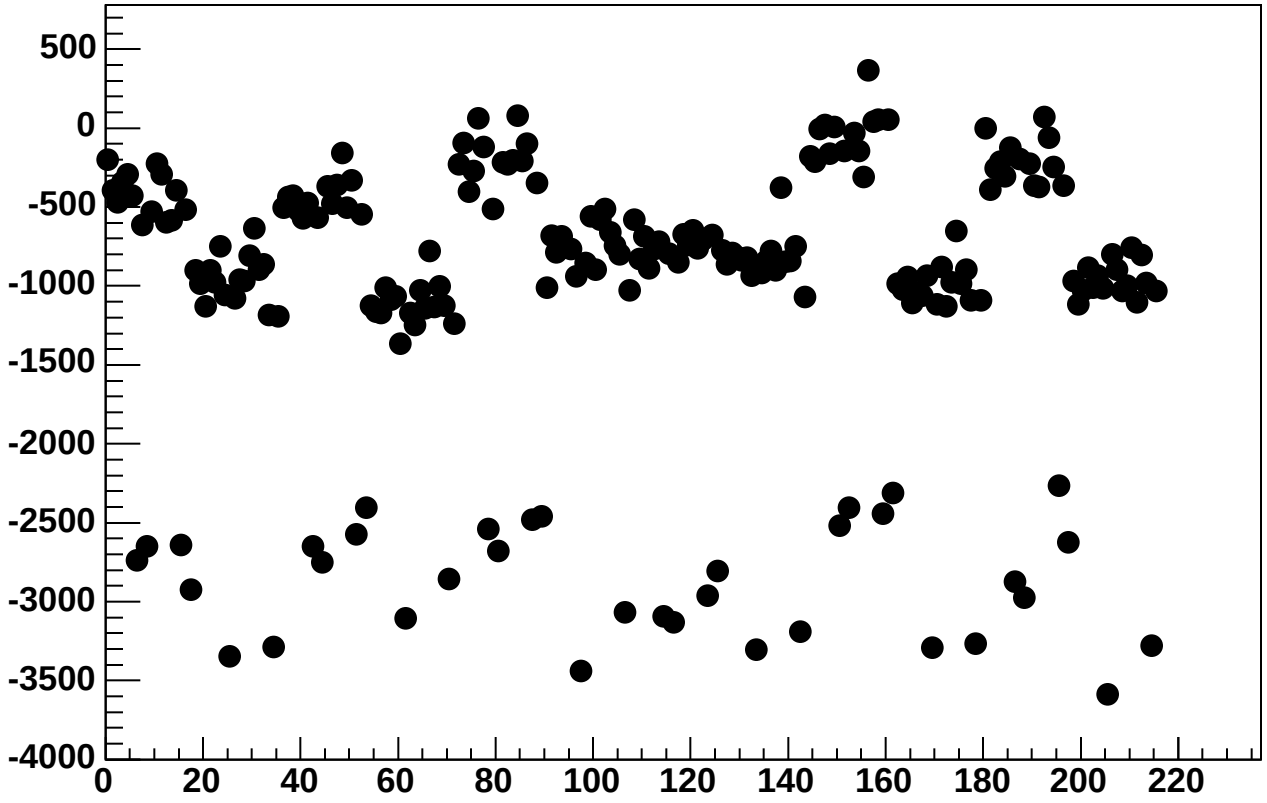
Enable 2, DAC=1600, Hold=185, ADC Mean vs 18*Chip+Chan



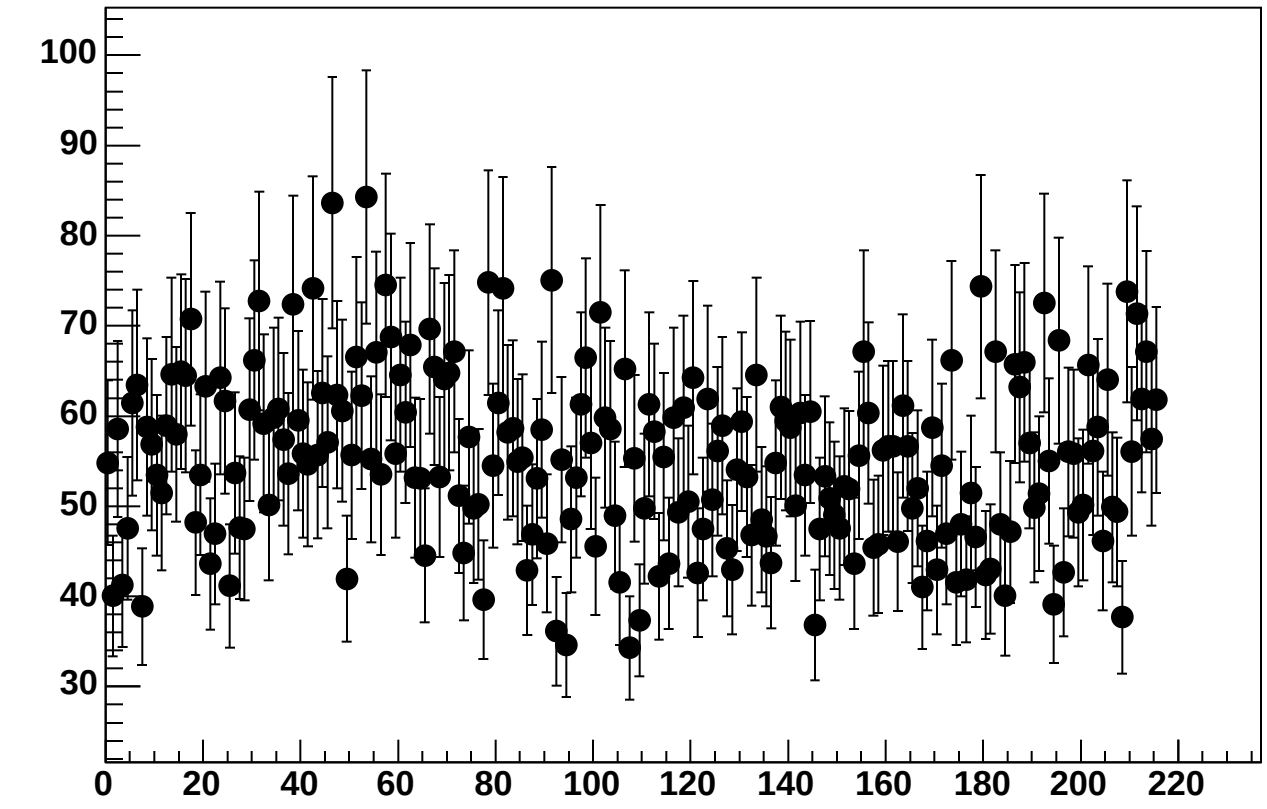
Enable 2, DAC=1600, Hold=185, ADC Noise vs 18*Chip+Chan



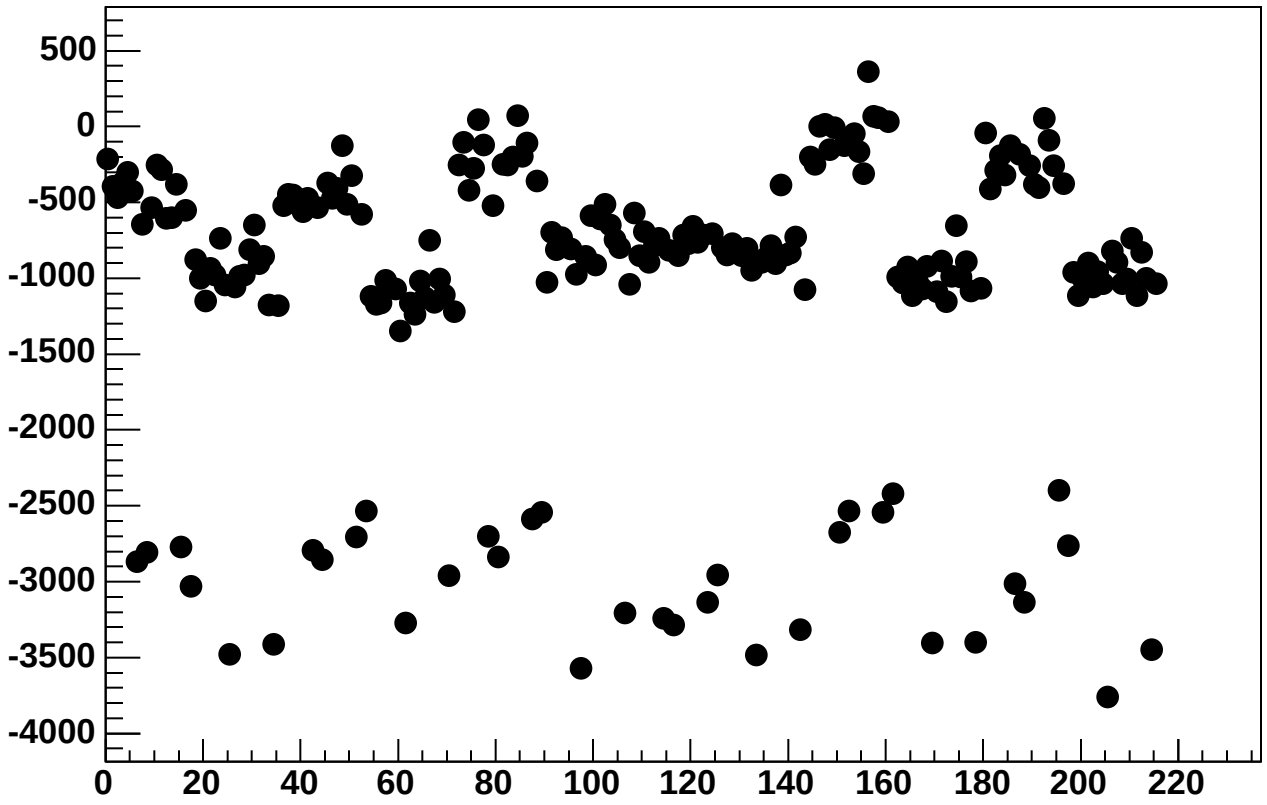
Enable 2, DAC=1600, Hold=190, ADC Mean vs 18*Chip+Chan



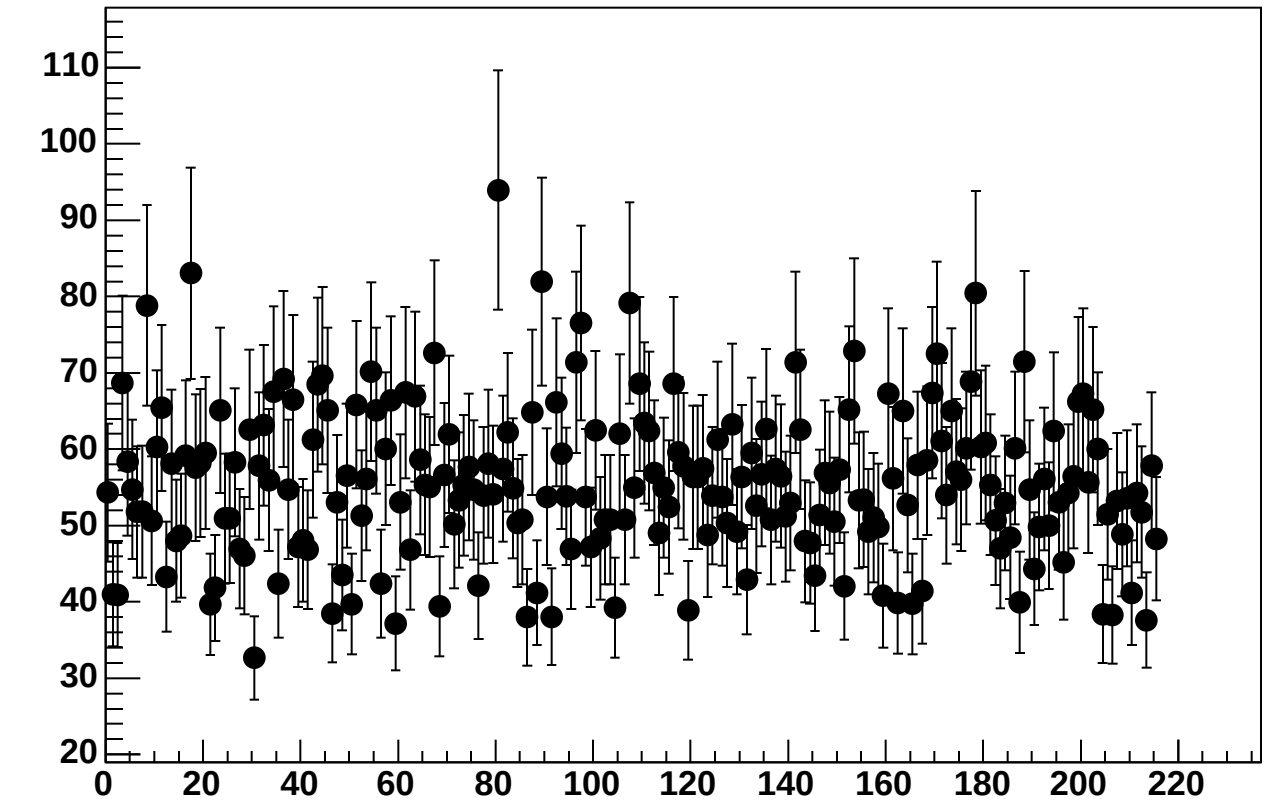
Enable 2, DAC=1600, Hold=190, ADC Noise vs 18*Chip+Chan



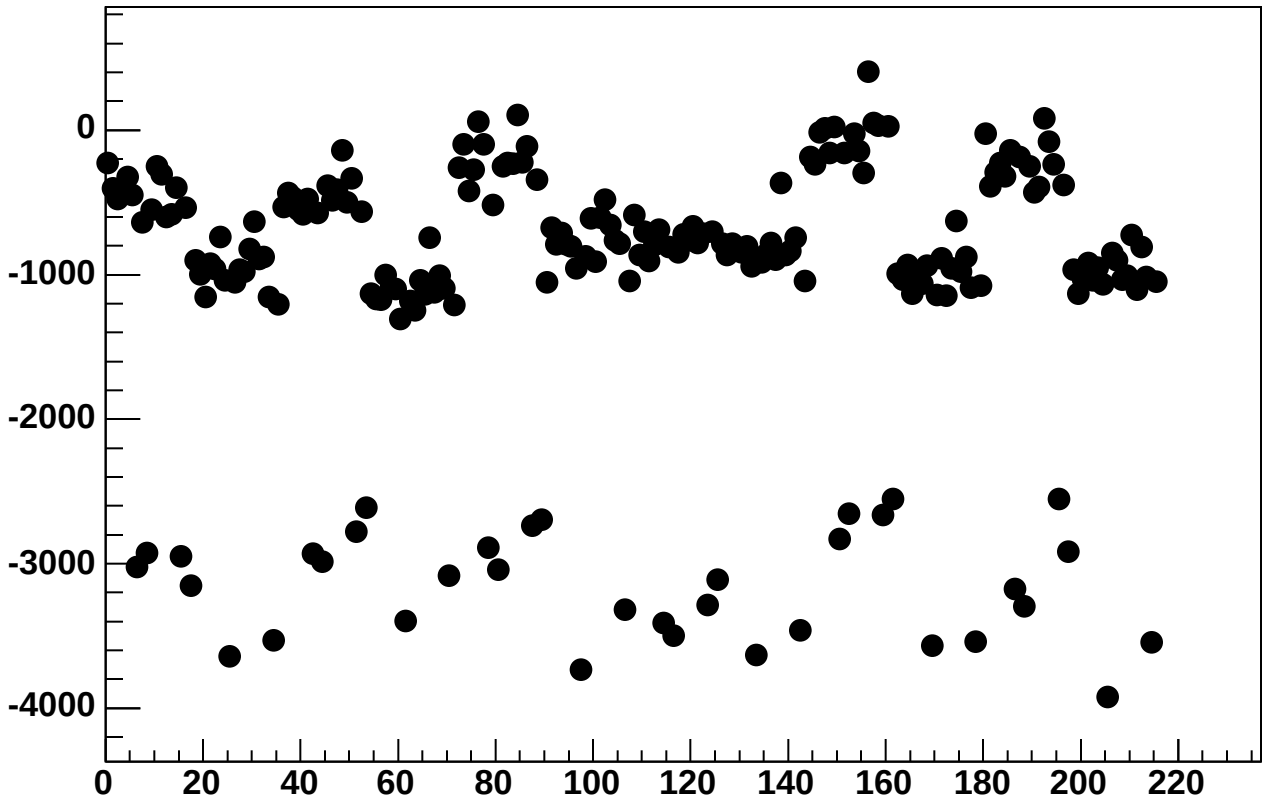
Enable 2, DAC=1600, Hold=195, ADC Mean vs 18*Chip+Chan



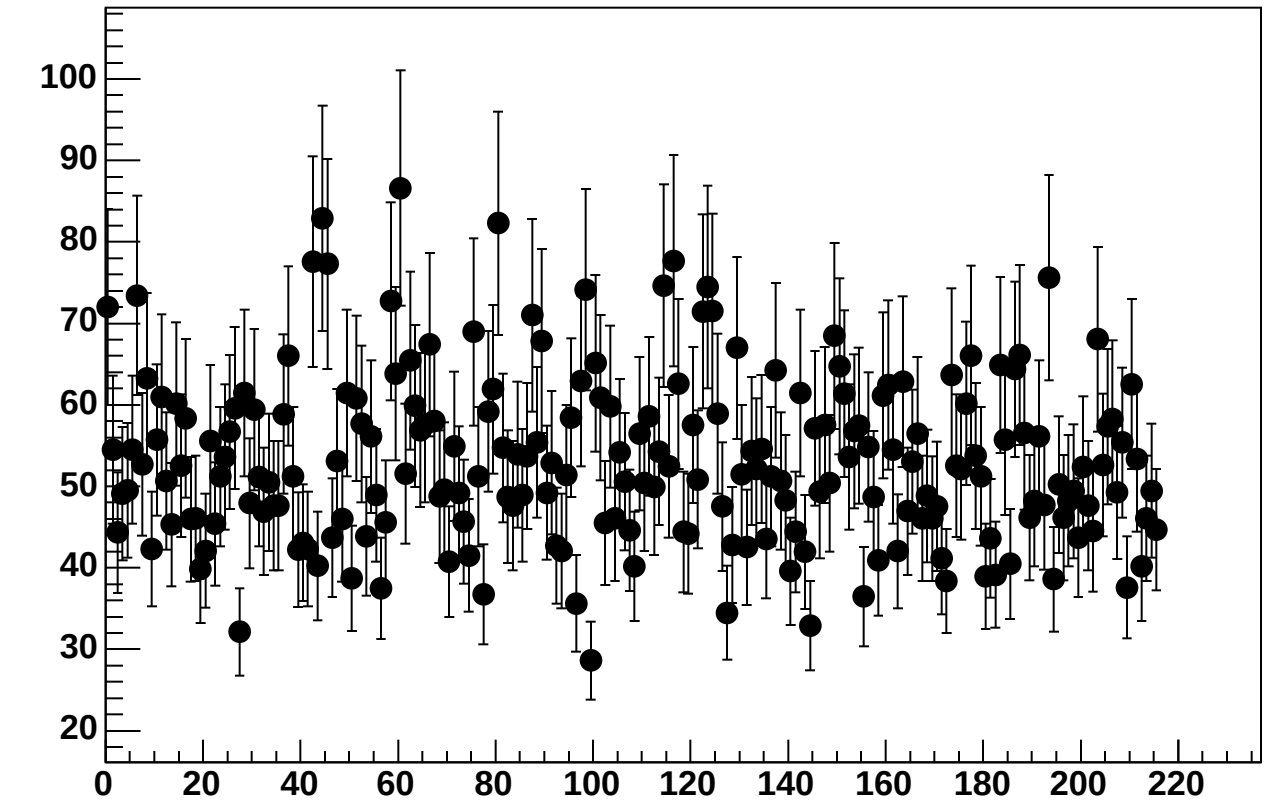
Enable 2, DAC=1600, Hold=195, ADC Noise vs 18*Chip+Chan



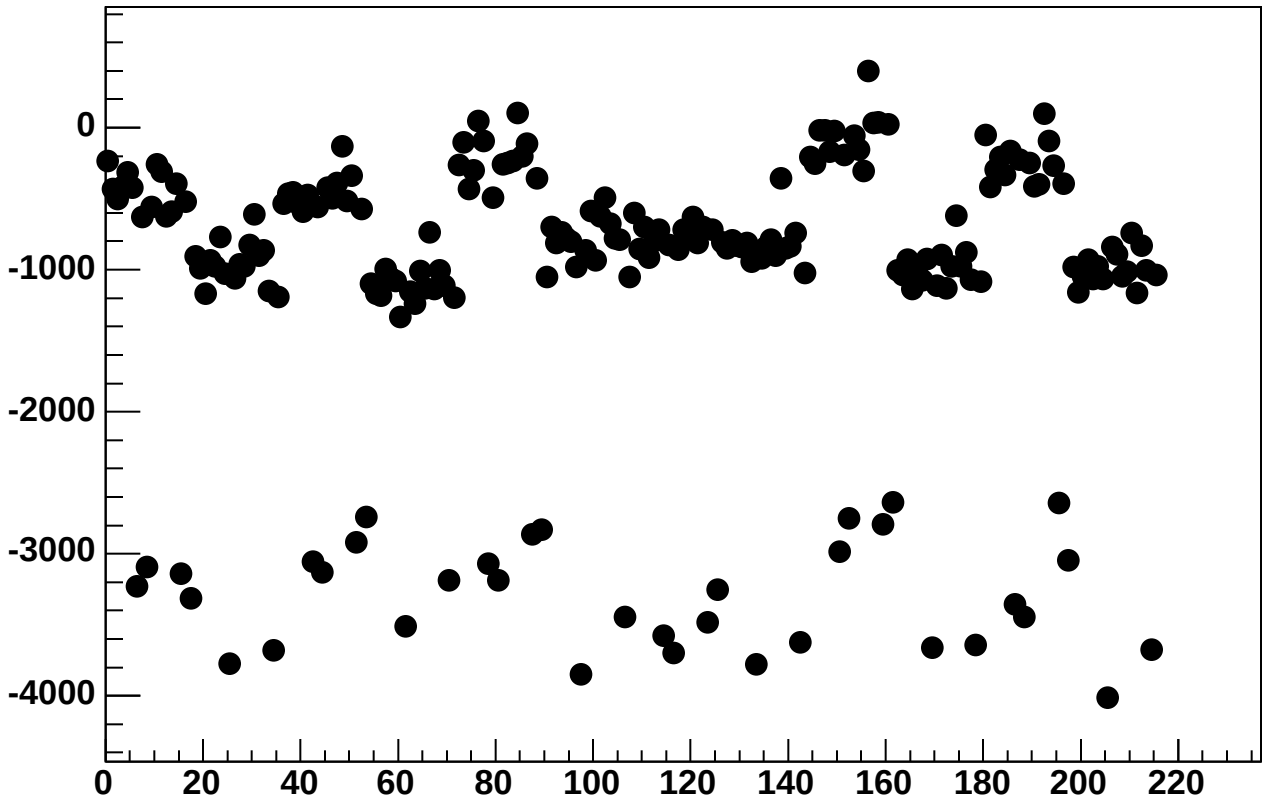
Enable 2, DAC=1600, Hold=200, ADC Mean vs 18*Chip+Chan



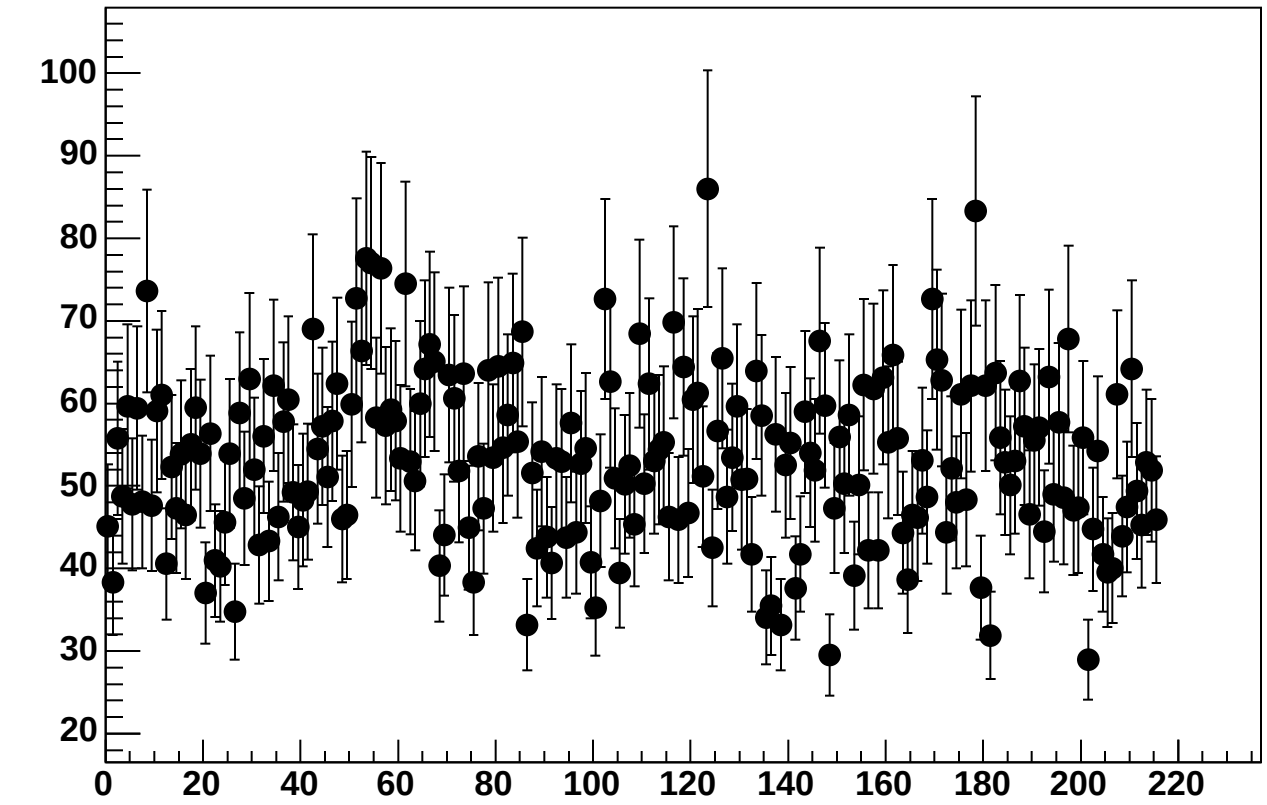
Enable 2, DAC=1600, Hold=200, ADC Noise vs 18*Chip+Chan



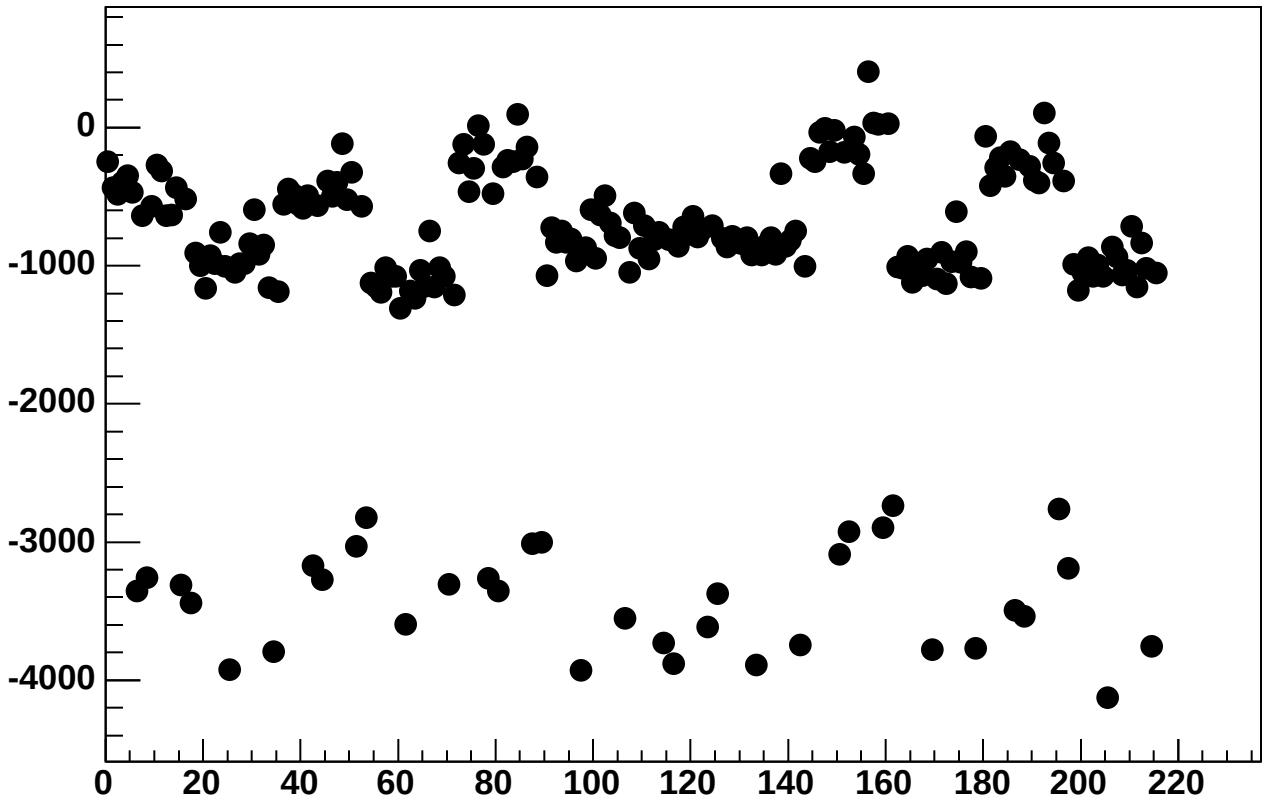
Enable 2, DAC=1600, Hold=205, ADC Mean vs 18*Chip+Chan



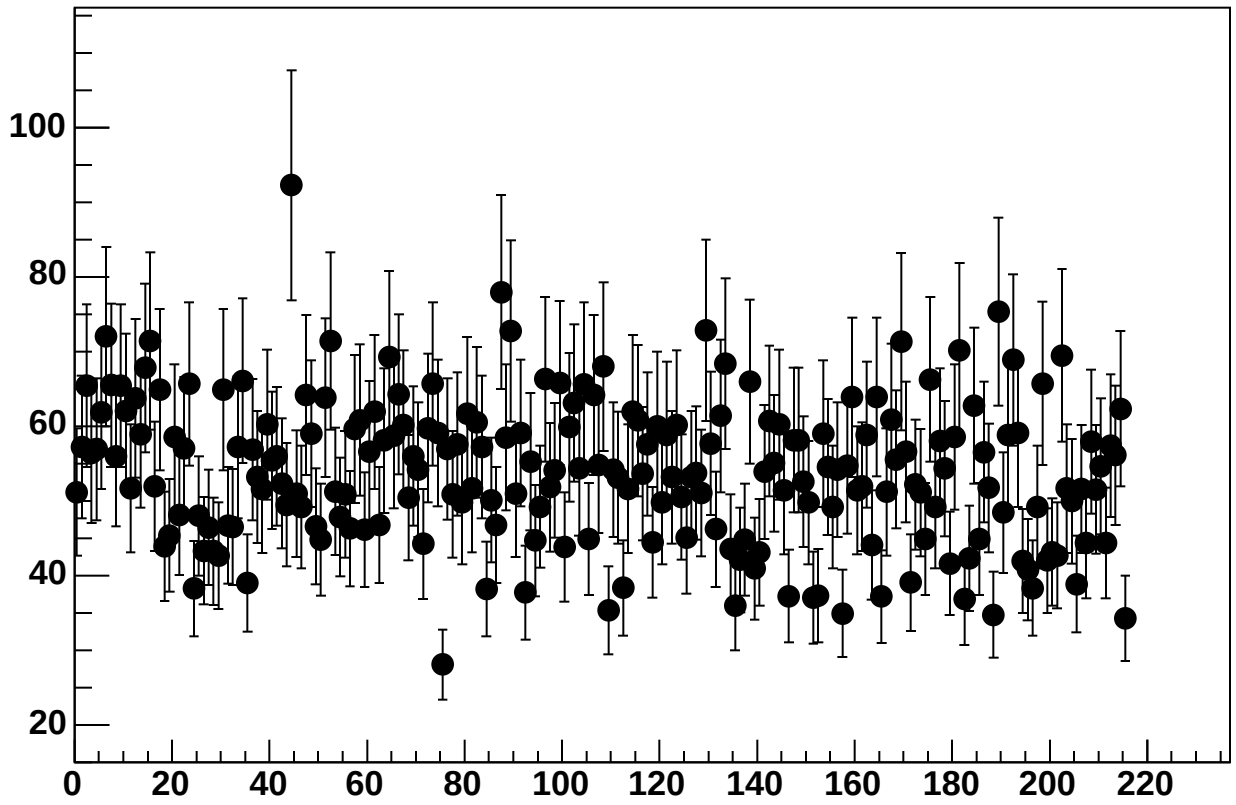
Enable 2, DAC=1600, Hold=205, ADC Noise vs 18*Chip+Chan



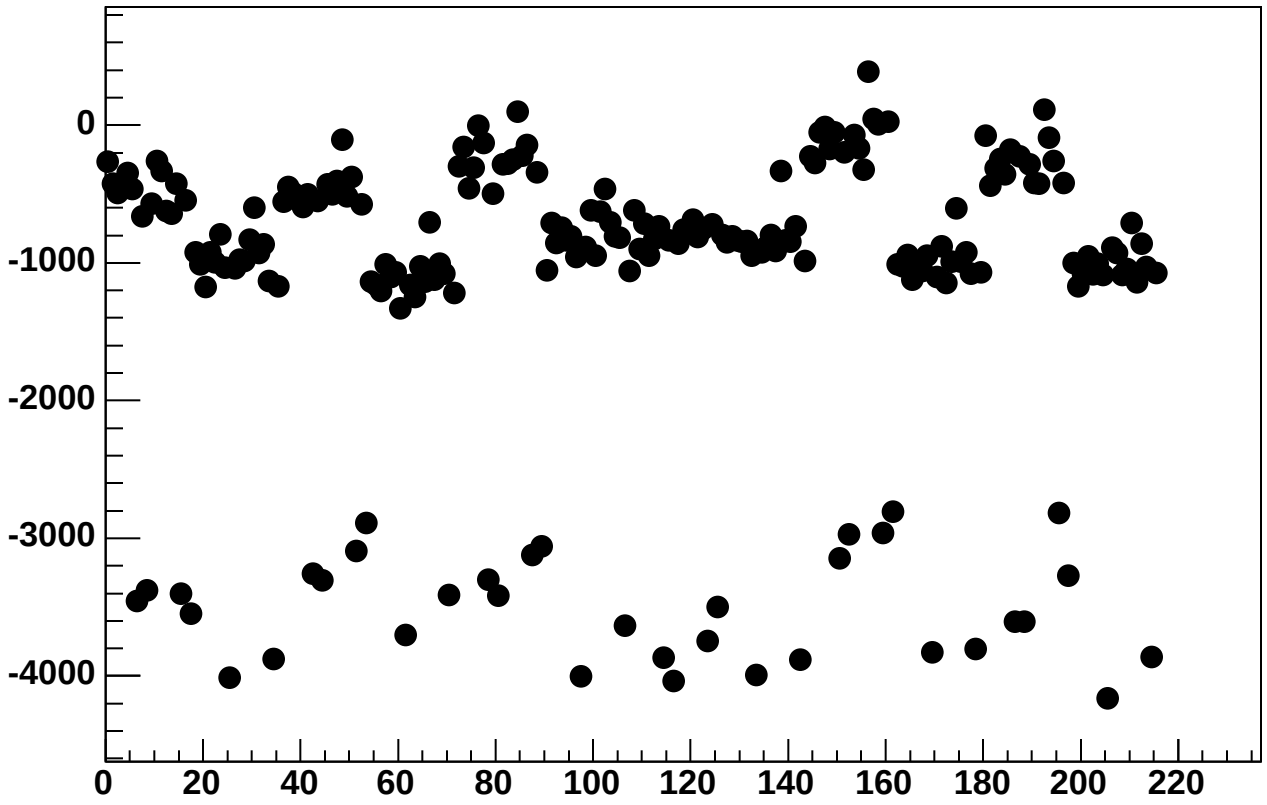
Enable 2, DAC=1600, Hold=210, ADC Mean vs 18*Chip+Chan



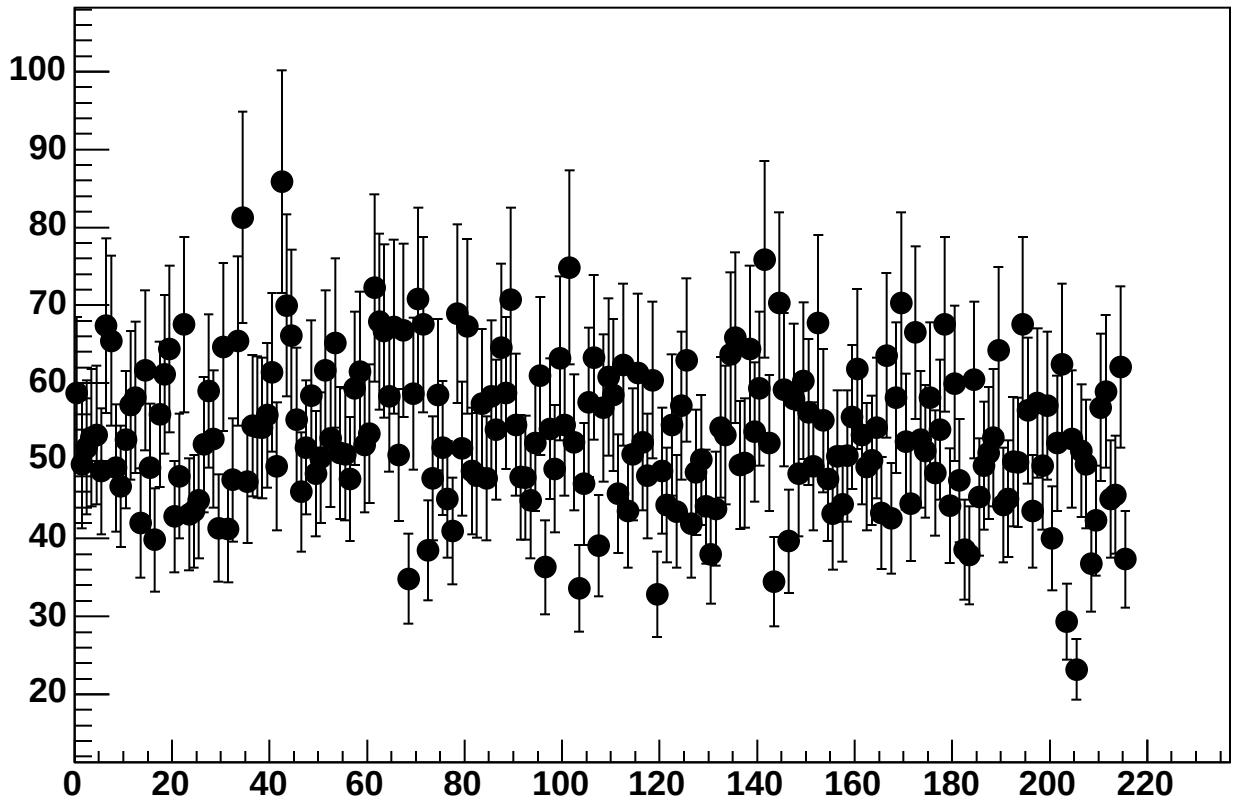
Enable 2, DAC=1600, Hold=210, ADC Noise vs 18*Chip+Chan



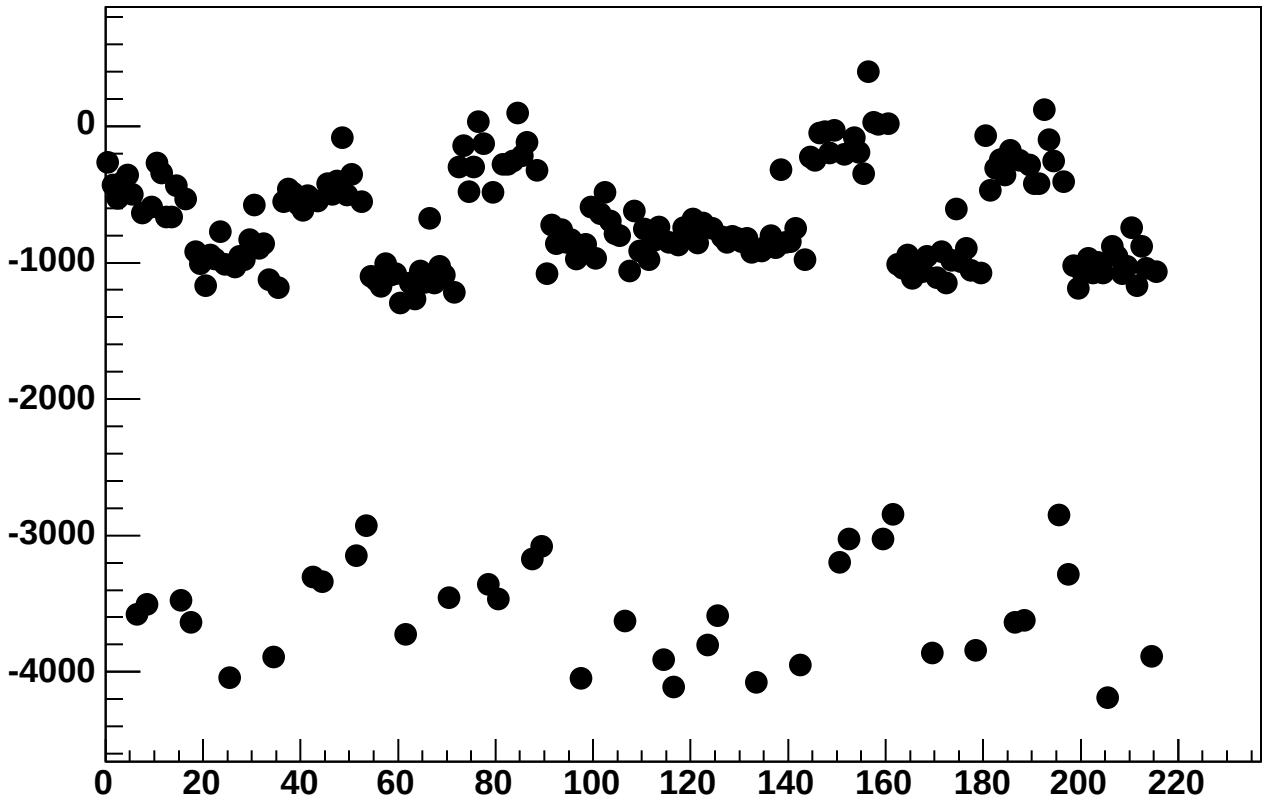
Enable 2, DAC=1600, Hold=215, ADC Mean vs 18*Chip+Chan



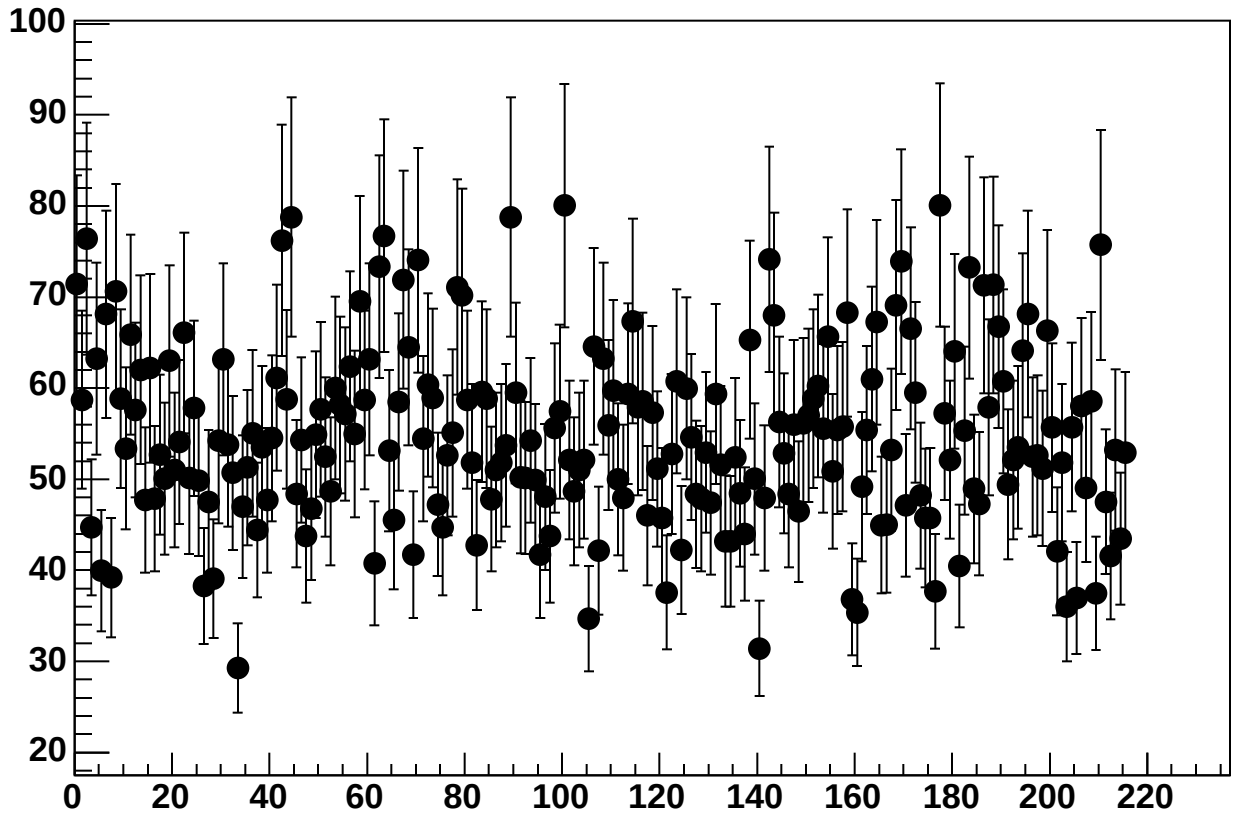
Enable 2, DAC=1600, Hold=215, ADC Noise vs 18*Chip+Chan



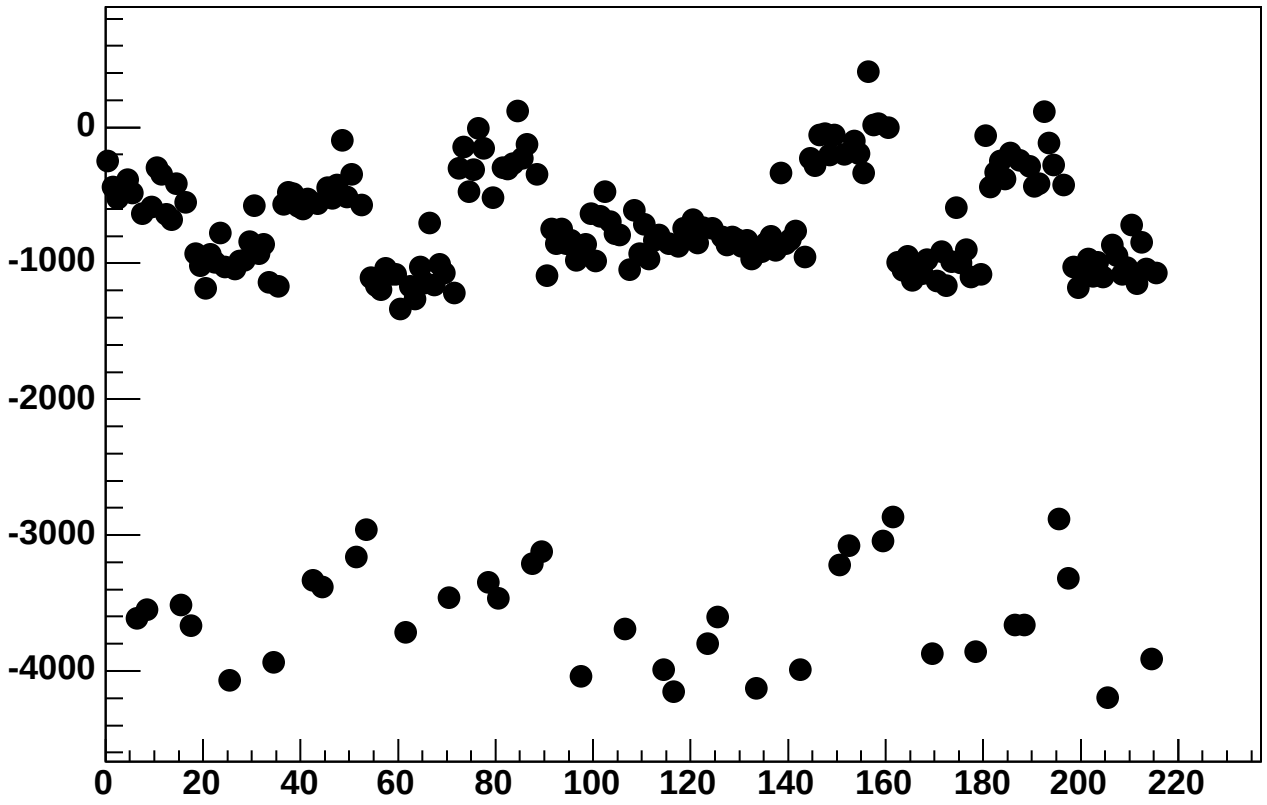
Enable 2, DAC=1600, Hold=220, ADC Mean vs 18*Chip+Chan



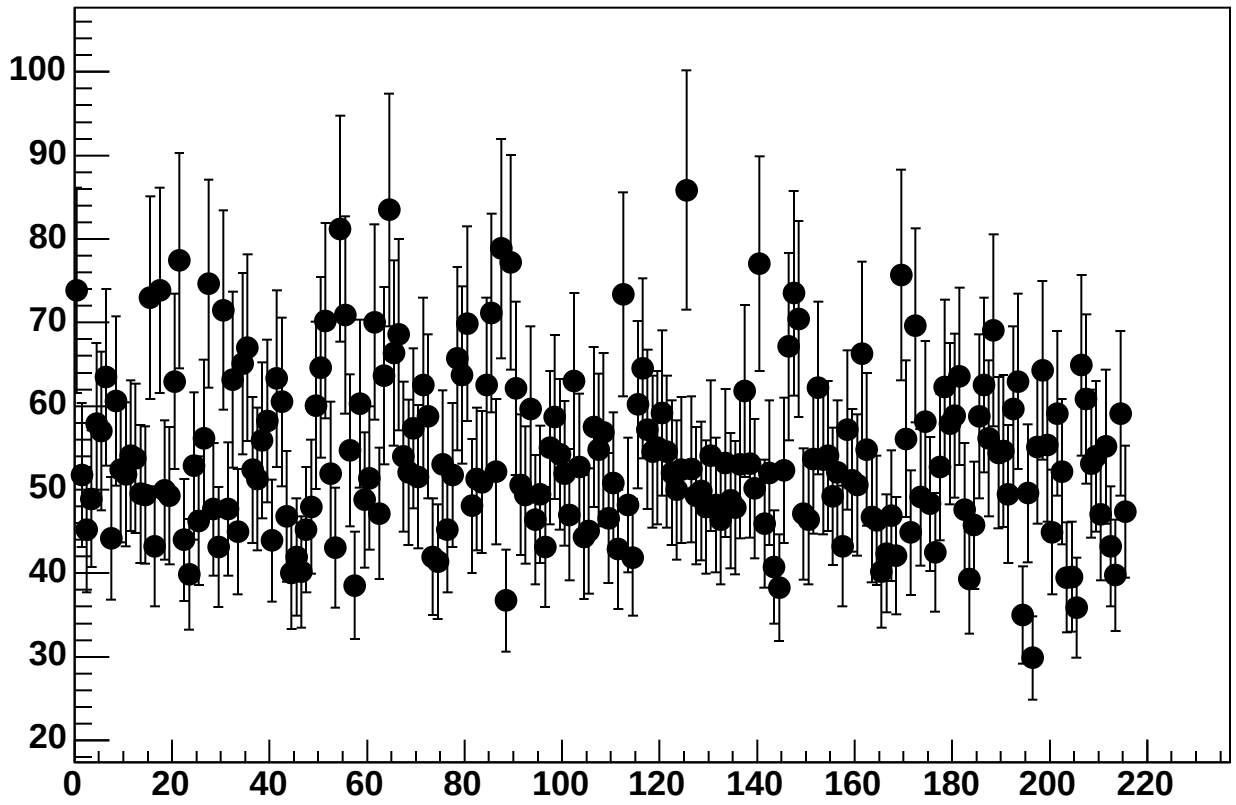
Enable 2, DAC=1600, Hold=220, ADC Noise vs 18*Chip+Chan



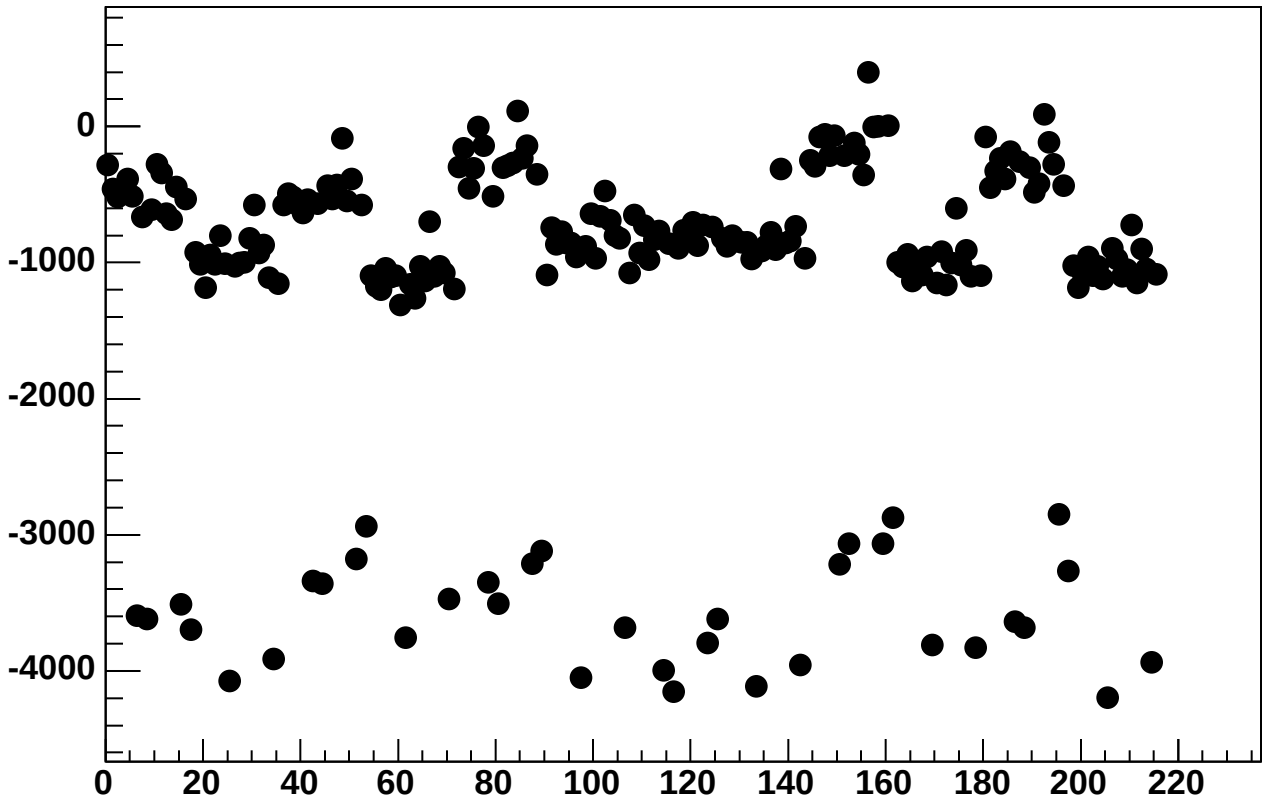
Enable 2, DAC=1600, Hold=225, ADC Mean vs 18*Chip+Chan



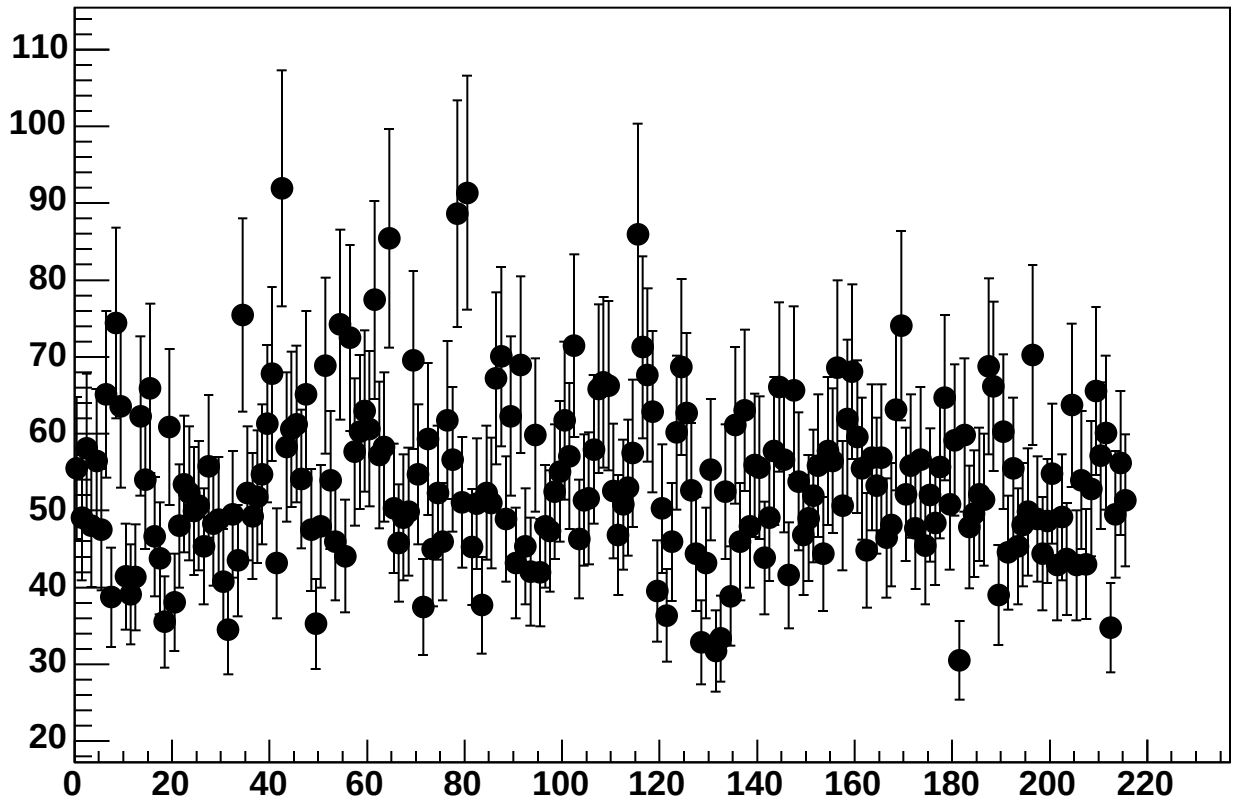
Enable 2, DAC=1600, Hold=225, ADC Noise vs 18*Chip+Chan



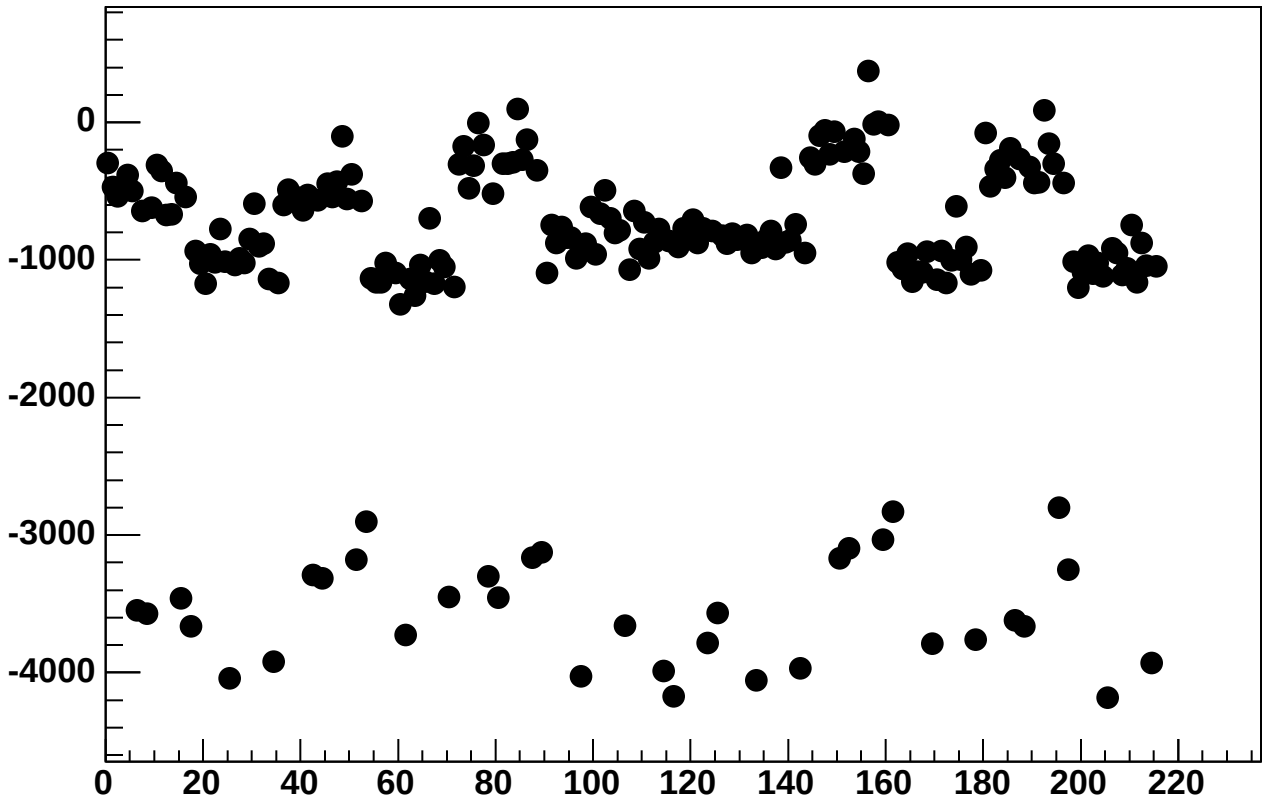
Enable 2, DAC=1600, Hold=230, ADC Mean vs 18*Chip+Chan



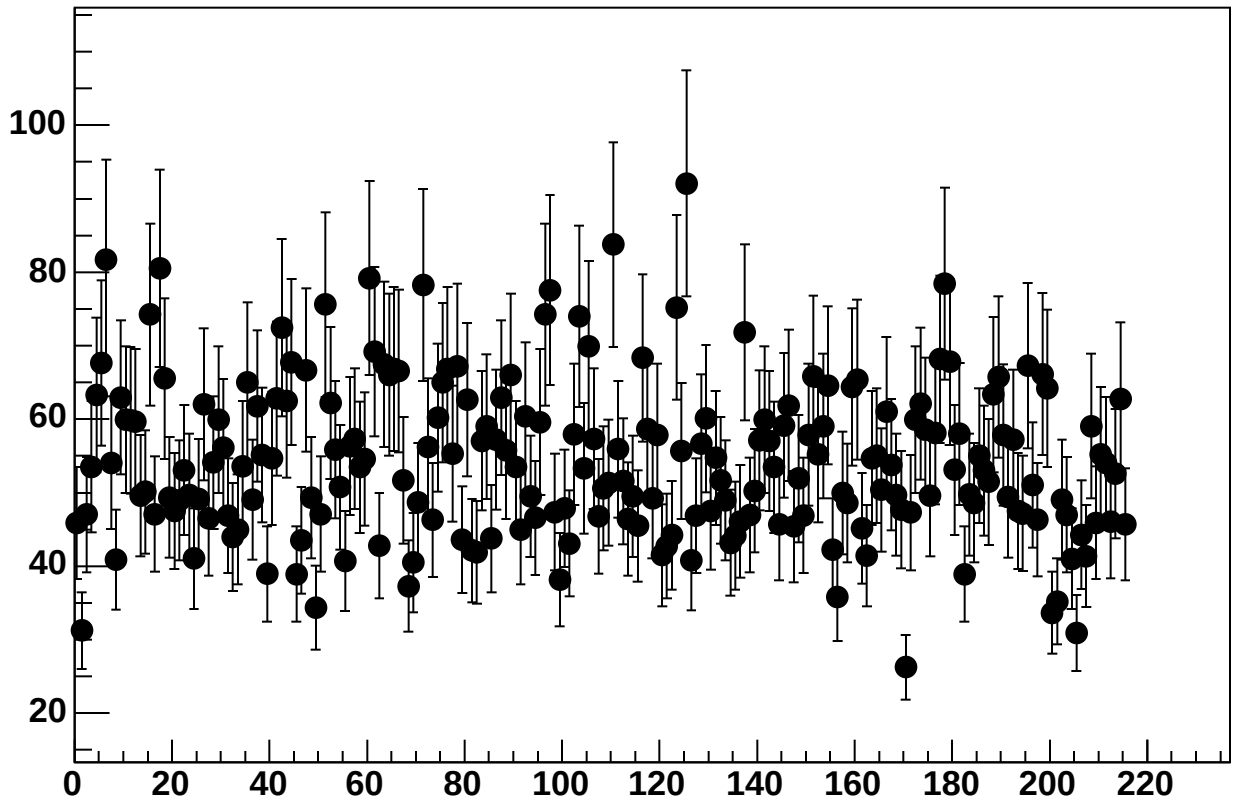
Enable 2, DAC=1600, Hold=230, ADC Noise vs 18*Chip+Chan



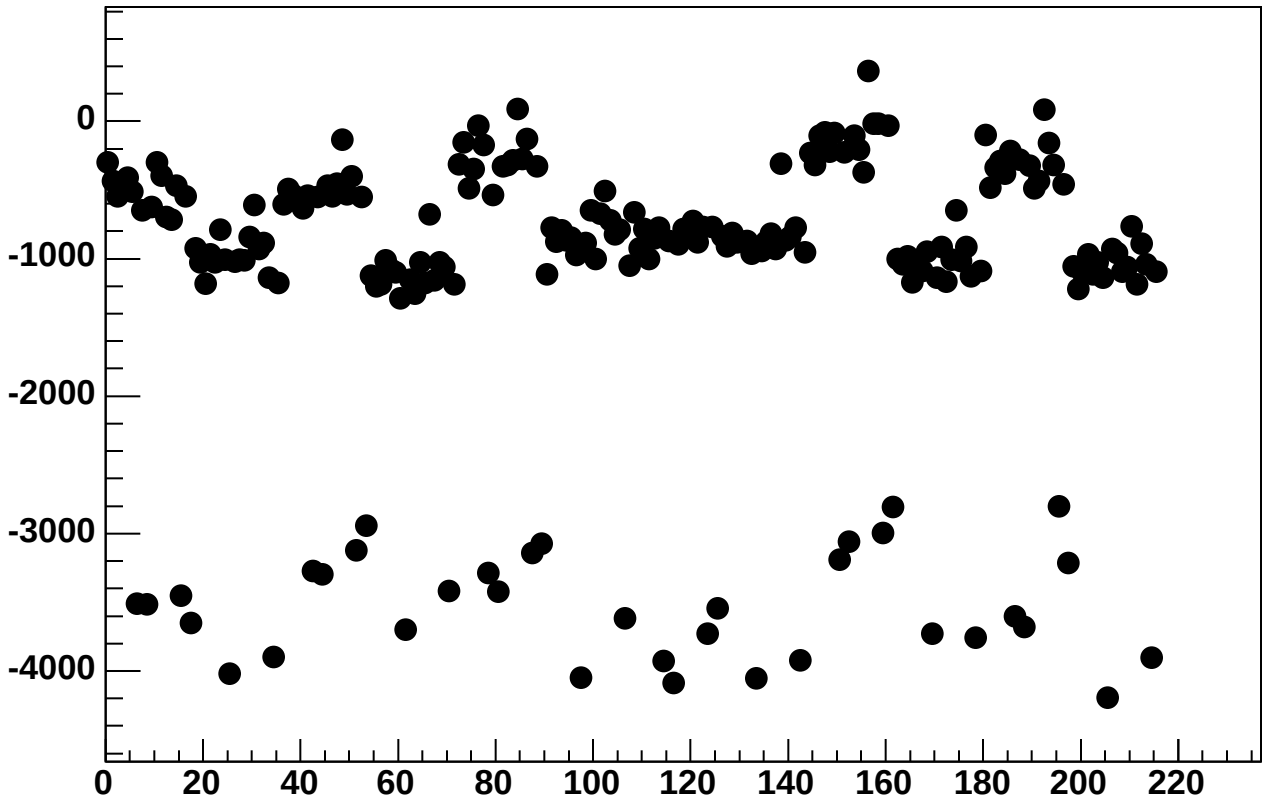
Enable 2, DAC=1600, Hold=235, ADC Mean vs 18*Chip+Chan



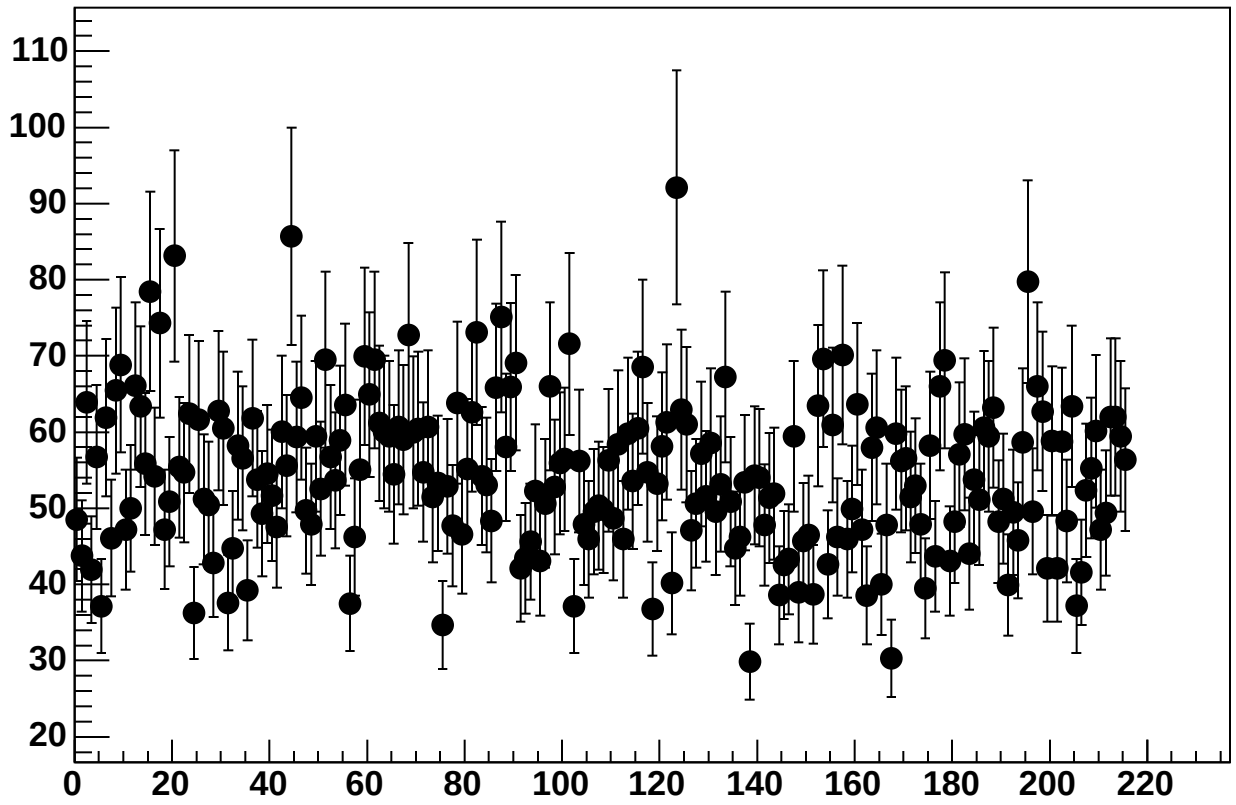
Enable 2, DAC=1600, Hold=235, ADC Noise vs 18*Chip+Chan



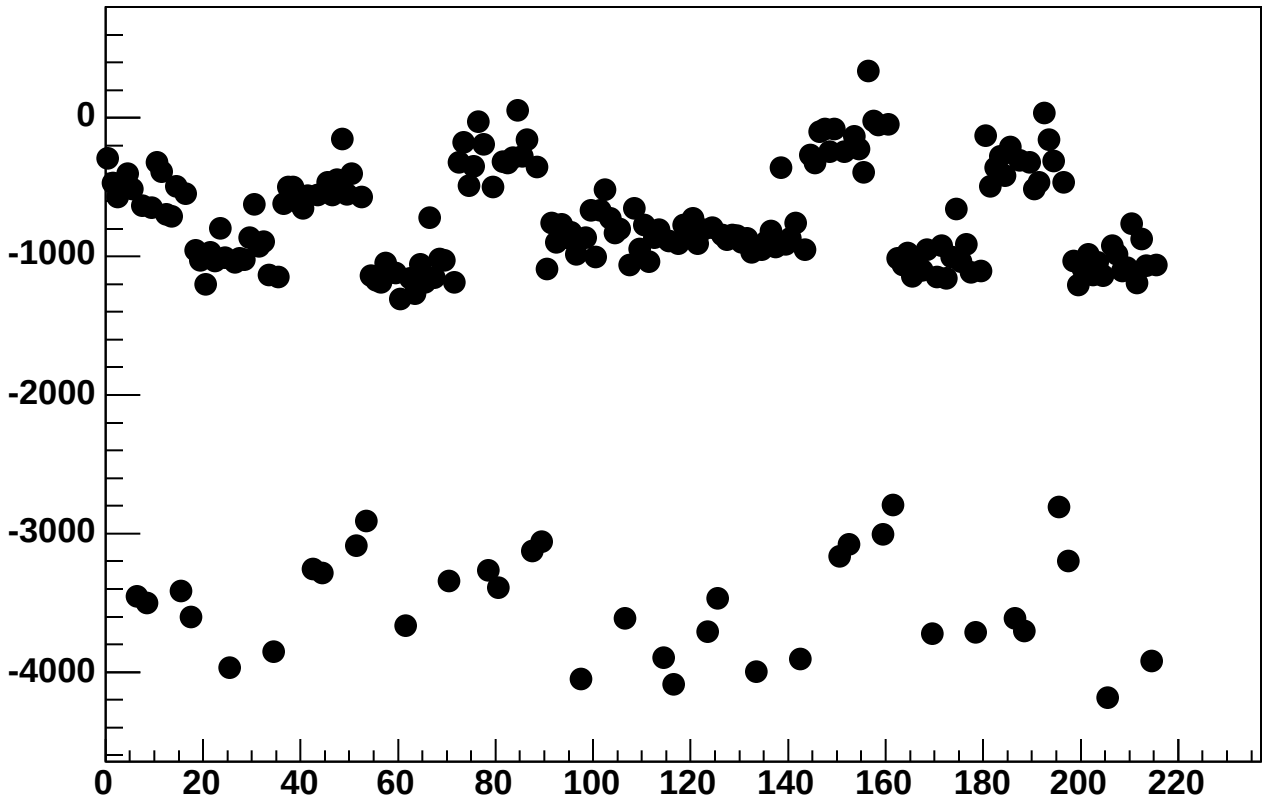
Enable 2, DAC=1600, Hold=240, ADC Mean vs 18*Chip+Chan



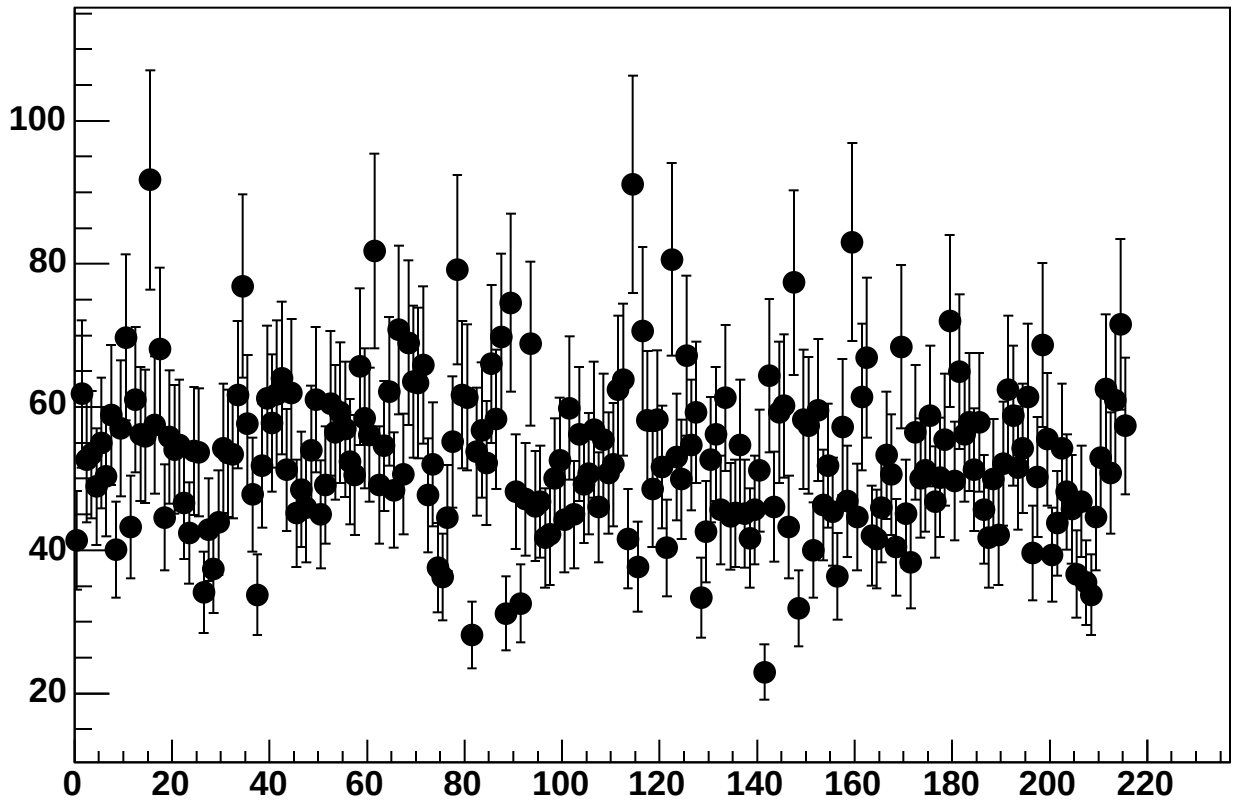
Enable 2, DAC=1600, Hold=240, ADC Noise vs 18*Chip+Chan



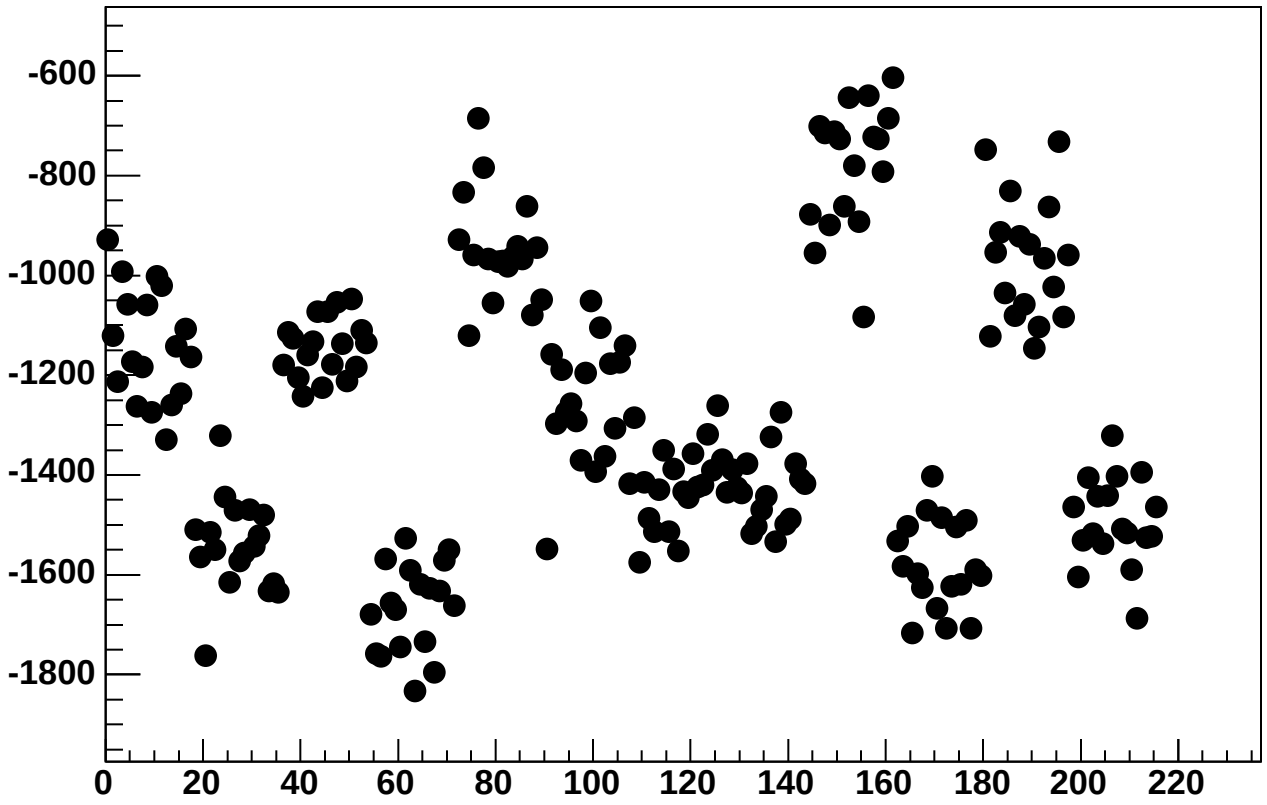
Enable 2, DAC=1600, Hold=245, ADC Mean vs 18*Chip+Chan



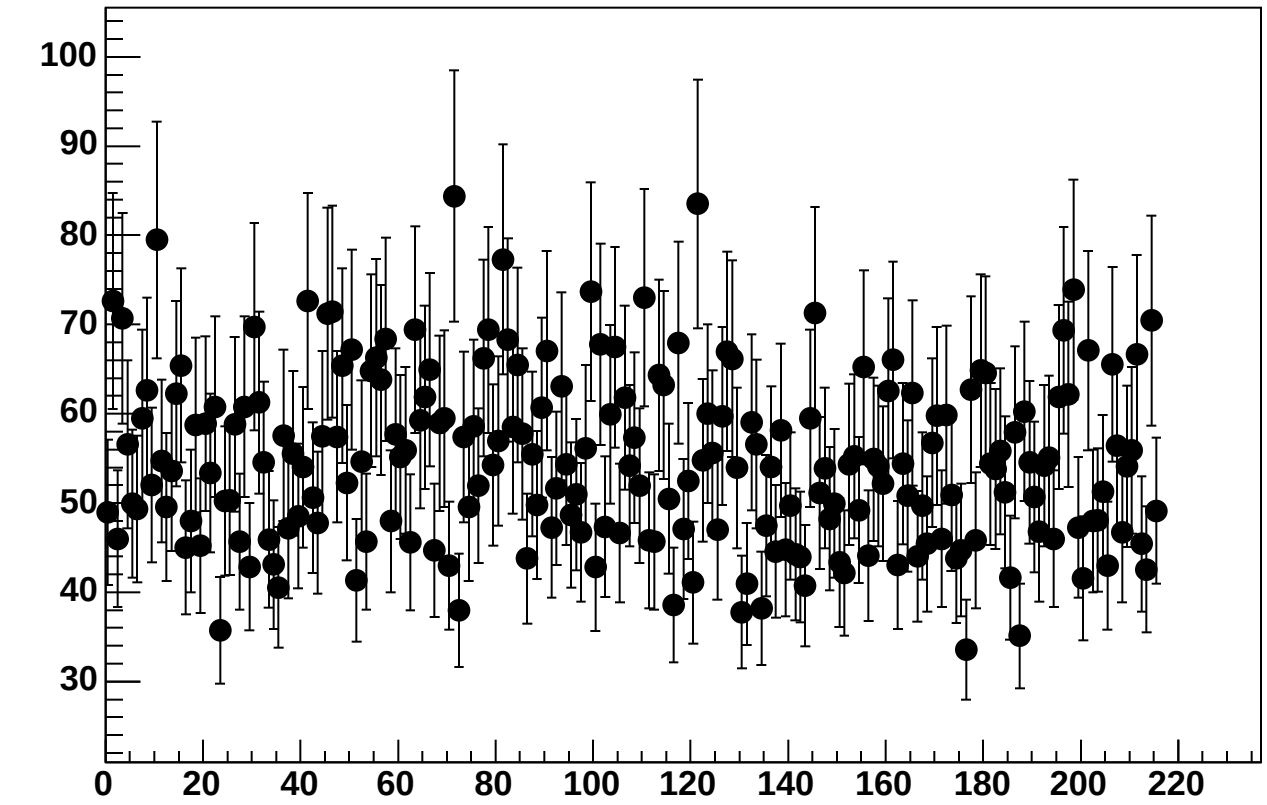
Enable 2, DAC=1600, Hold=245, ADC Noise vs 18*Chip+Chan



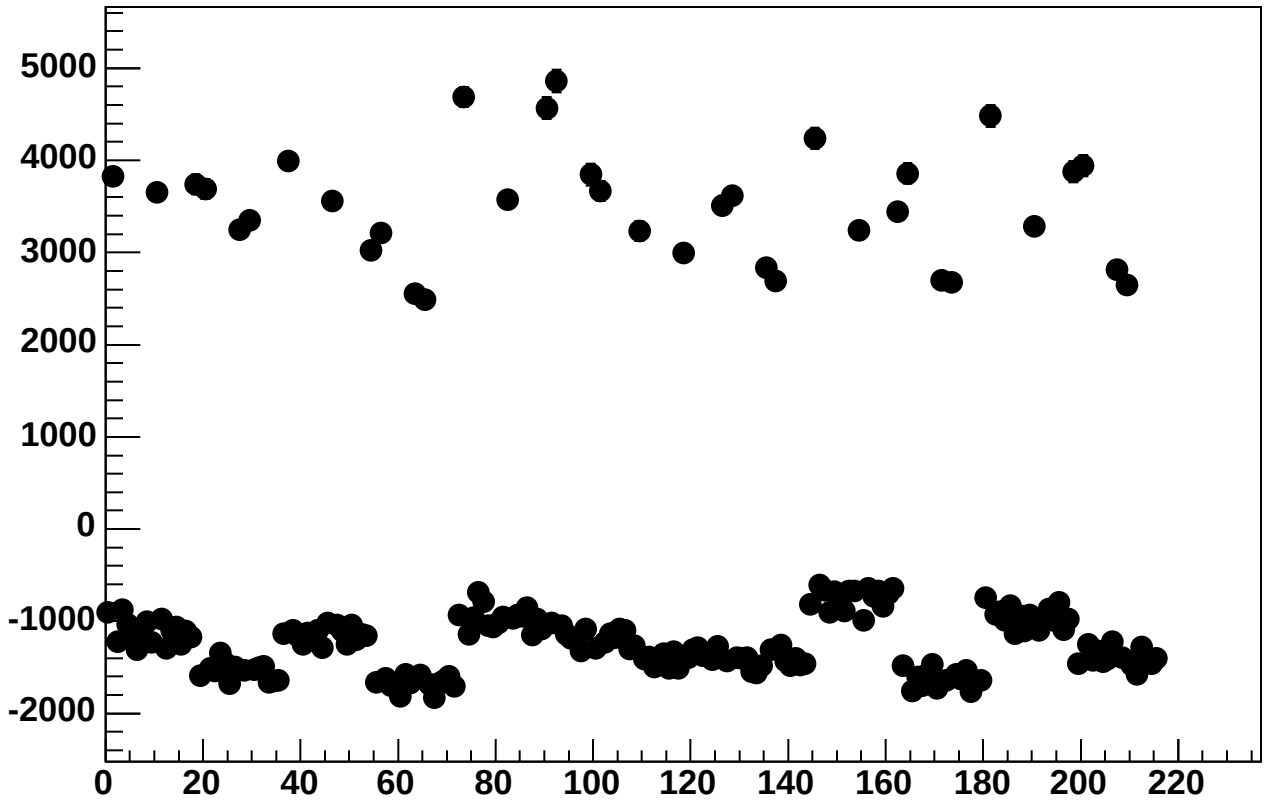
Enable 3, DAC=1600, Hold=0, ADC Mean vs 18*Chip+Chan



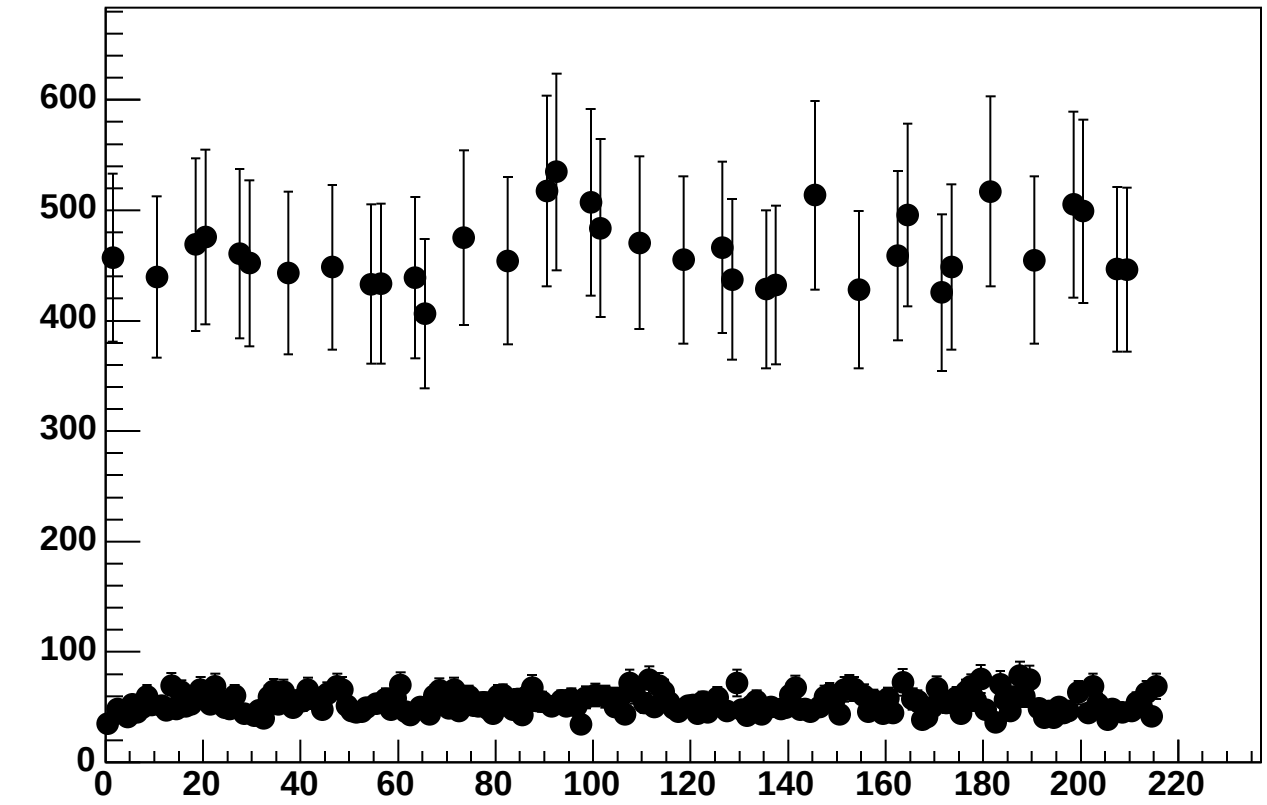
Enable 3, DAC=1600, Hold=0, ADC Noise vs 18*Chip+Chan



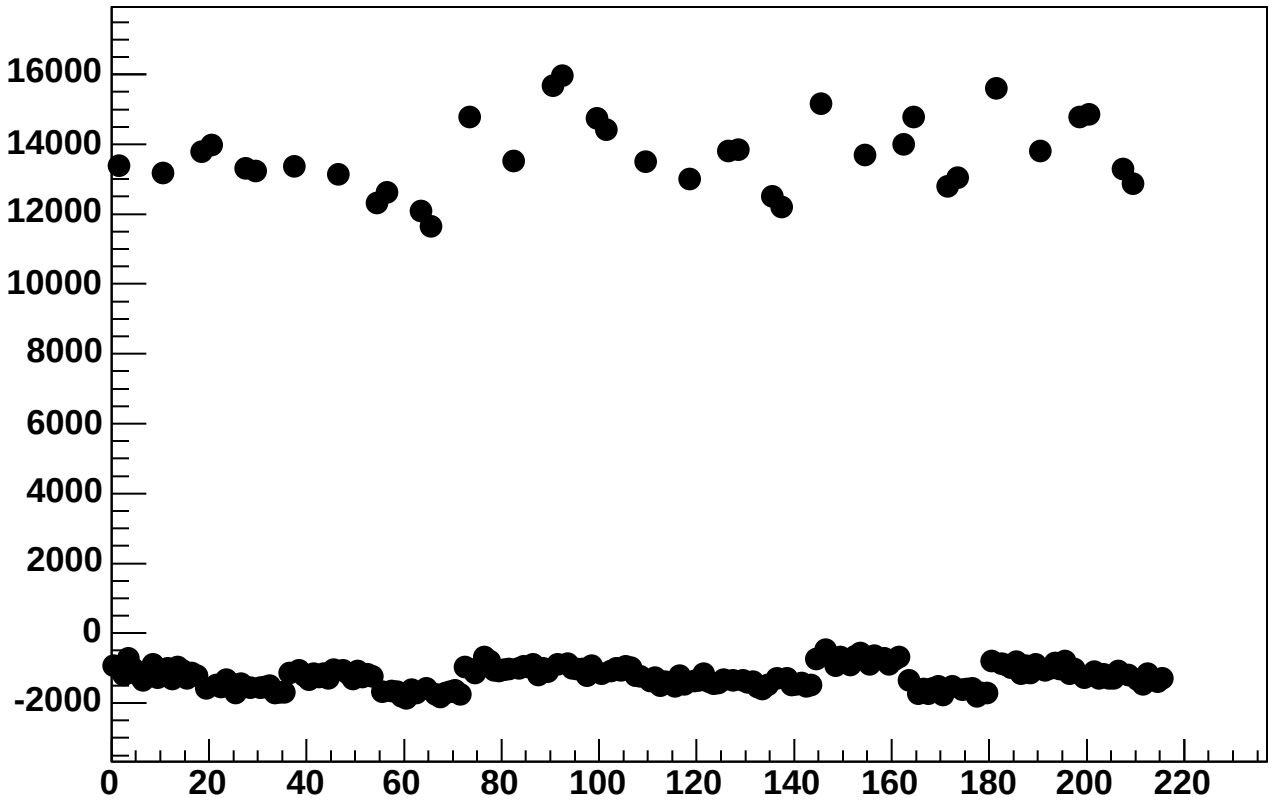
Enable 3, DAC=1600, Hold=5, ADC Mean vs 18*Chip+Chan



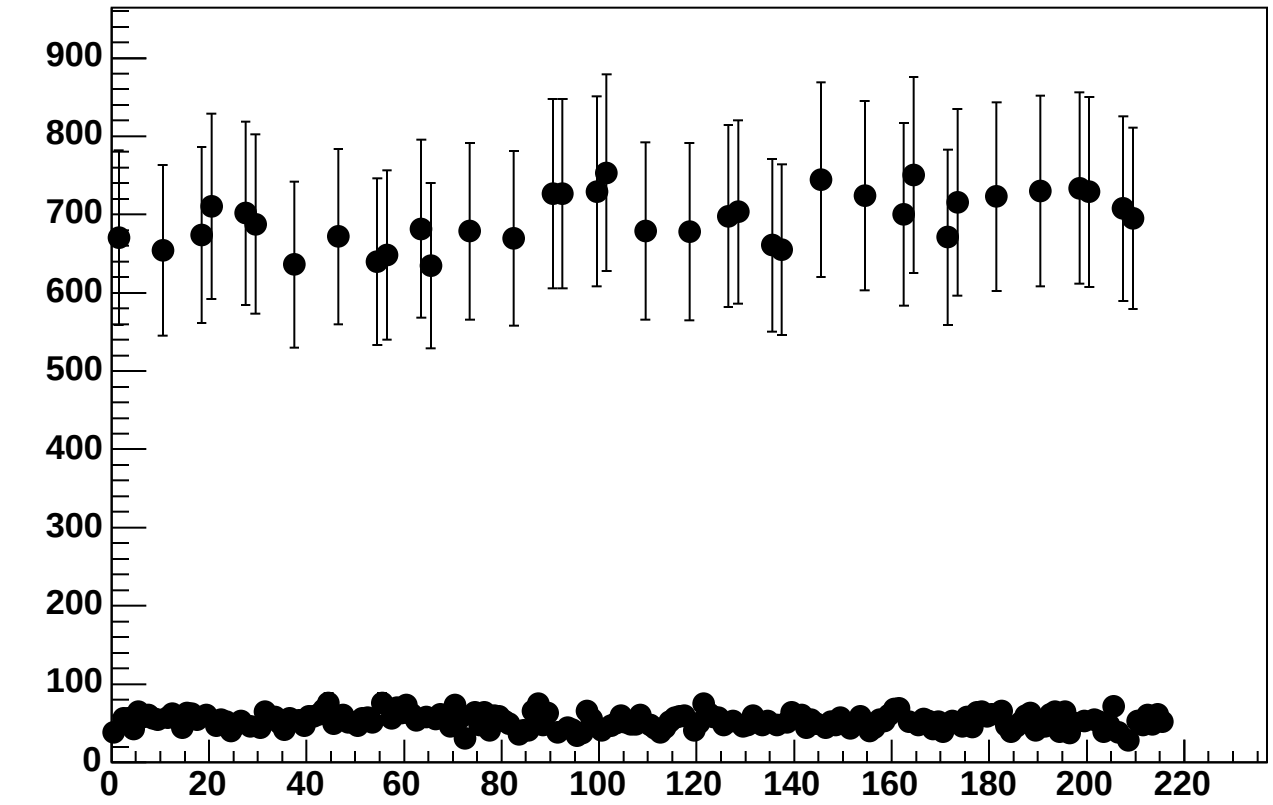
Enable 3, DAC=1600, Hold=5, ADC Noise vs 18*Chip+Chan



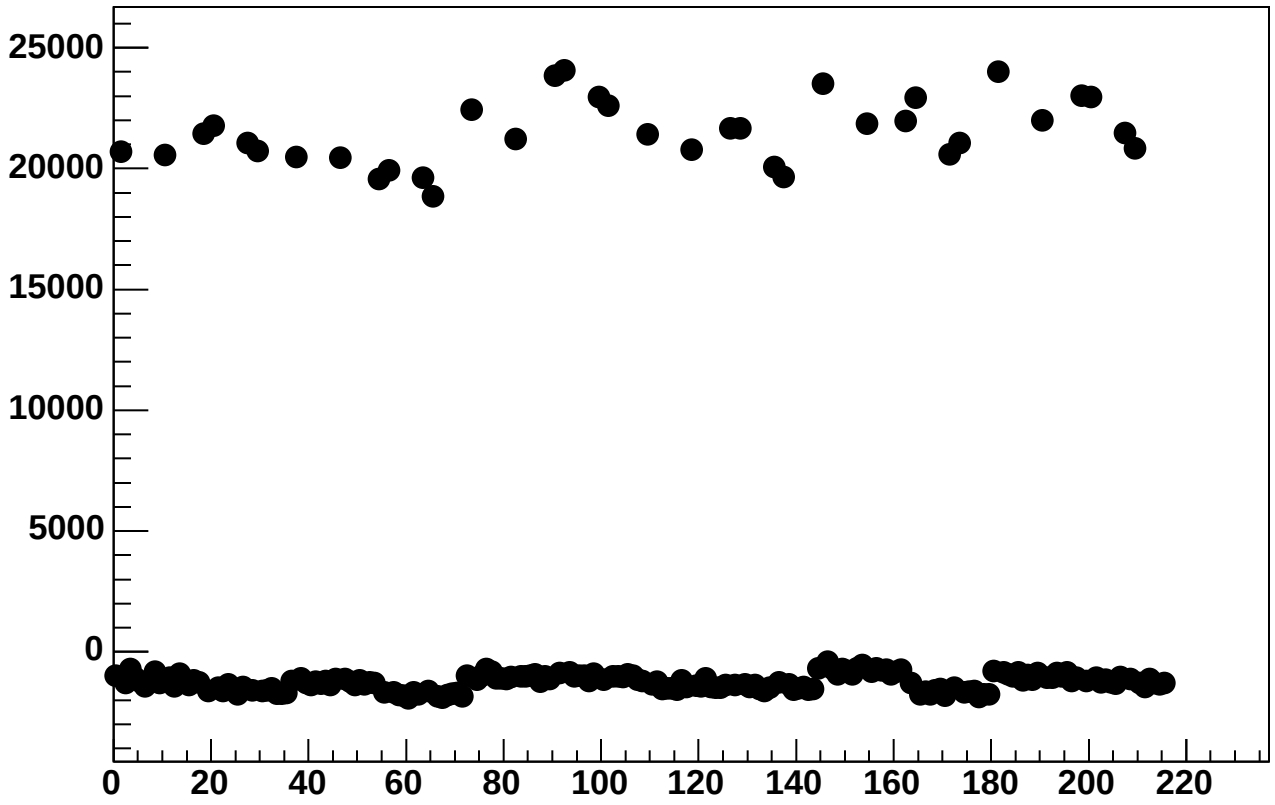
Enable 3, DAC=1600, Hold=10, ADC Mean vs 18*Chip+Chan



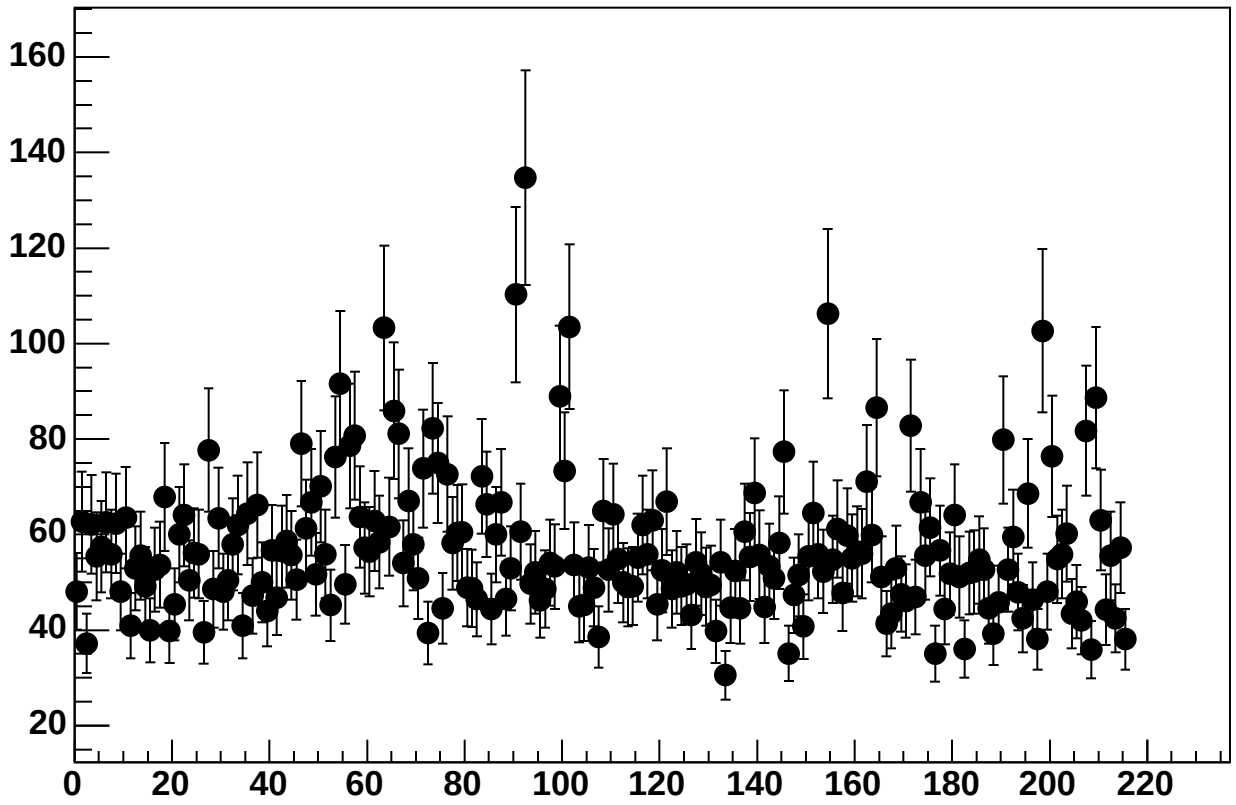
Enable 3, DAC=1600, Hold=10, ADC Noise vs 18*Chip+Chan



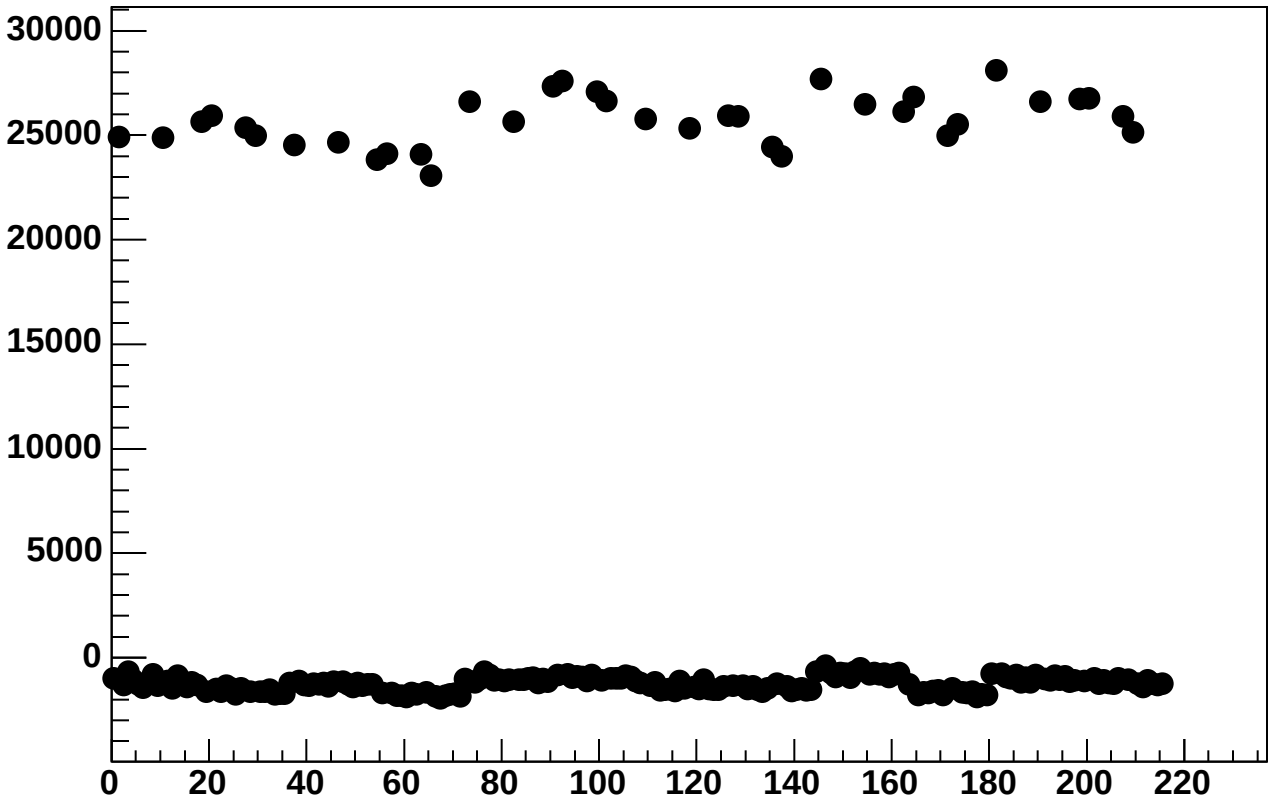
Enable 3, DAC=1600, Hold=15, ADC Mean vs 18*Chip+Chan



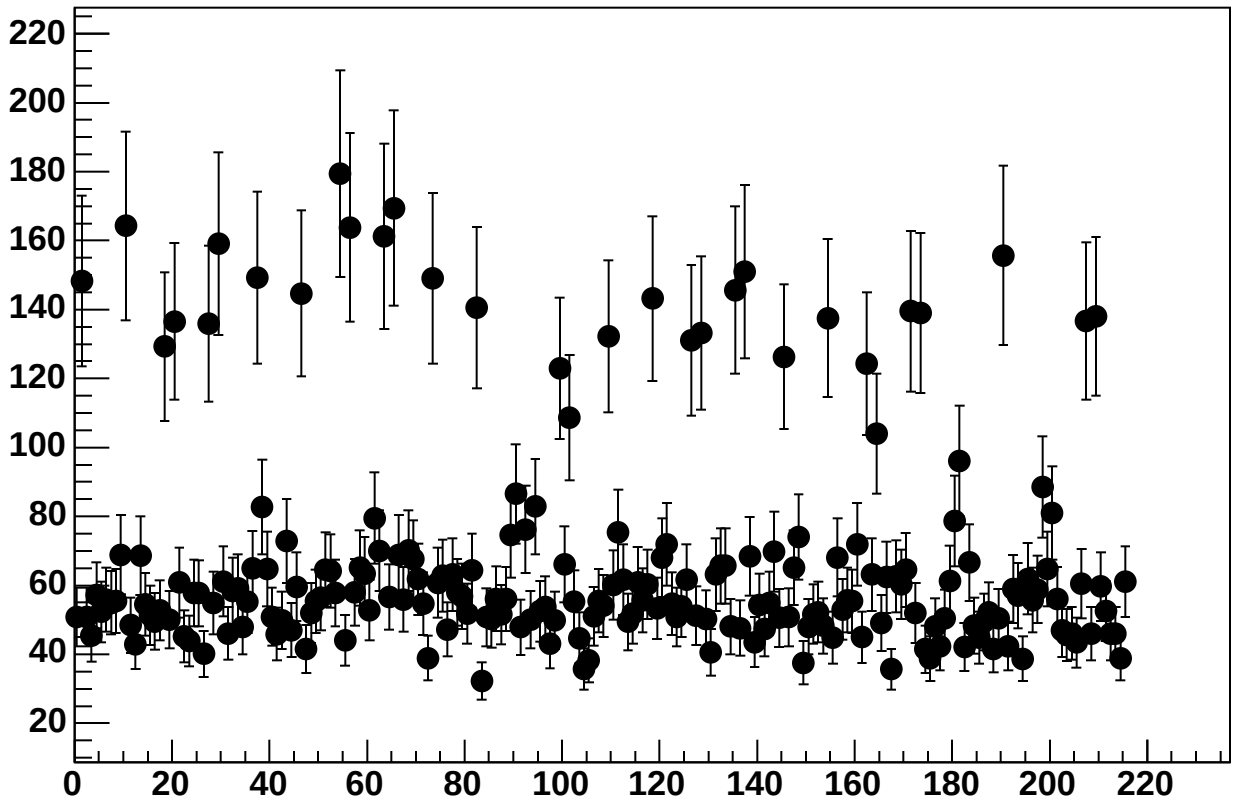
Enable 3, DAC=1600, Hold=15, ADC Noise vs 18*Chip+Chan



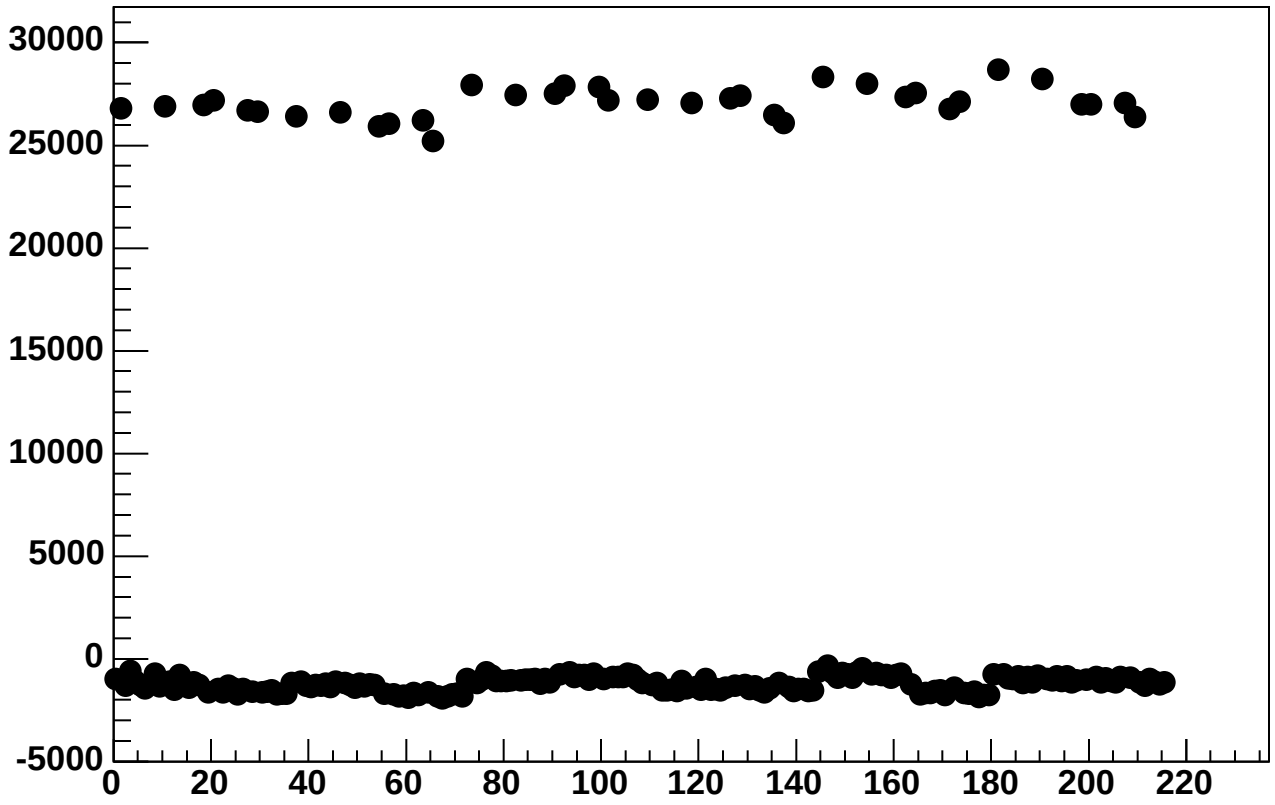
Enable 3, DAC=1600, Hold=20, ADC Mean vs 18*Chip+Chan



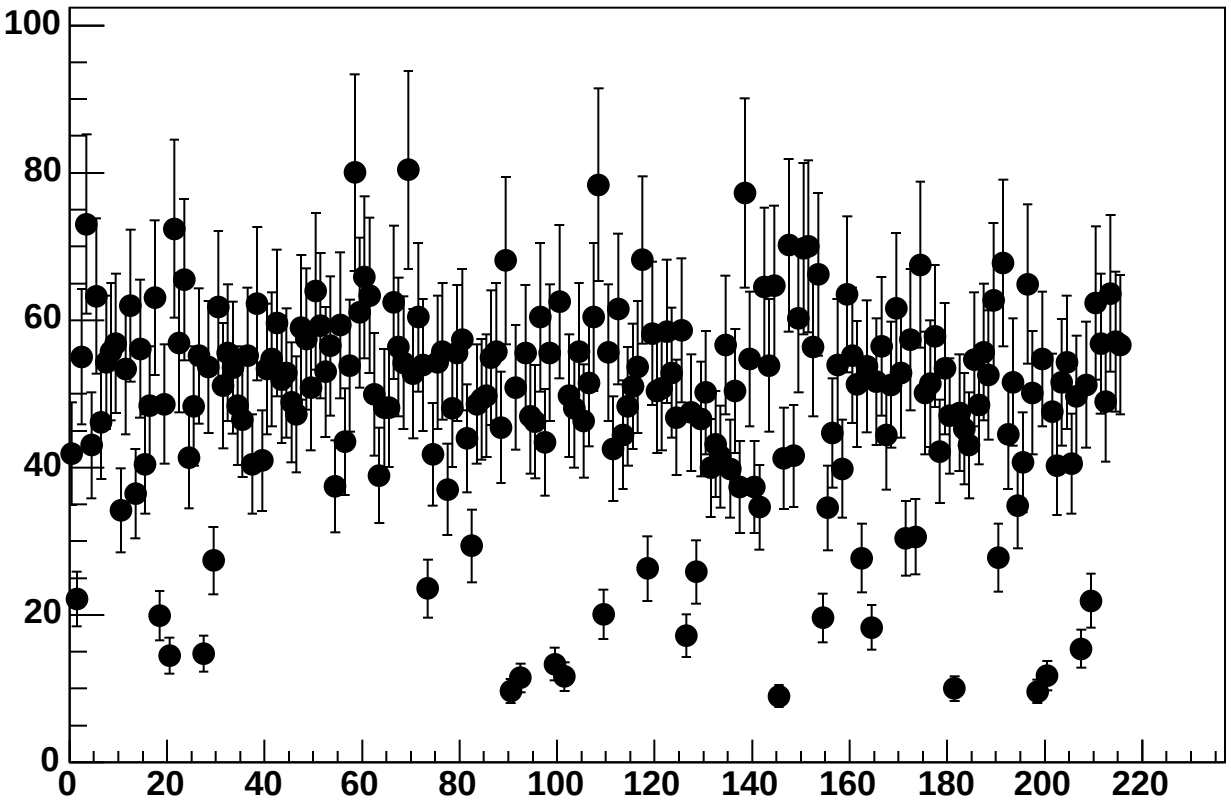
Enable 3, DAC=1600, Hold=20, ADC Noise vs 18*Chip+Chan



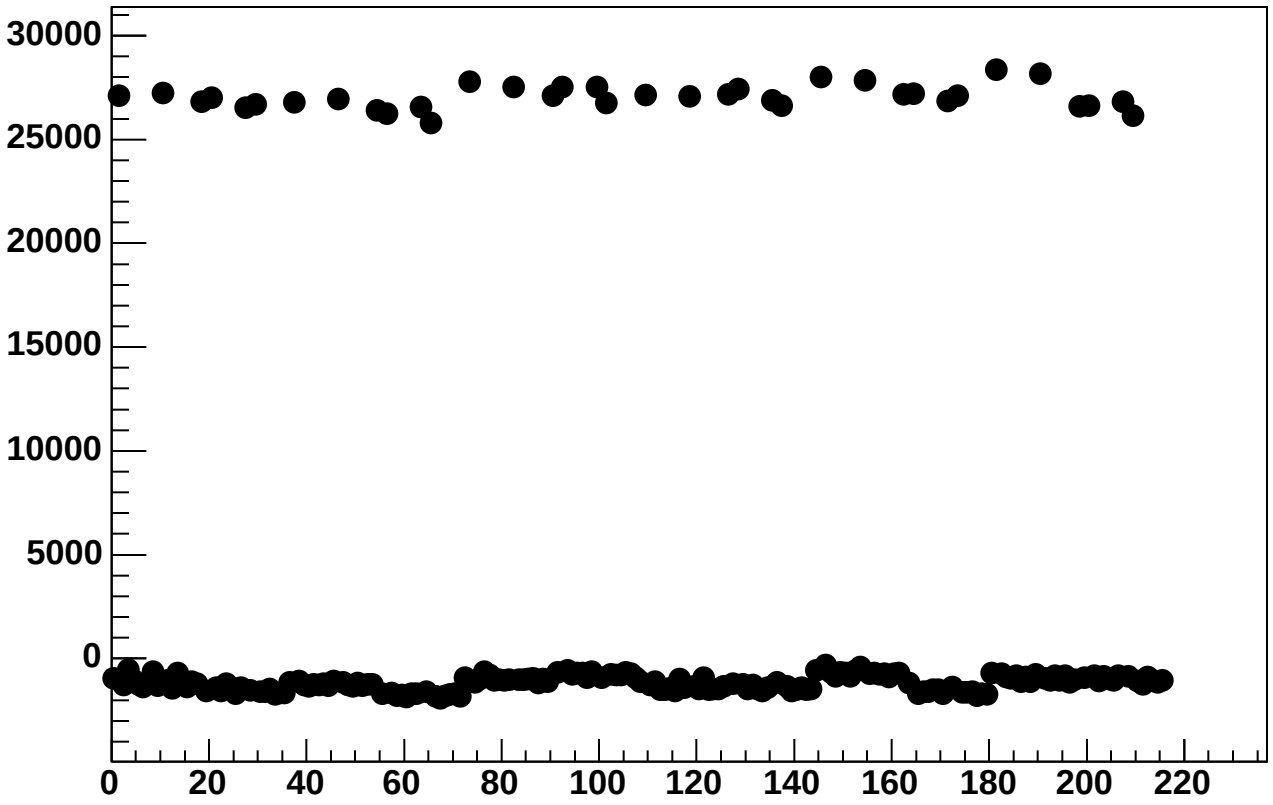
Enable 3, DAC=1600, Hold=25, ADC Mean vs 18*Chip+Chan



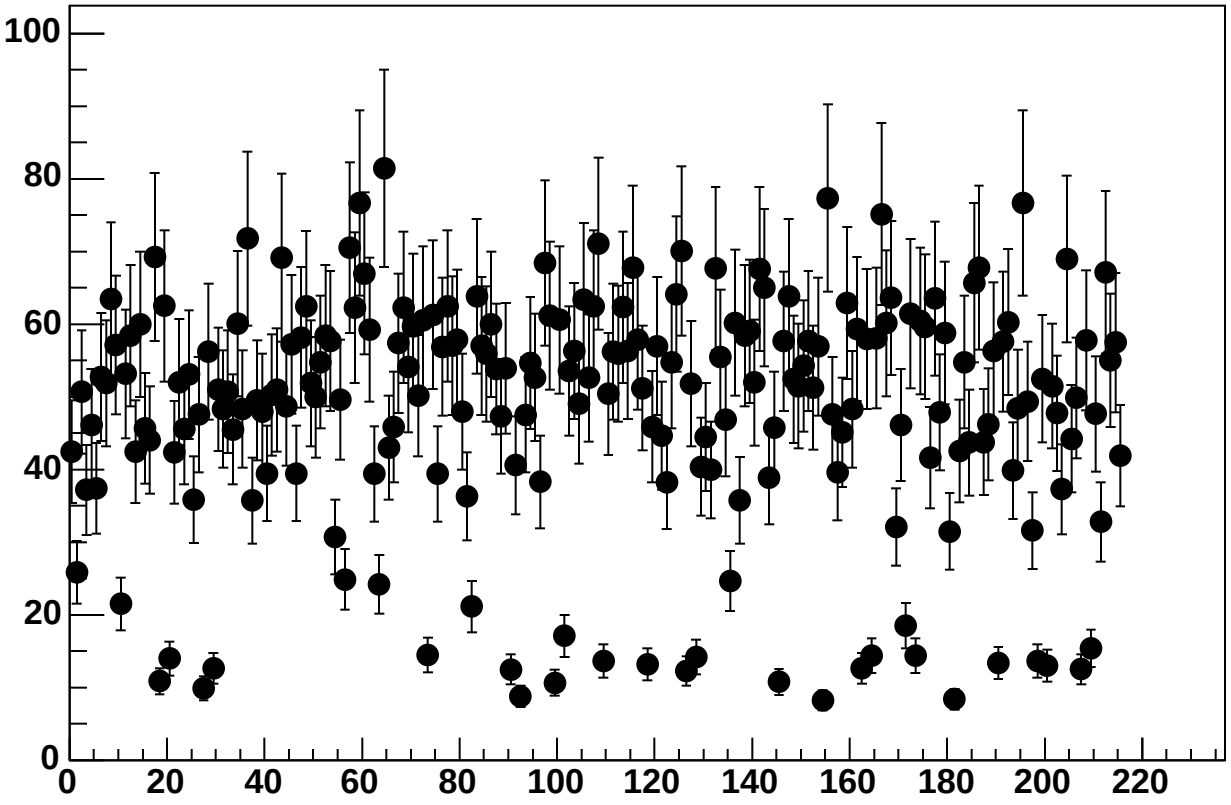
Enable 3, DAC=1600, Hold=25, ADC Noise vs 18*Chip+Chan



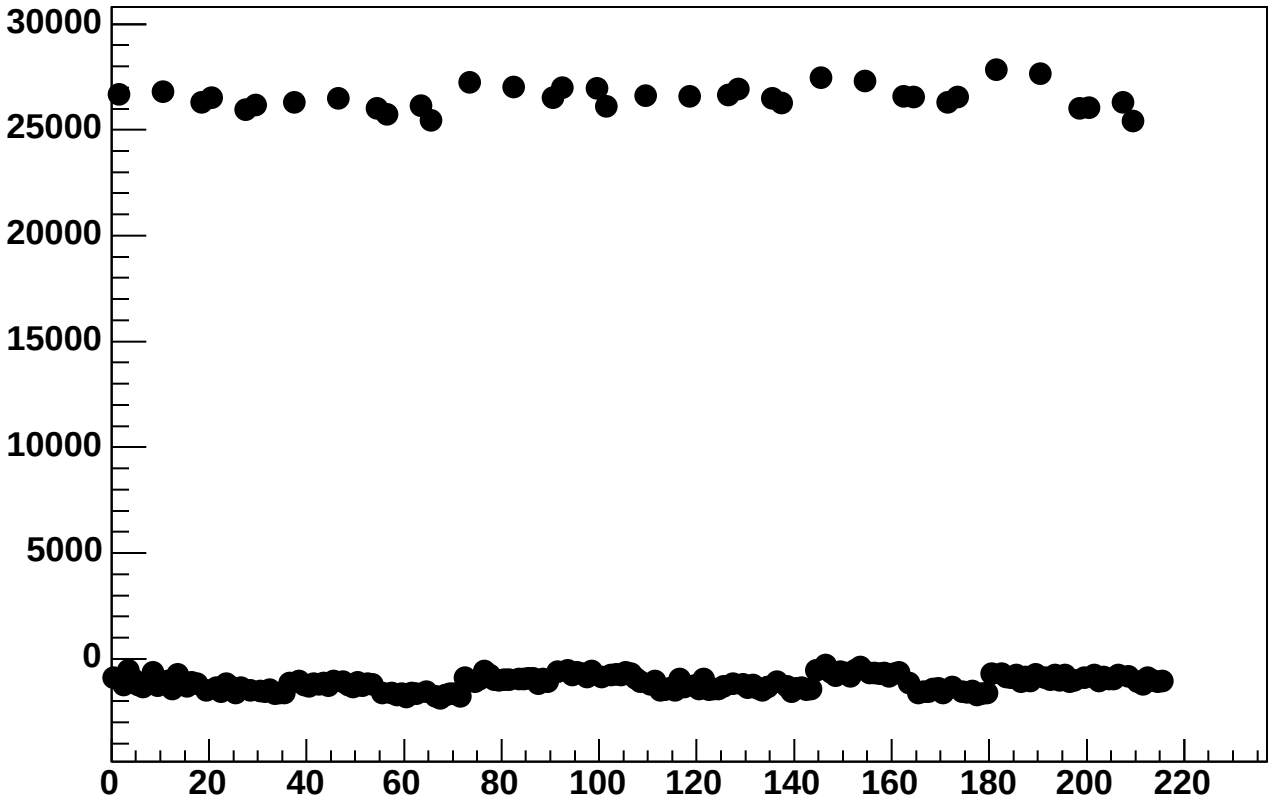
Enable 3, DAC=1600, Hold=30, ADC Mean vs 18*Chip+Chan



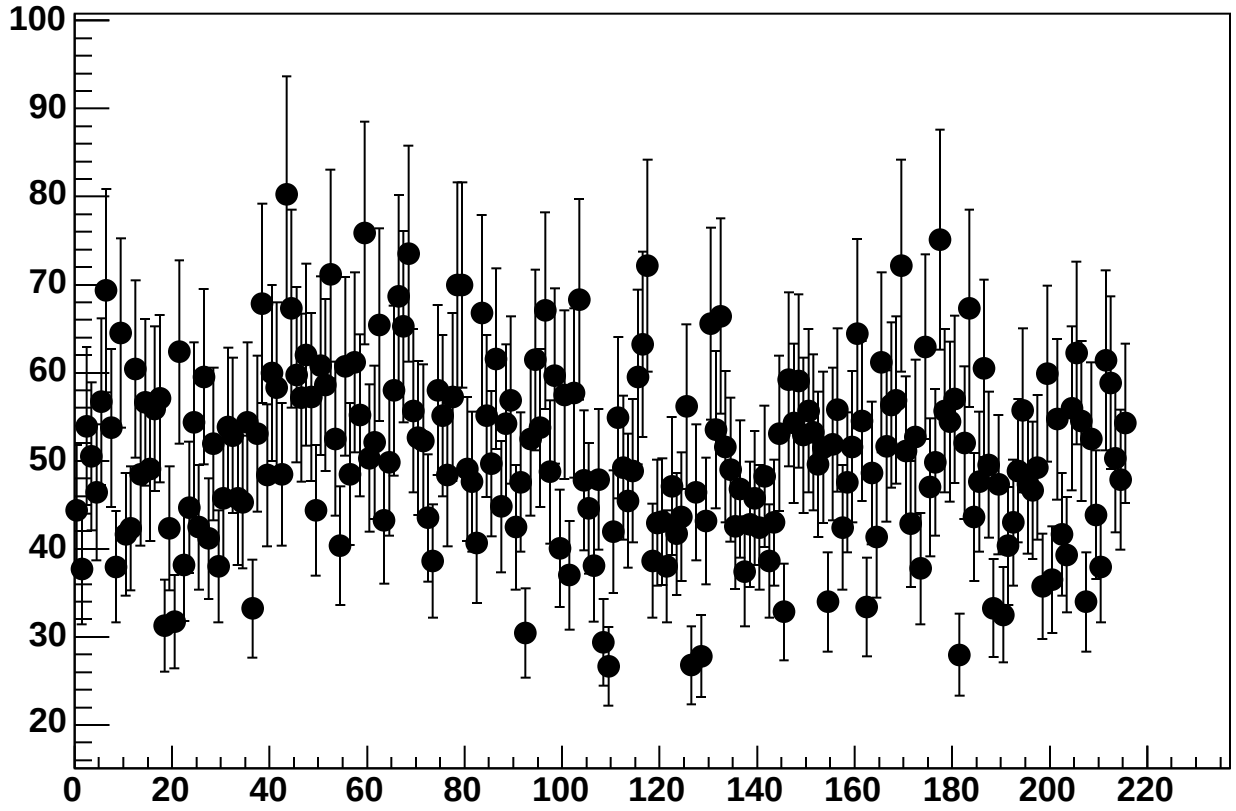
Enable 3, DAC=1600, Hold=30, ADC Noise vs 18*Chip+Chan



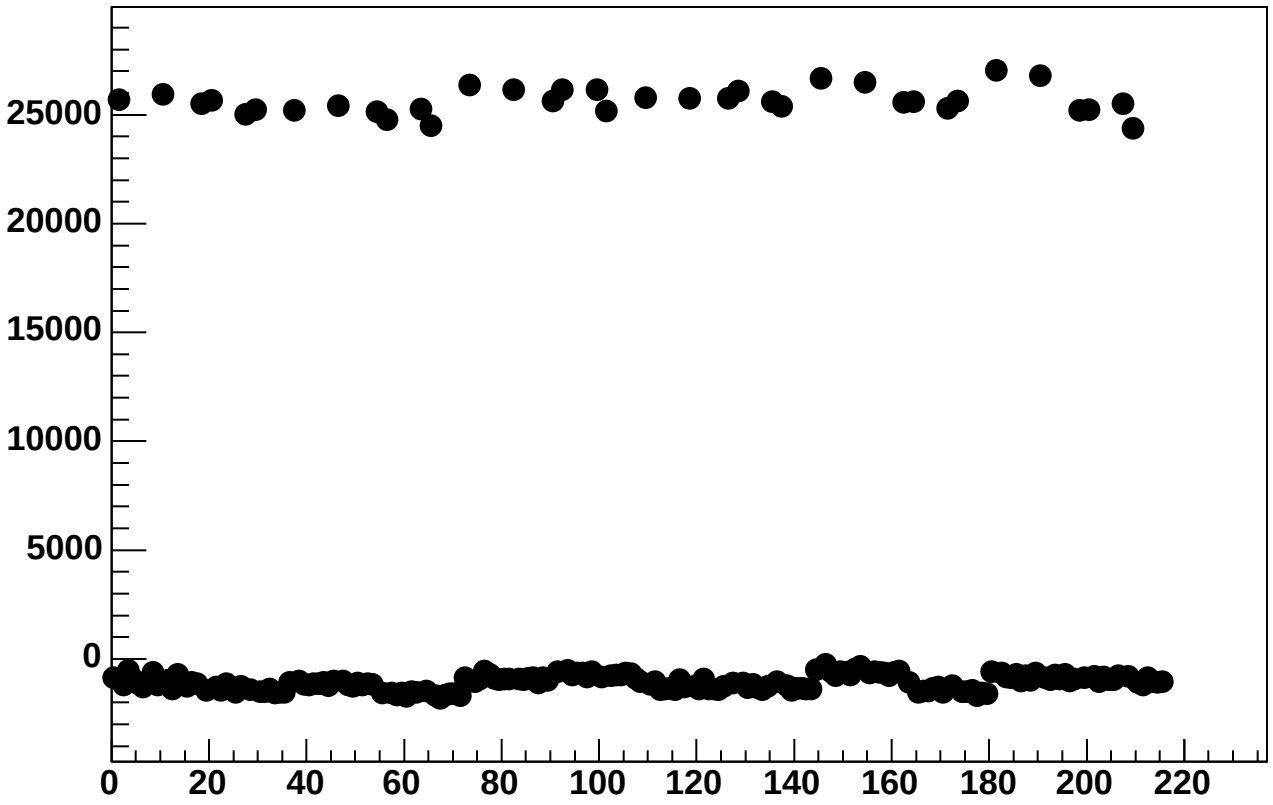
Enable 3, DAC=1600, Hold=35, ADC Mean vs 18*Chip+Chan



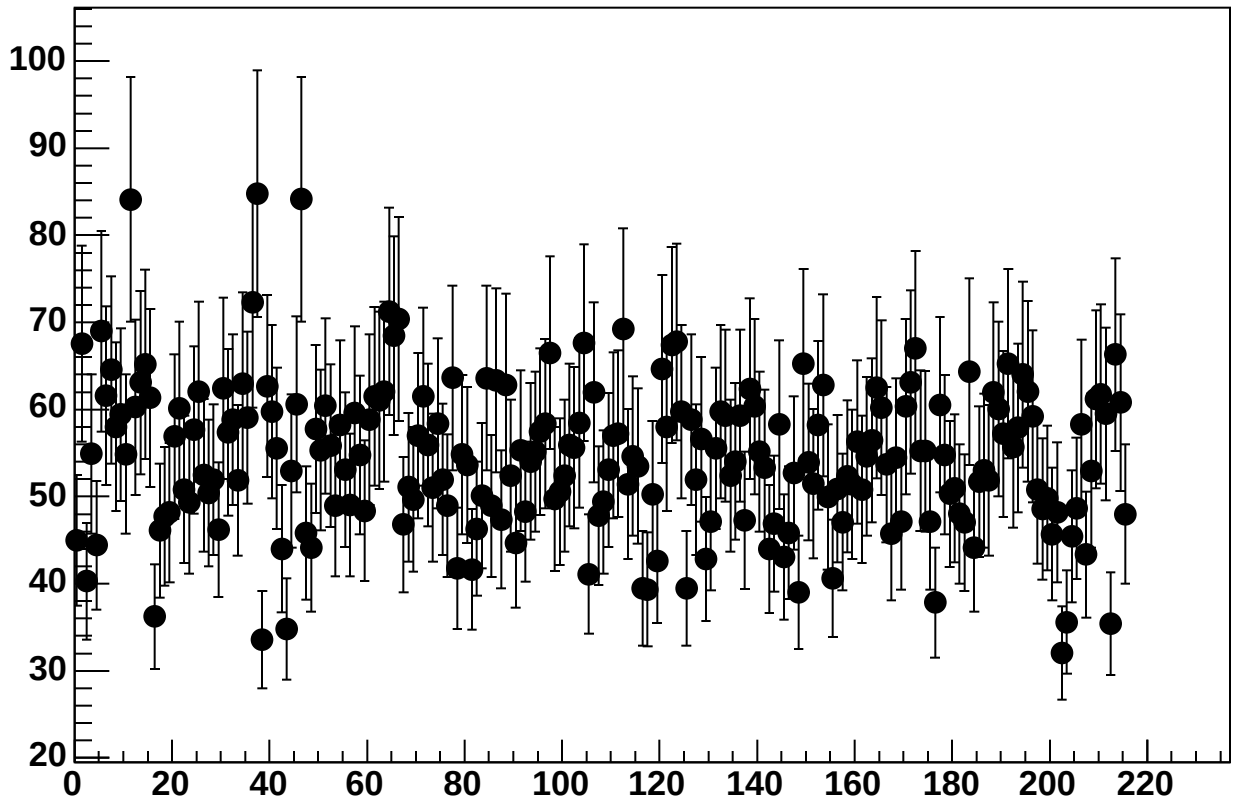
Enable 3, DAC=1600, Hold=35, ADC Noise vs 18*Chip+Chan



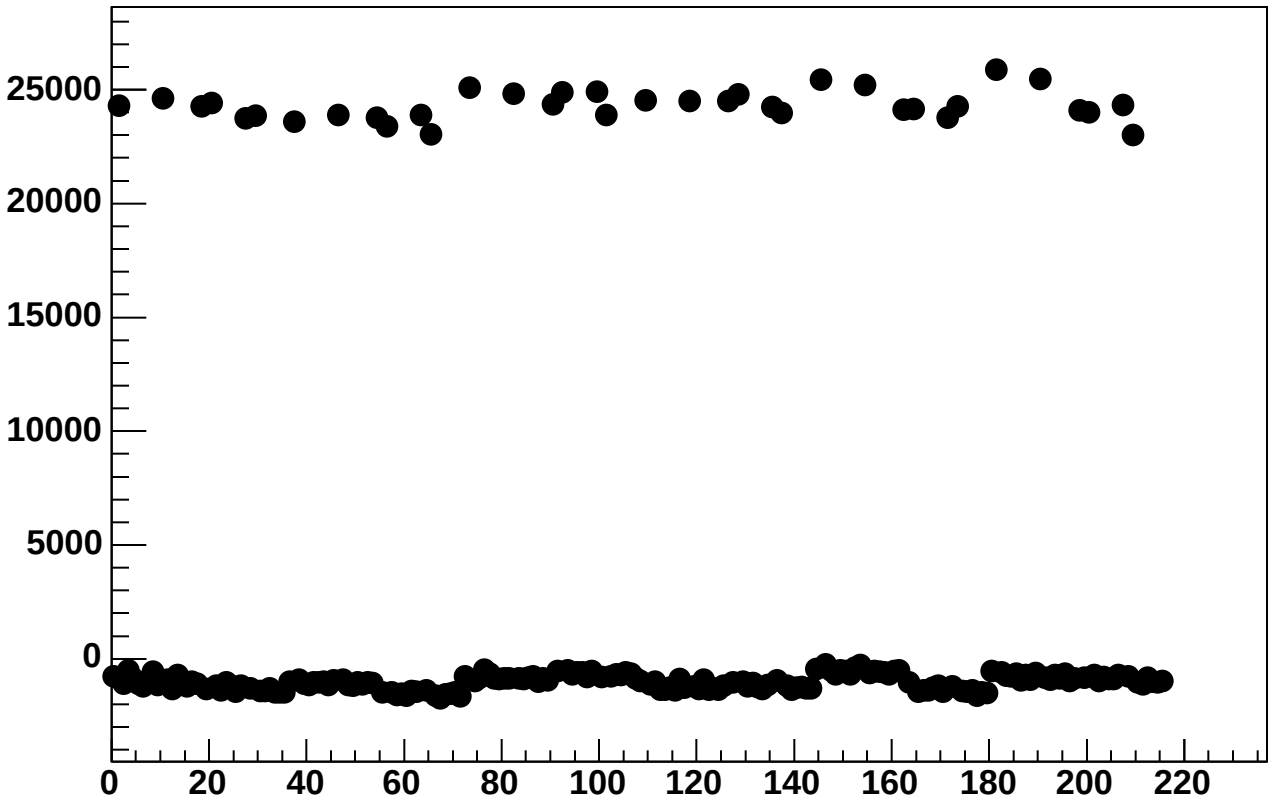
Enable 3, DAC=1600, Hold=40, ADC Mean vs 18*Chip+Chan



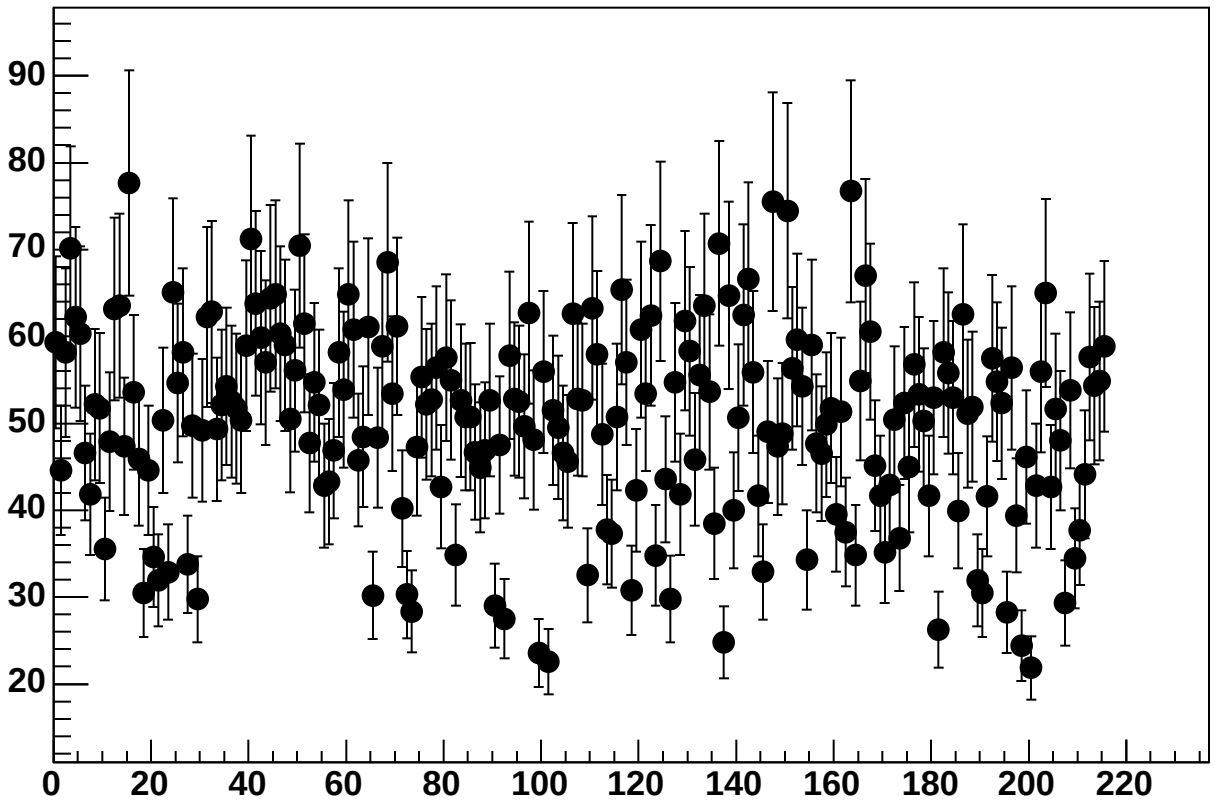
Enable 3, DAC=1600, Hold=40, ADC Noise vs 18*Chip+Chan



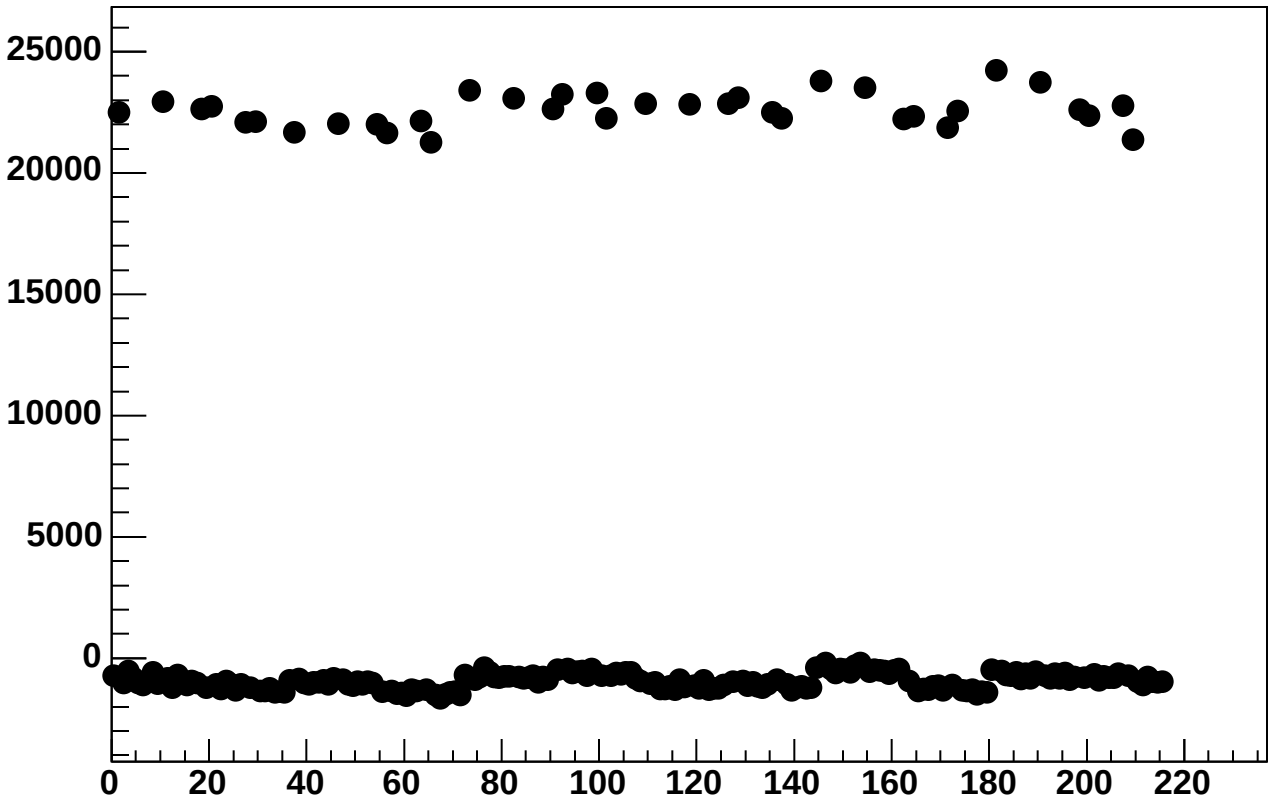
Enable 3, DAC=1600, Hold=45, ADC Mean vs 18*Chip+Chan



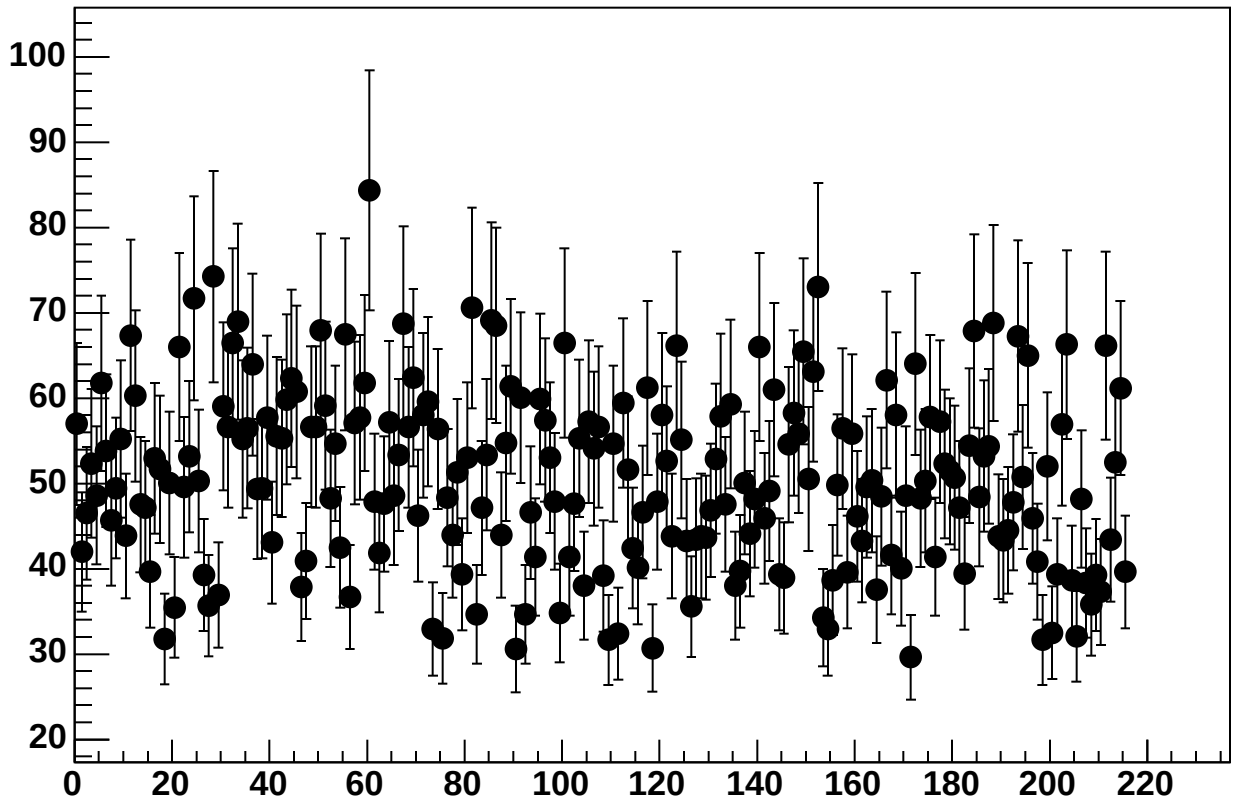
Enable 3, DAC=1600, Hold=45, ADC Noise vs 18*Chip+Chan



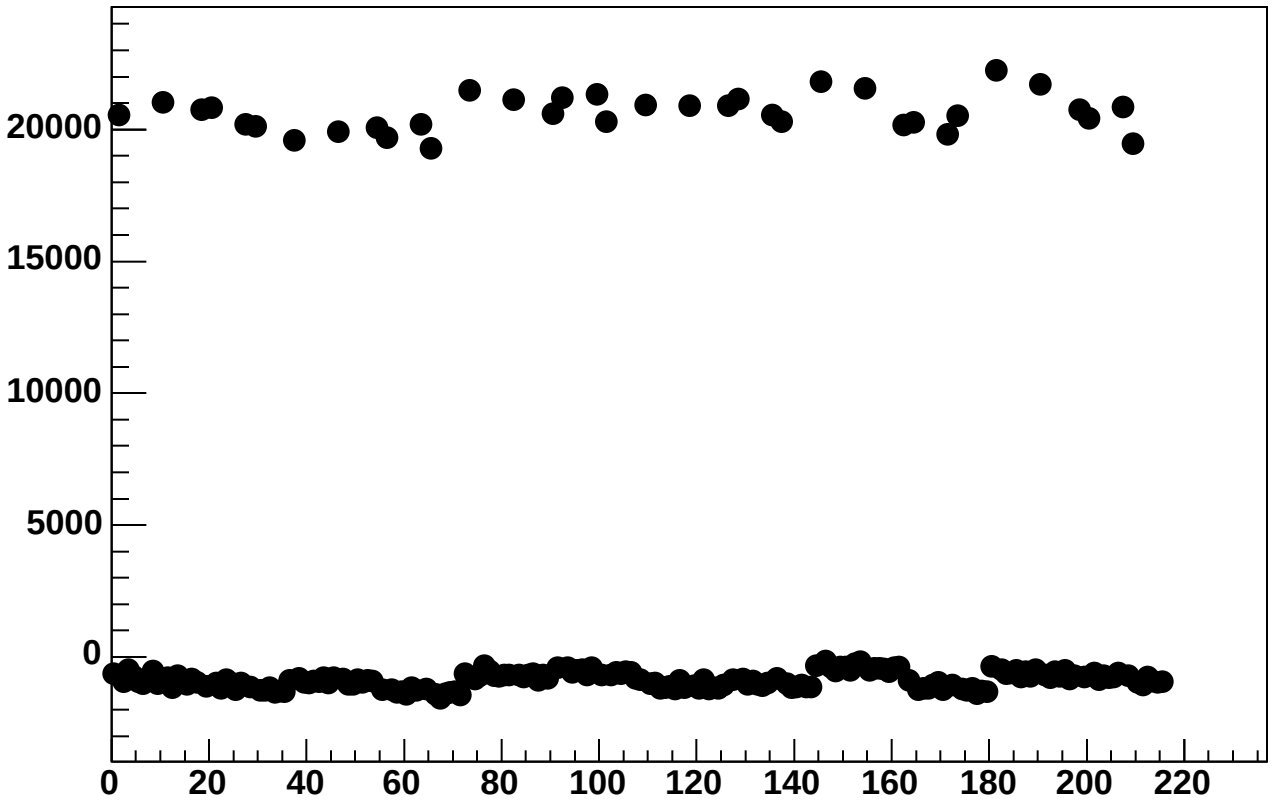
Enable 3, DAC=1600, Hold=50, ADC Mean vs 18*Chip+Chan



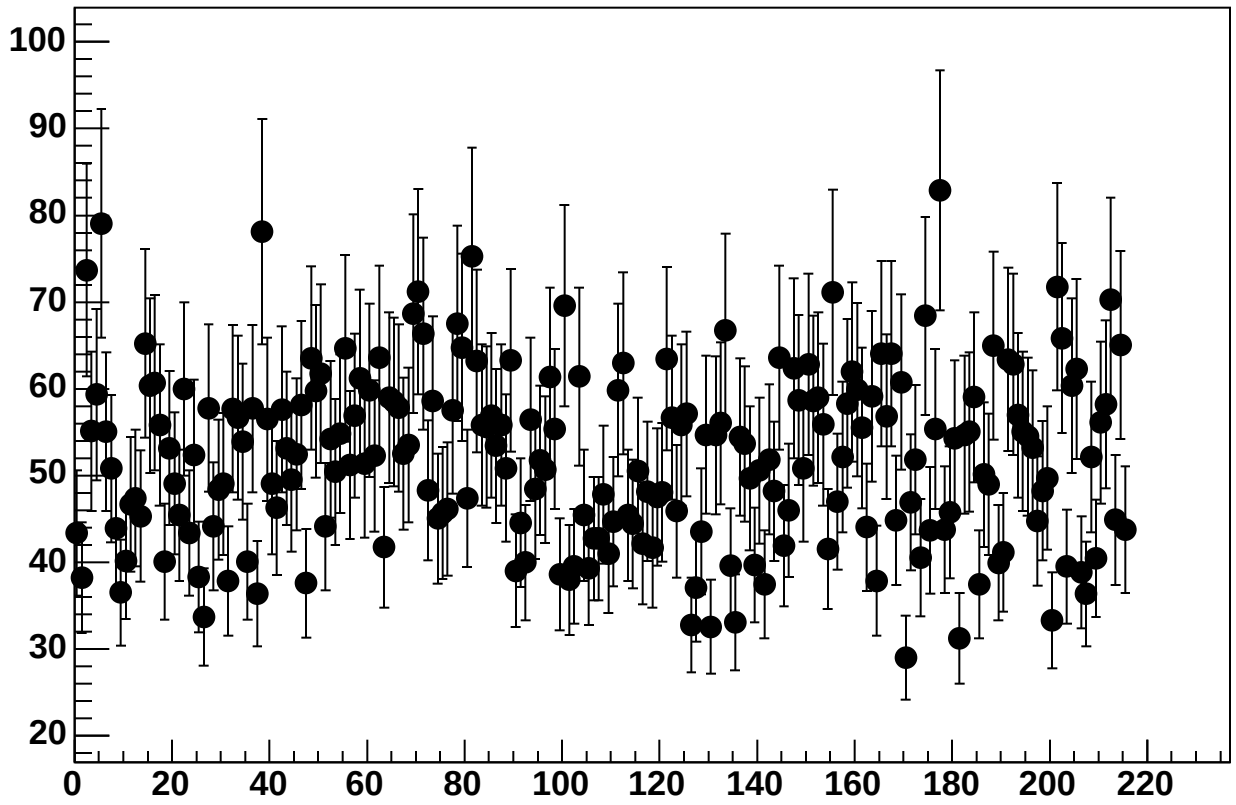
Enable 3, DAC=1600, Hold=50, ADC Noise vs 18*Chip+Chan



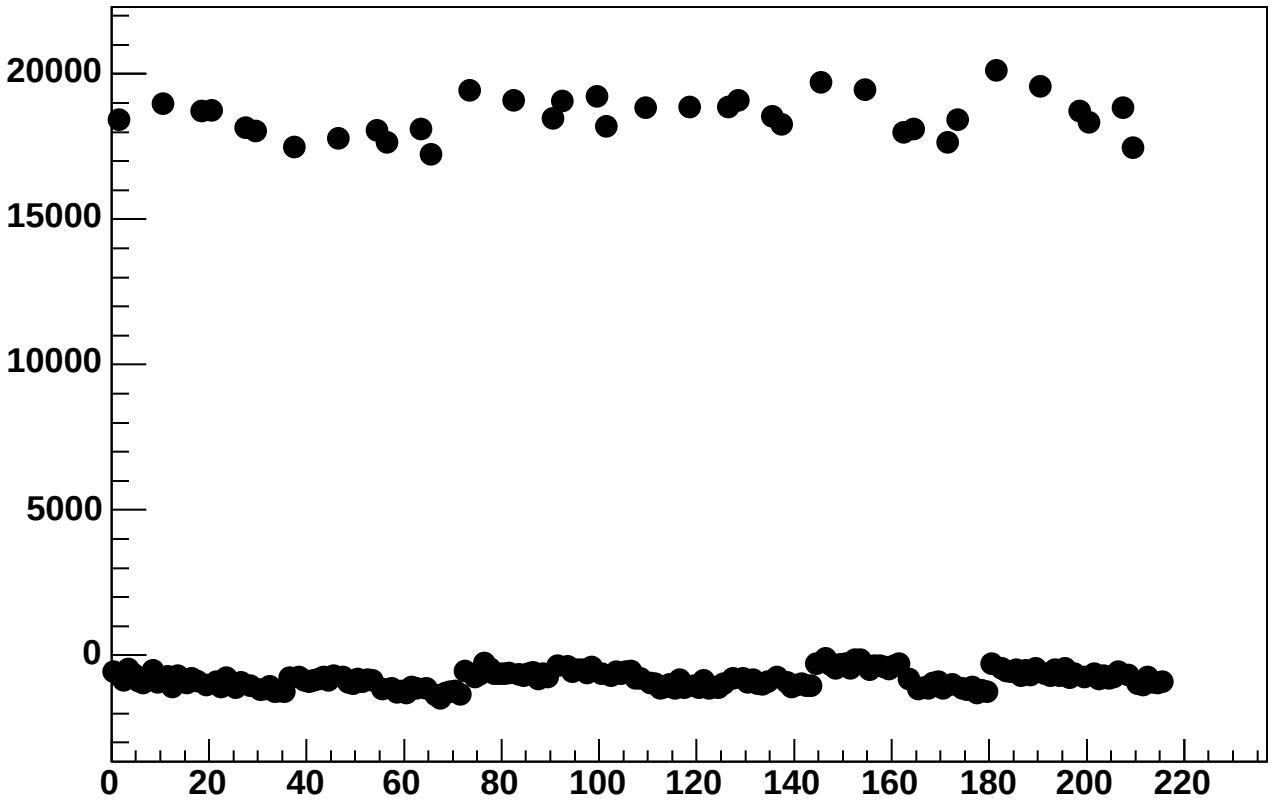
Enable 3, DAC=1600, Hold=55, ADC Mean vs 18*Chip+Chan



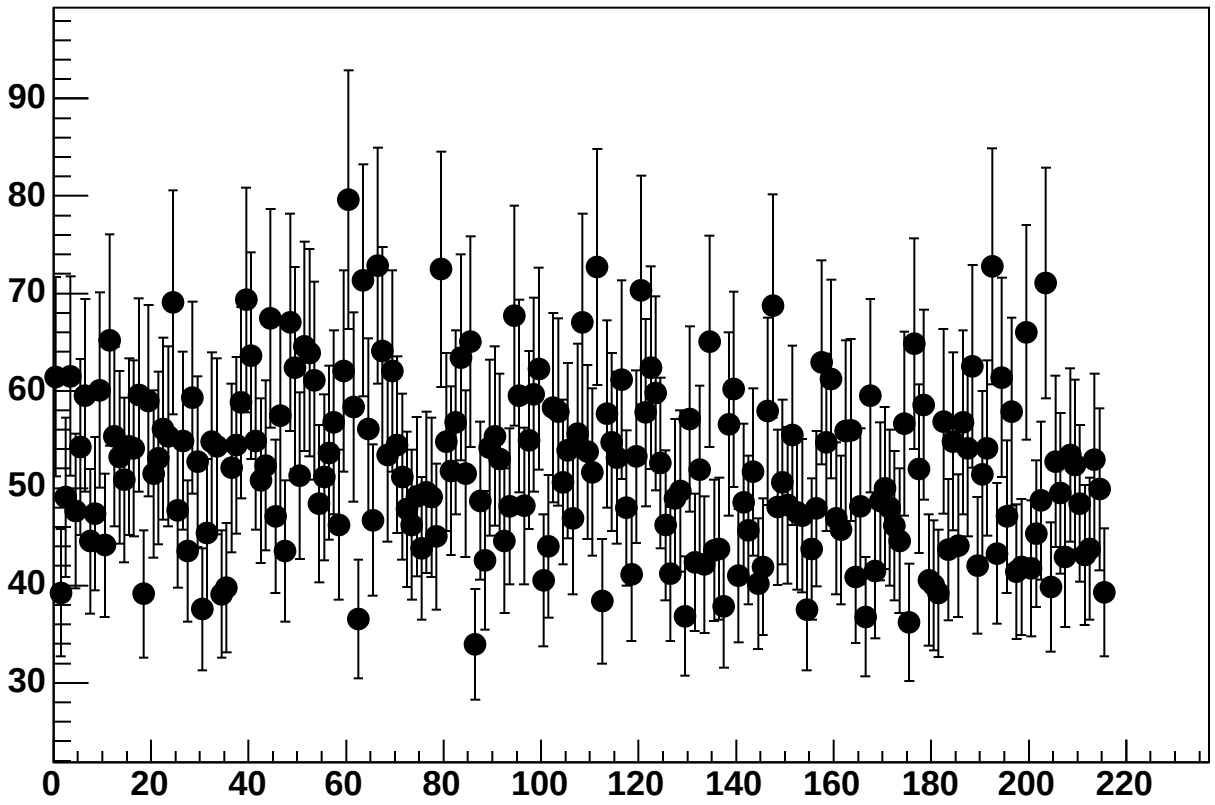
Enable 3, DAC=1600, Hold=55, ADC Noise vs 18*Chip+Chan



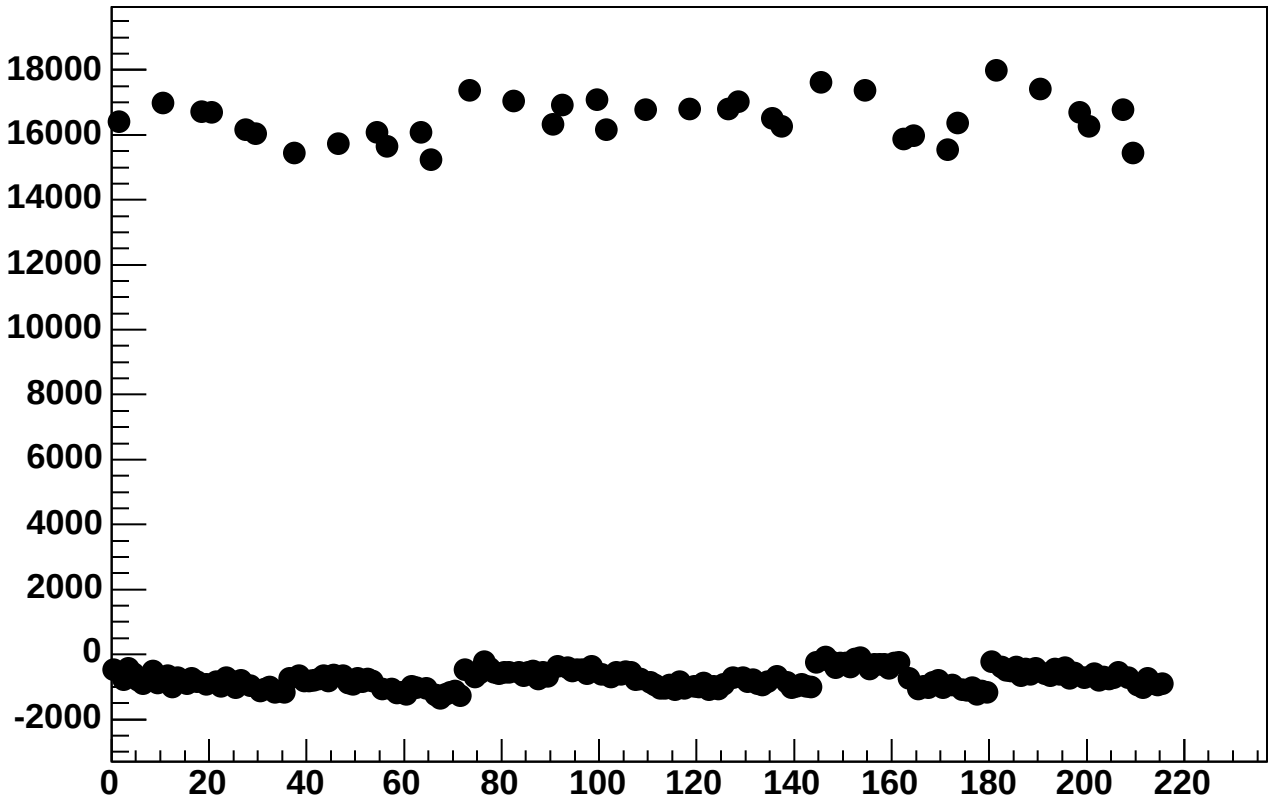
Enable 3, DAC=1600, Hold=60, ADC Mean vs 18*Chip+Chan



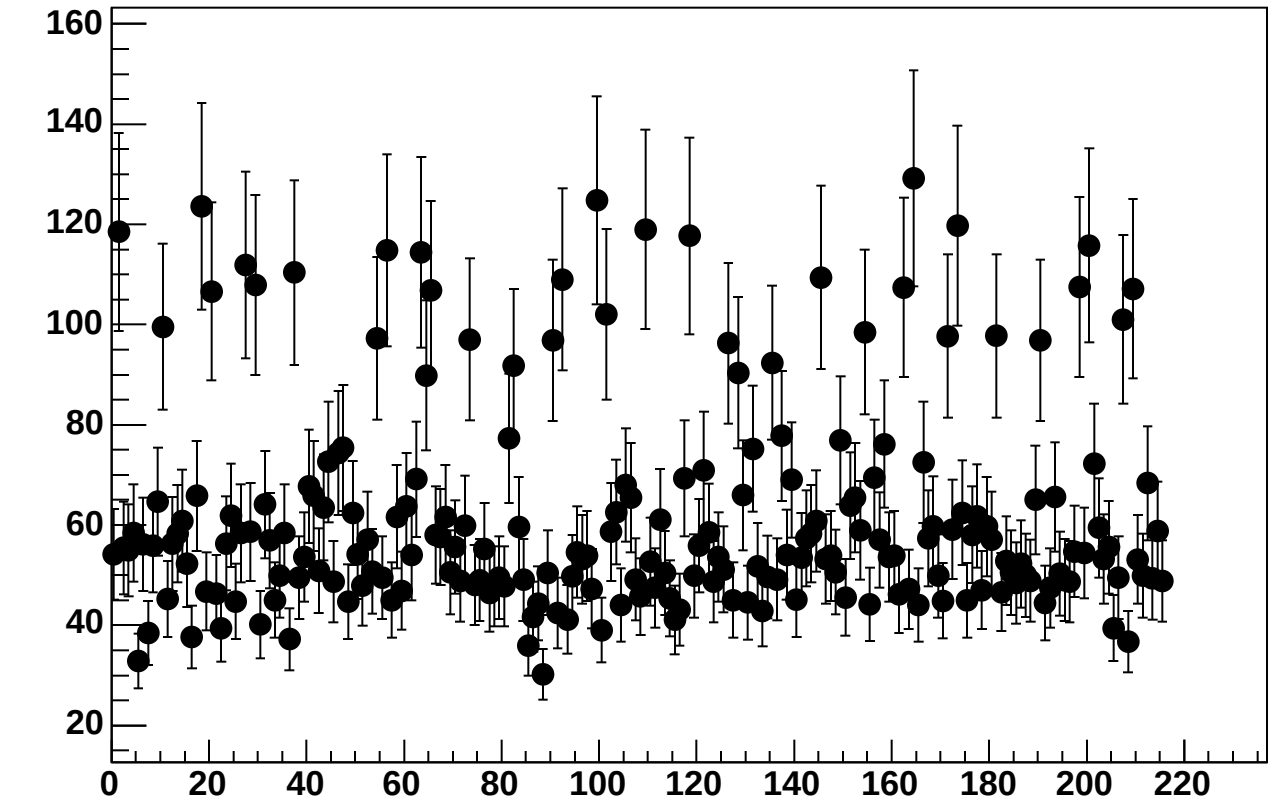
Enable 3, DAC=1600, Hold=60, ADC Noise vs 18*Chip+Chan



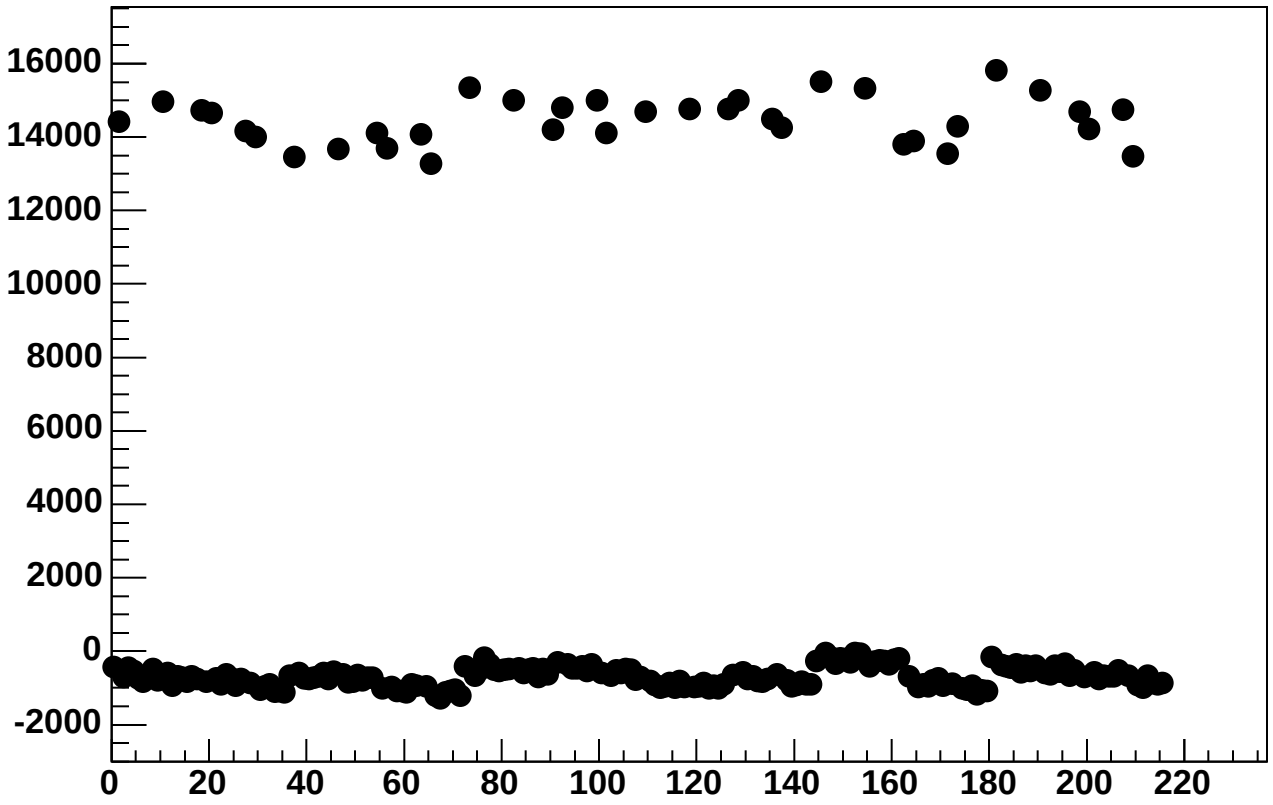
Enable 3, DAC=1600, Hold=65, ADC Mean vs 18*Chip+Chan



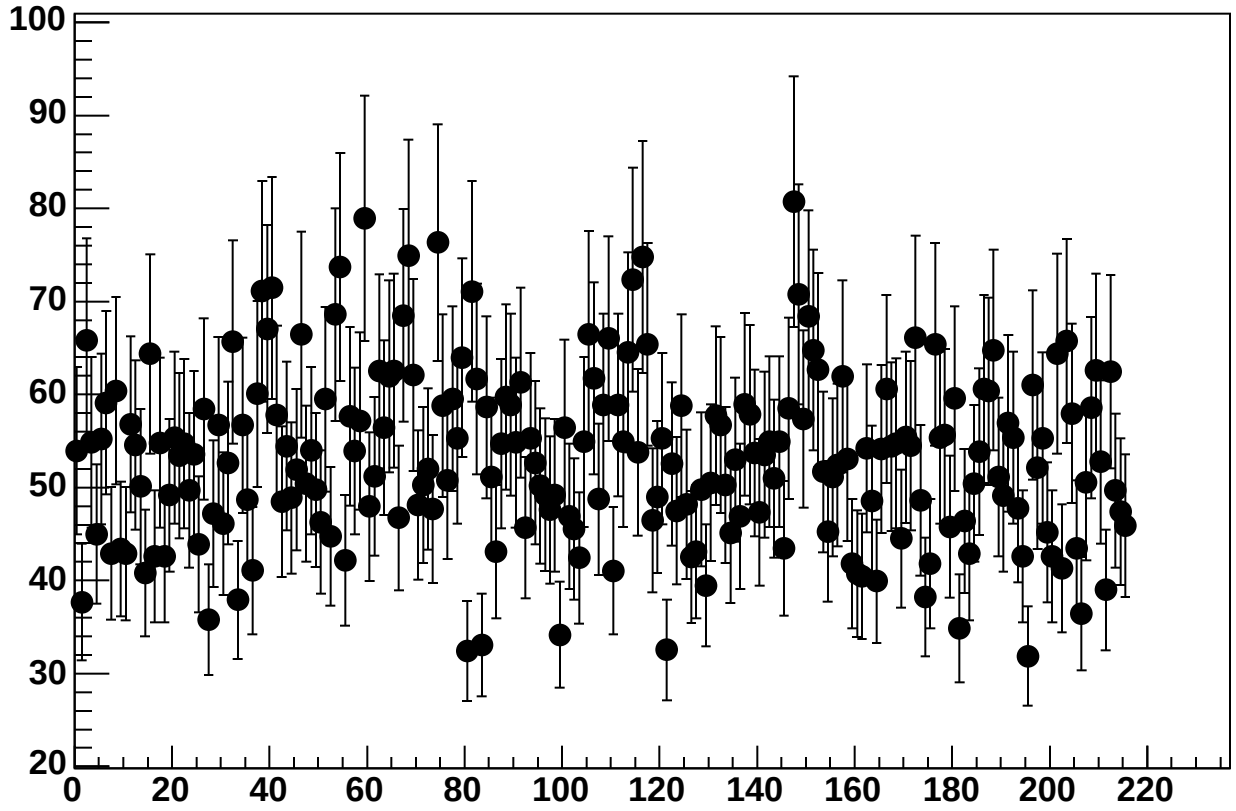
Enable 3, DAC=1600, Hold=65, ADC Noise vs 18*Chip+Chan



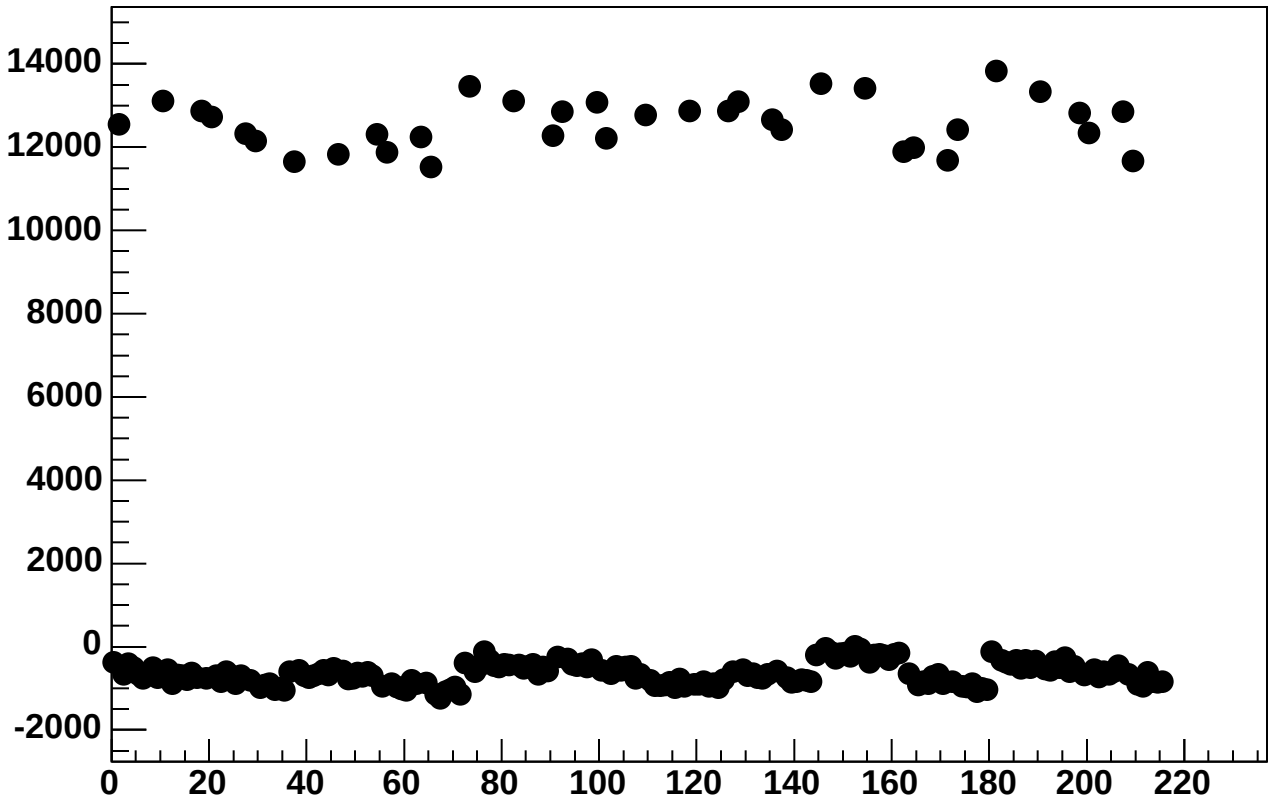
Enable 3, DAC=1600, Hold=70, ADC Mean vs 18*Chip+Chan



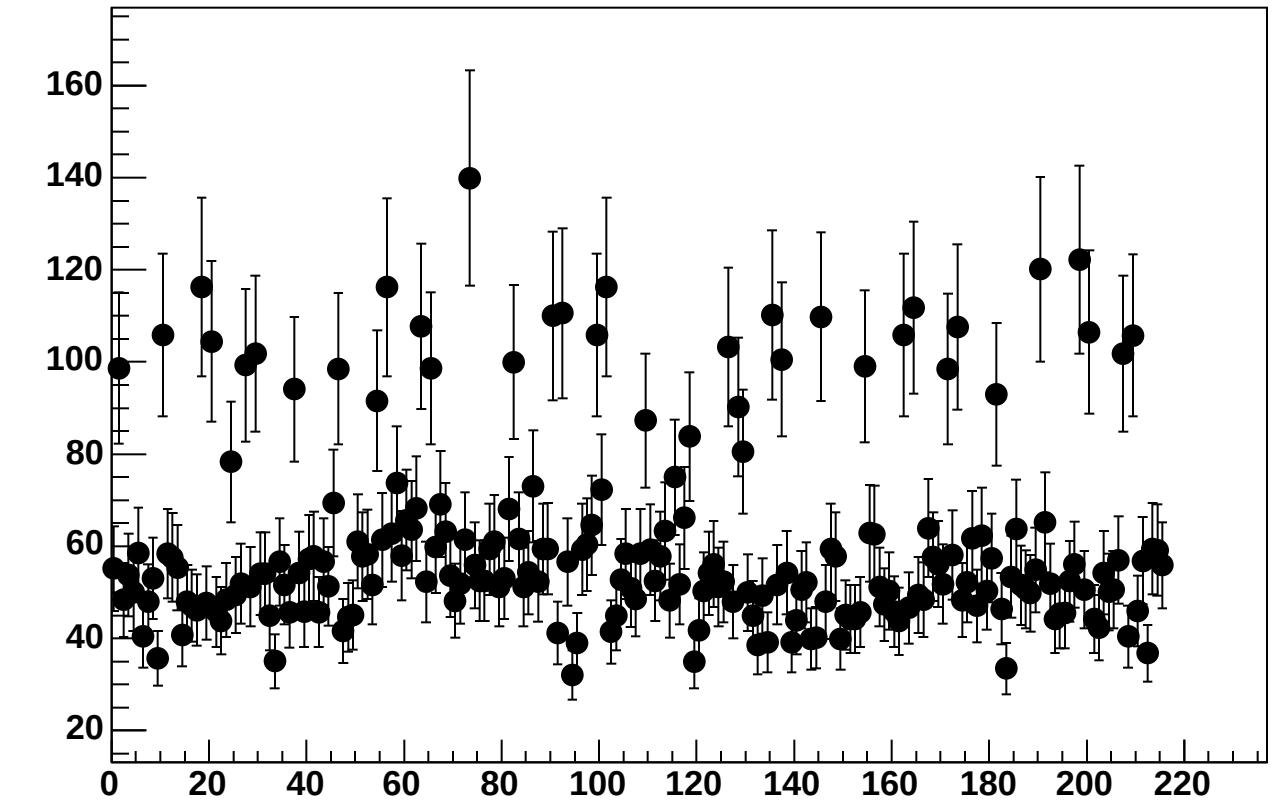
Enable 3, DAC=1600, Hold=70, ADC Noise vs 18*Chip+Chan



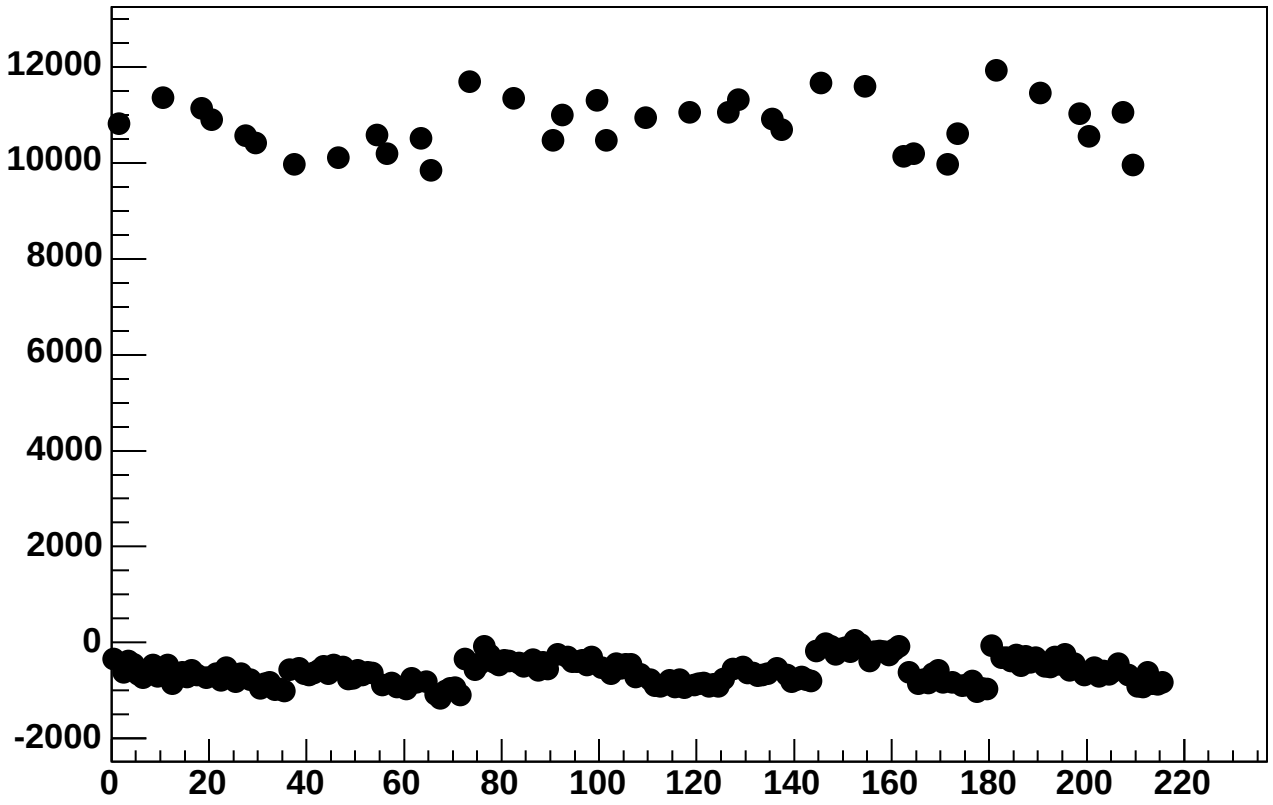
Enable 3, DAC=1600, Hold=75, ADC Mean vs 18*Chip+Chan



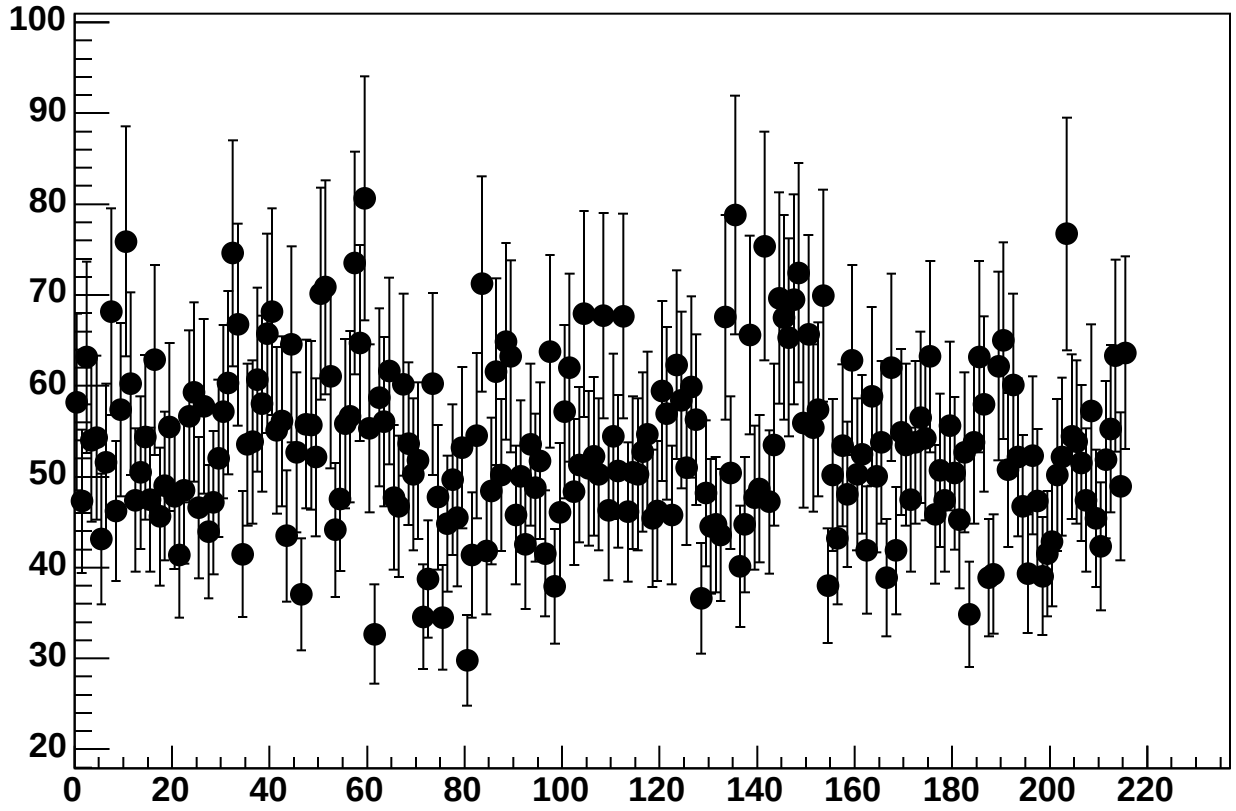
Enable 3, DAC=1600, Hold=75, ADC Noise vs 18*Chip+Chan



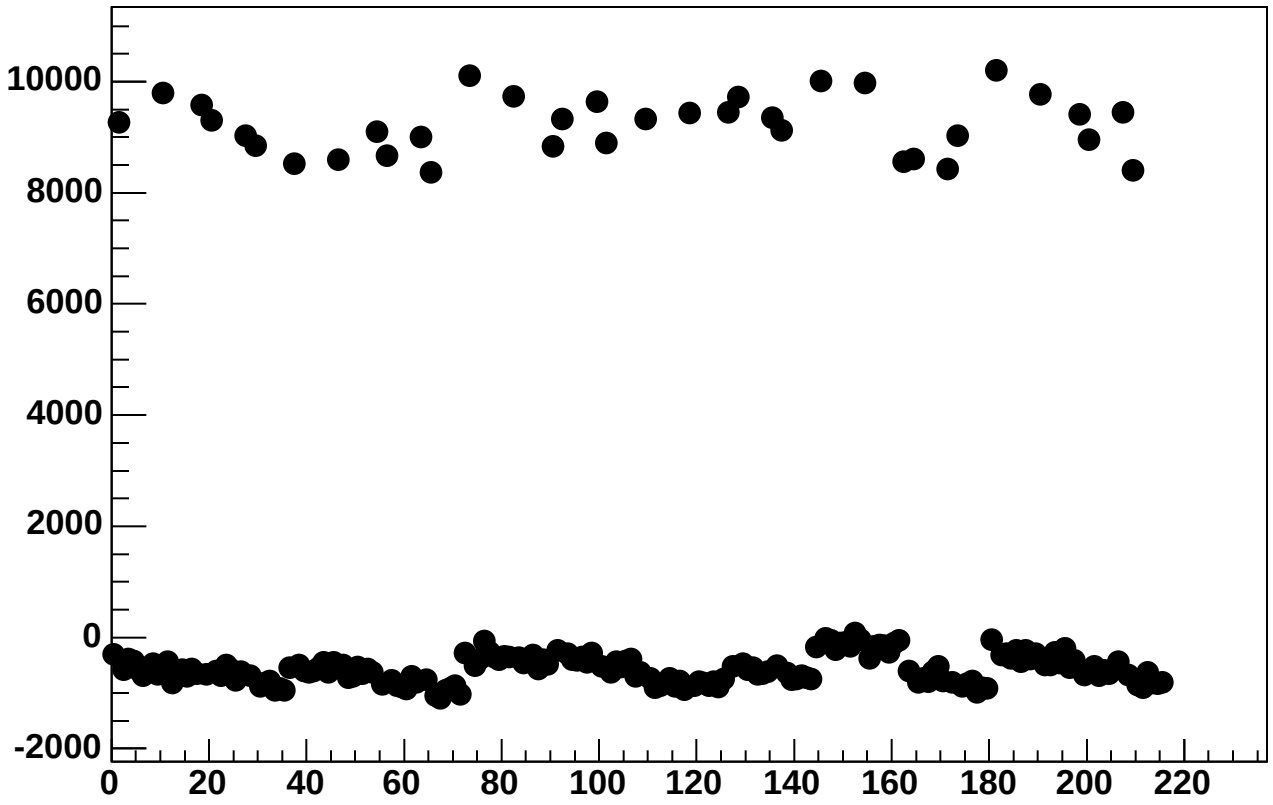
Enable 3, DAC=1600, Hold=80, ADC Mean vs 18*Chip+Chan



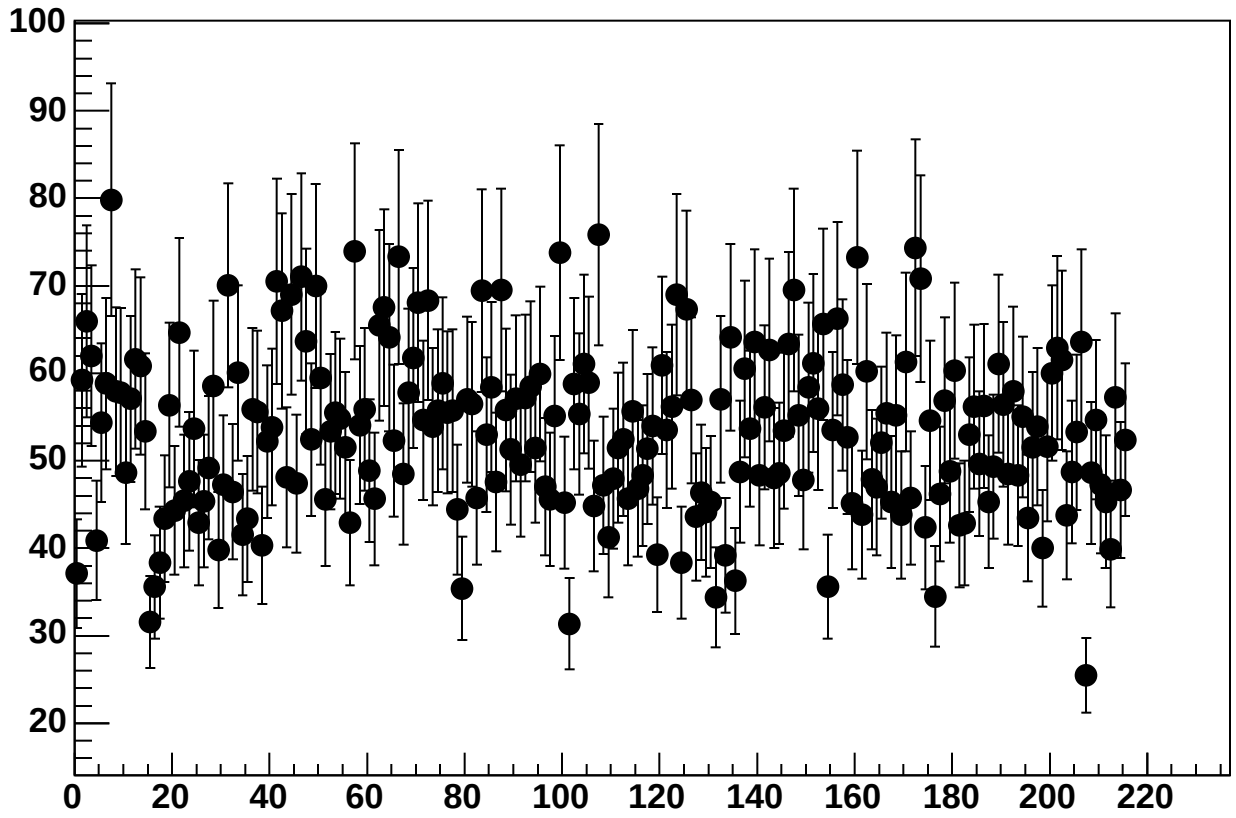
Enable 3, DAC=1600, Hold=80, ADC Noise vs 18*Chip+Chan



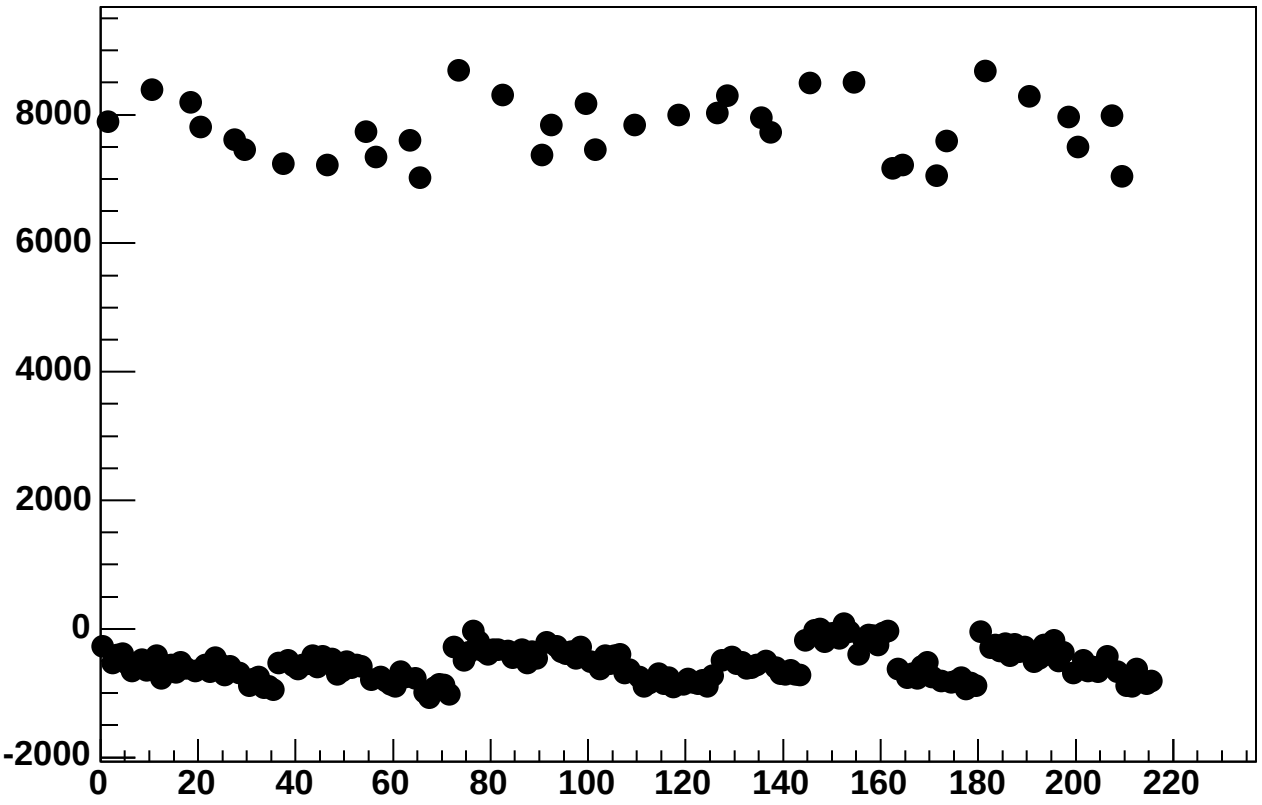
Enable 3, DAC=1600, Hold=85, ADC Mean vs 18*Chip+Chan



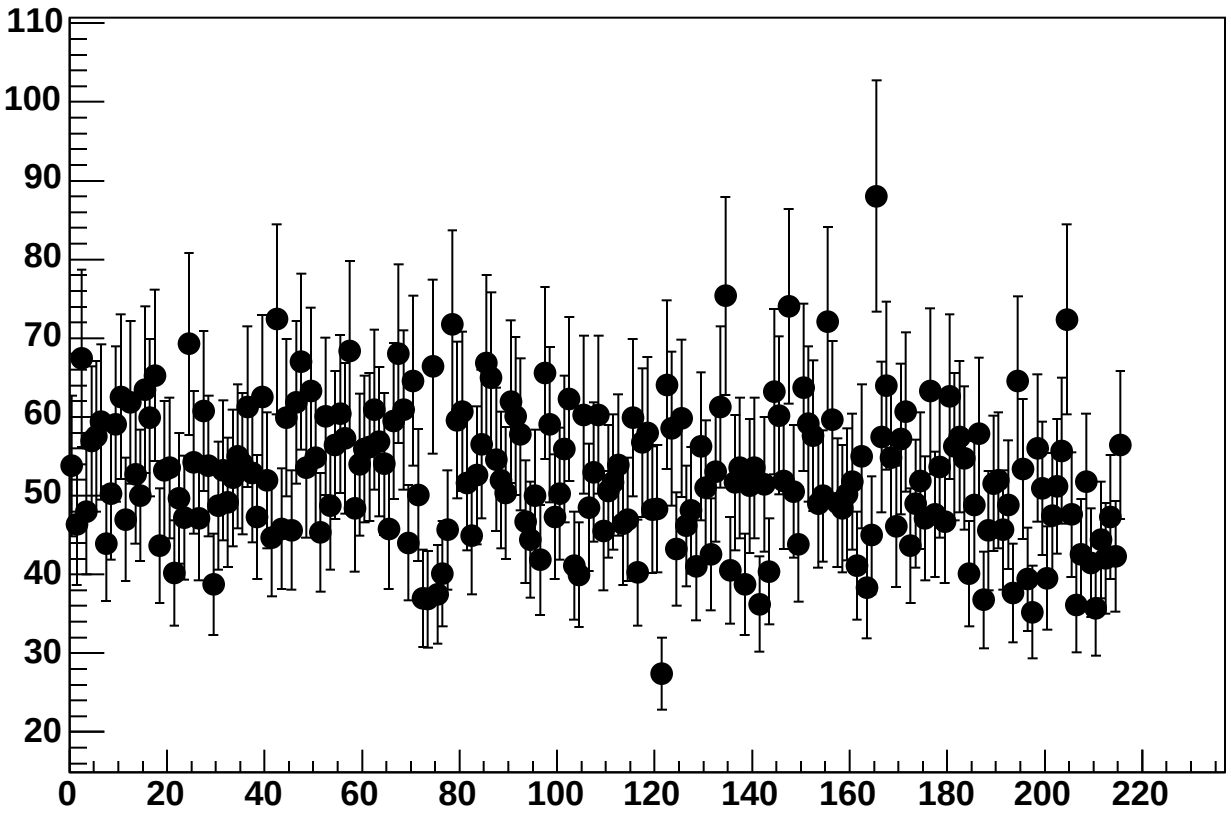
Enable 3, DAC=1600, Hold=85, ADC Noise vs 18*Chip+Chan



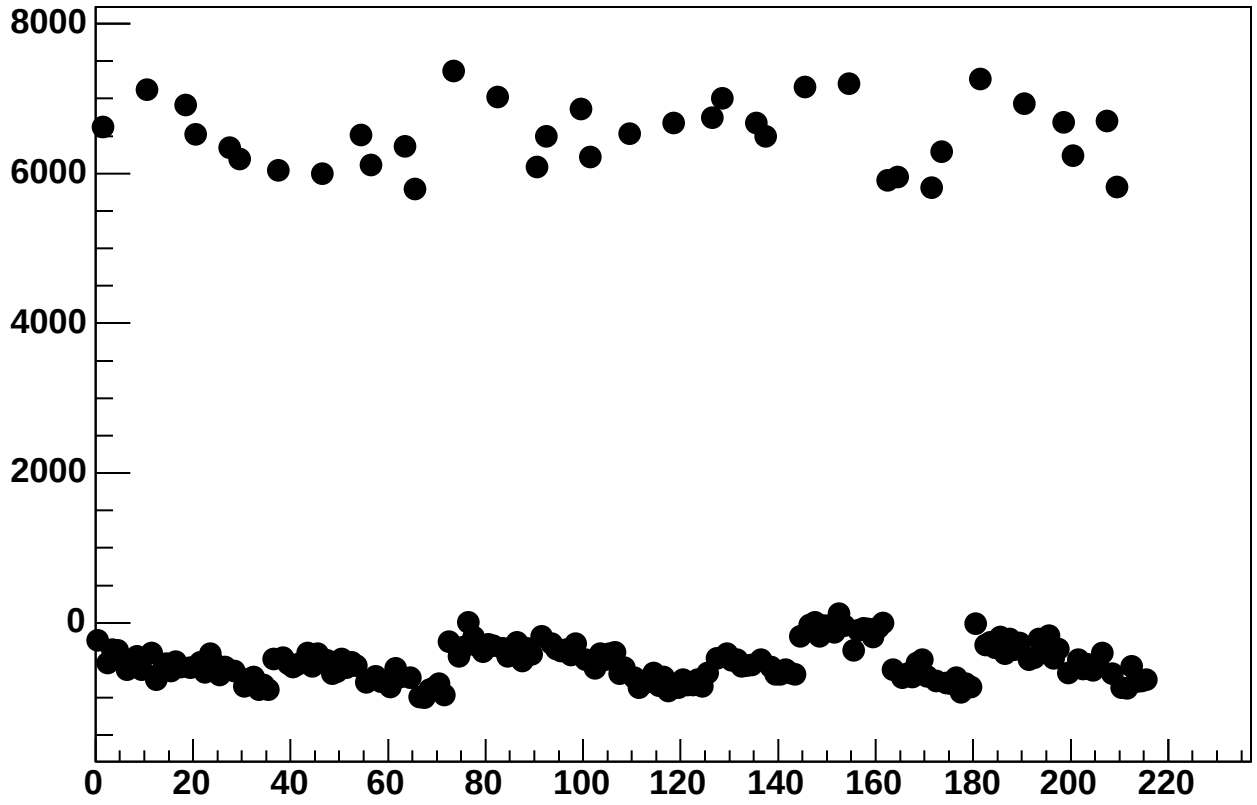
Enable 3, DAC=1600, Hold=90, ADC Mean vs 18*Chip+Chan



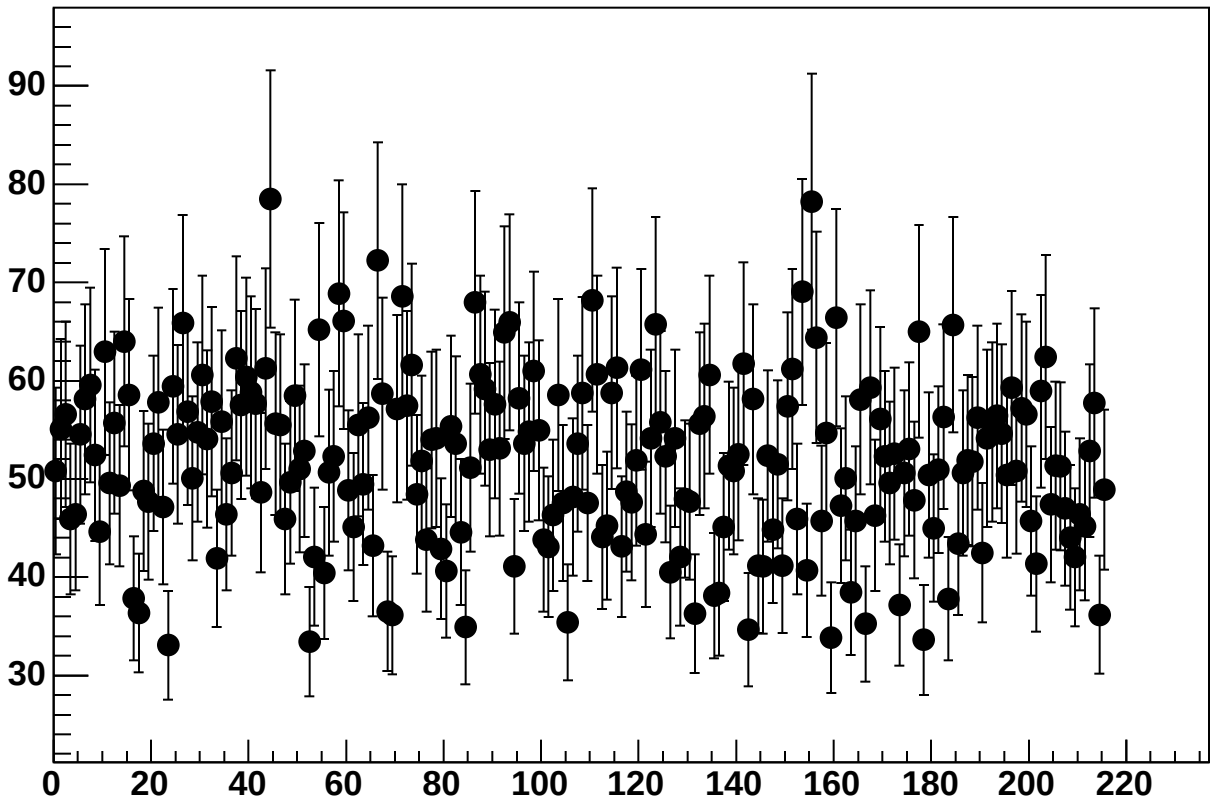
Enable 3, DAC=1600, Hold=90, ADC Noise vs 18*Chip+Chan



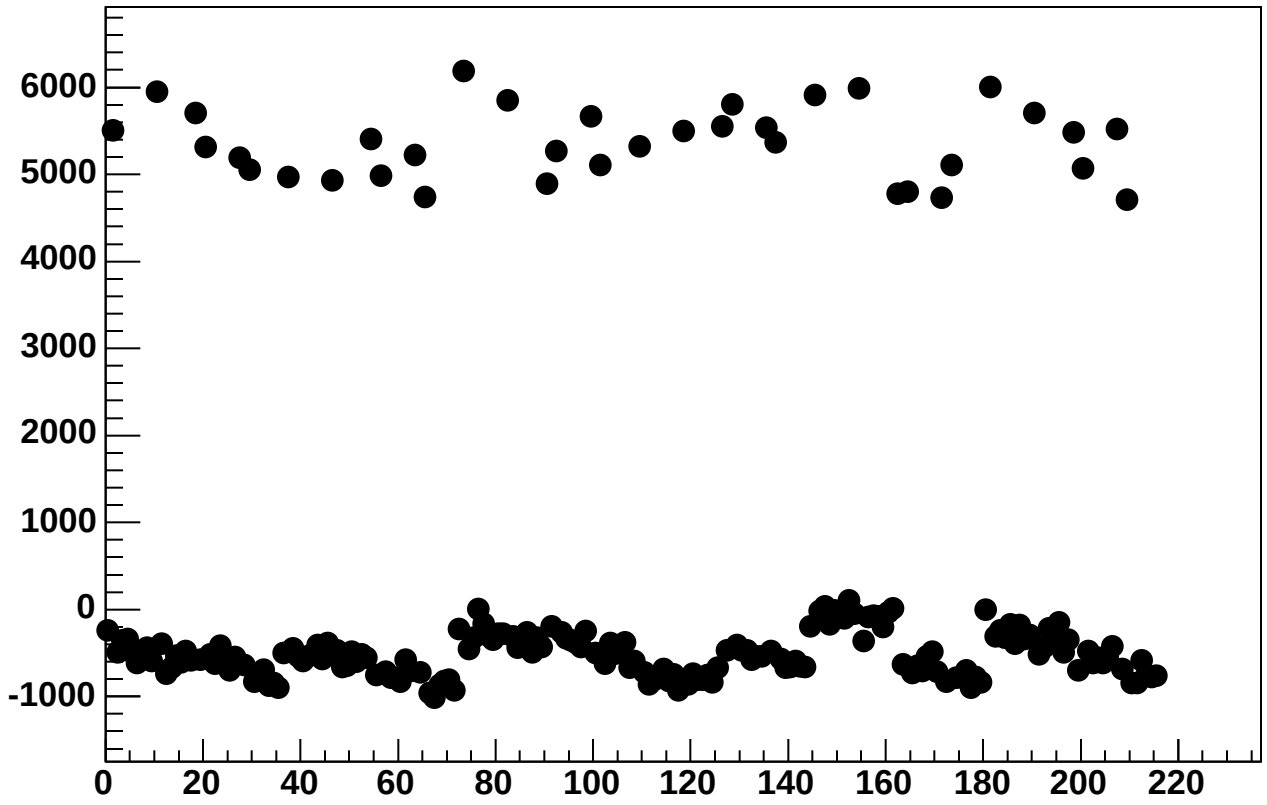
Enable 3, DAC=1600, Hold=95, ADC Mean vs 18*Chip+Chan



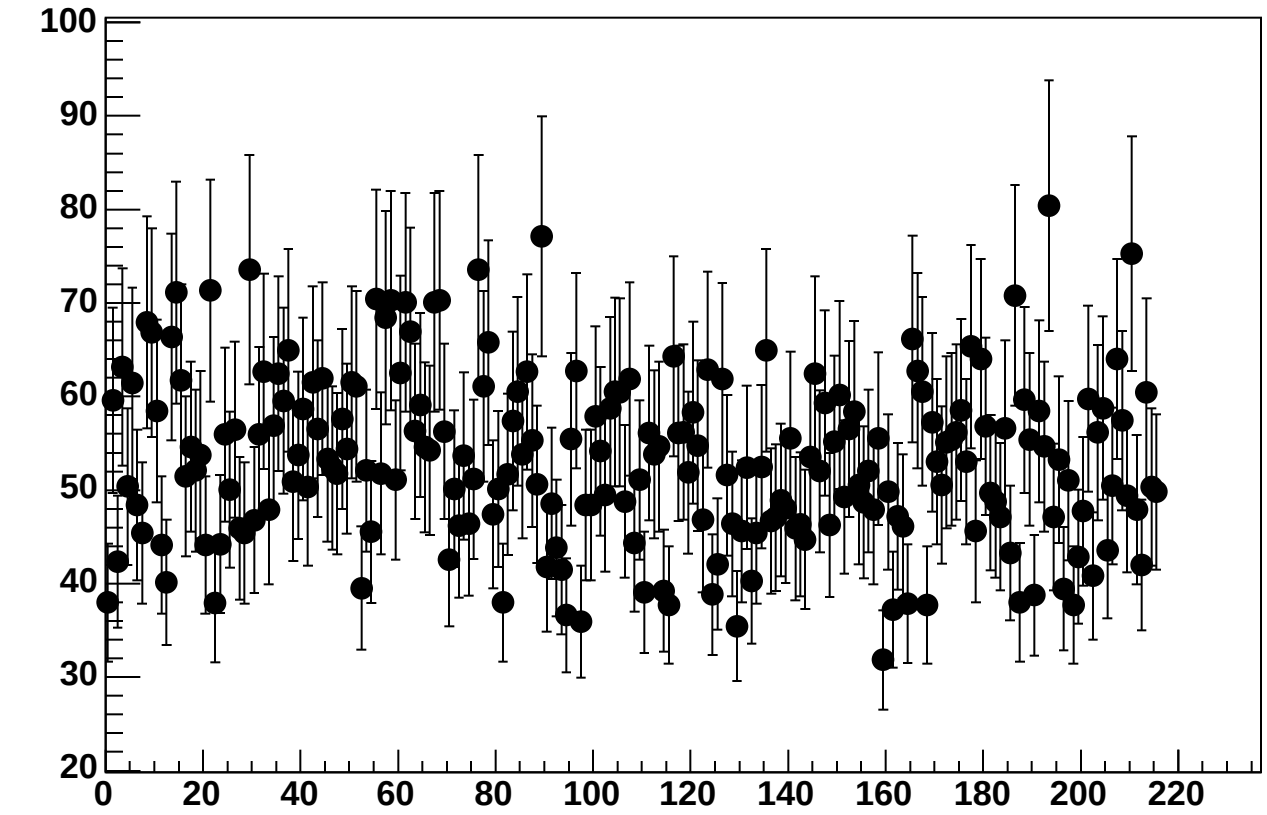
Enable 3, DAC=1600, Hold=95, ADC Noise vs 18*Chip+Chan



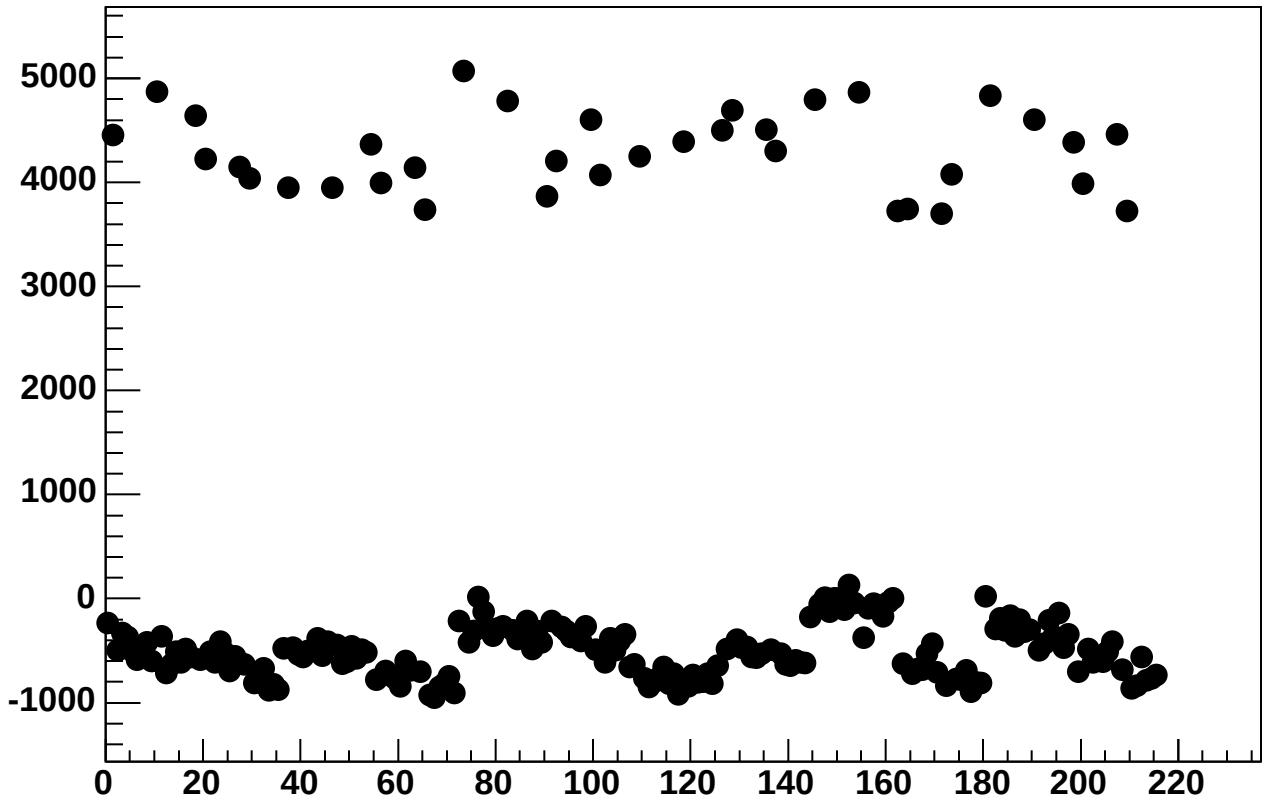
Enable 3, DAC=1600, Hold=100, ADC Mean vs 18*Chip+Chan



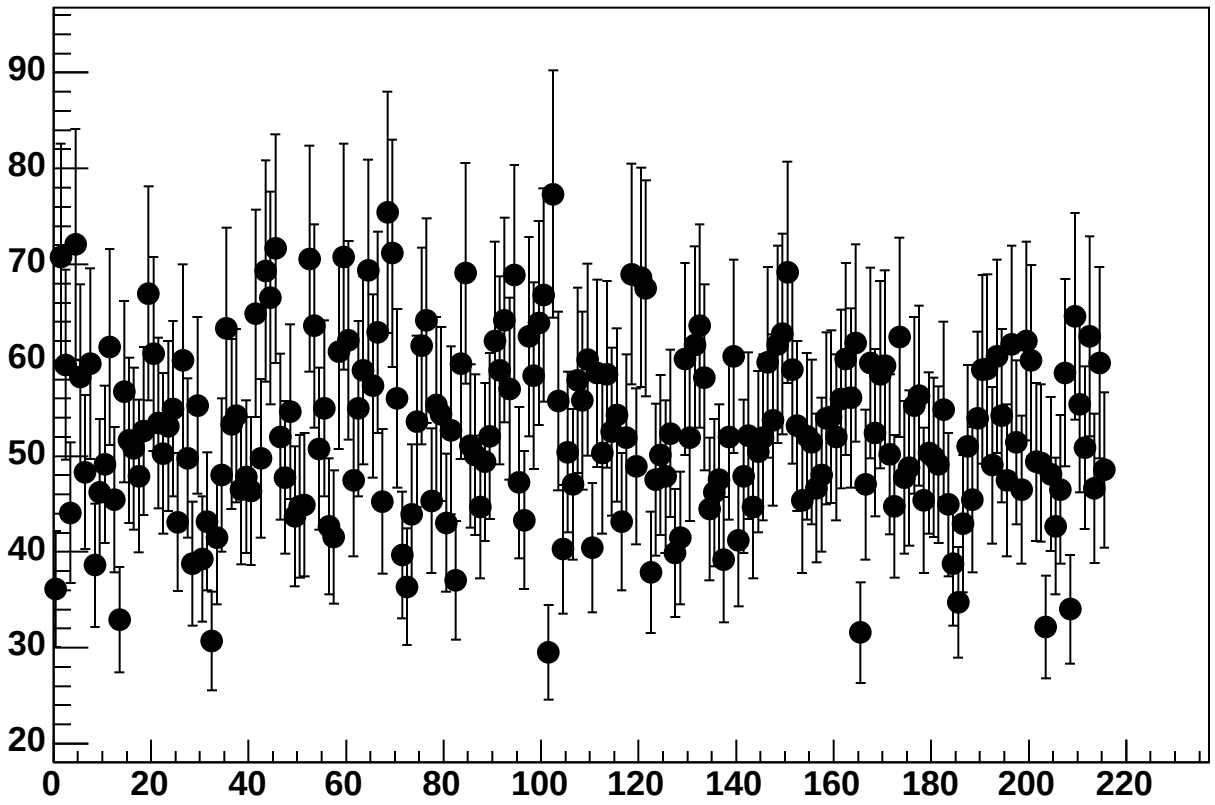
Enable 3, DAC=1600, Hold=100, ADC Noise vs 18*Chip+Chan



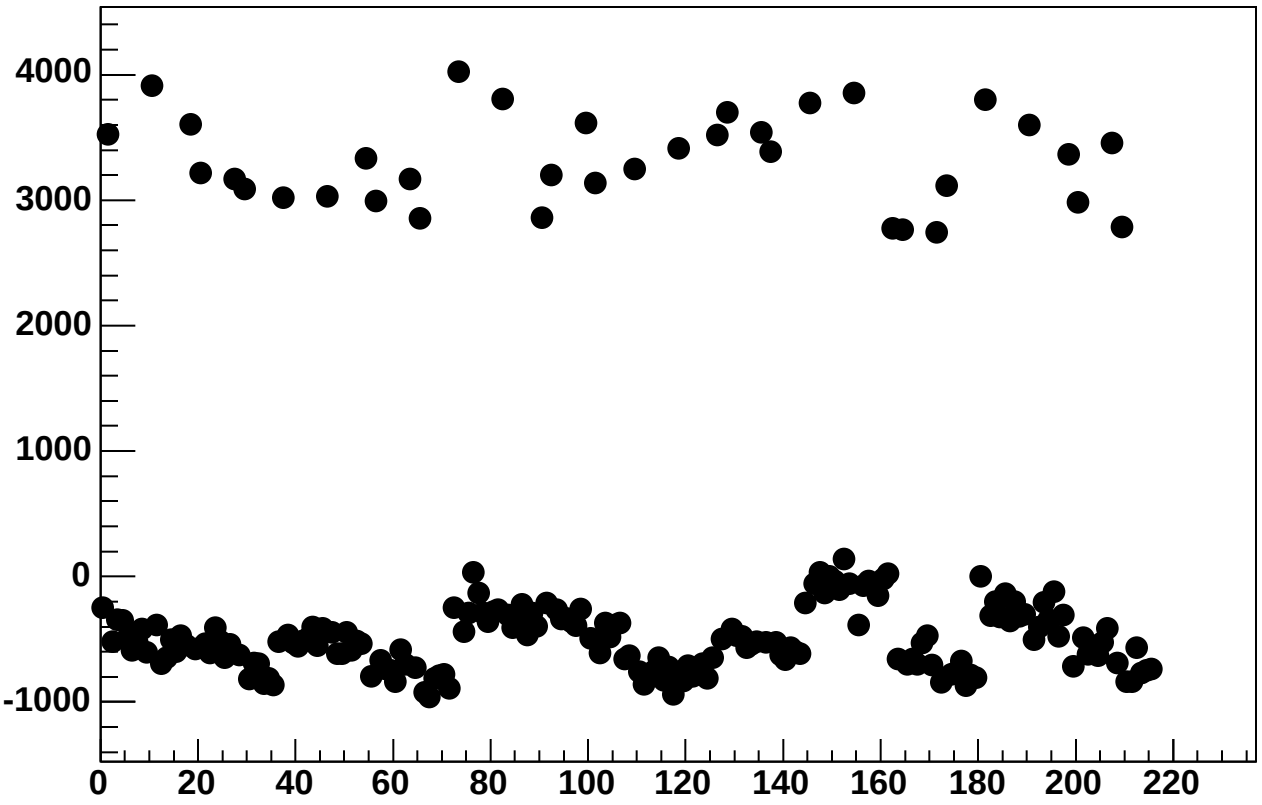
Enable 3, DAC=1600, Hold=105, ADC Mean vs 18*Chip+Chan



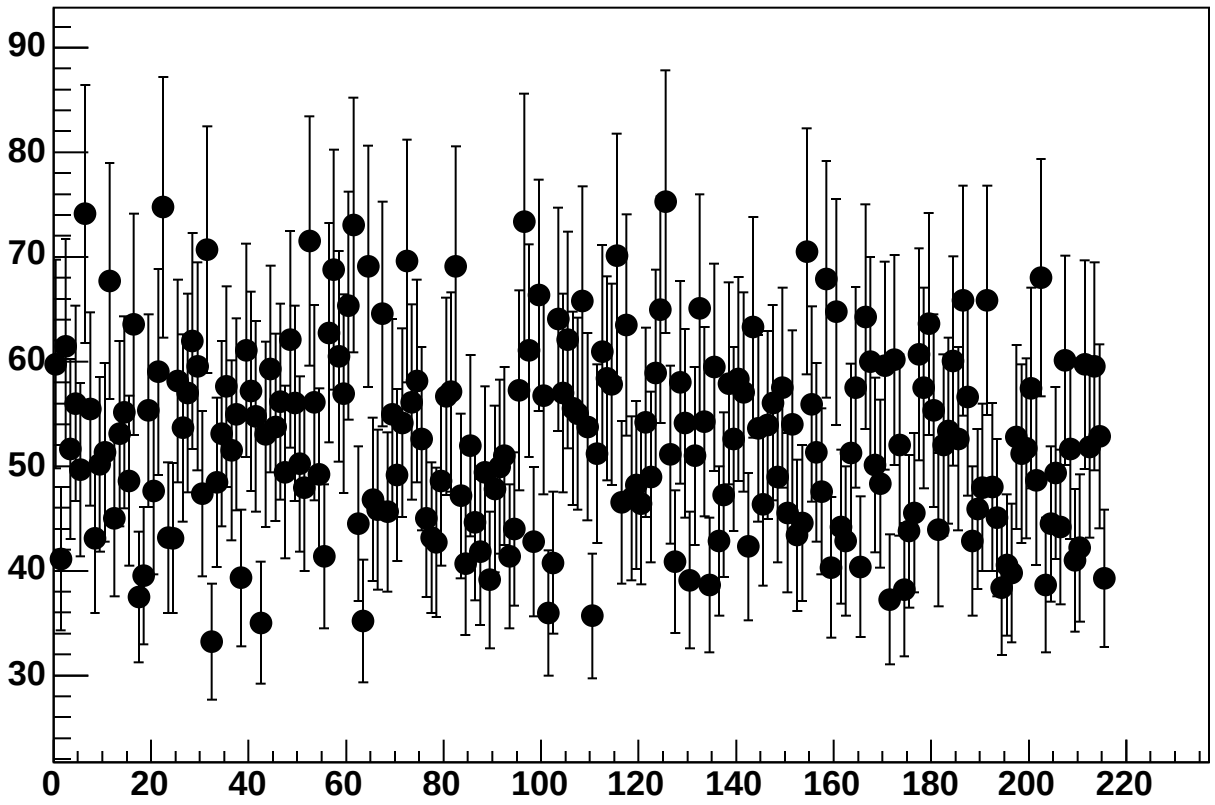
Enable 3, DAC=1600, Hold=105, ADC Noise vs 18*Chip+Chan



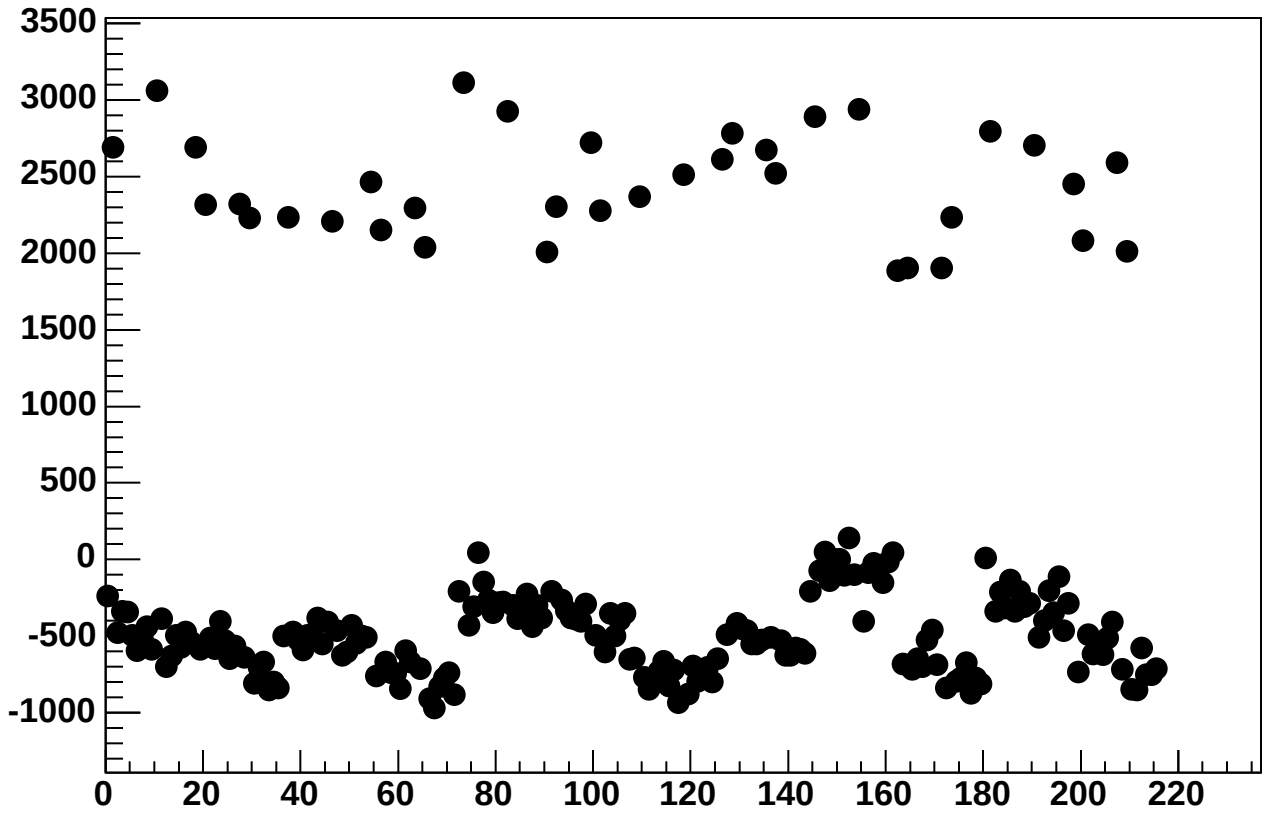
Enable 3, DAC=1600, Hold=110, ADC Mean vs 18*Chip+Chan



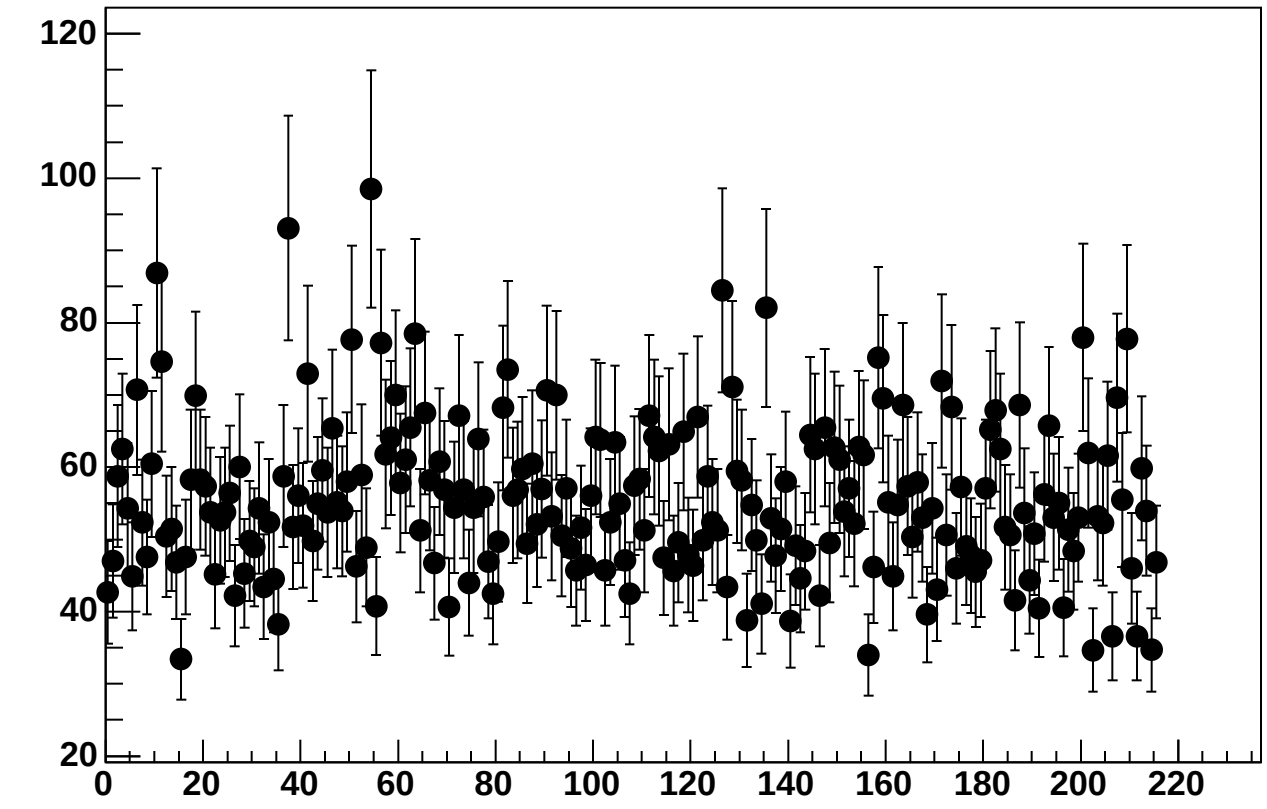
Enable 3, DAC=1600, Hold=110, ADC Noise vs 18*Chip+Chan



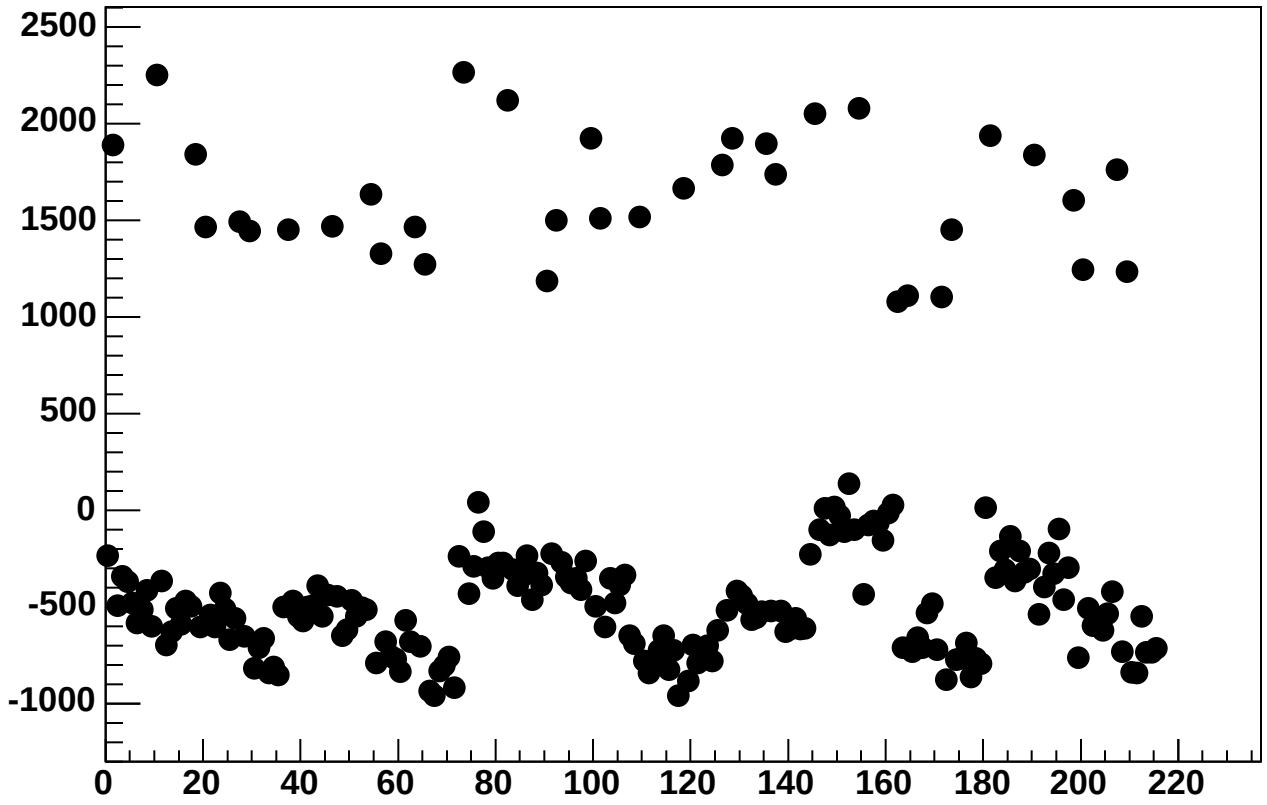
Enable 3, DAC=1600, Hold=115, ADC Mean vs 18*Chip+Chan



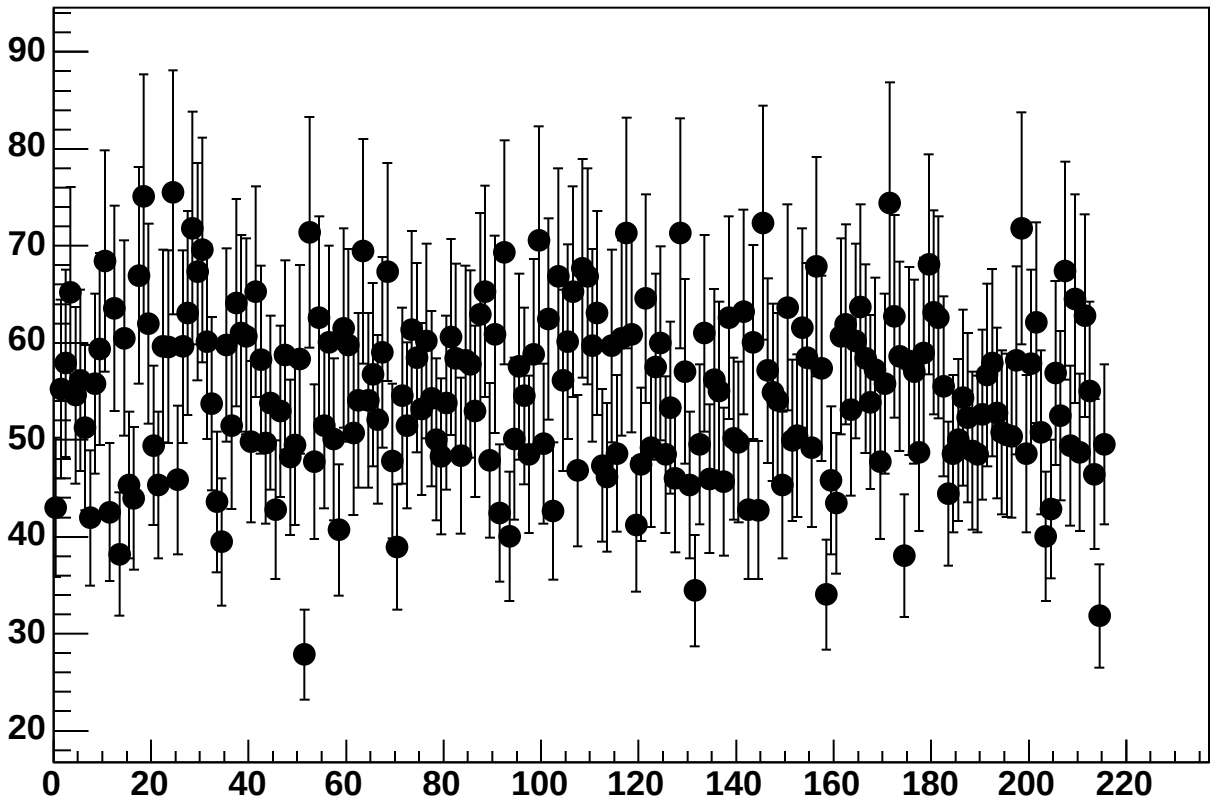
Enable 3, DAC=1600, Hold=115, ADC Noise vs 18*Chip+Chan



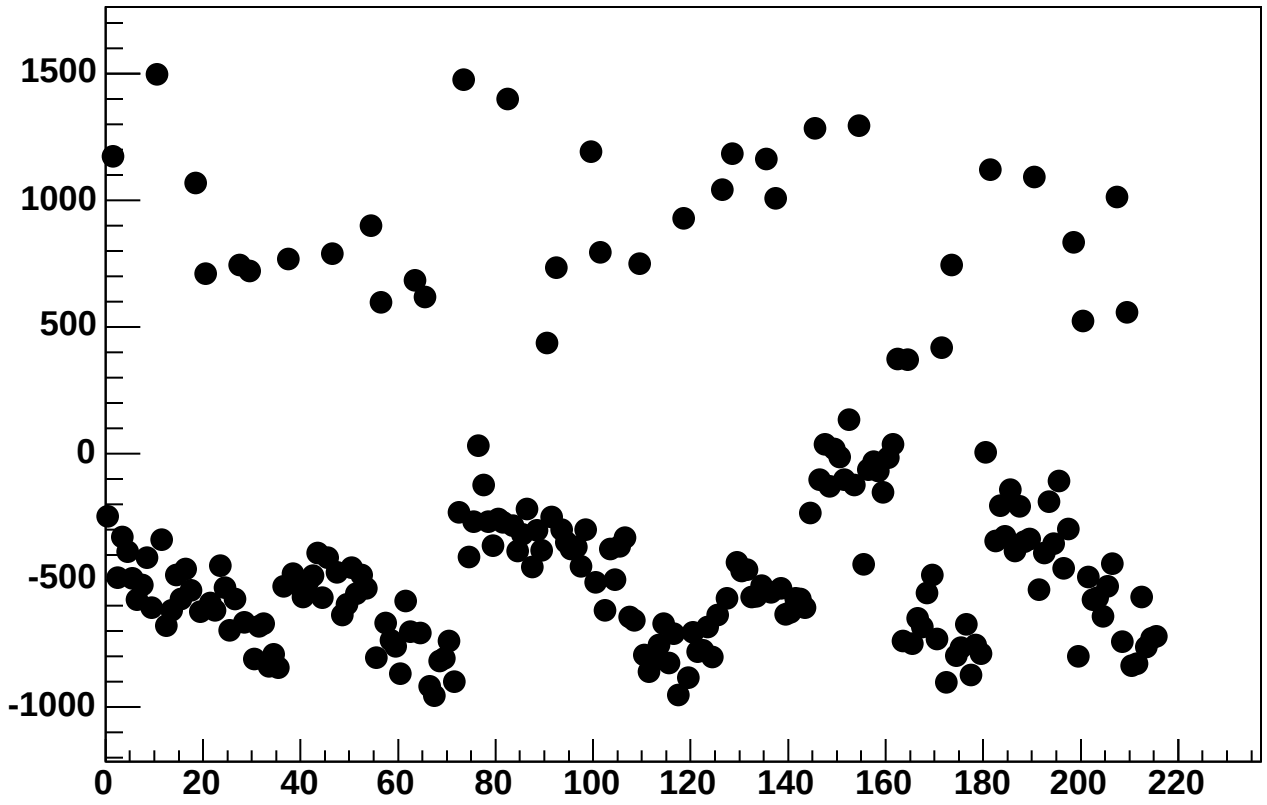
Enable 3, DAC=1600, Hold=120, ADC Mean vs 18*Chip+Chan



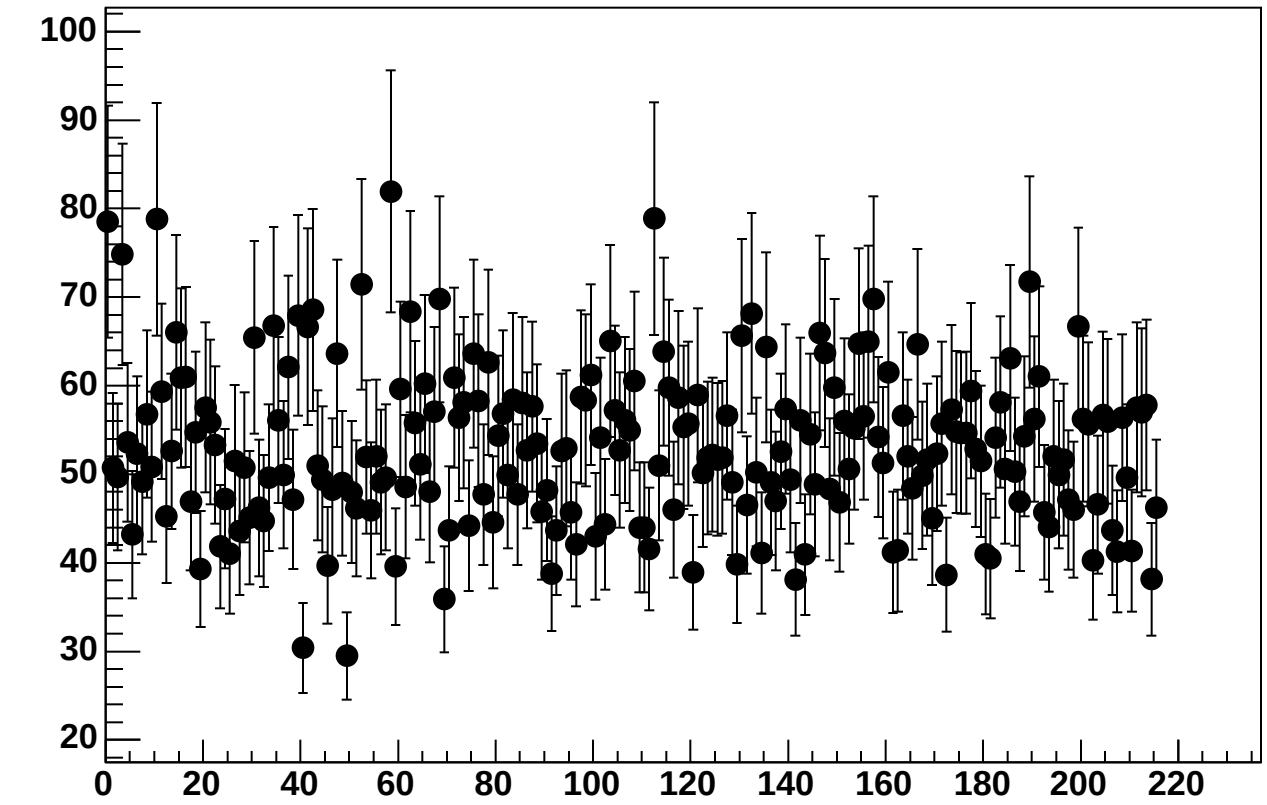
Enable 3, DAC=1600, Hold=120, ADC Noise vs 18*Chip+Chan



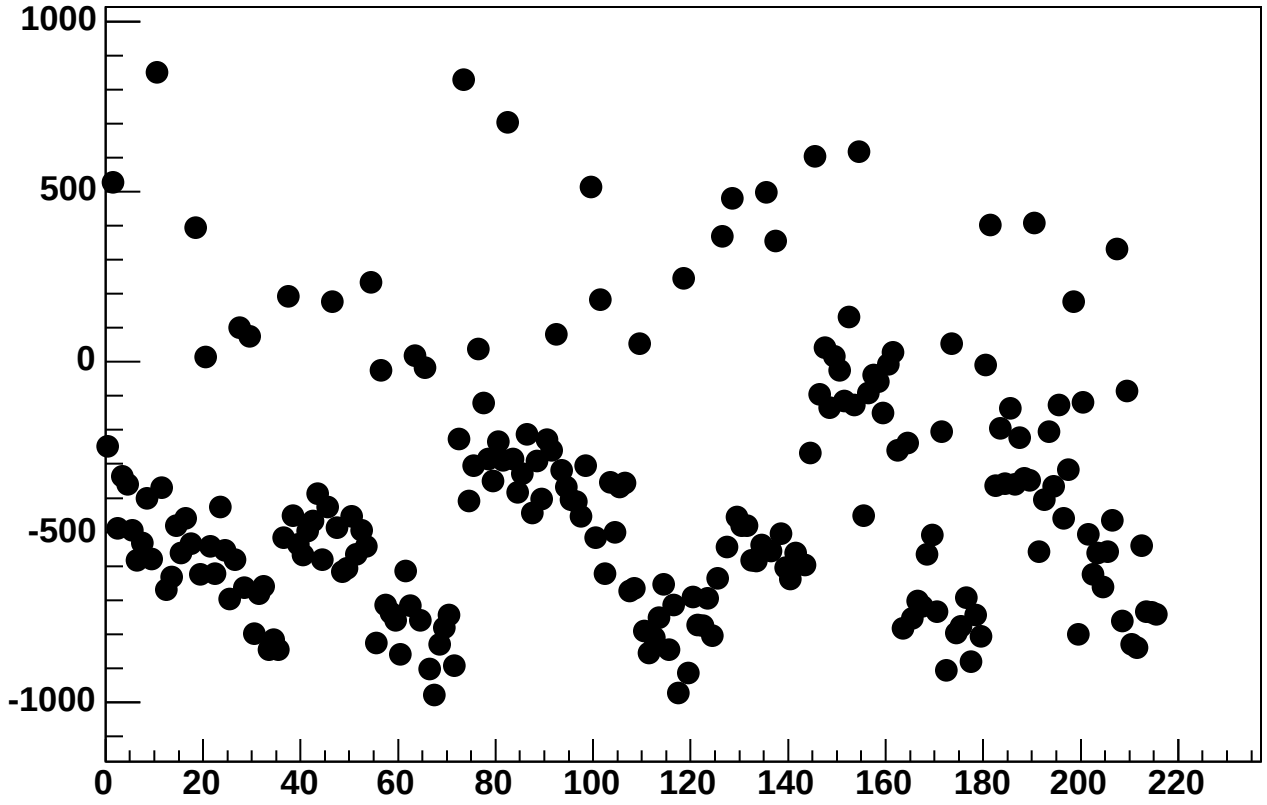
Enable 3, DAC=1600, Hold=125, ADC Mean vs 18*Chip+Chan



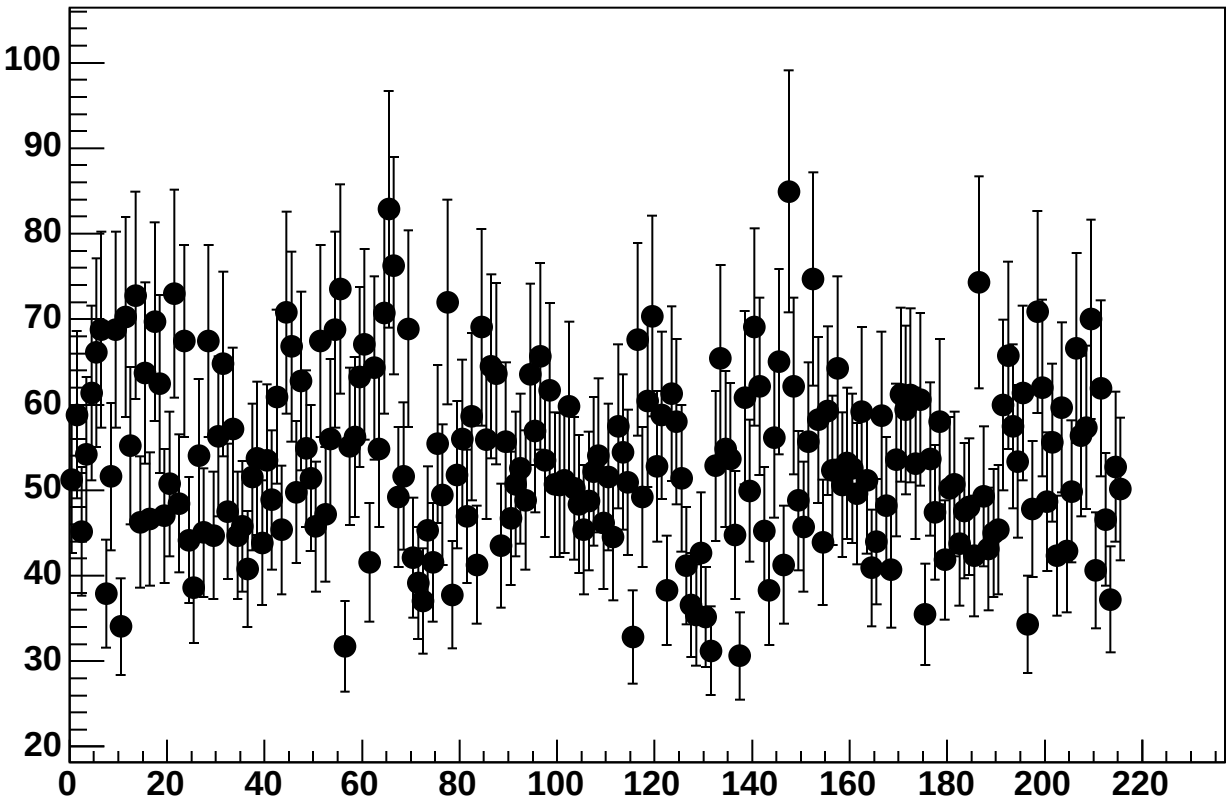
Enable 3, DAC=1600, Hold=125, ADC Noise vs 18*Chip+Chan



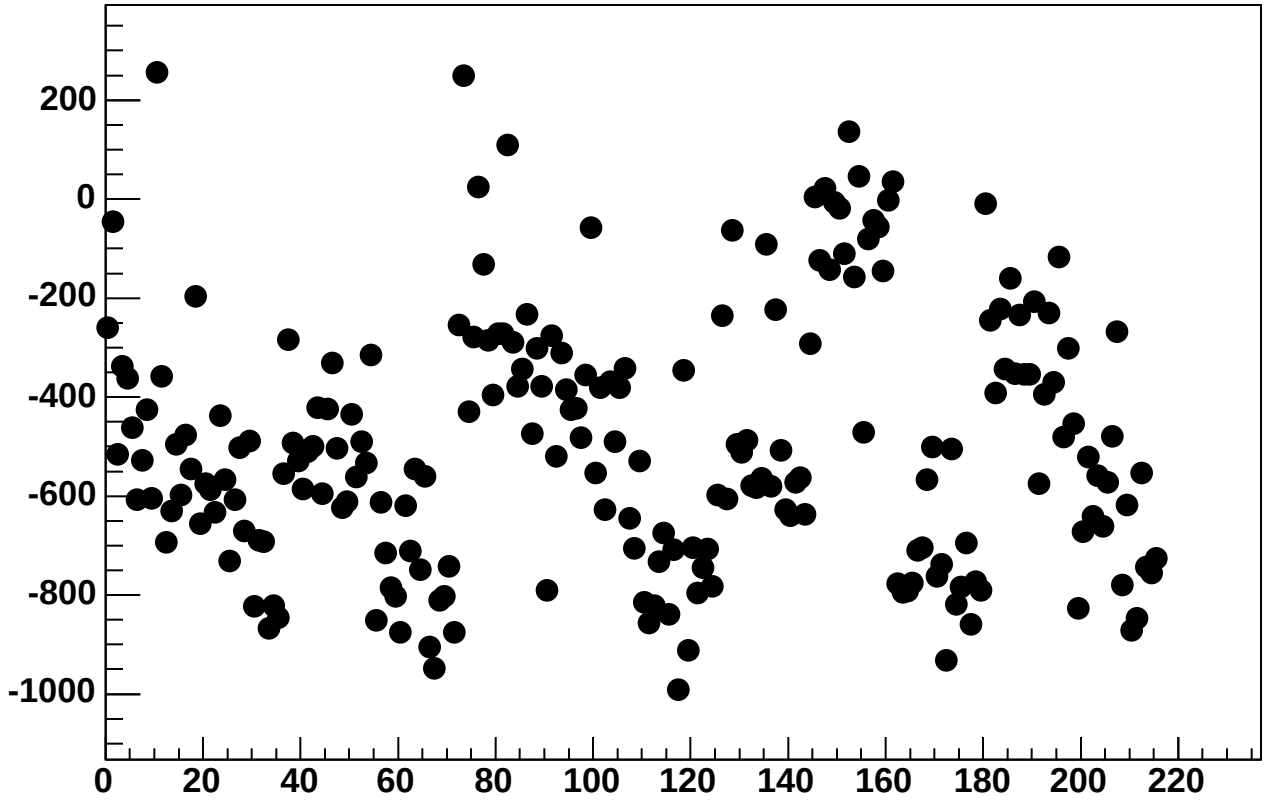
Enable 3, DAC=1600, Hold=130, ADC Mean vs 18*Chip+Chan



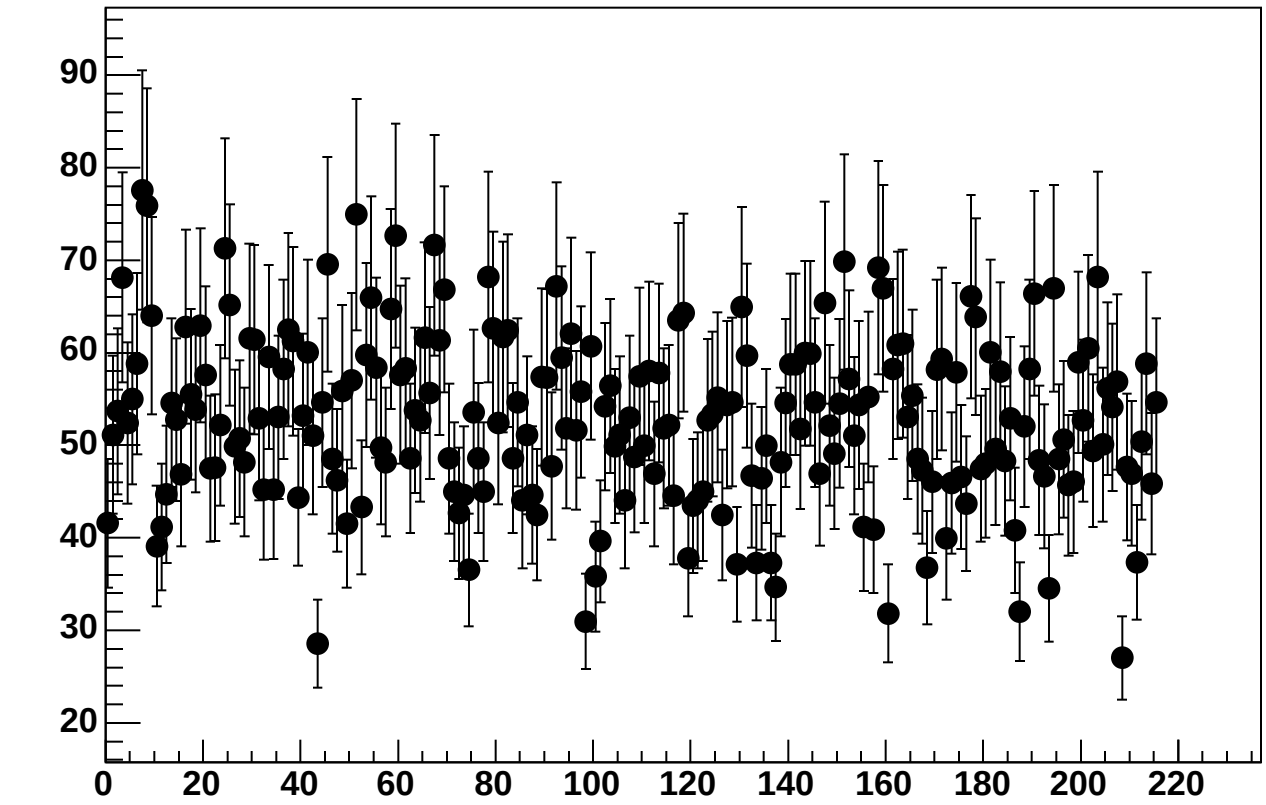
Enable 3, DAC=1600, Hold=130, ADC Noise vs 18*Chip+Chan



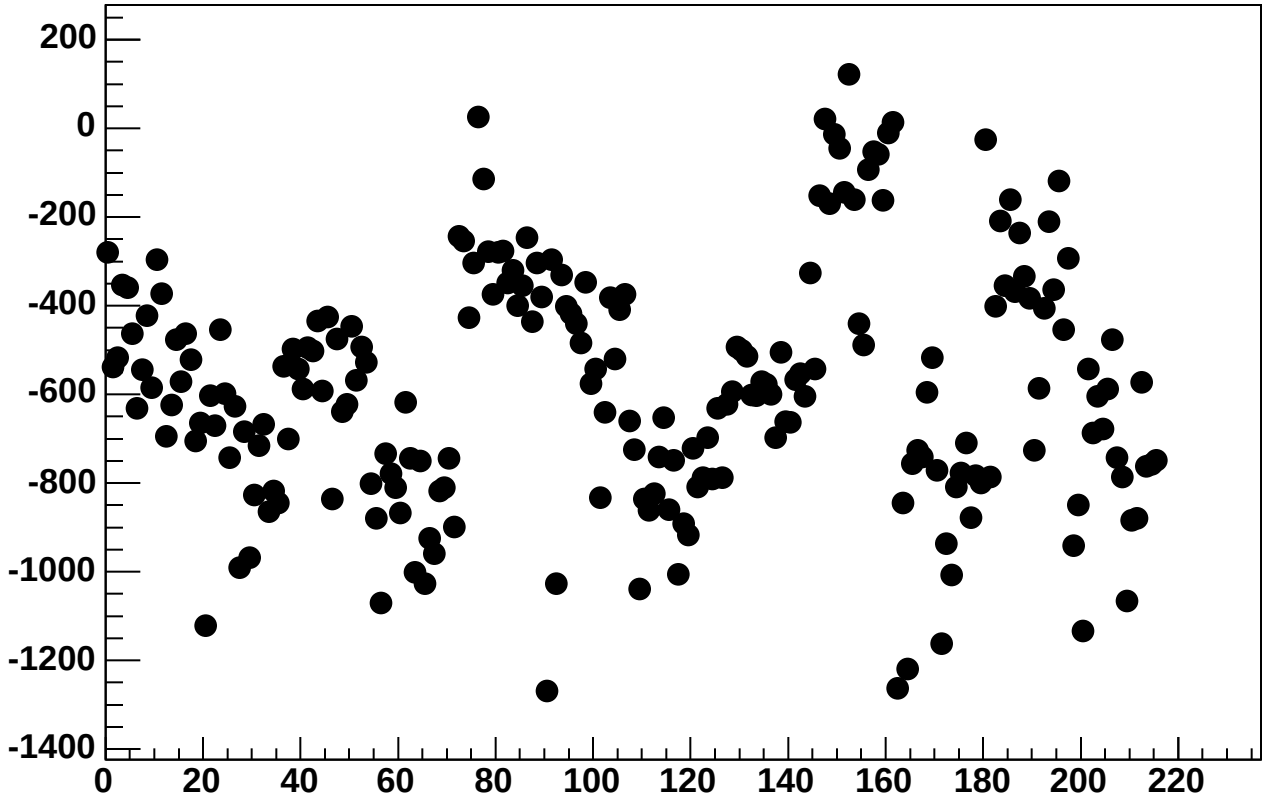
Enable 3, DAC=1600, Hold=135, ADC Mean vs 18*Chip+Chan



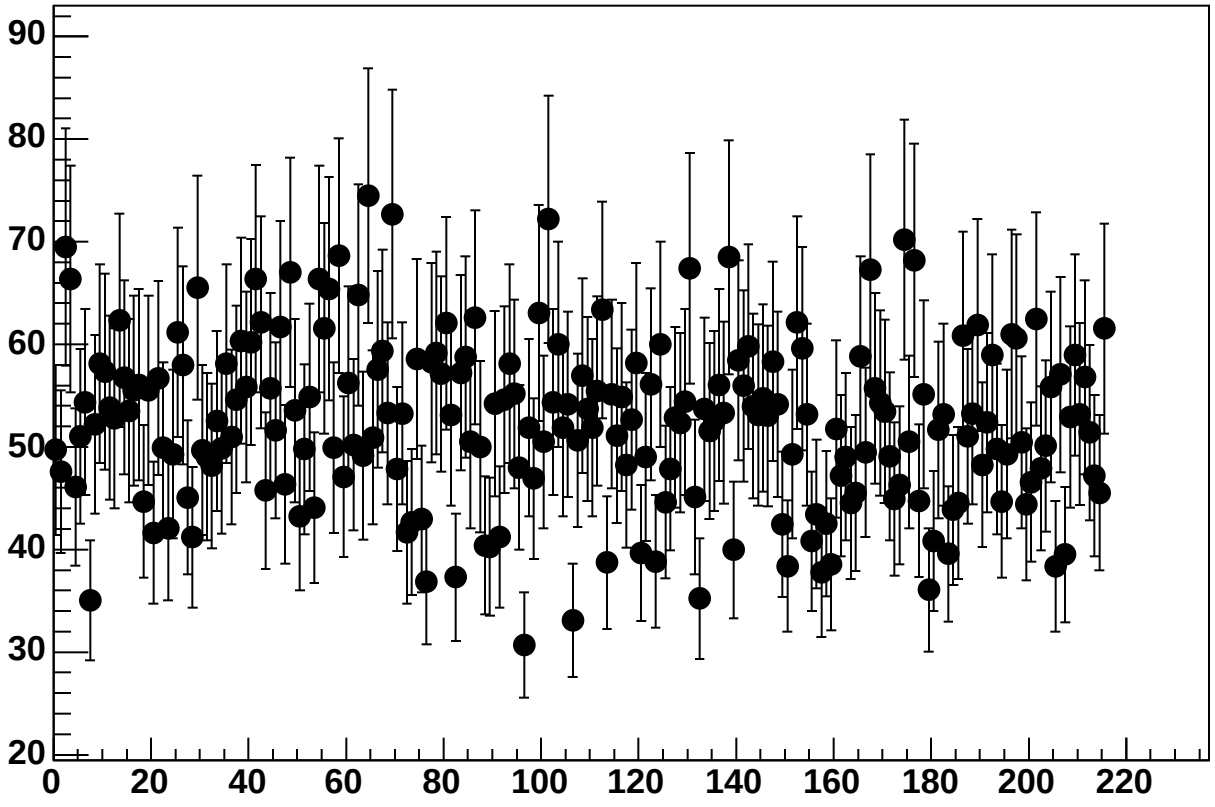
Enable 3, DAC=1600, Hold=135, ADC Noise vs 18*Chip+Chan



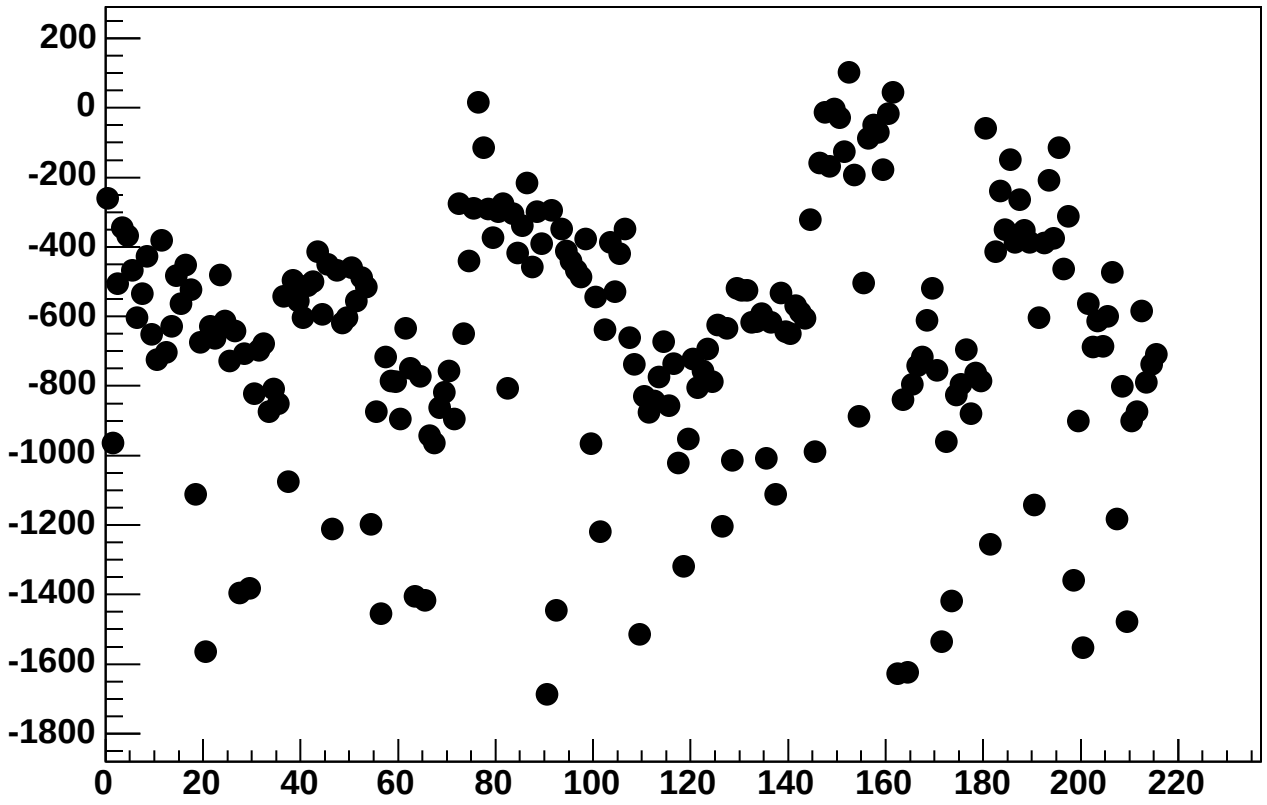
Enable 3, DAC=1600, Hold=140, ADC Mean vs 18*Chip+Chan



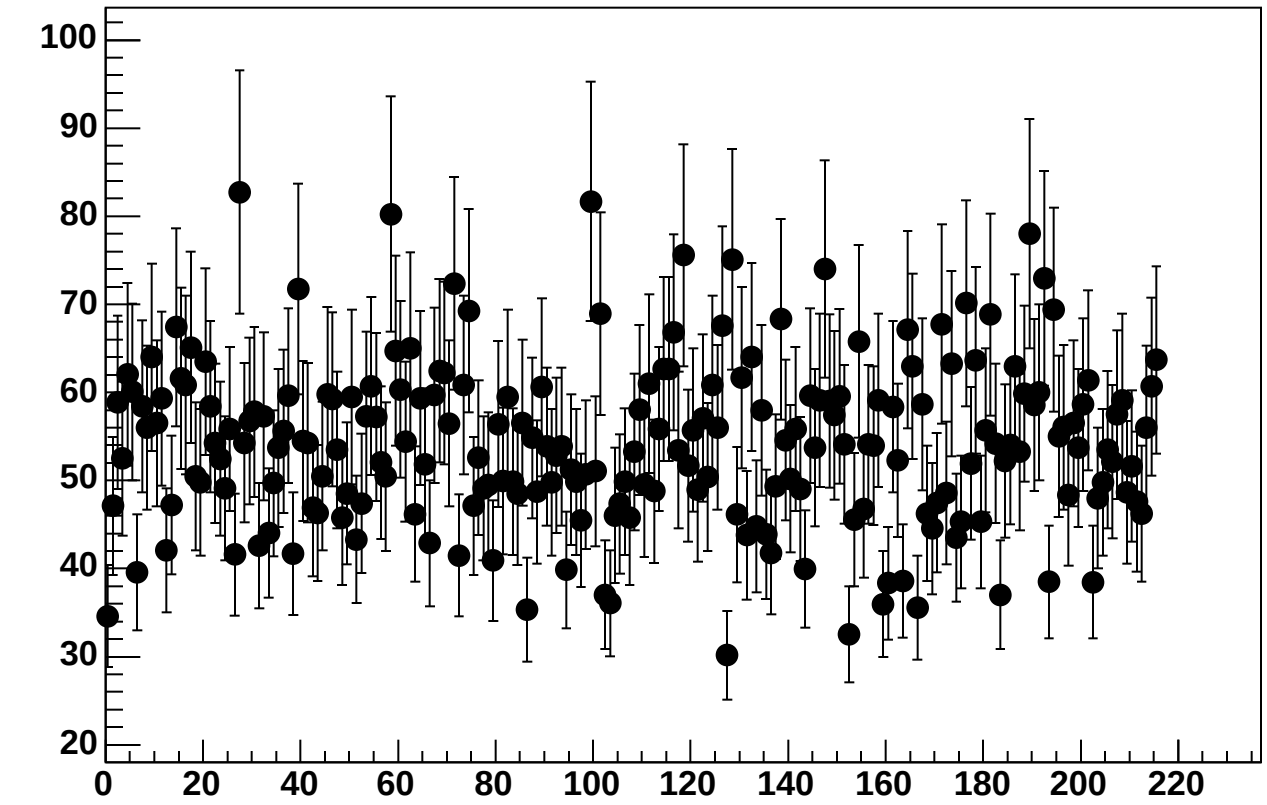
Enable 3, DAC=1600, Hold=140, ADC Noise vs 18*Chip+Chan



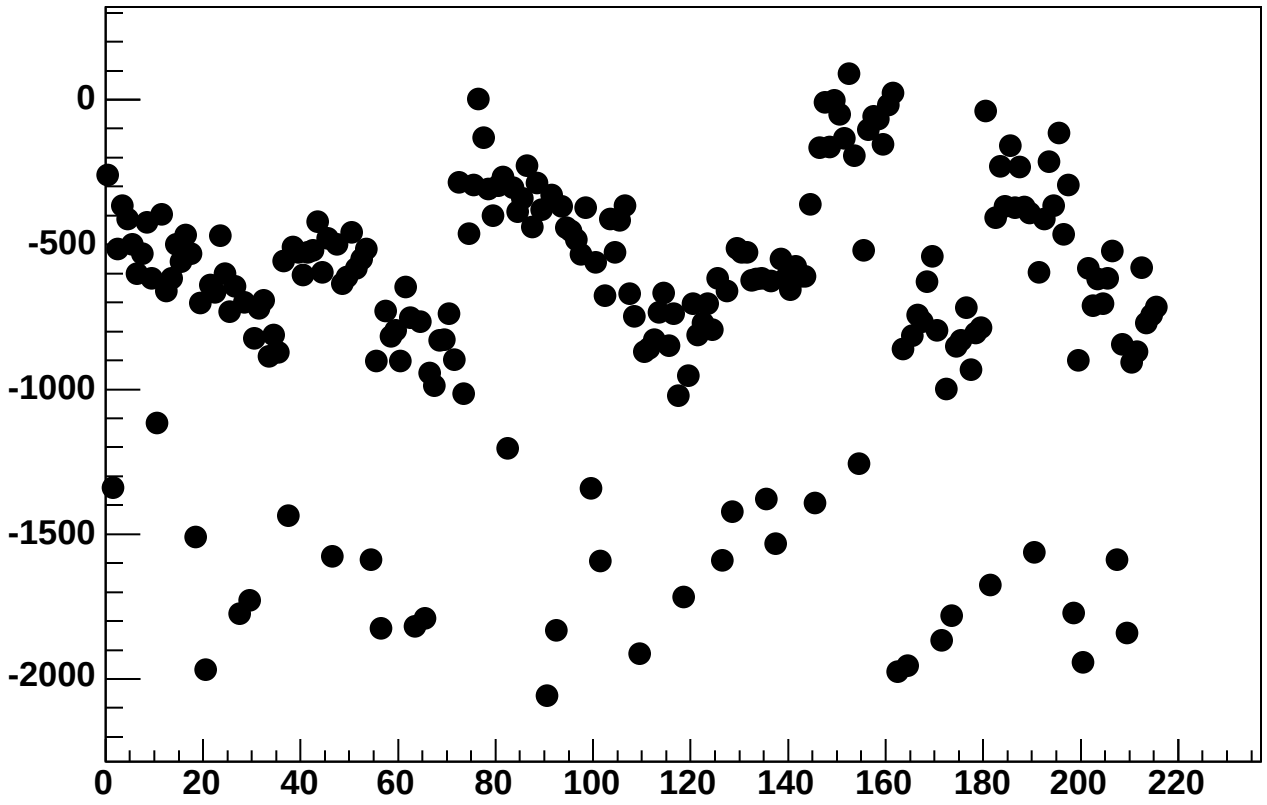
Enable 3, DAC=1600, Hold=145, ADC Mean vs 18*Chip+Chan



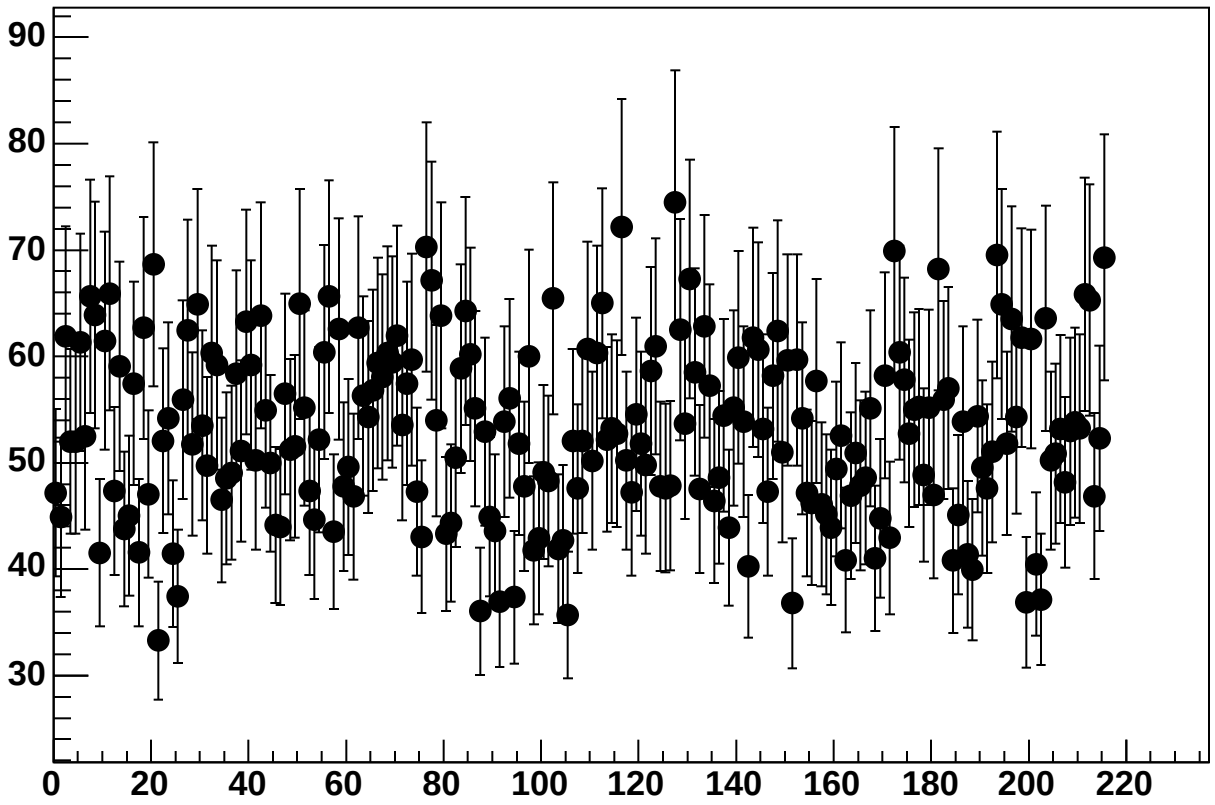
Enable 3, DAC=1600, Hold=145, ADC Noise vs 18*Chip+Chan



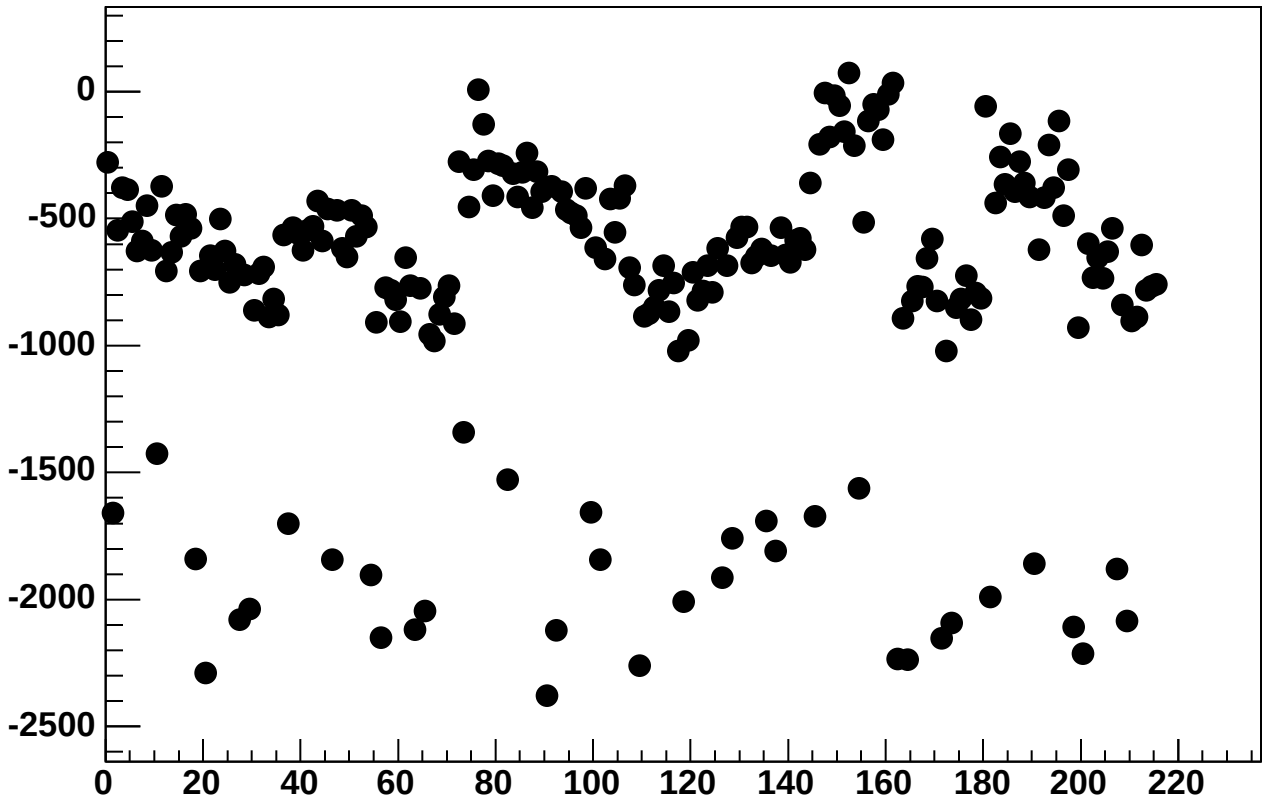
Enable 3, DAC=1600, Hold=150, ADC Mean vs 18*Chip+Chan



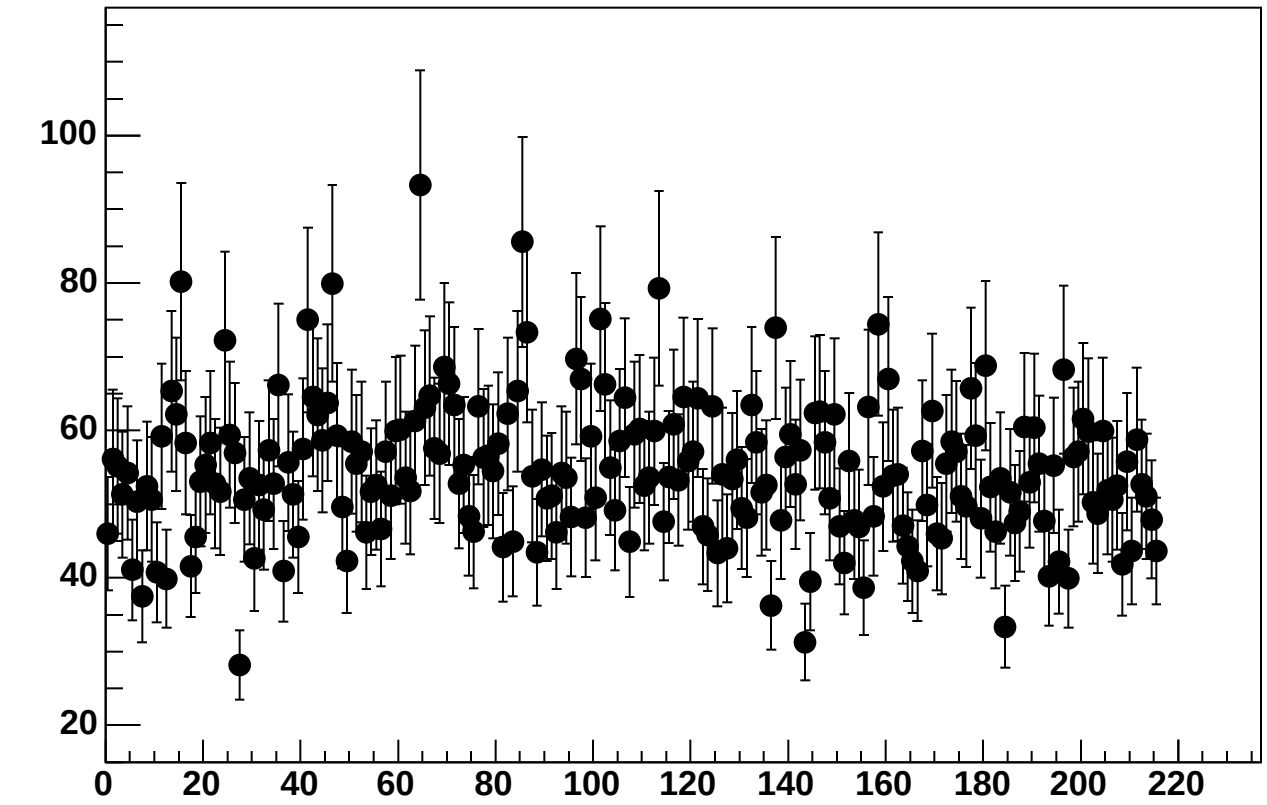
Enable 3, DAC=1600, Hold=150, ADC Noise vs 18*Chip+Chan



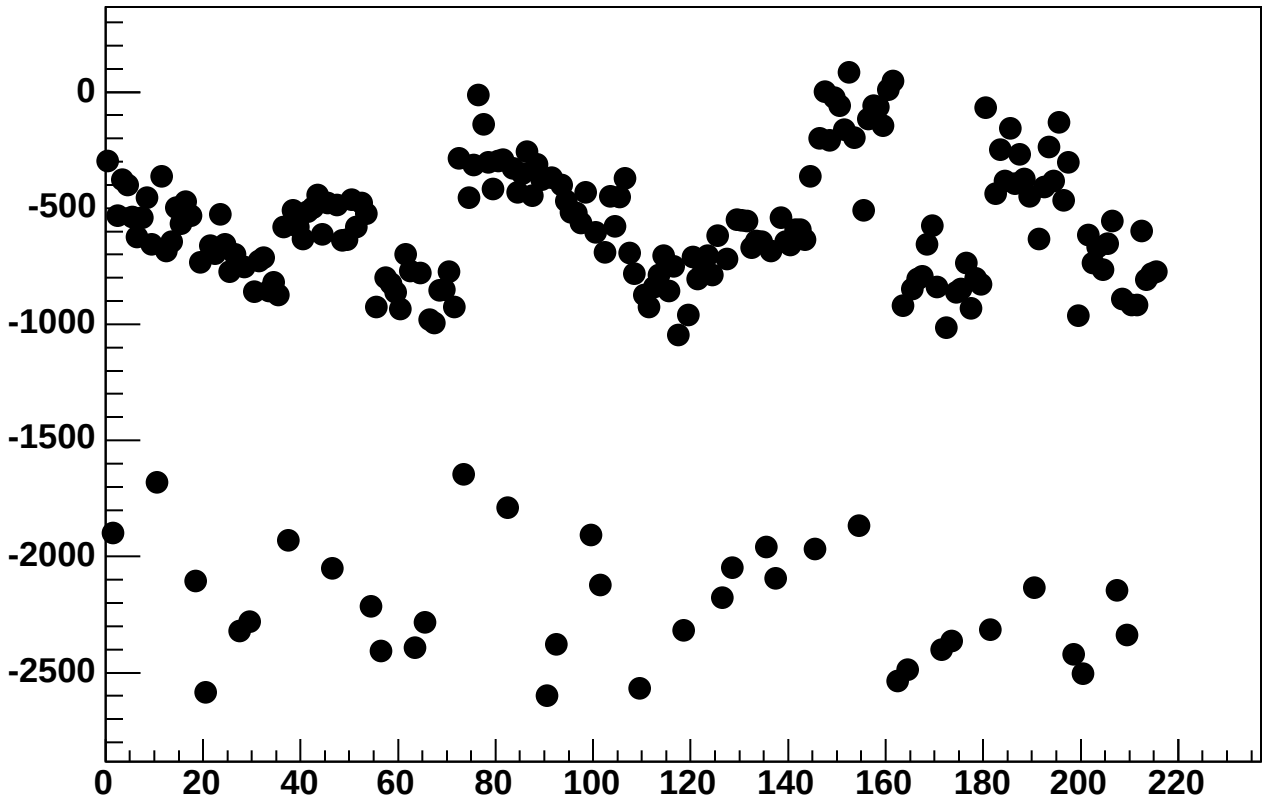
Enable 3, DAC=1600, Hold=155, ADC Mean vs 18*Chip+Chan



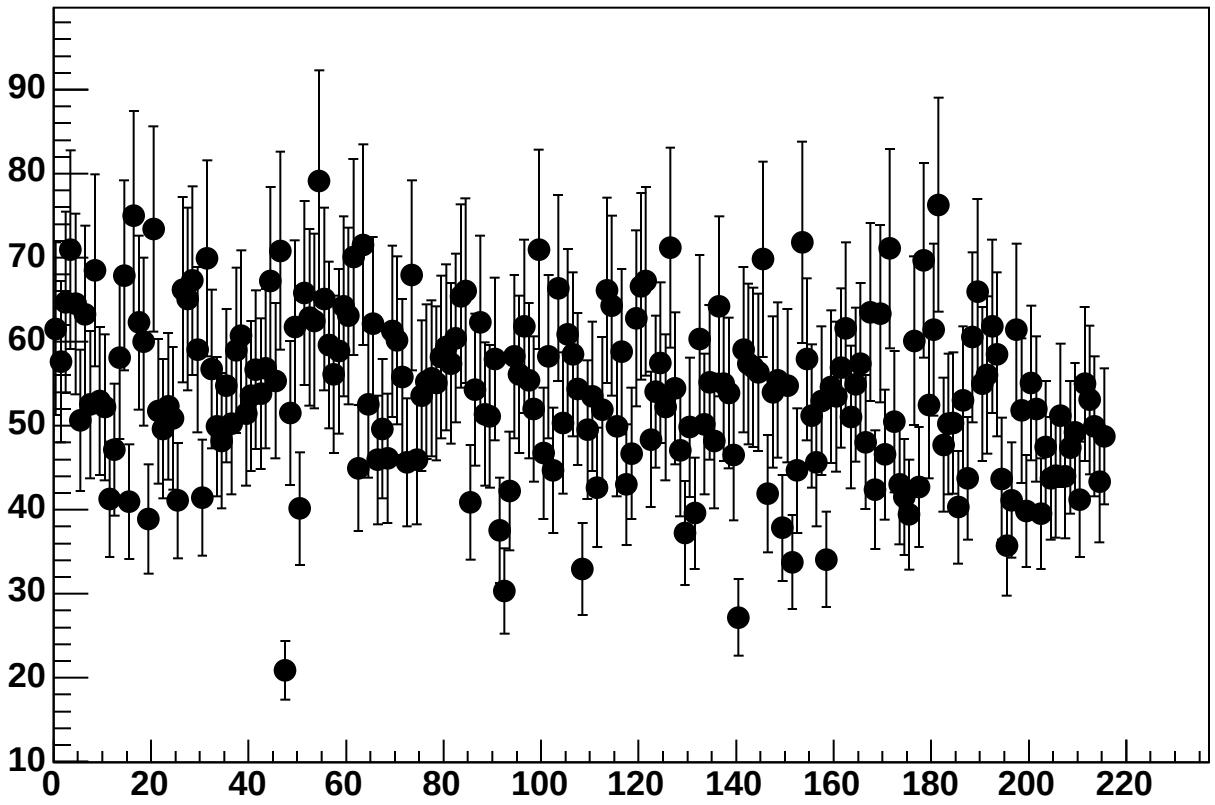
Enable 3, DAC=1600, Hold=155, ADC Noise vs 18*Chip+Chan



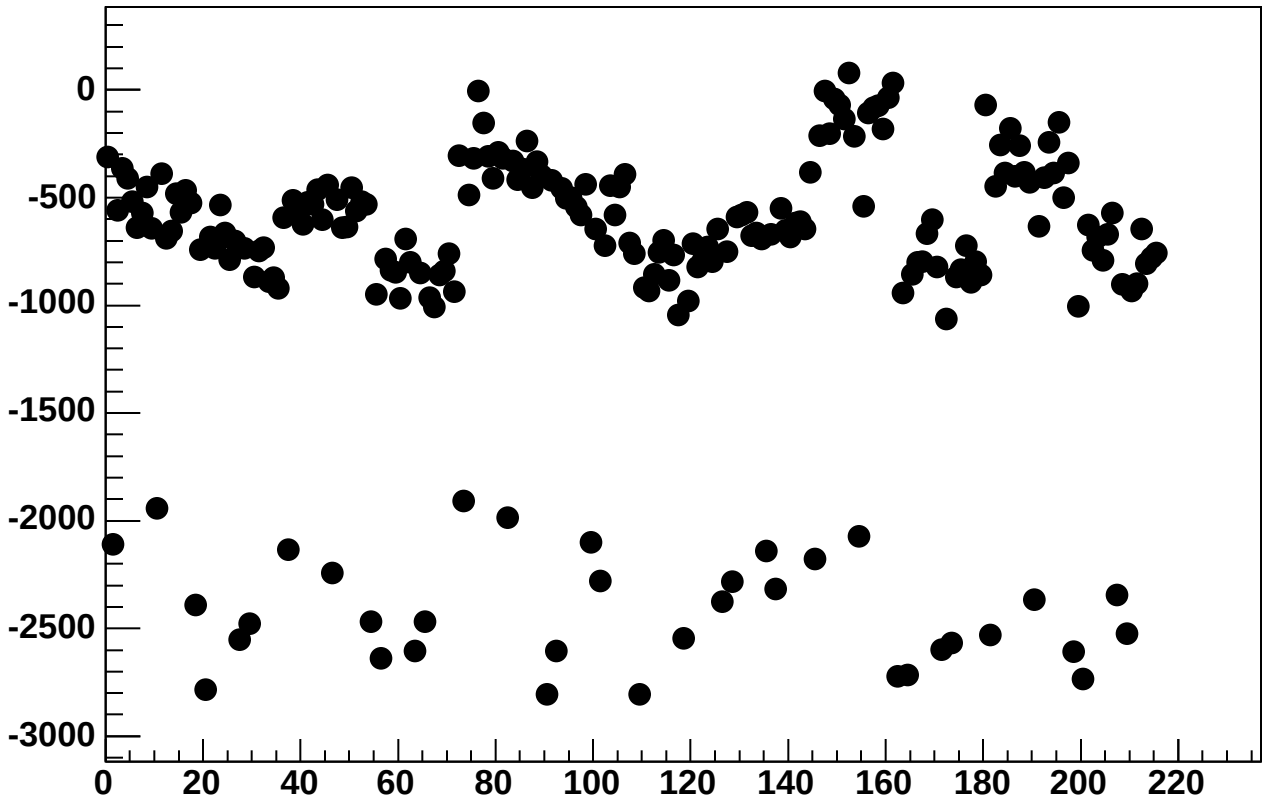
Enable 3, DAC=1600, Hold=160, ADC Mean vs 18*Chip+Chan



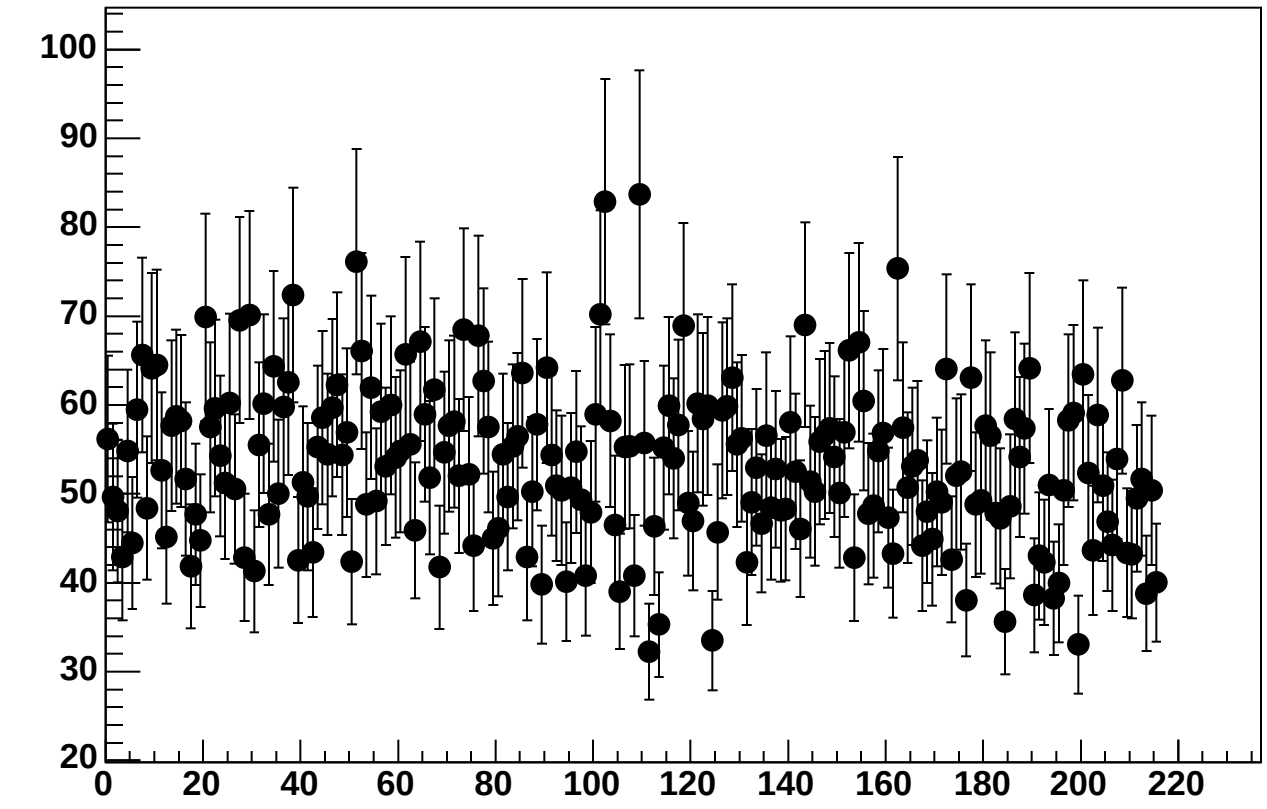
Enable 3, DAC=1600, Hold=160, ADC Noise vs 18*Chip+Chan



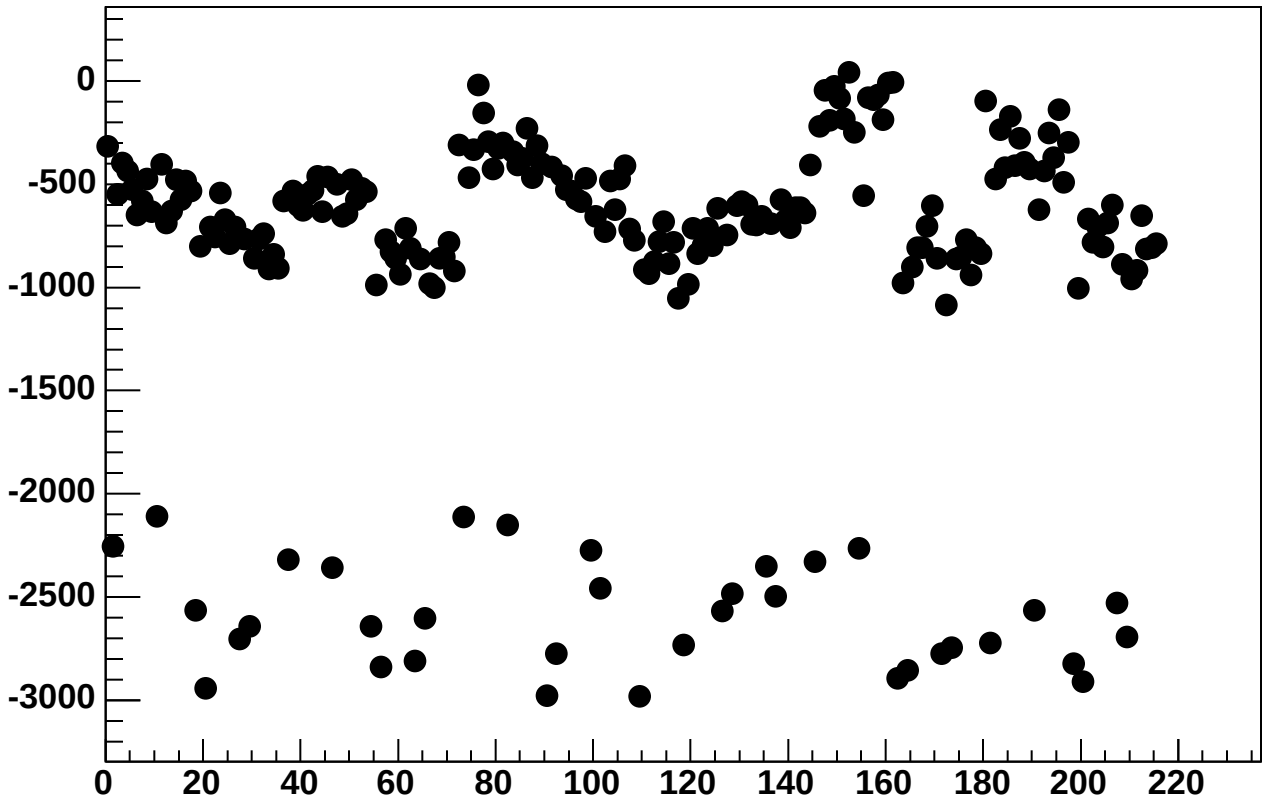
Enable 3, DAC=1600, Hold=165, ADC Mean vs 18*Chip+Chan



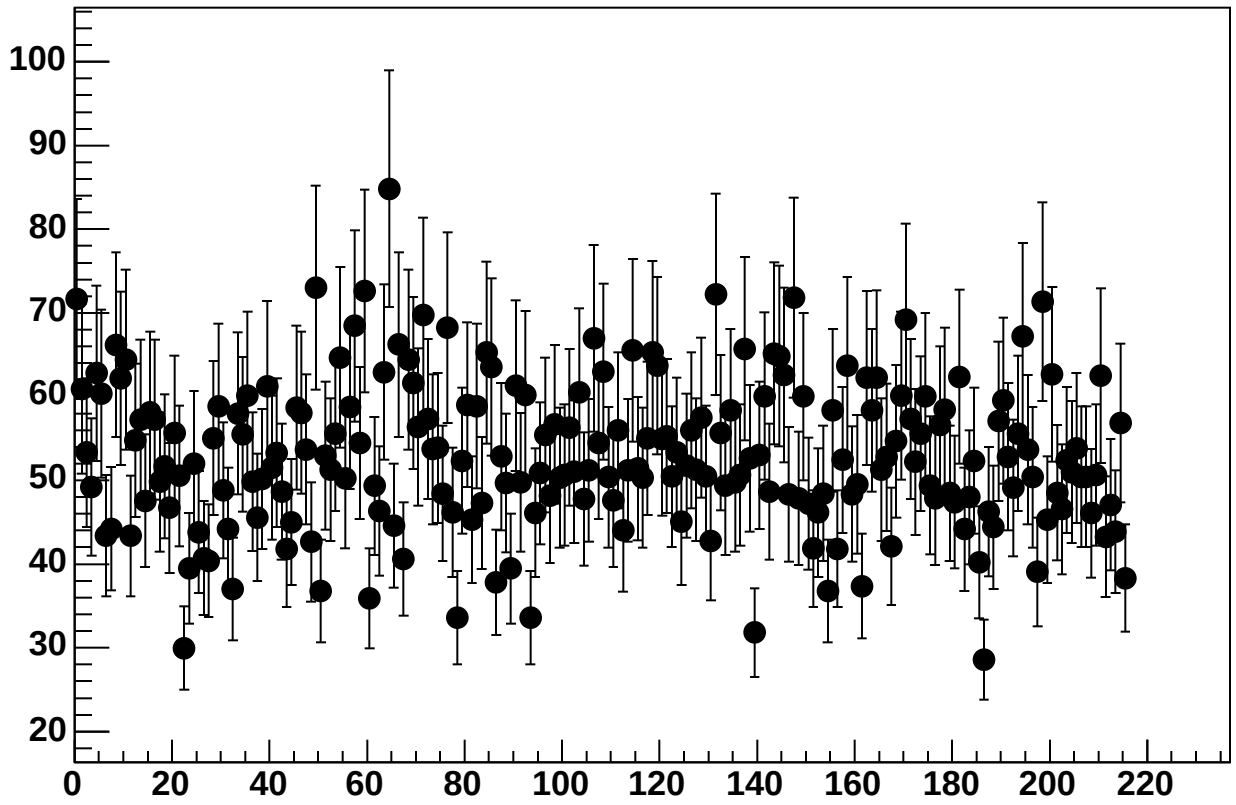
Enable 3, DAC=1600, Hold=165, ADC Noise vs 18*Chip+Chan



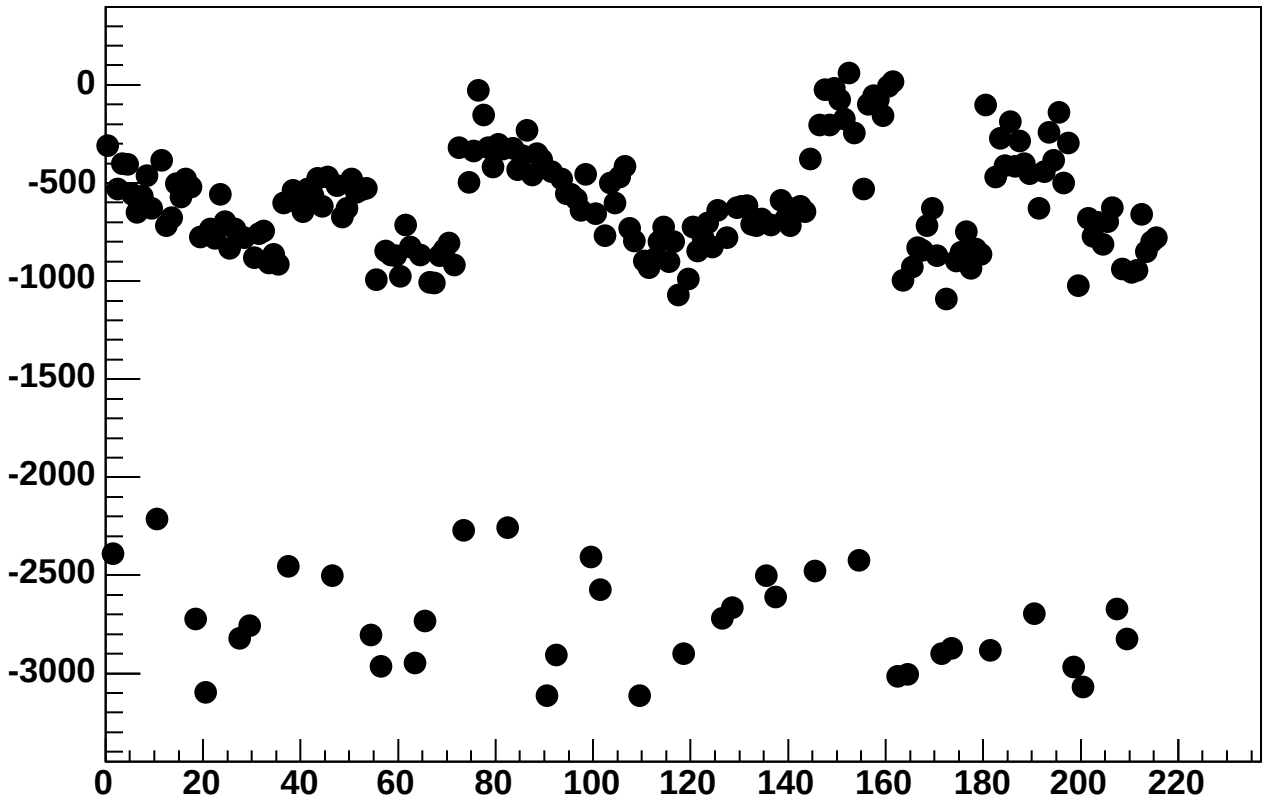
Enable 3, DAC=1600, Hold=170, ADC Mean vs 18*Chip+Chan



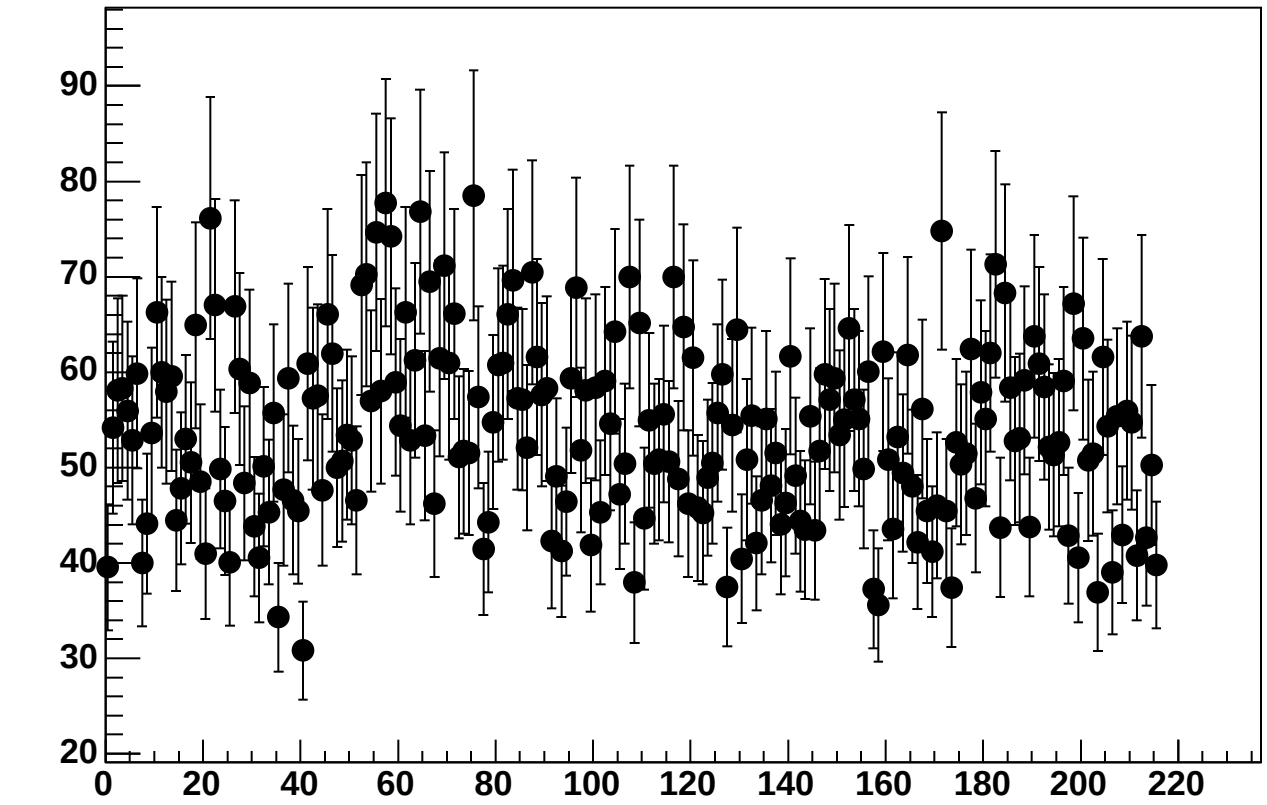
Enable 3, DAC=1600, Hold=170, ADC Noise vs 18*Chip+Chan



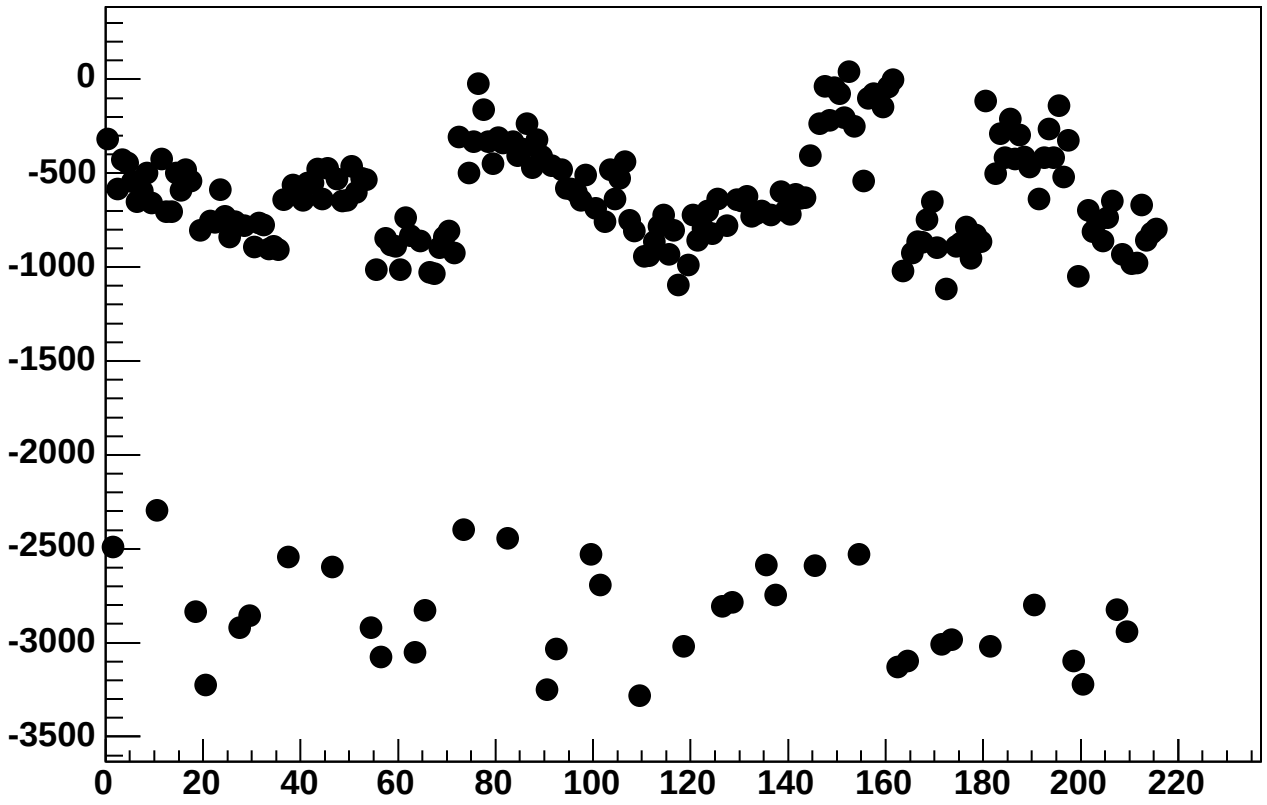
Enable 3, DAC=1600, Hold=175, ADC Mean vs 18*Chip+Chan



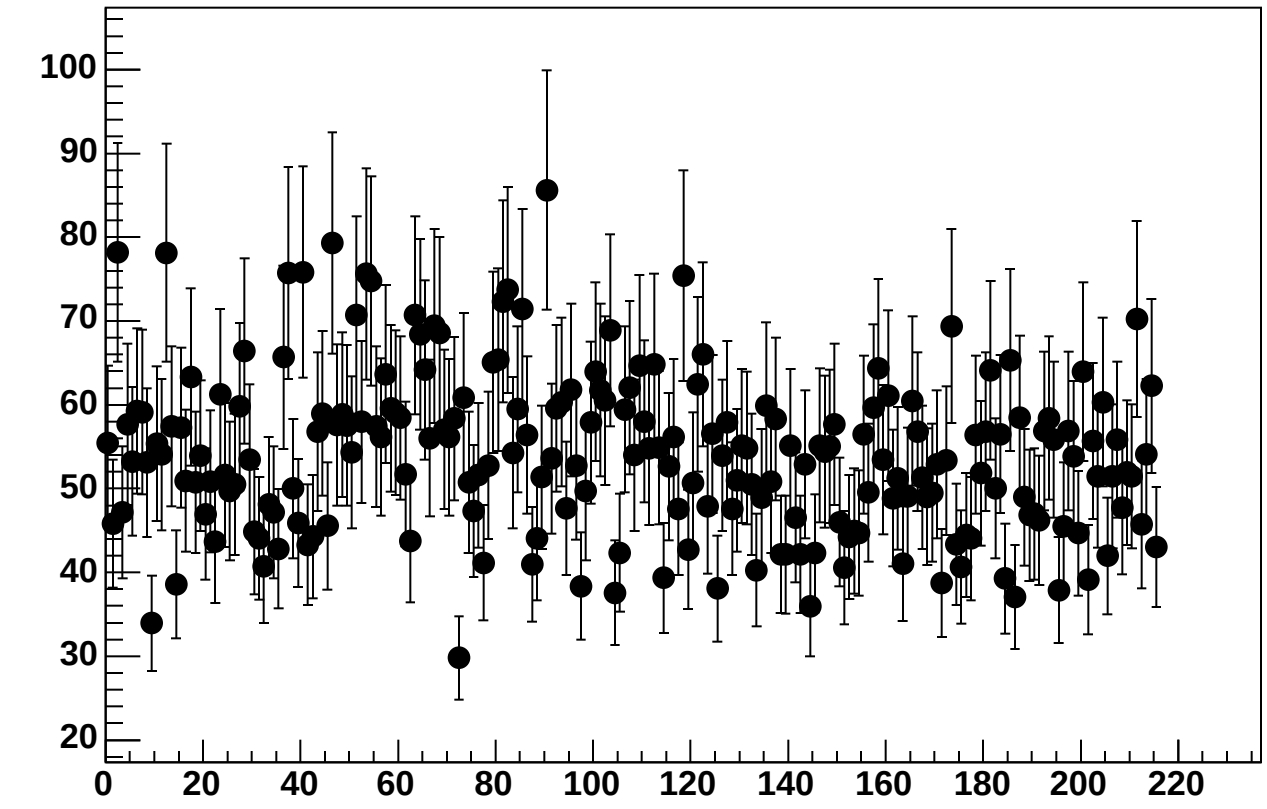
Enable 3, DAC=1600, Hold=175, ADC Noise vs 18*Chip+Chan



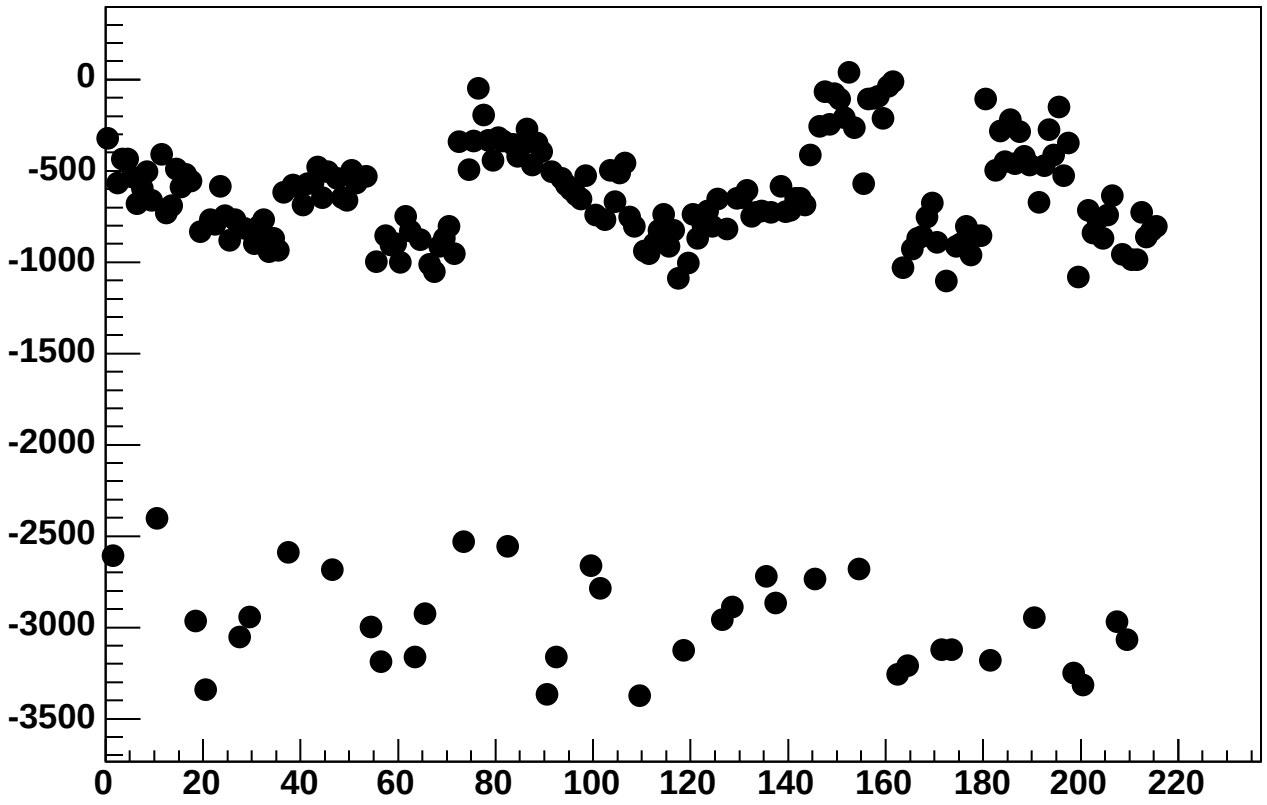
Enable 3, DAC=1600, Hold=180, ADC Mean vs 18*Chip+Chan



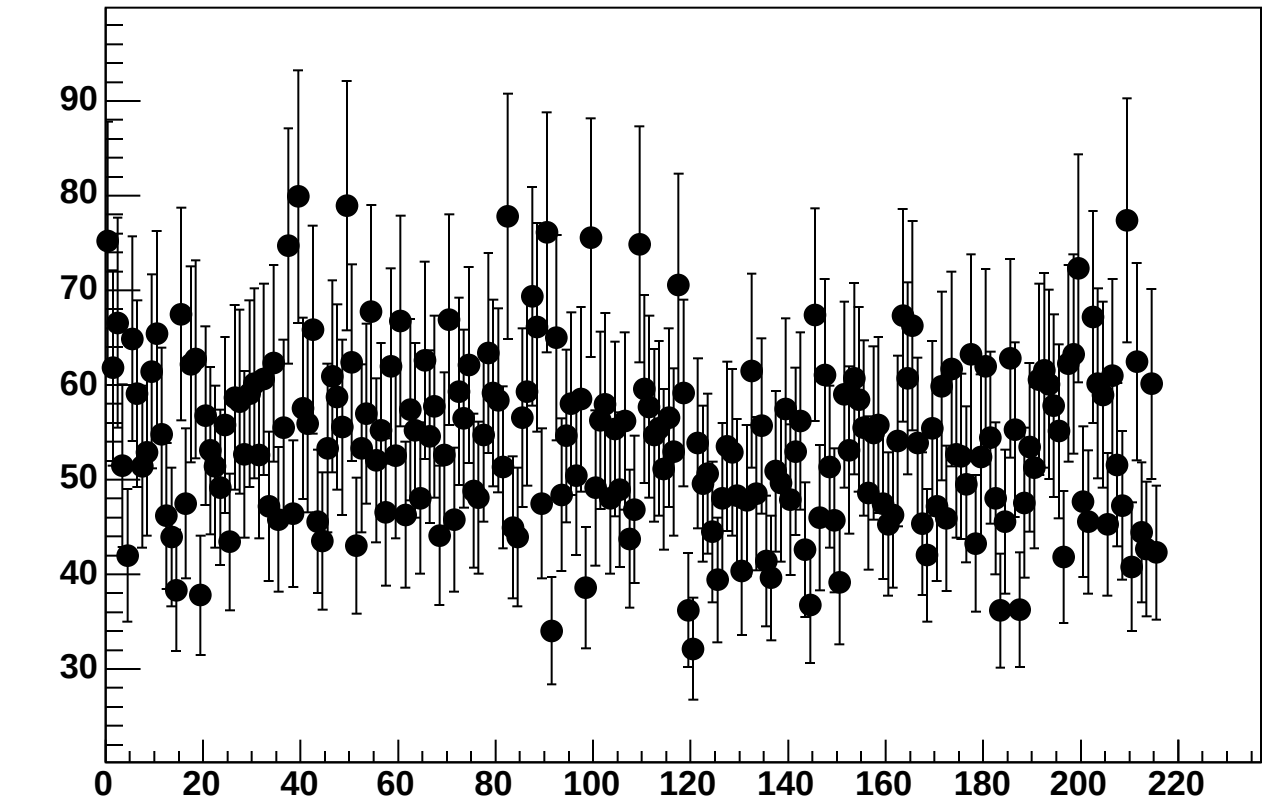
Enable 3, DAC=1600, Hold=180, ADC Noise vs 18*Chip+Chan



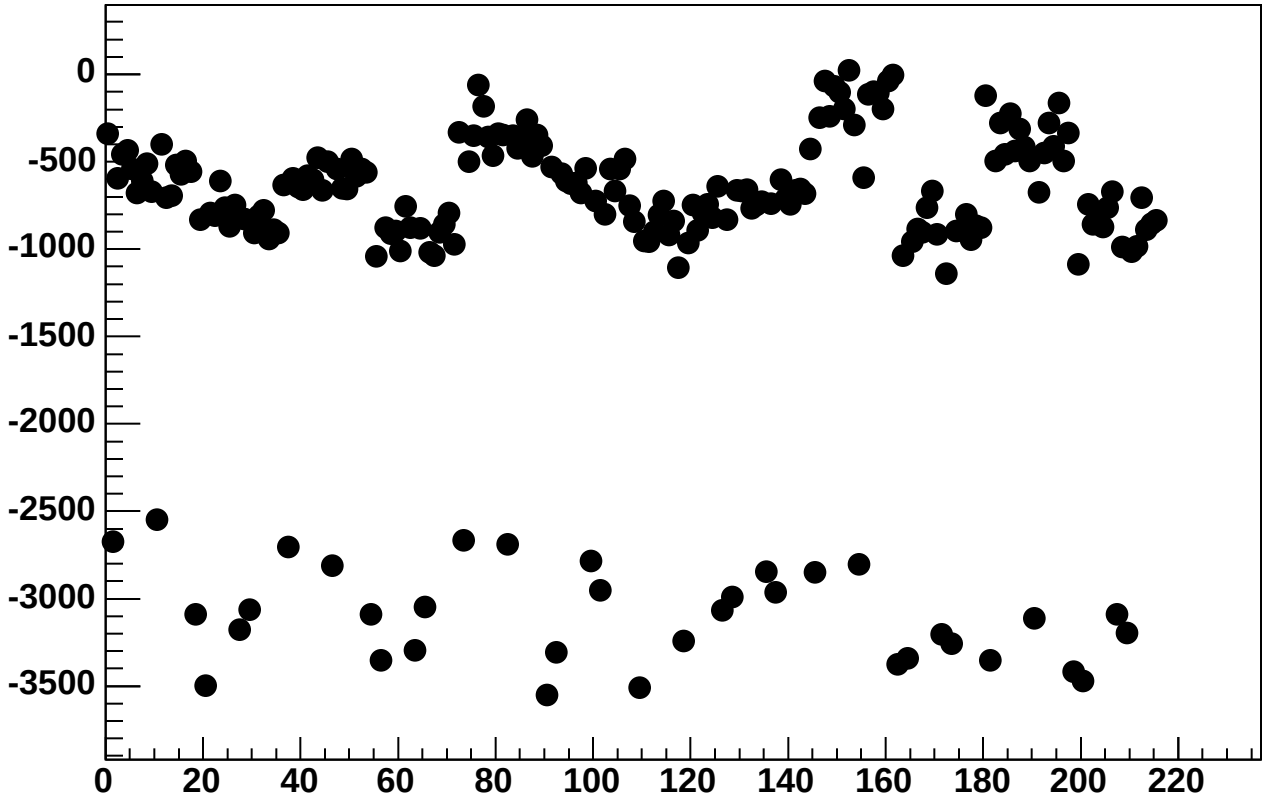
Enable 3, DAC=1600, Hold=185, ADC Mean vs 18*Chip+Chan



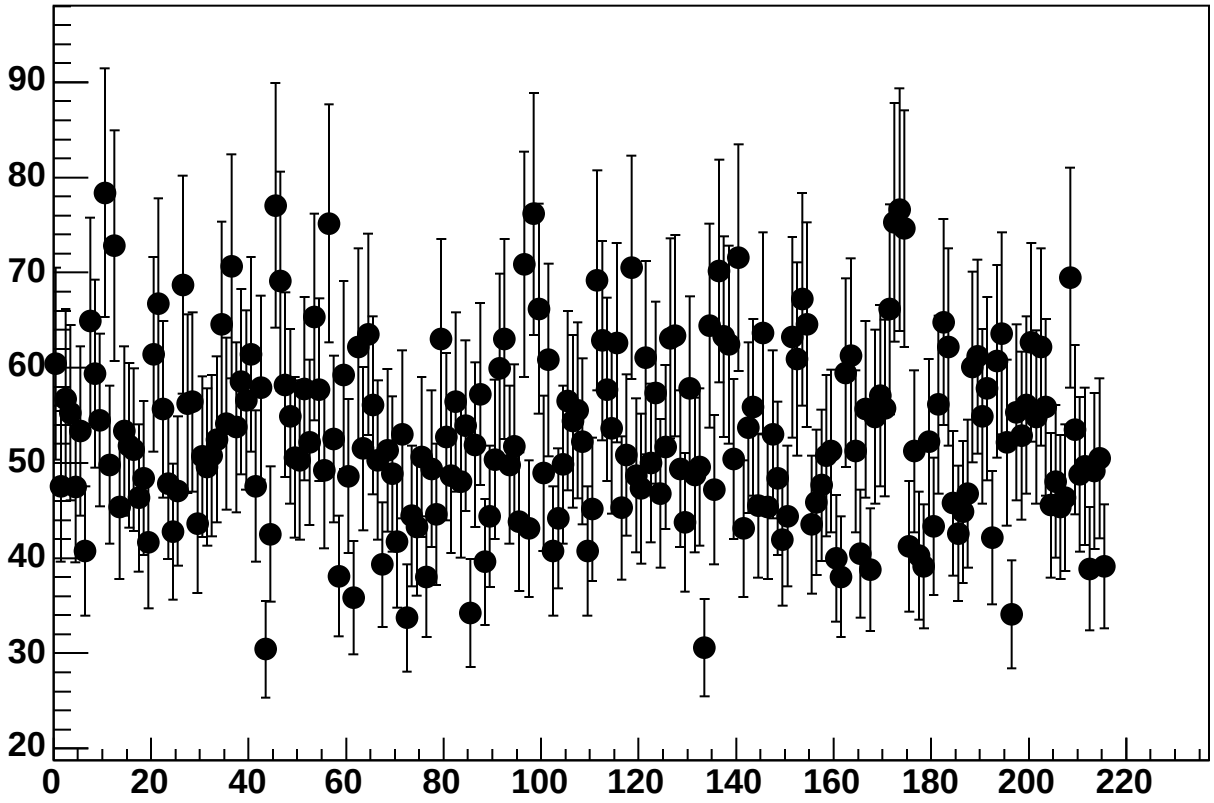
Enable 3, DAC=1600, Hold=185, ADC Noise vs 18*Chip+Chan



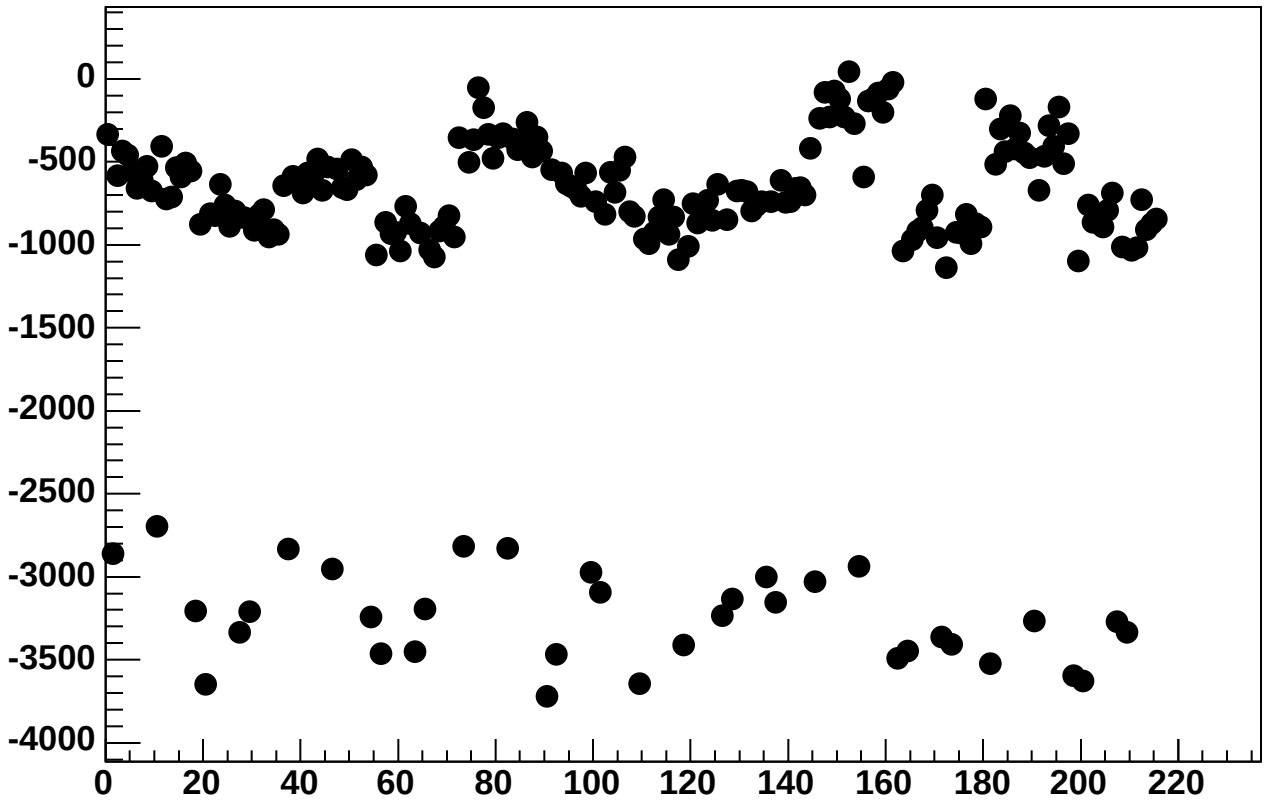
Enable 3, DAC=1600, Hold=190, ADC Mean vs 18*Chip+Chan



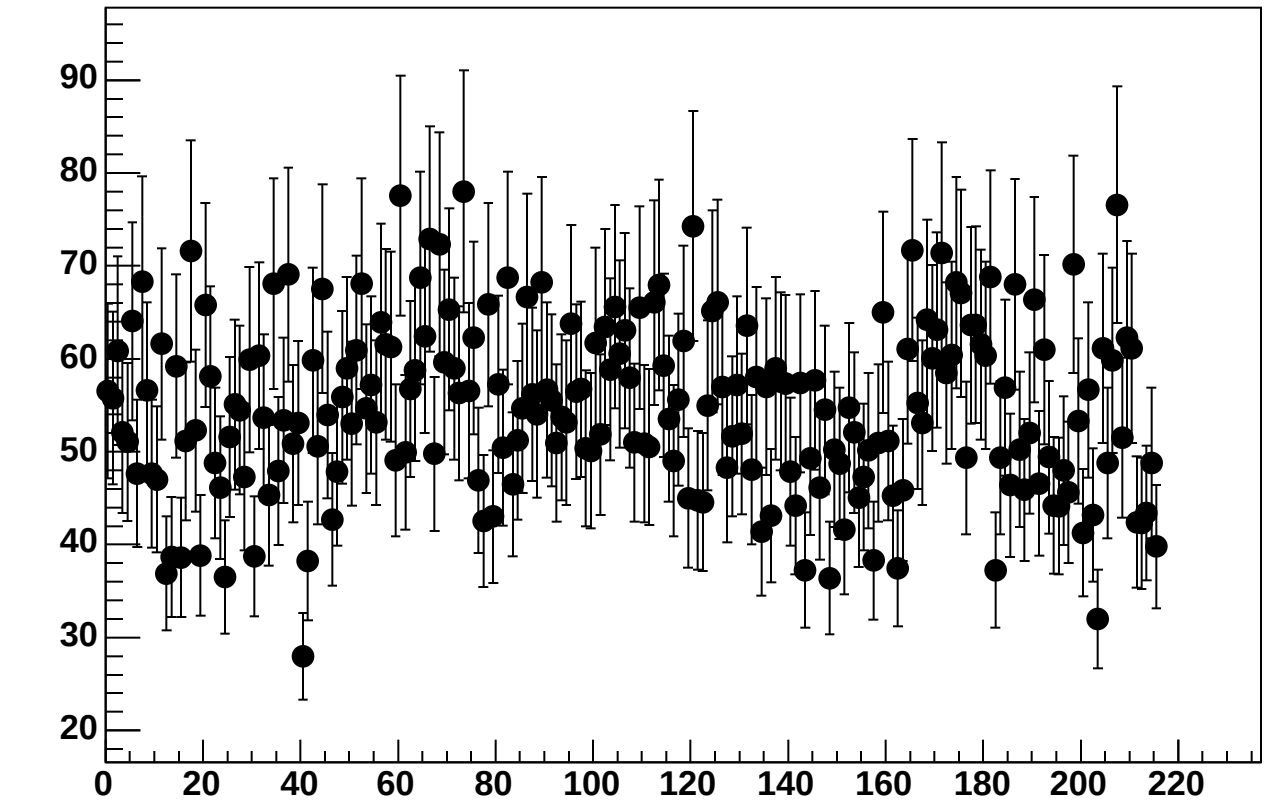
Enable 3, DAC=1600, Hold=190, ADC Noise vs 18*Chip+Chan



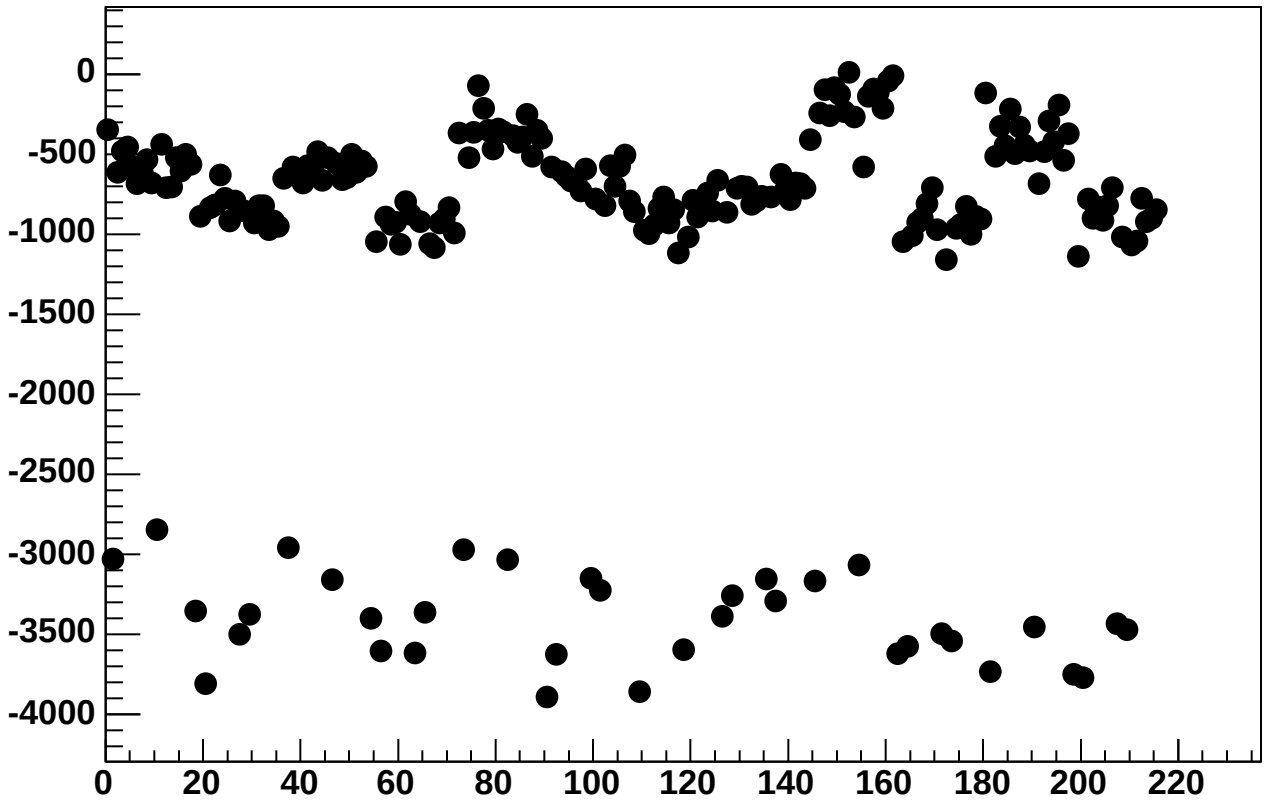
Enable 3, DAC=1600, Hold=195, ADC Mean vs 18*Chip+Chan



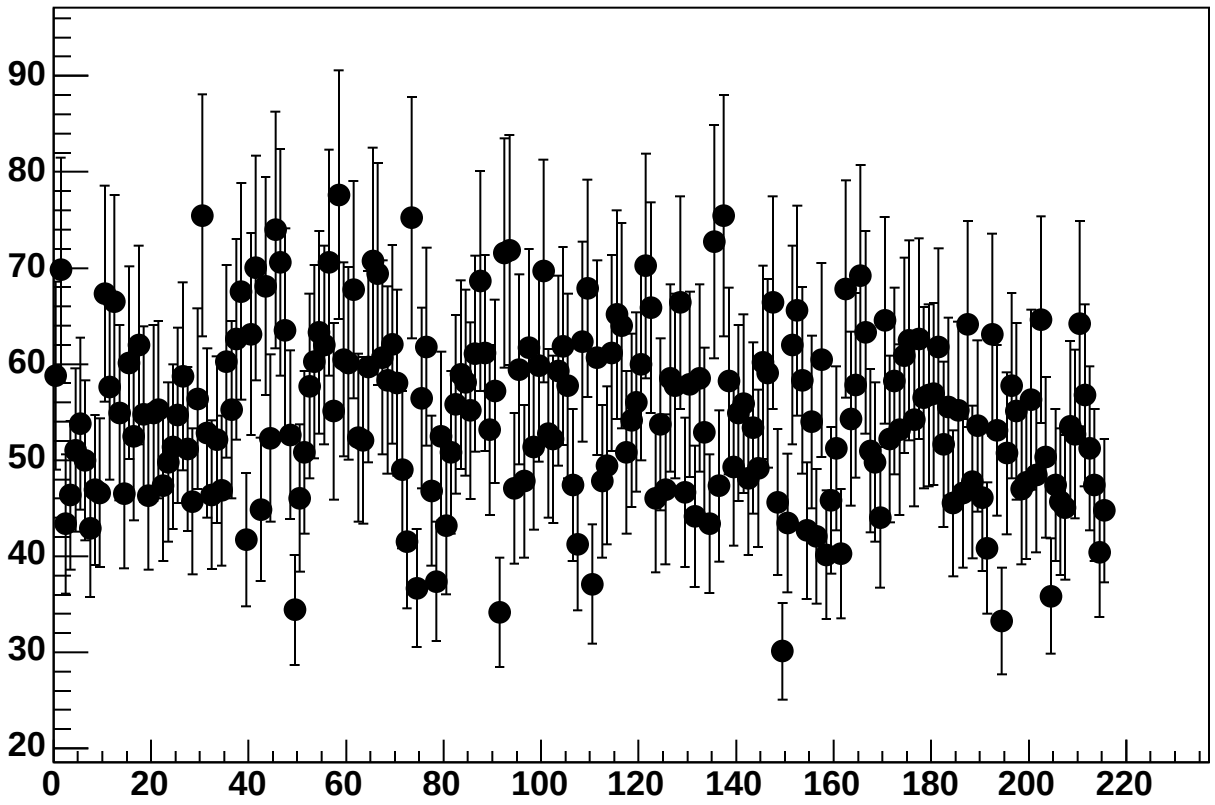
Enable 3, DAC=1600, Hold=195, ADC Noise vs 18*Chip+Chan



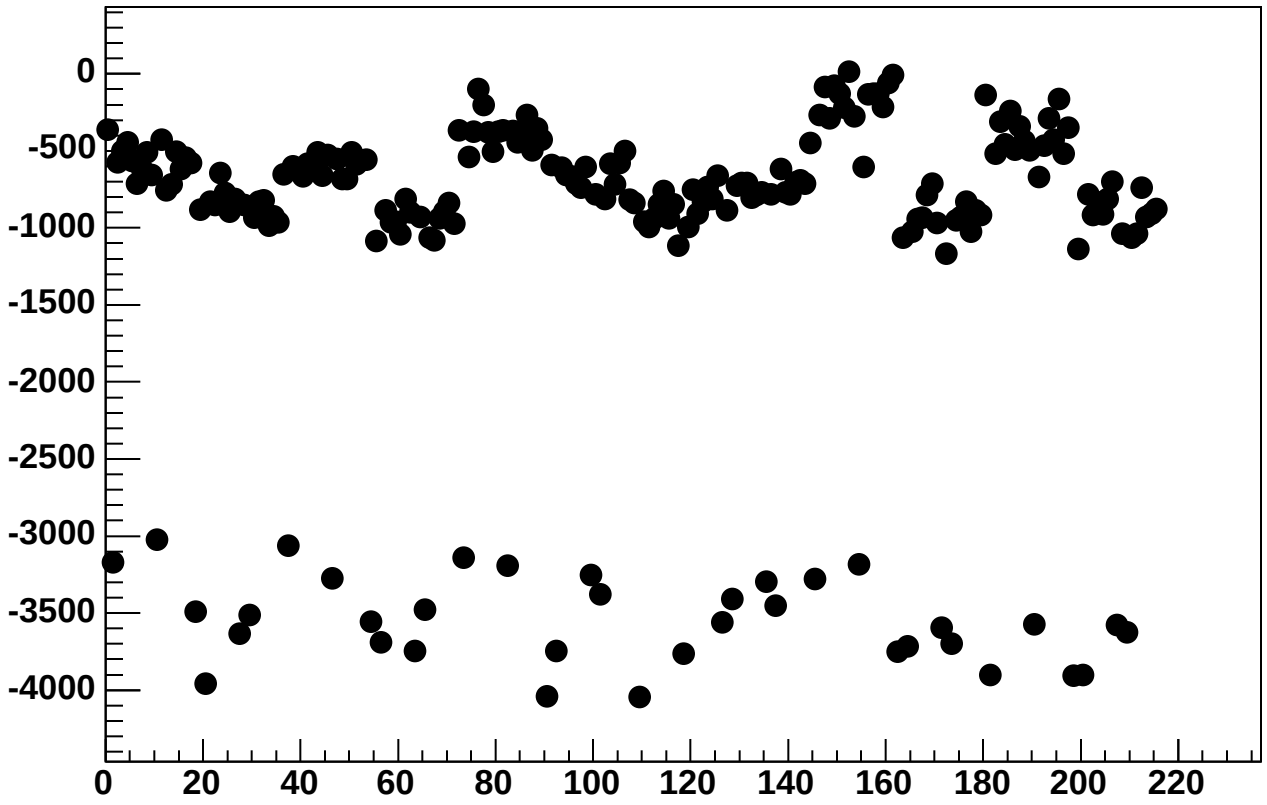
Enable 3, DAC=1600, Hold=200, ADC Mean vs 18*Chip+Chan



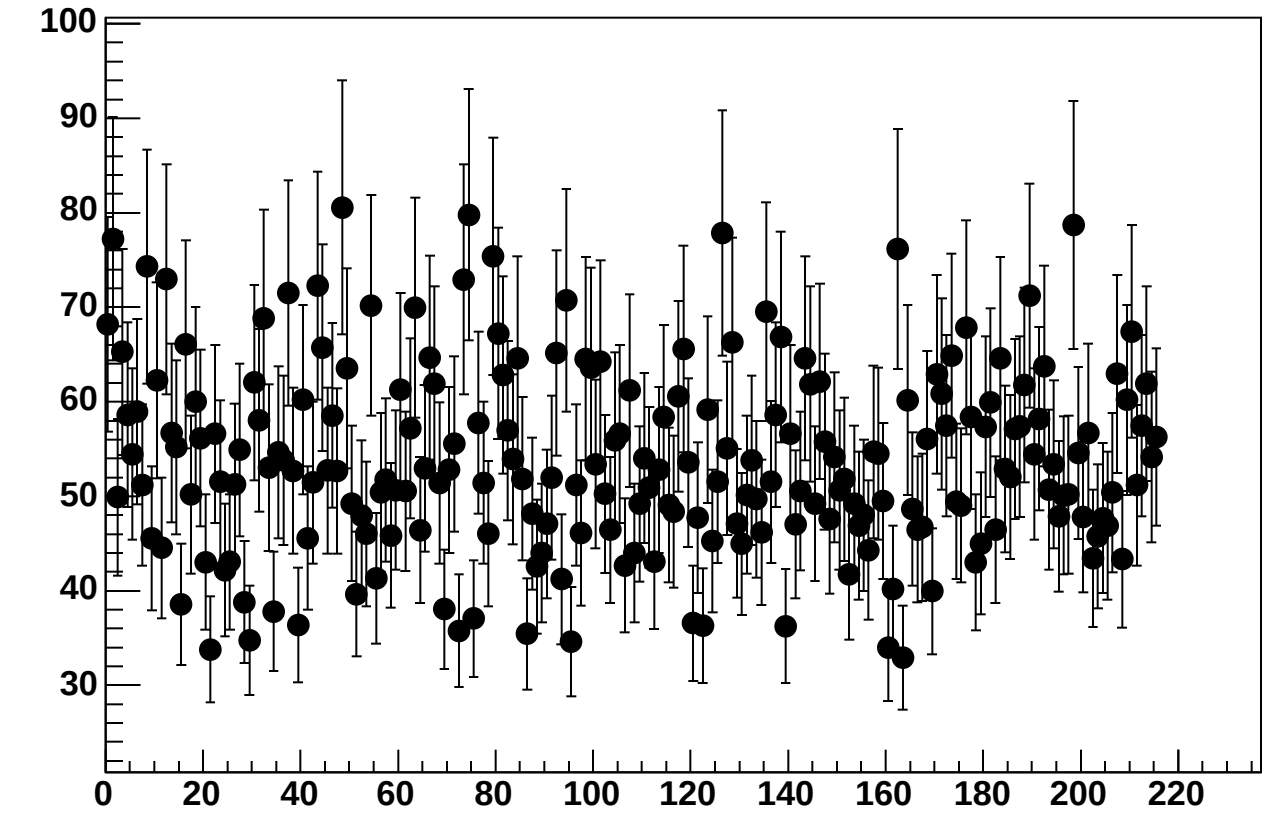
Enable 3, DAC=1600, Hold=200, ADC Noise vs 18*Chip+Chan



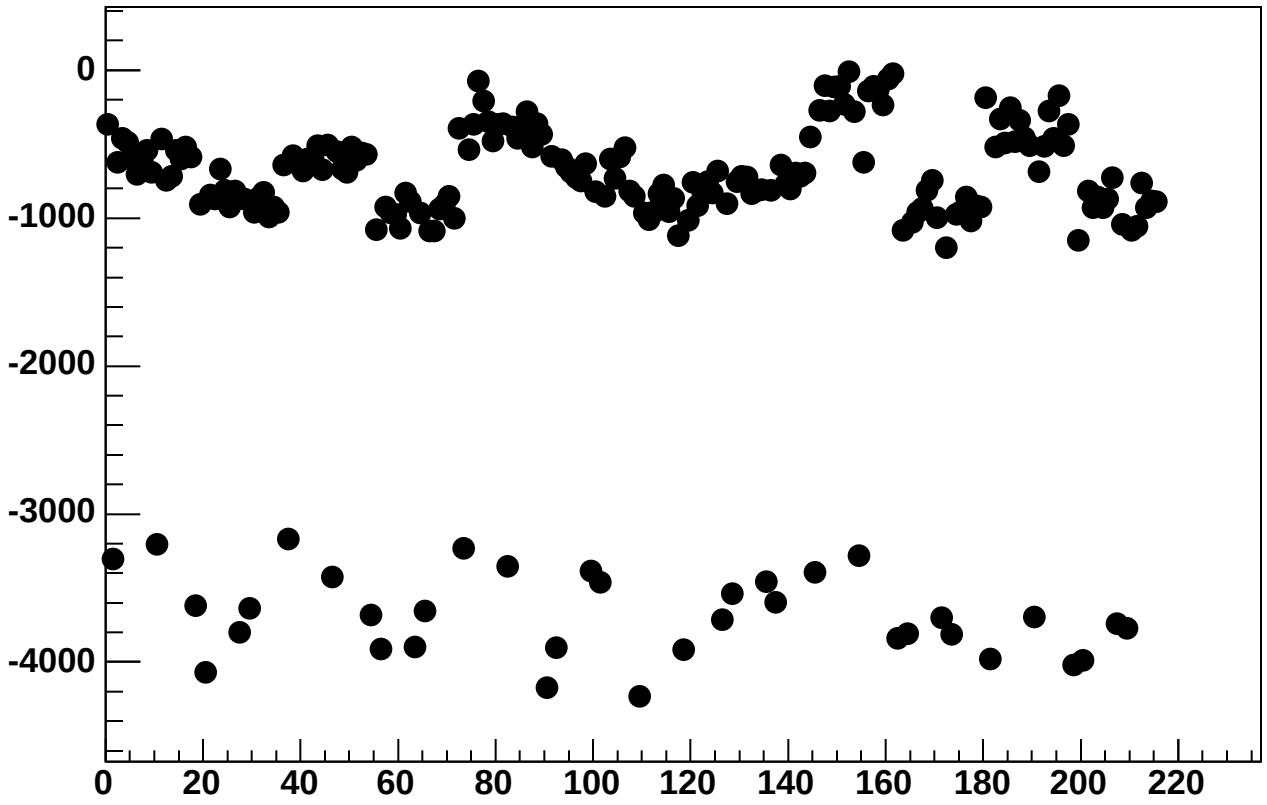
Enable 3, DAC=1600, Hold=205, ADC Mean vs 18*Chip+Chan



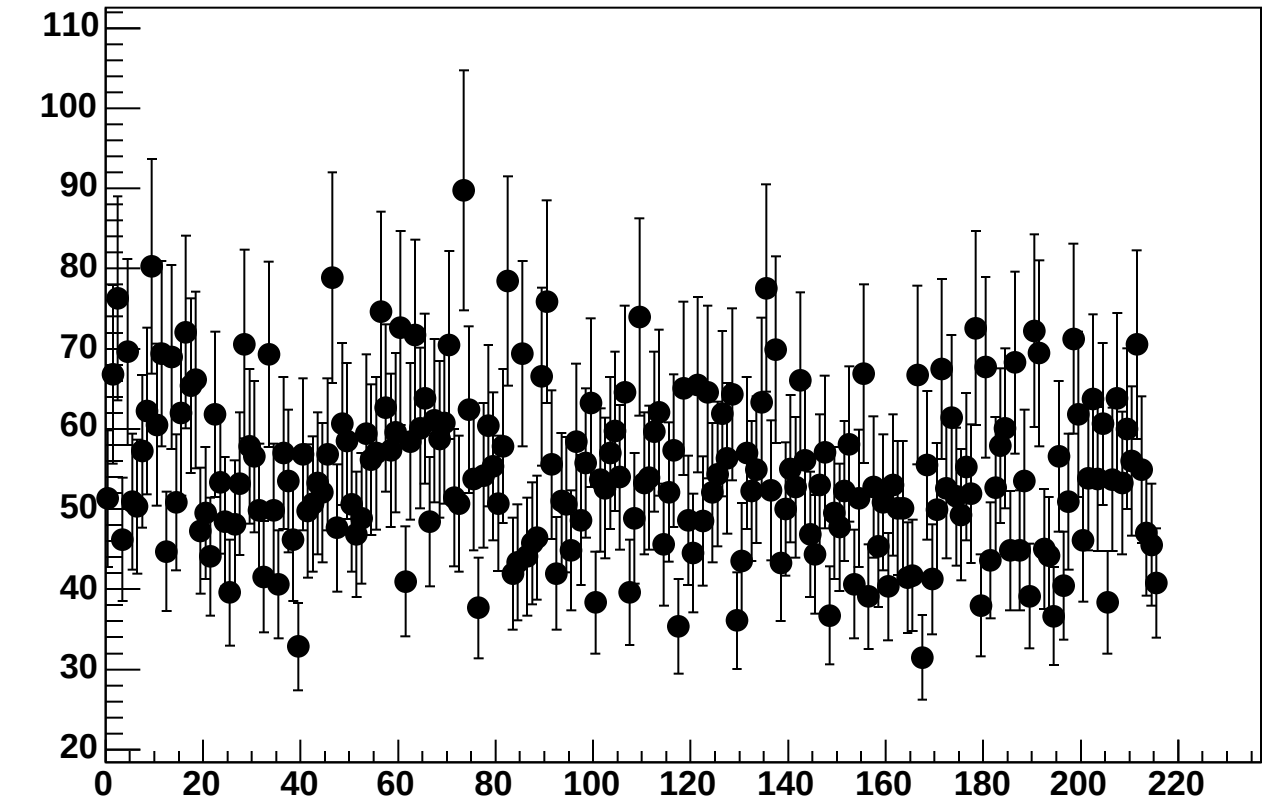
Enable 3, DAC=1600, Hold=205, ADC Noise vs 18*Chip+Chan



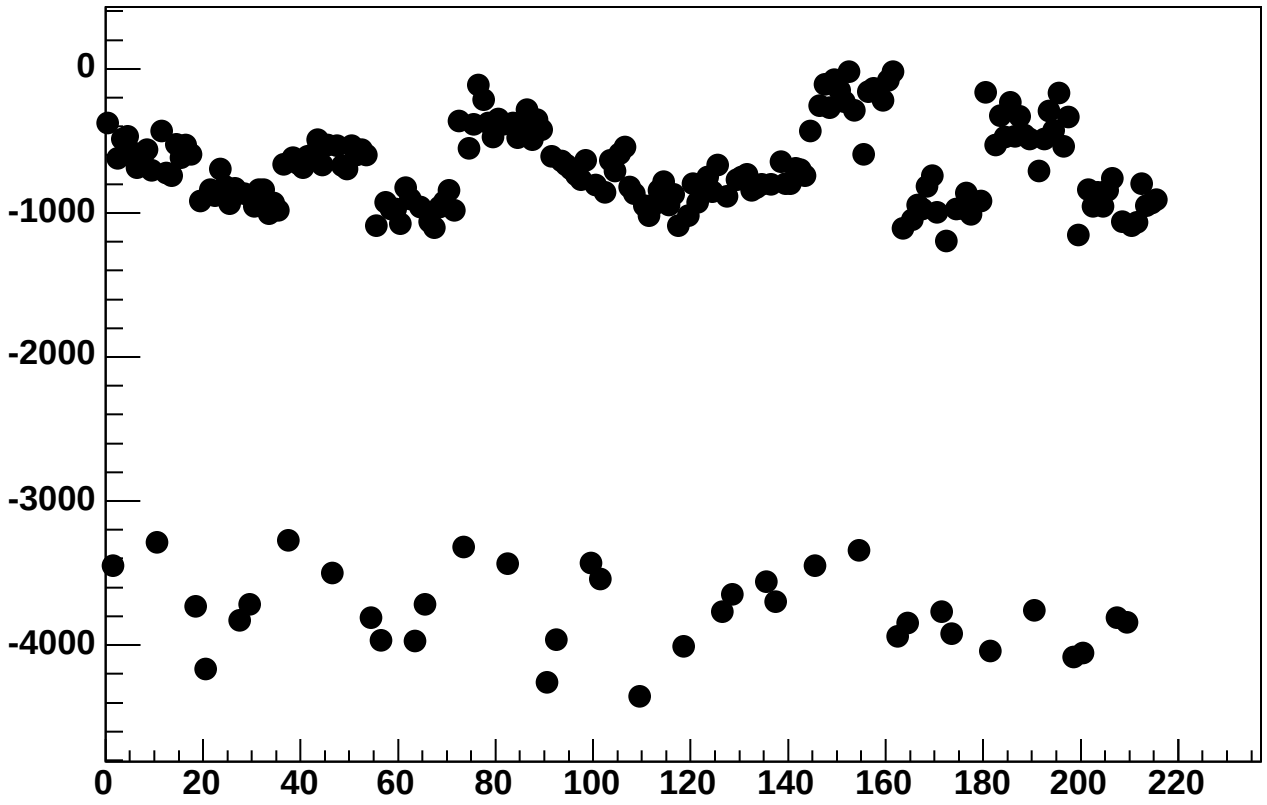
Enable 3, DAC=1600, Hold=210, ADC Mean vs 18*Chip+Chan



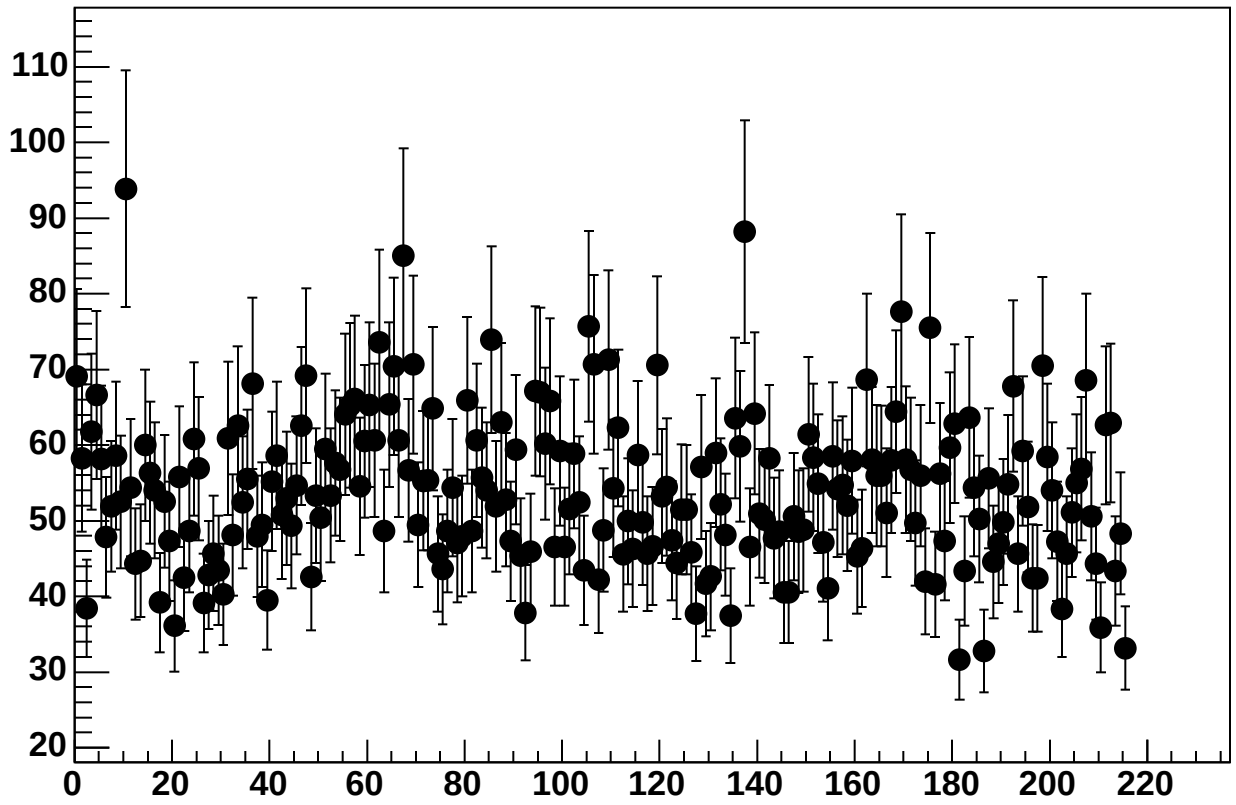
Enable 3, DAC=1600, Hold=210, ADC Noise vs 18*Chip+Chan



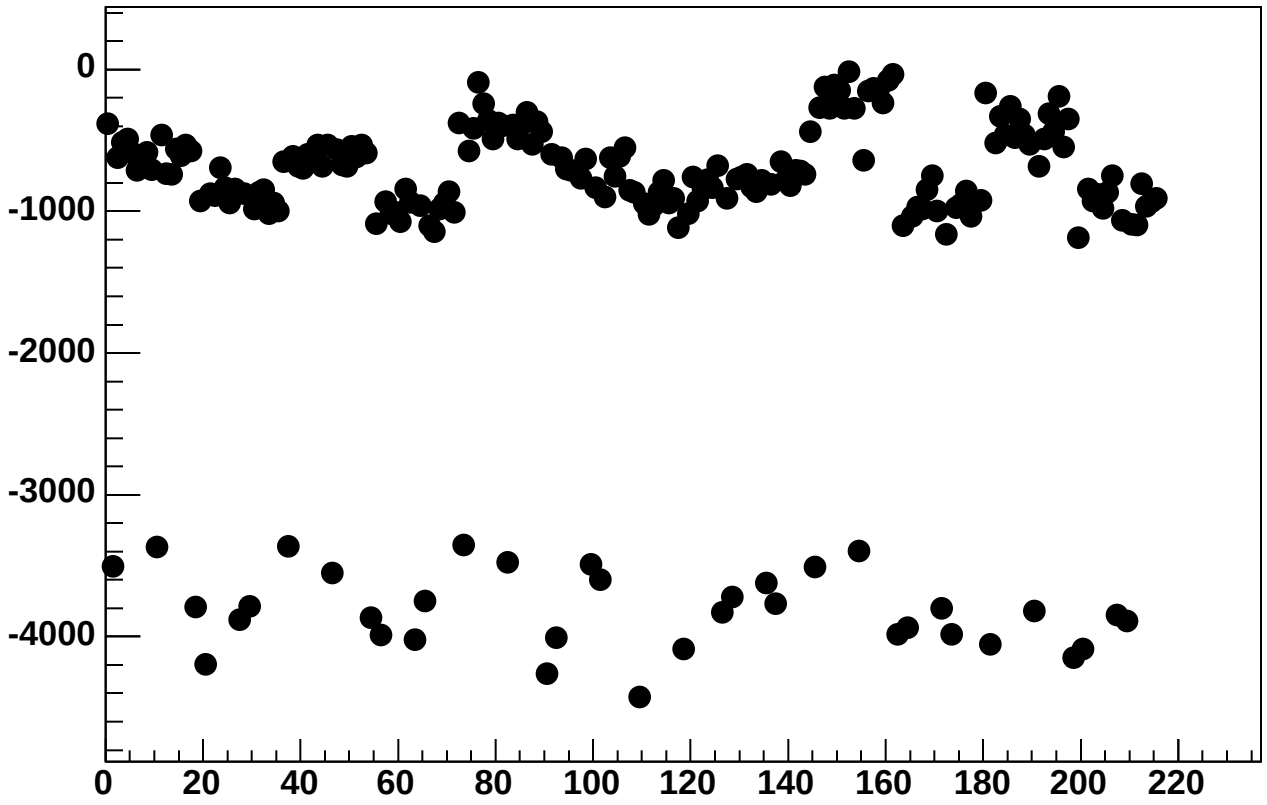
Enable 3, DAC=1600, Hold=215, ADC Mean vs 18*Chip+Chan



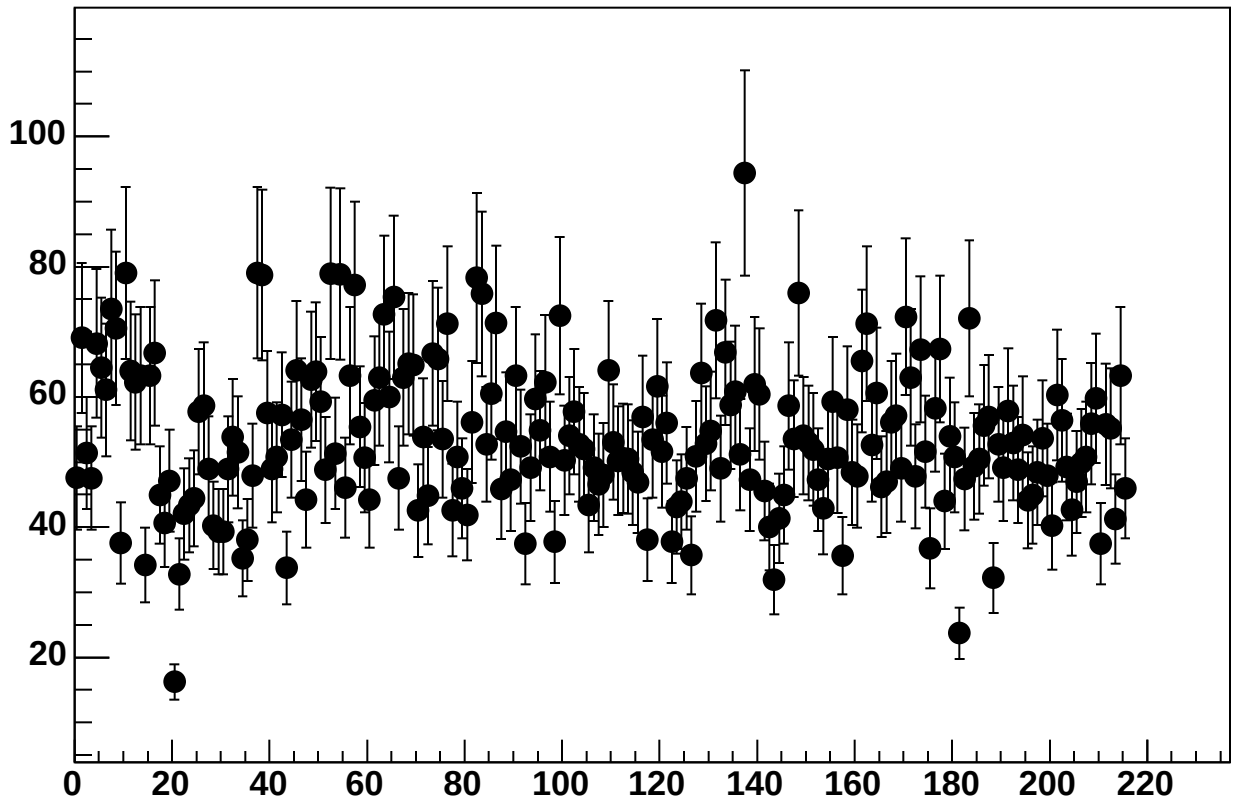
Enable 3, DAC=1600, Hold=215, ADC Noise vs 18*Chip+Chan



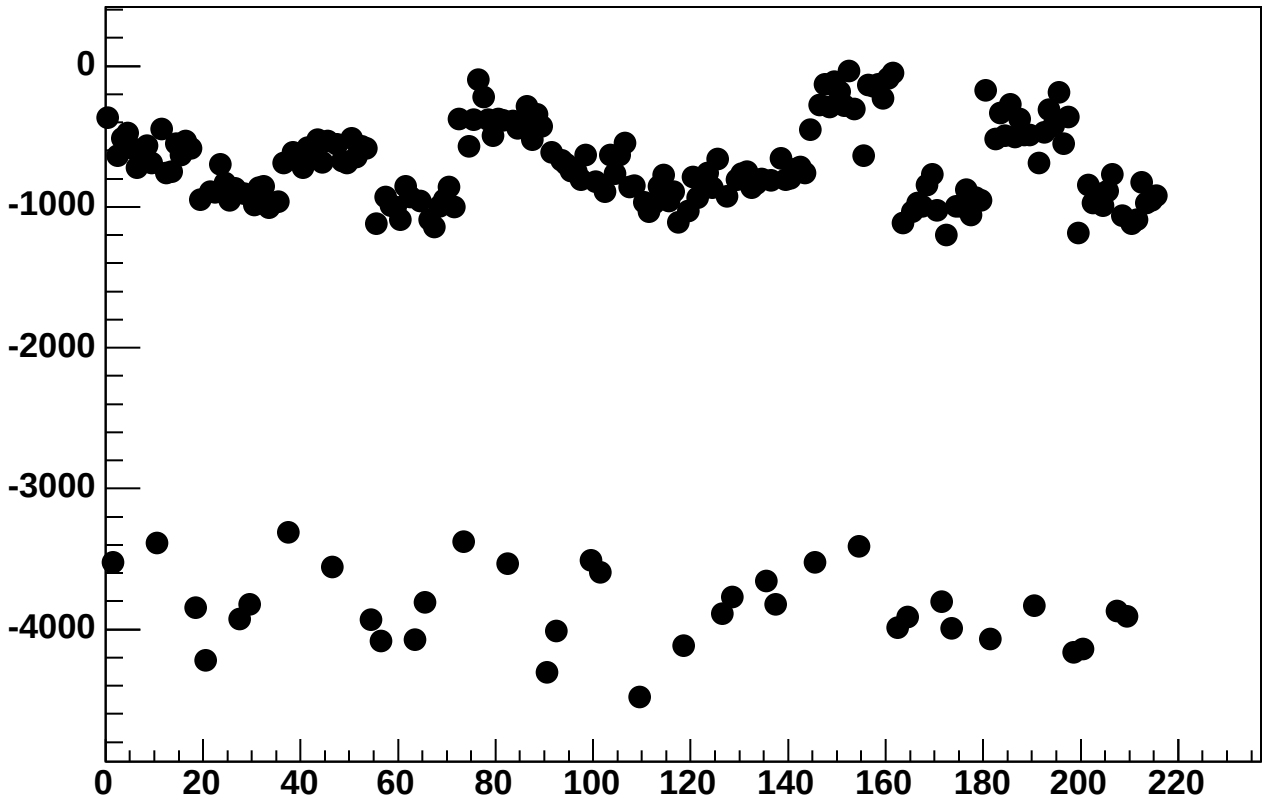
Enable 3, DAC=1600, Hold=220, ADC Mean vs 18*Chip+Chan



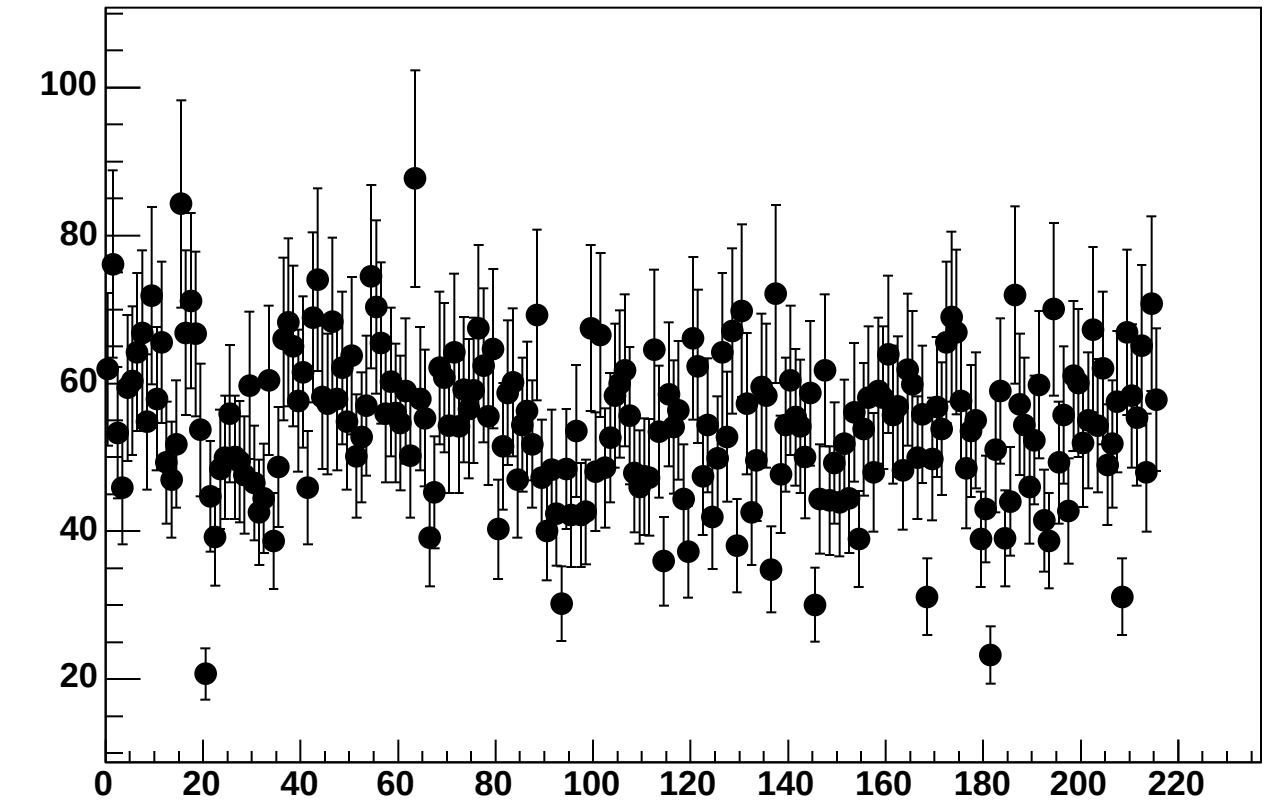
Enable 3, DAC=1600, Hold=220, ADC Noise vs 18*Chip+Chan



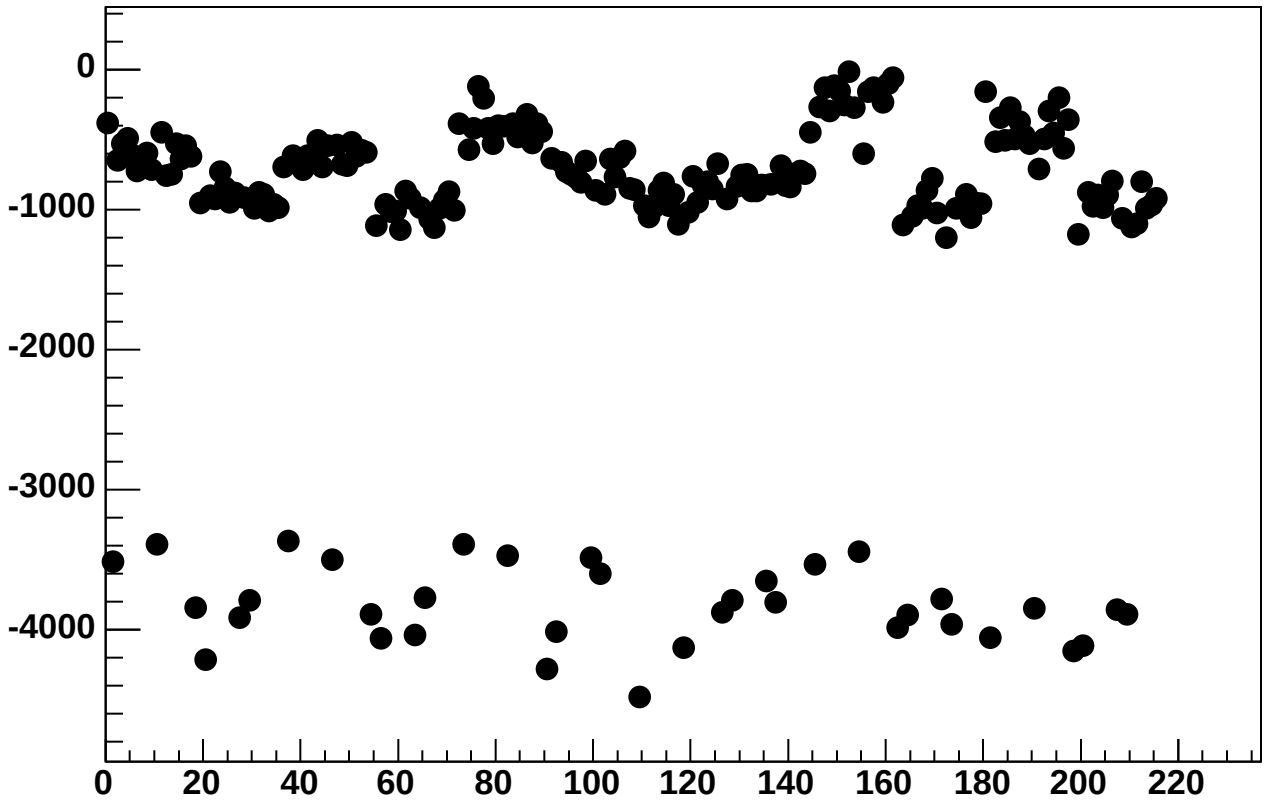
Enable 3, DAC=1600, Hold=225, ADC Mean vs 18*Chip+Chan



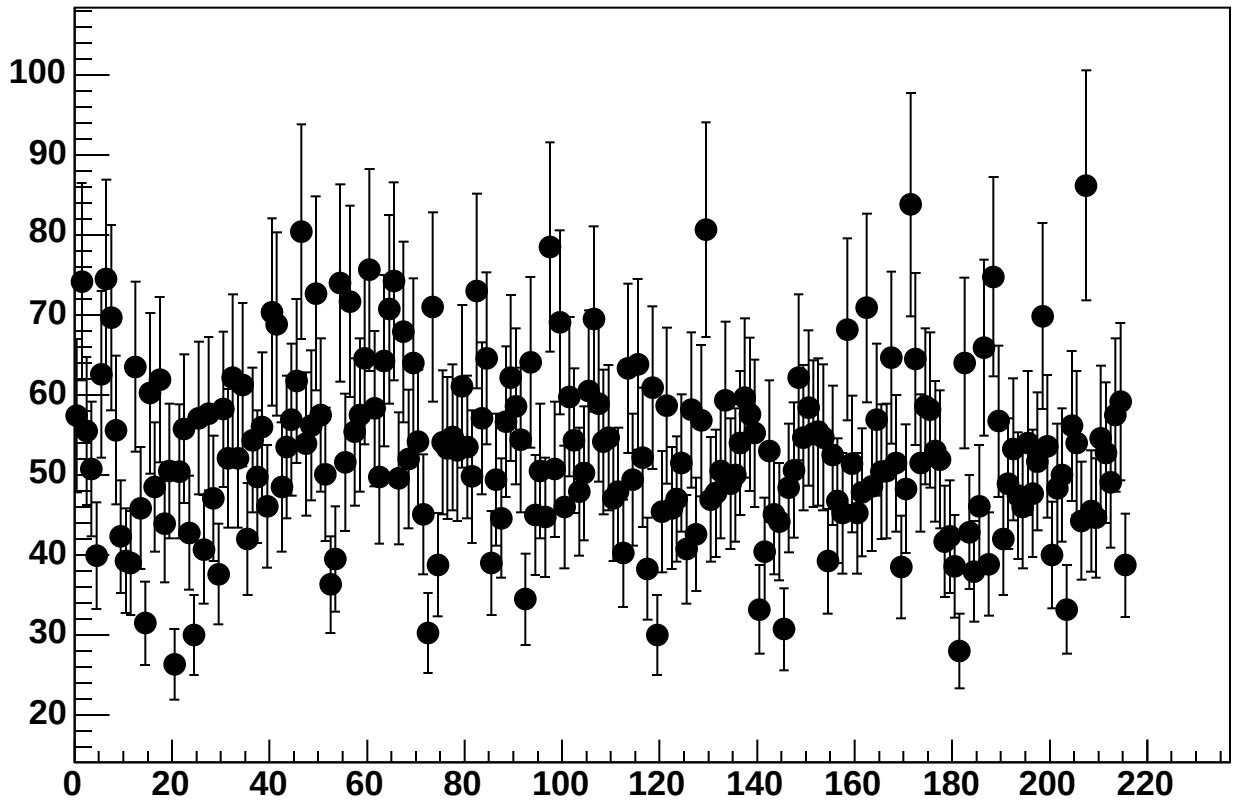
Enable 3, DAC=1600, Hold=225, ADC Noise vs 18*Chip+Chan



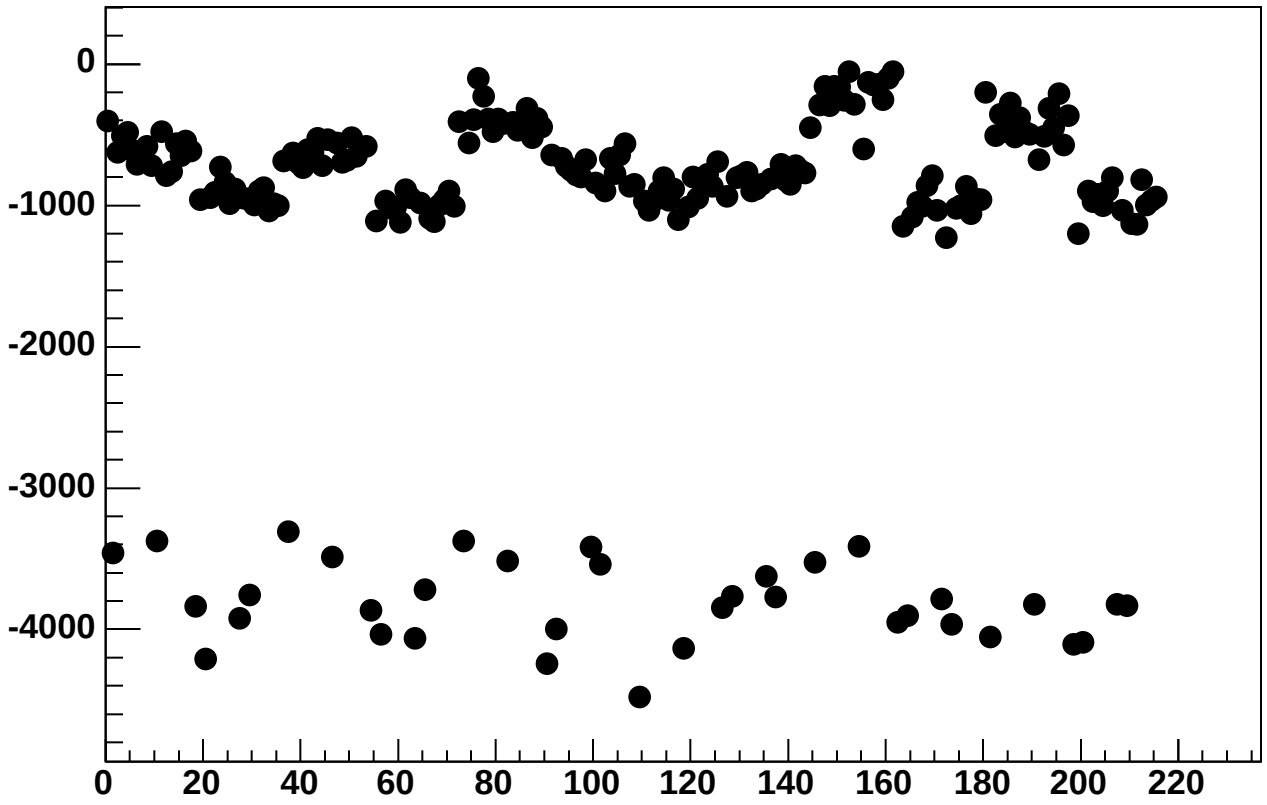
Enable 3, DAC=1600, Hold=230, ADC Mean vs 18*Chip+Chan



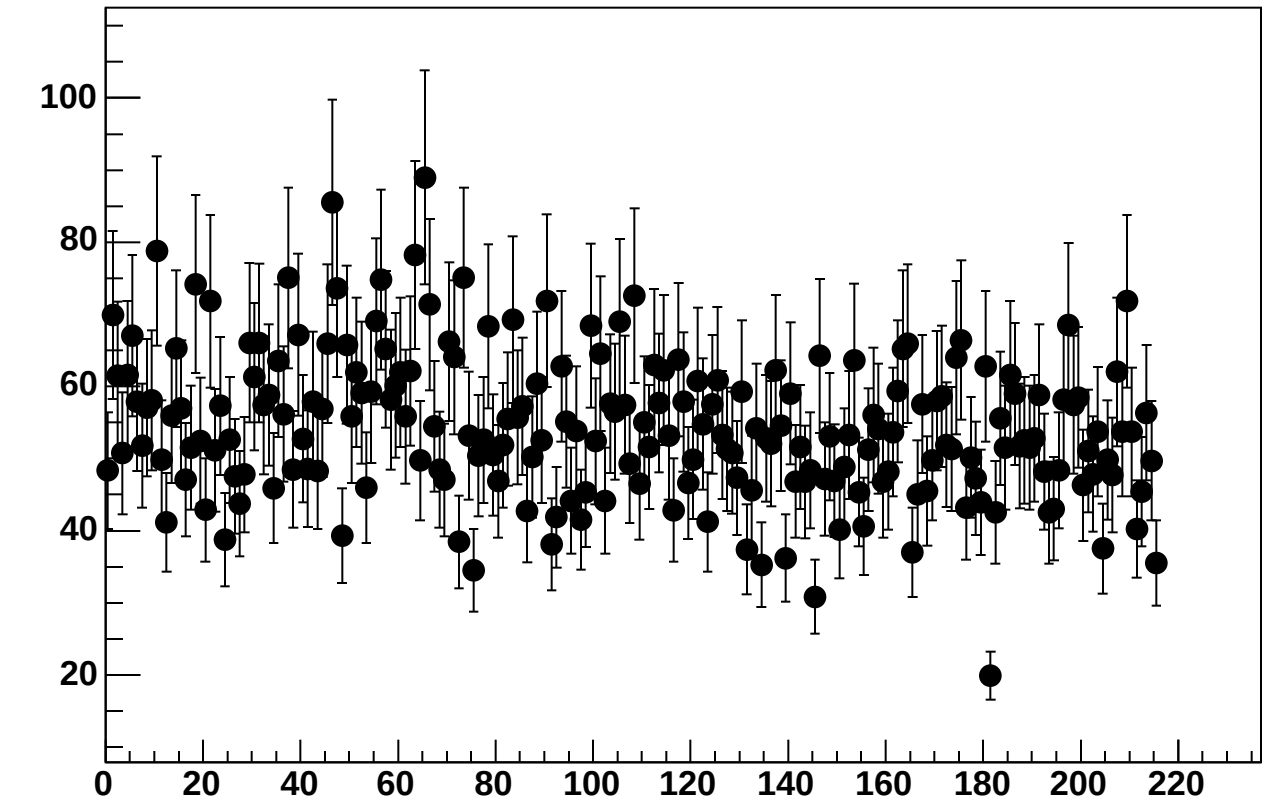
Enable 3, DAC=1600, Hold=230, ADC Noise vs 18*Chip+Chan



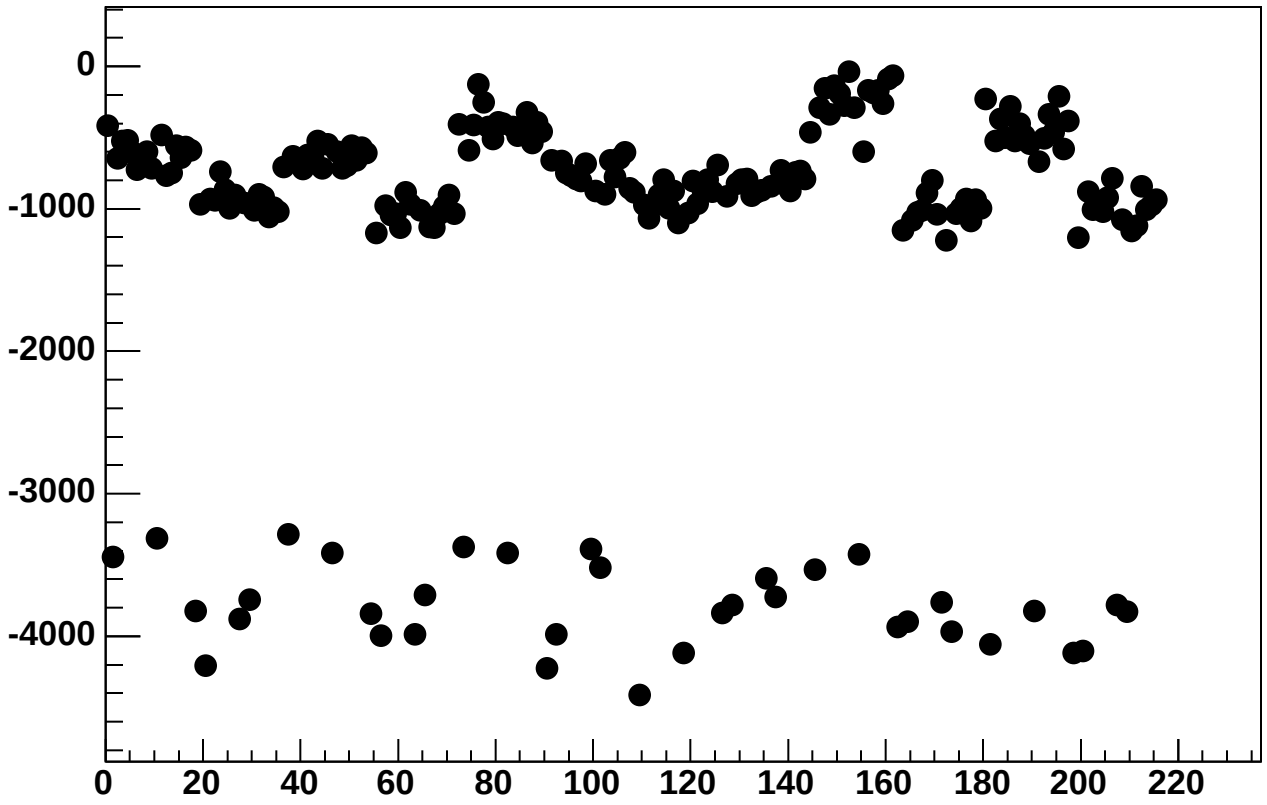
Enable 3, DAC=1600, Hold=235, ADC Mean vs 18*Chip+Chan



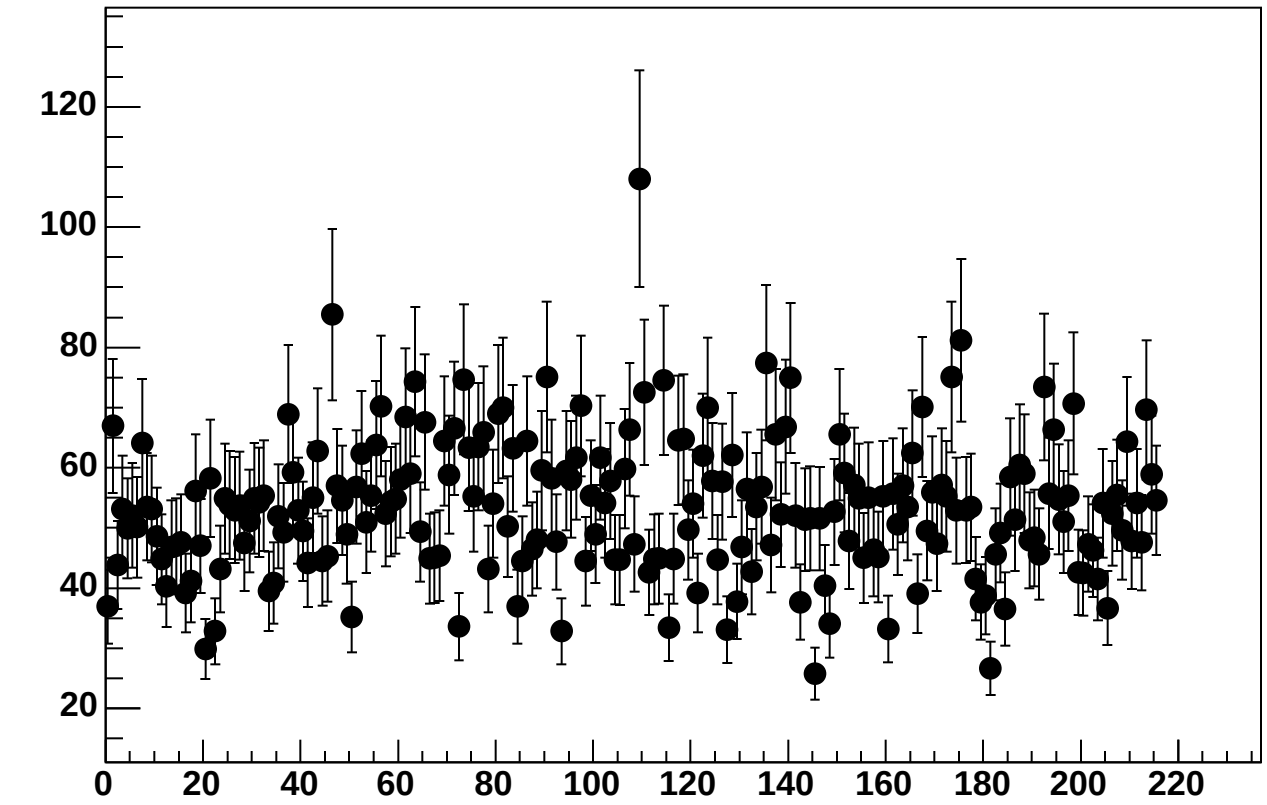
Enable 3, DAC=1600, Hold=235, ADC Noise vs 18*Chip+Chan



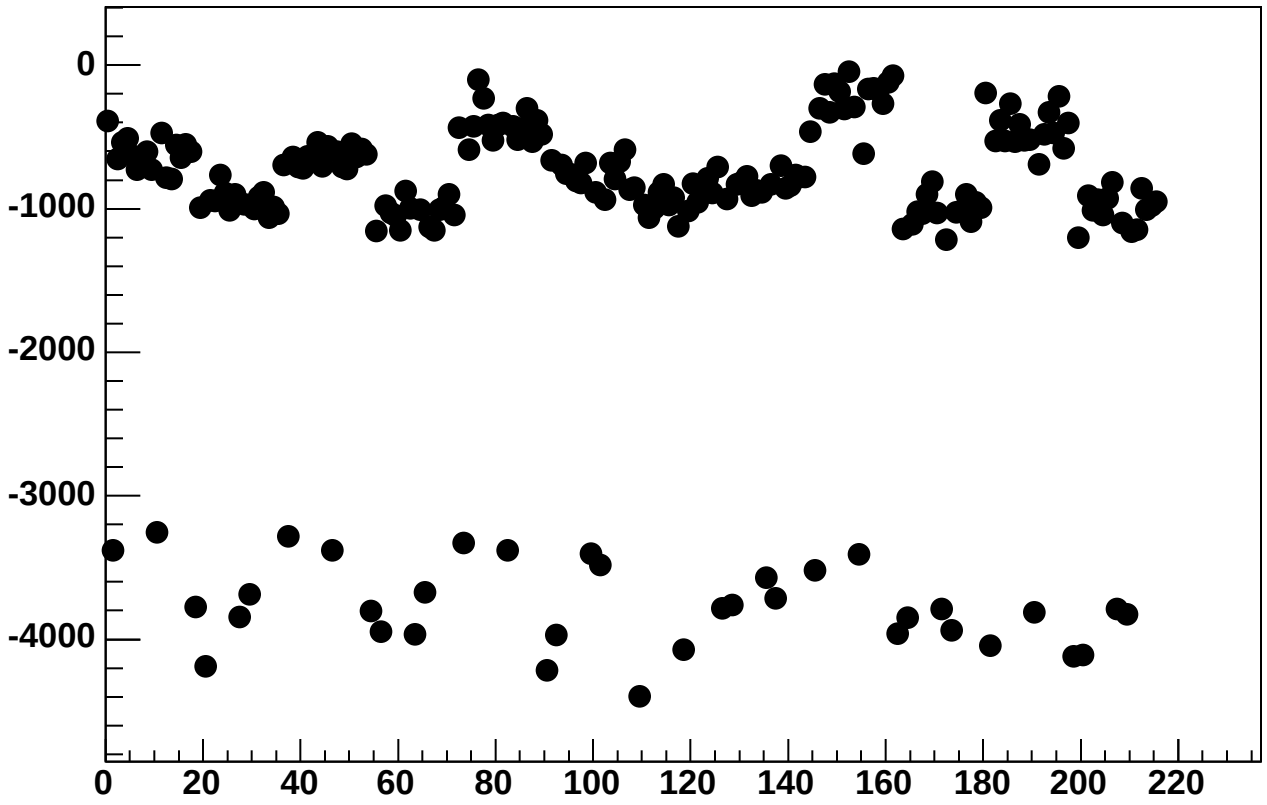
Enable 3, DAC=1600, Hold=240, ADC Mean vs 18*Chip+Chan



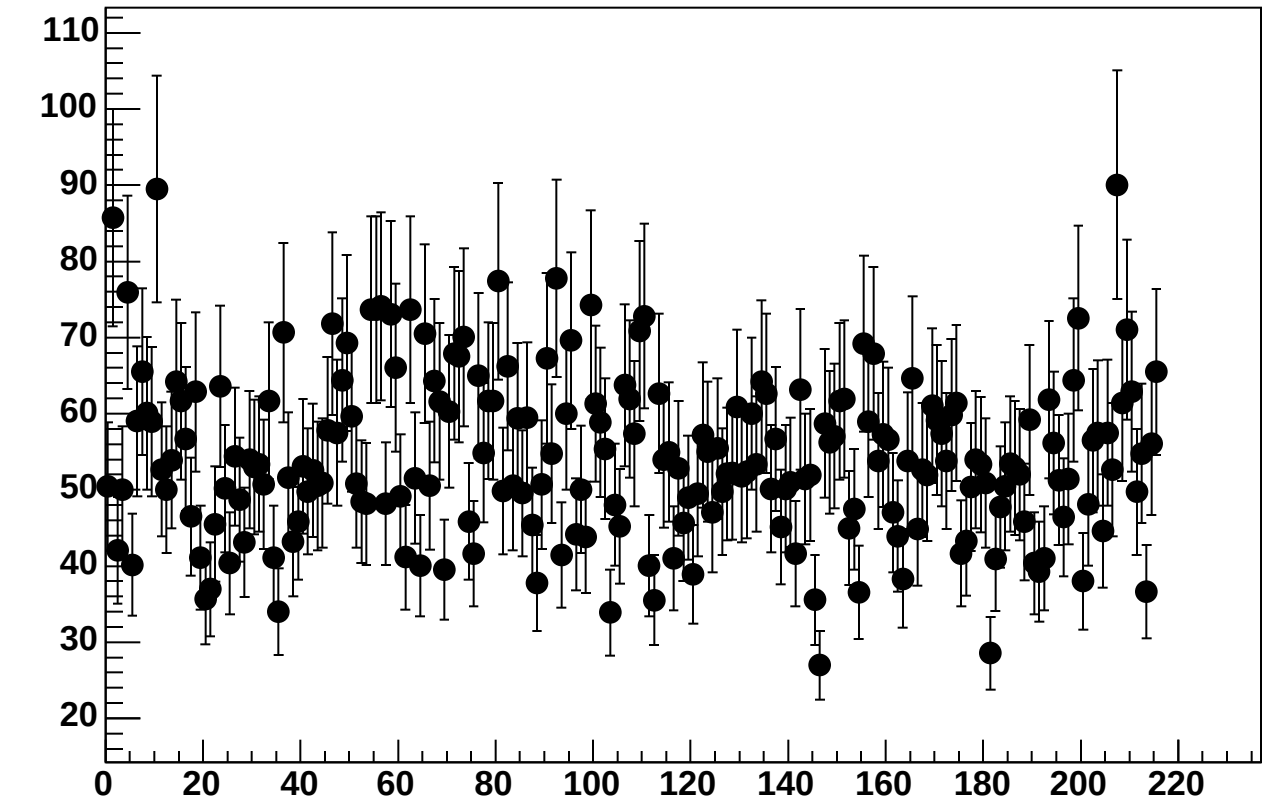
Enable 3, DAC=1600, Hold=240, ADC Noise vs 18*Chip+Chan



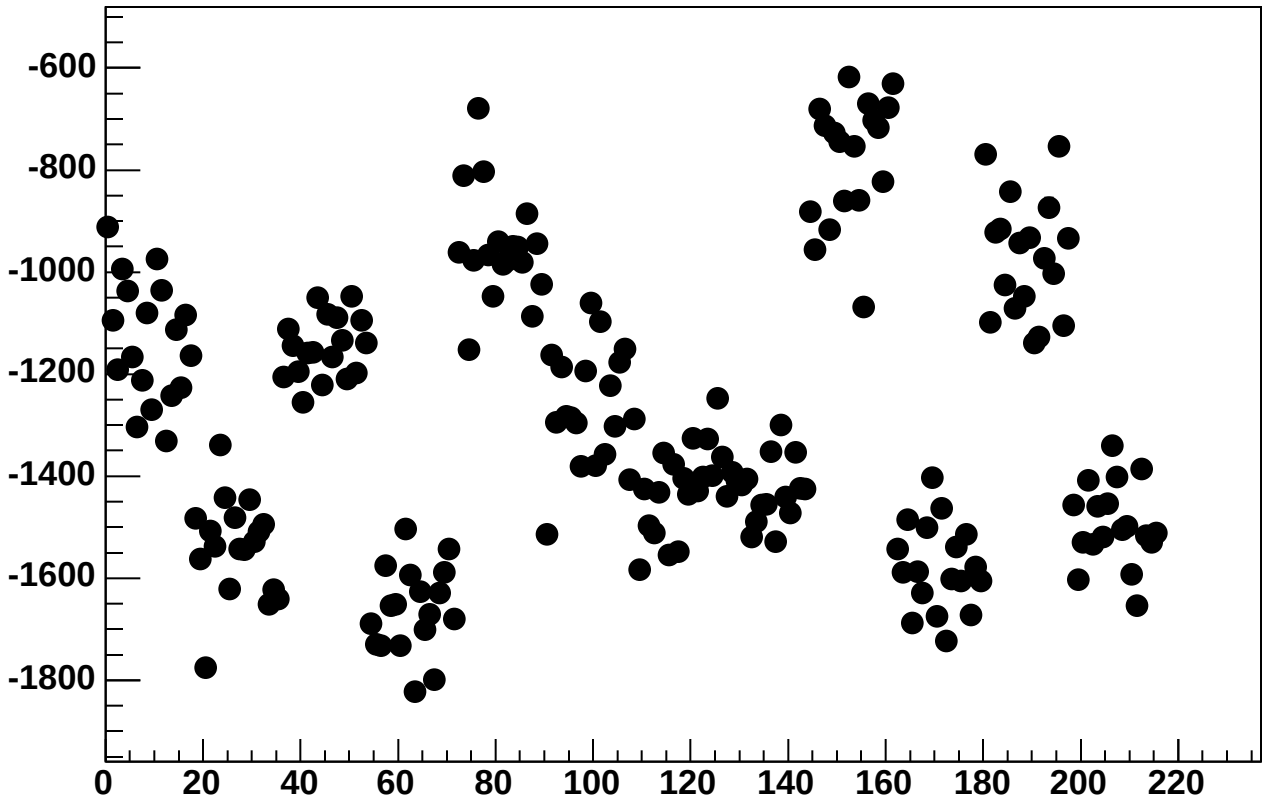
Enable 3, DAC=1600, Hold=245, ADC Mean vs 18*Chip+Chan



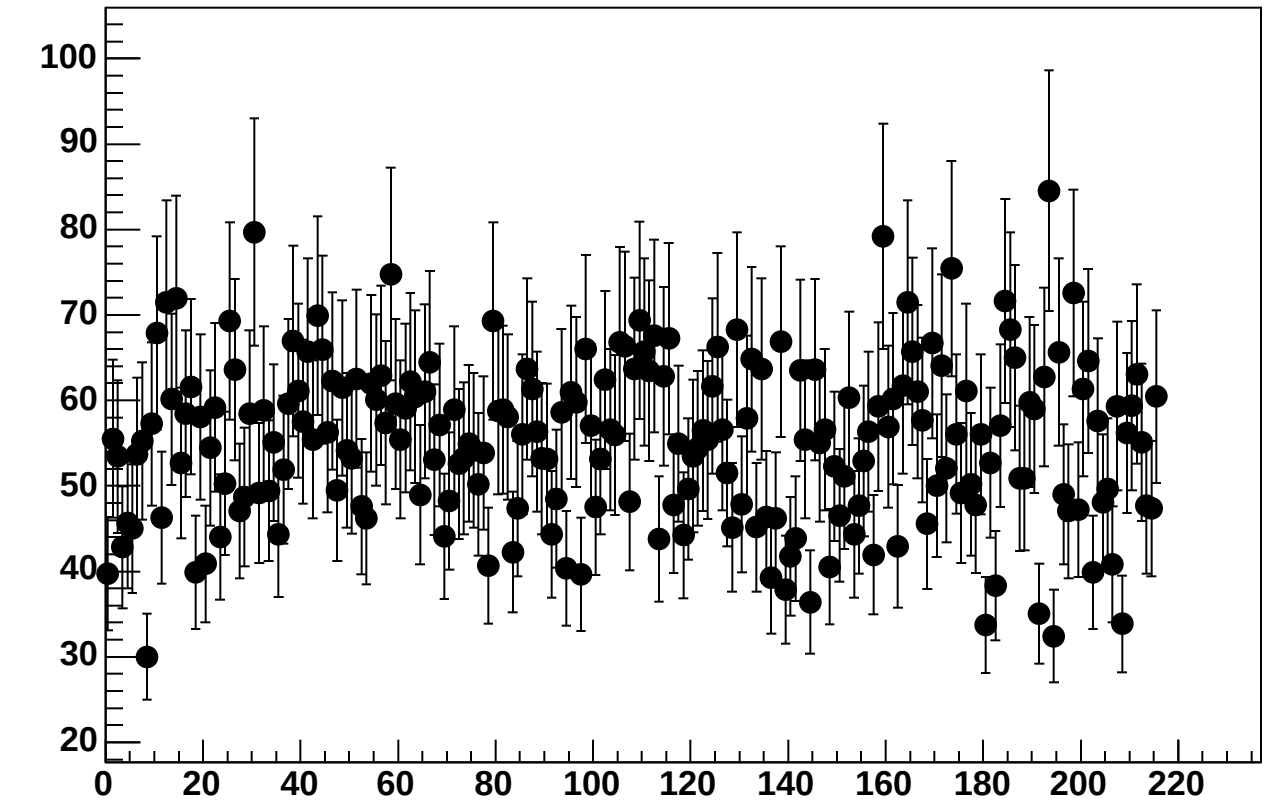
Enable 3, DAC=1600, Hold=245, ADC Noise vs 18*Chip+Chan



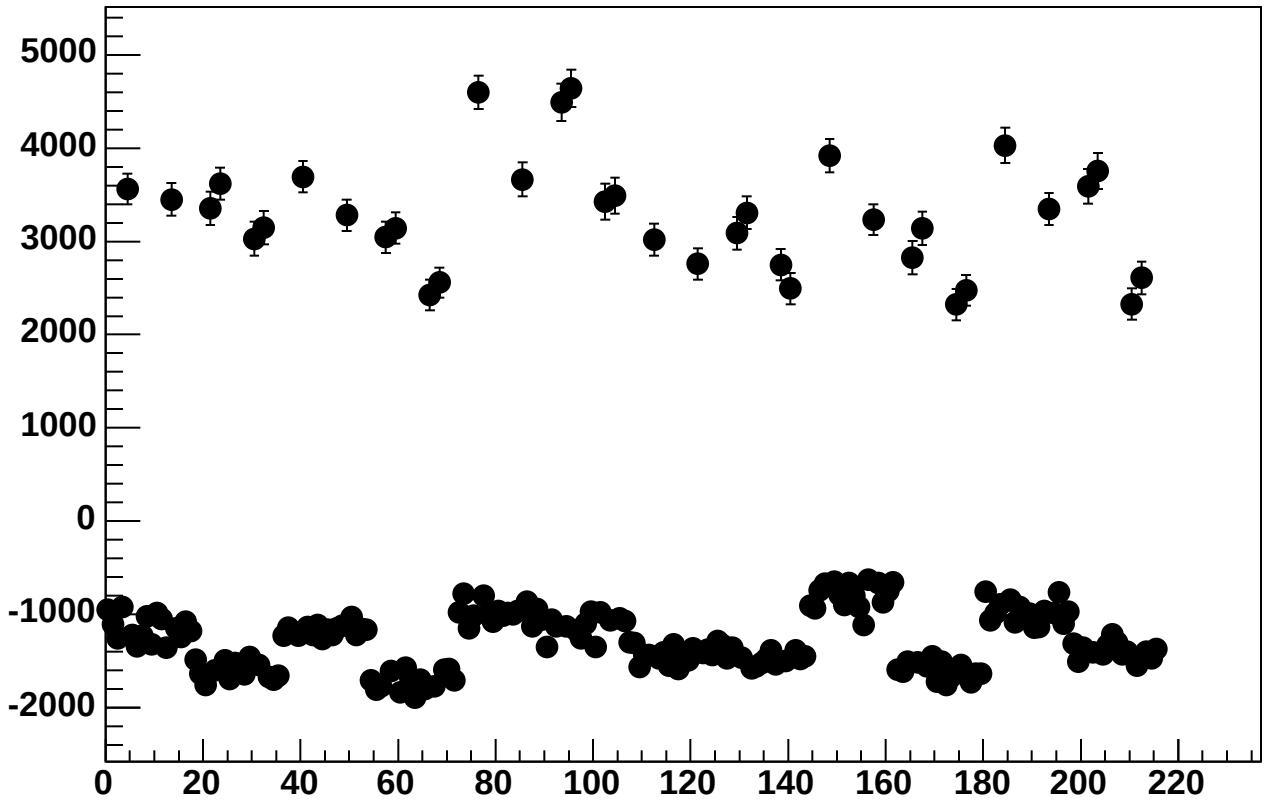
Enable 4, DAC=1600, Hold=0, ADC Mean vs 18*Chip+Chan



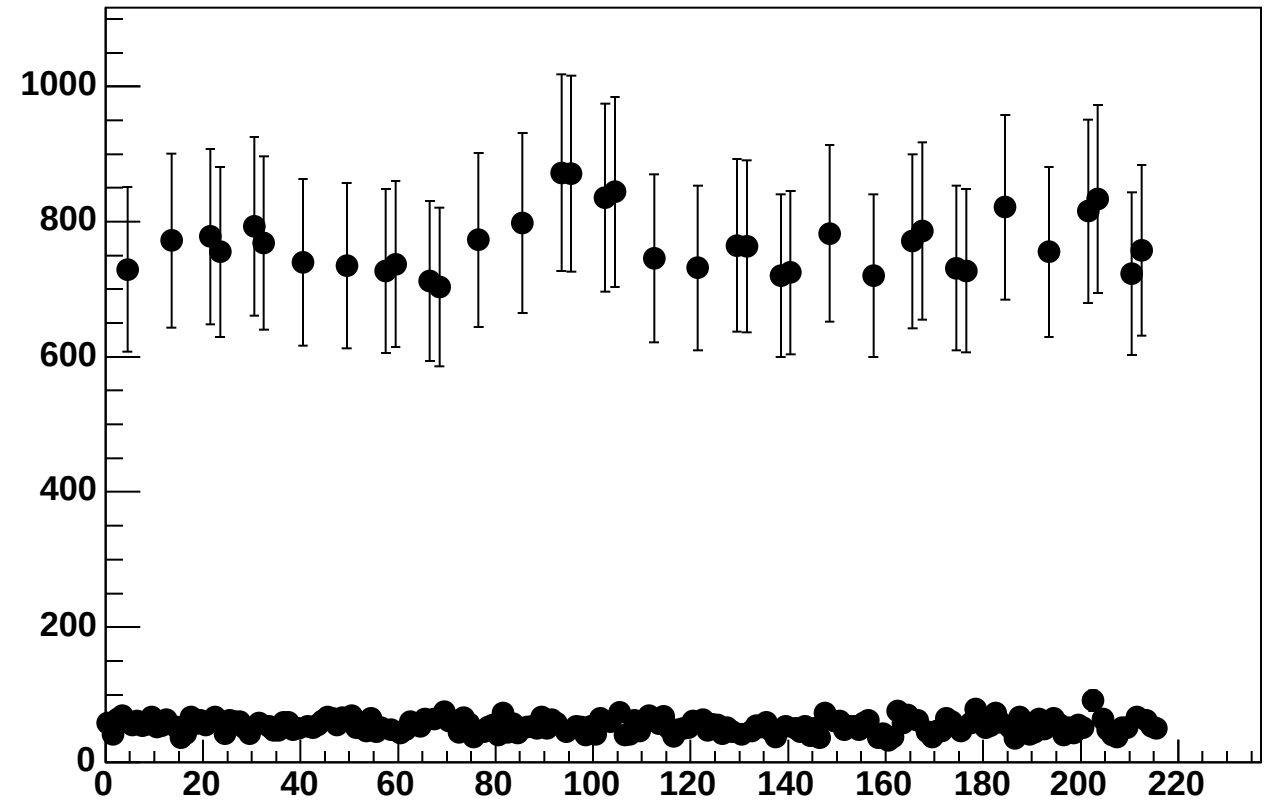
Enable 4, DAC=1600, Hold=0, ADC Noise vs 18*Chip+Chan



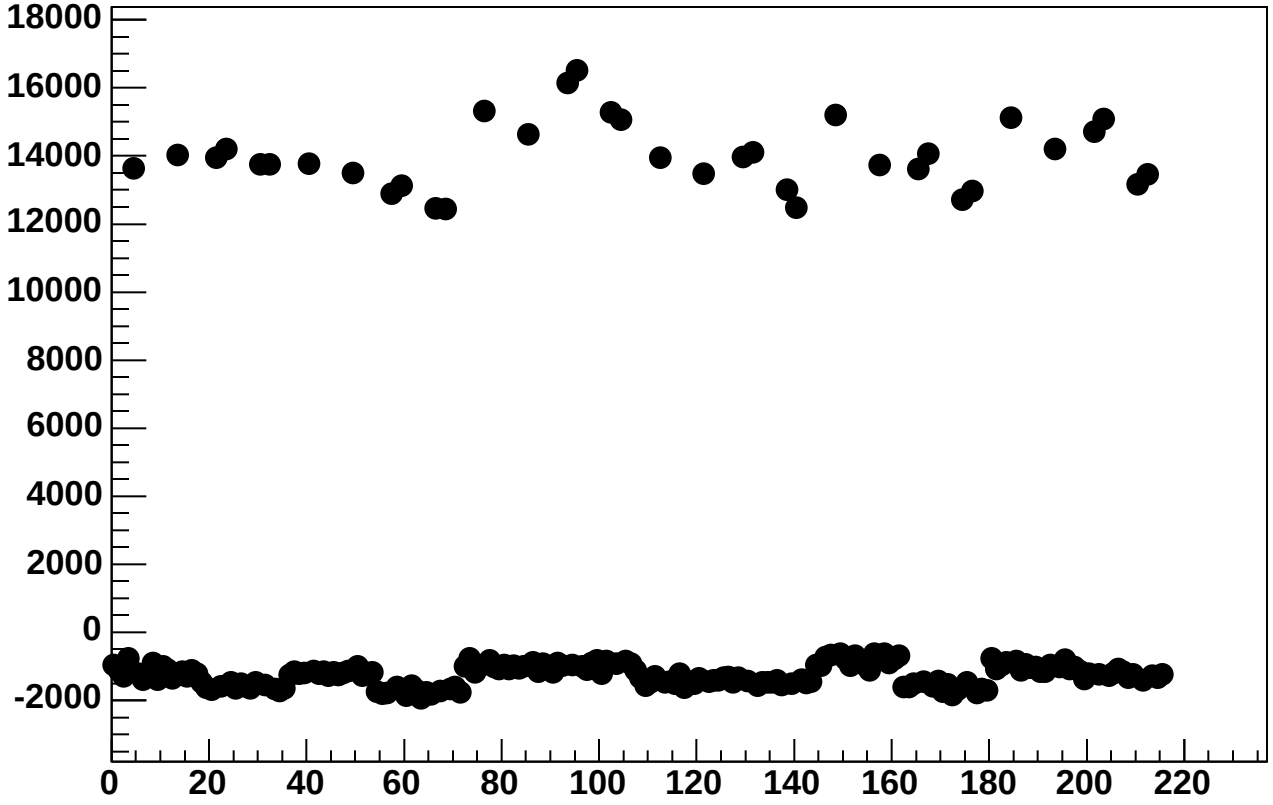
Enable 4, DAC=1600, Hold=5, ADC Mean vs 18*Chip+Chan



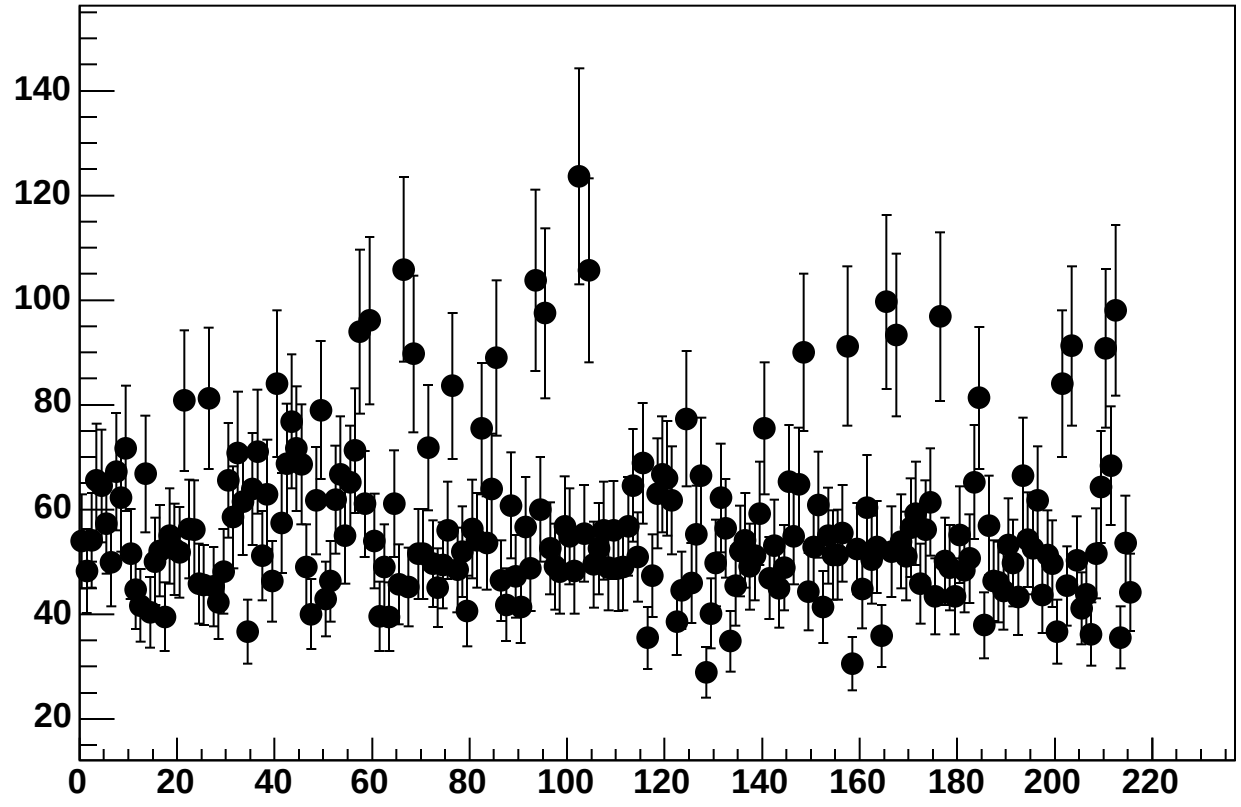
Enable 4, DAC=1600, Hold=5, ADC Noise vs 18*Chip+Chan



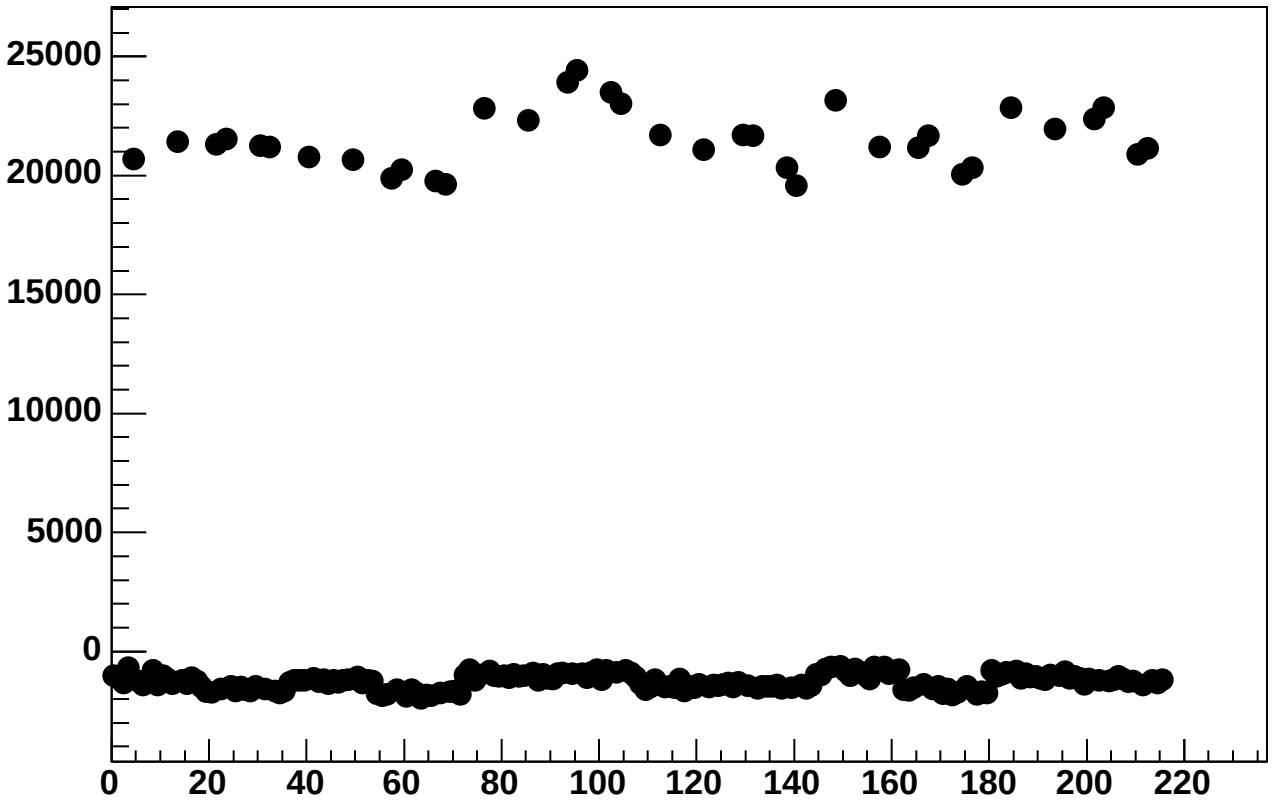
Enable 4, DAC=1600, Hold=10, ADC Mean vs 18*Chip+Chan



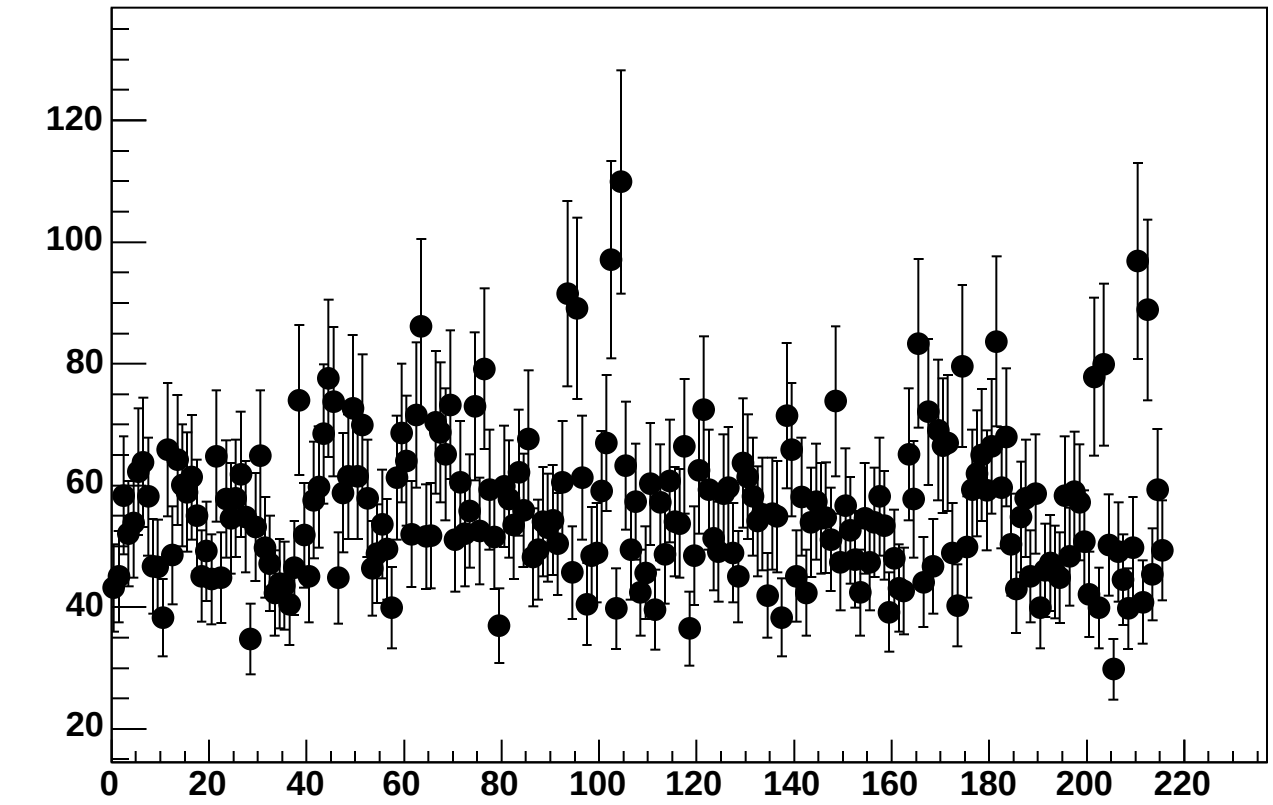
Enable 4, DAC=1600, Hold=10, ADC Noise vs 18*Chip+Chan



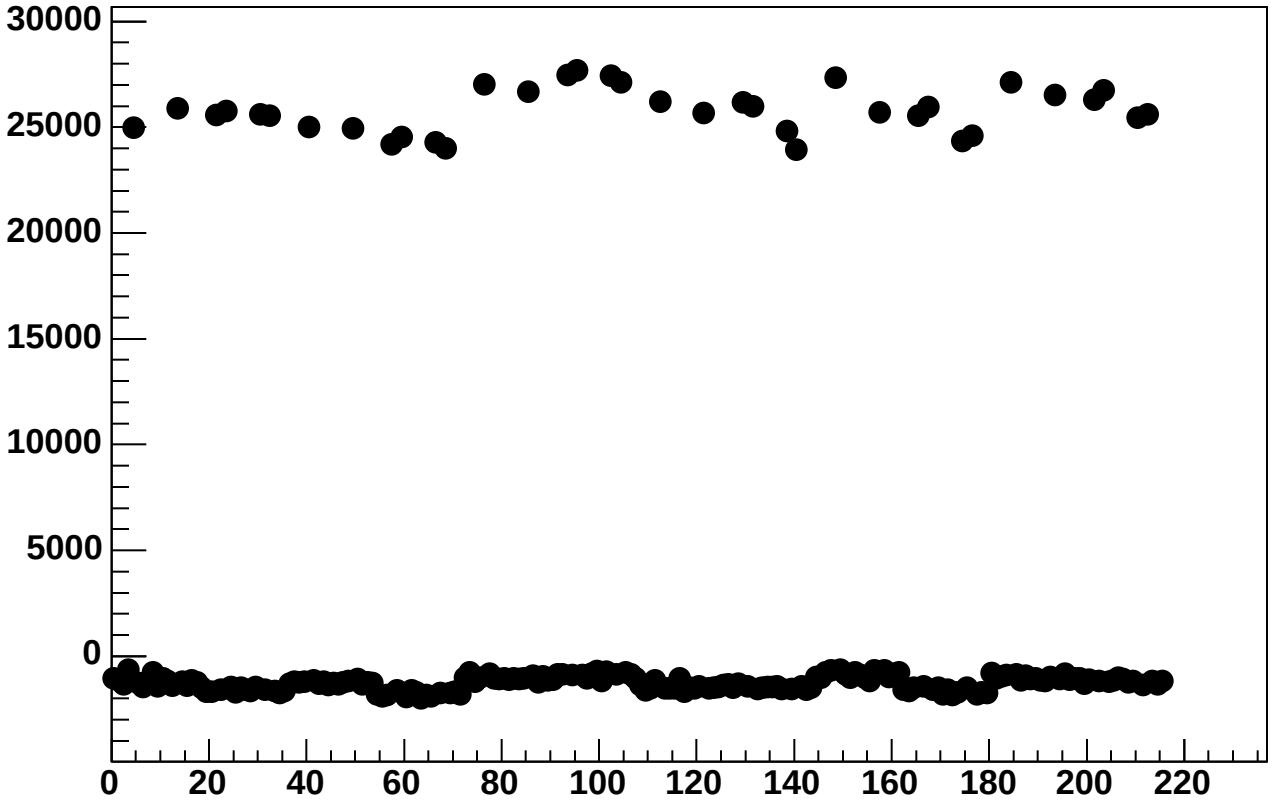
Enable 4, DAC=1600, Hold=15, ADC Mean vs 18*Chip+Chan



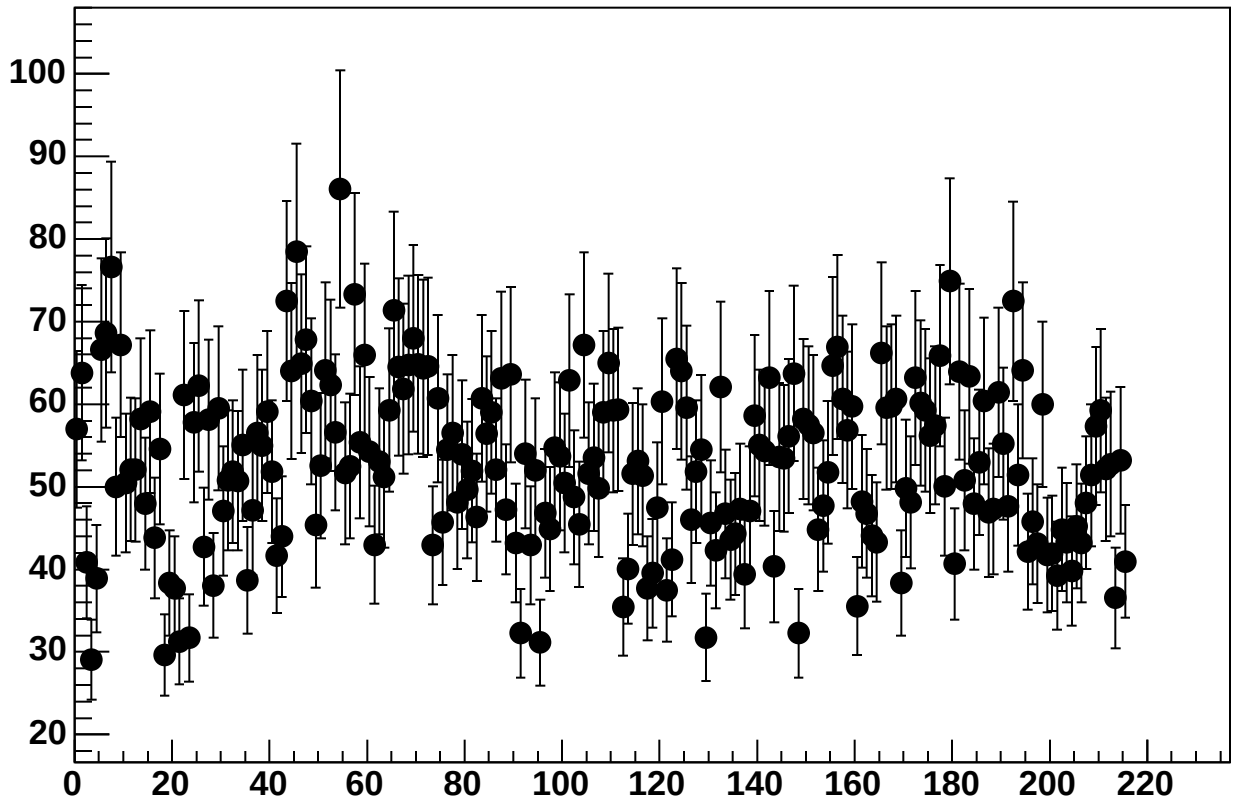
Enable 4, DAC=1600, Hold=15, ADC Noise vs 18*Chip+Chan



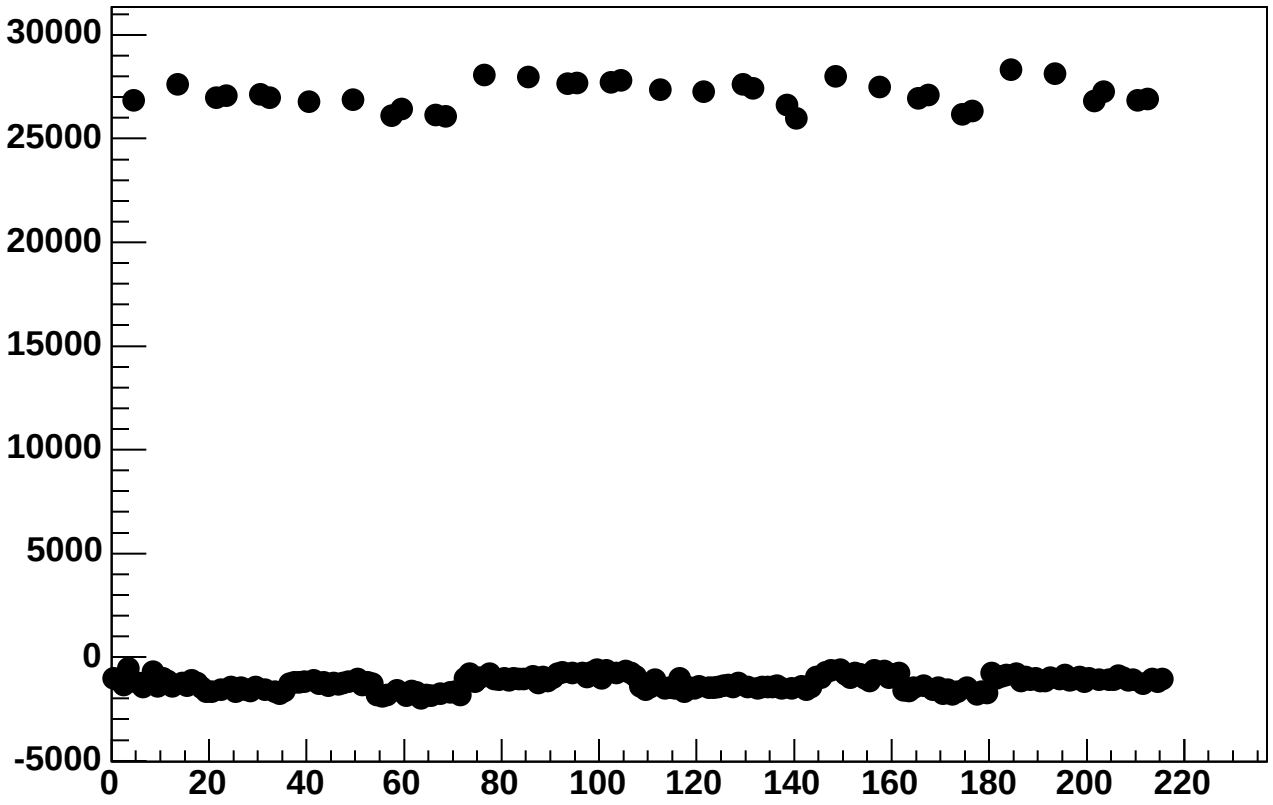
Enable 4, DAC=1600, Hold=20, ADC Mean vs 18*Chip+Chan



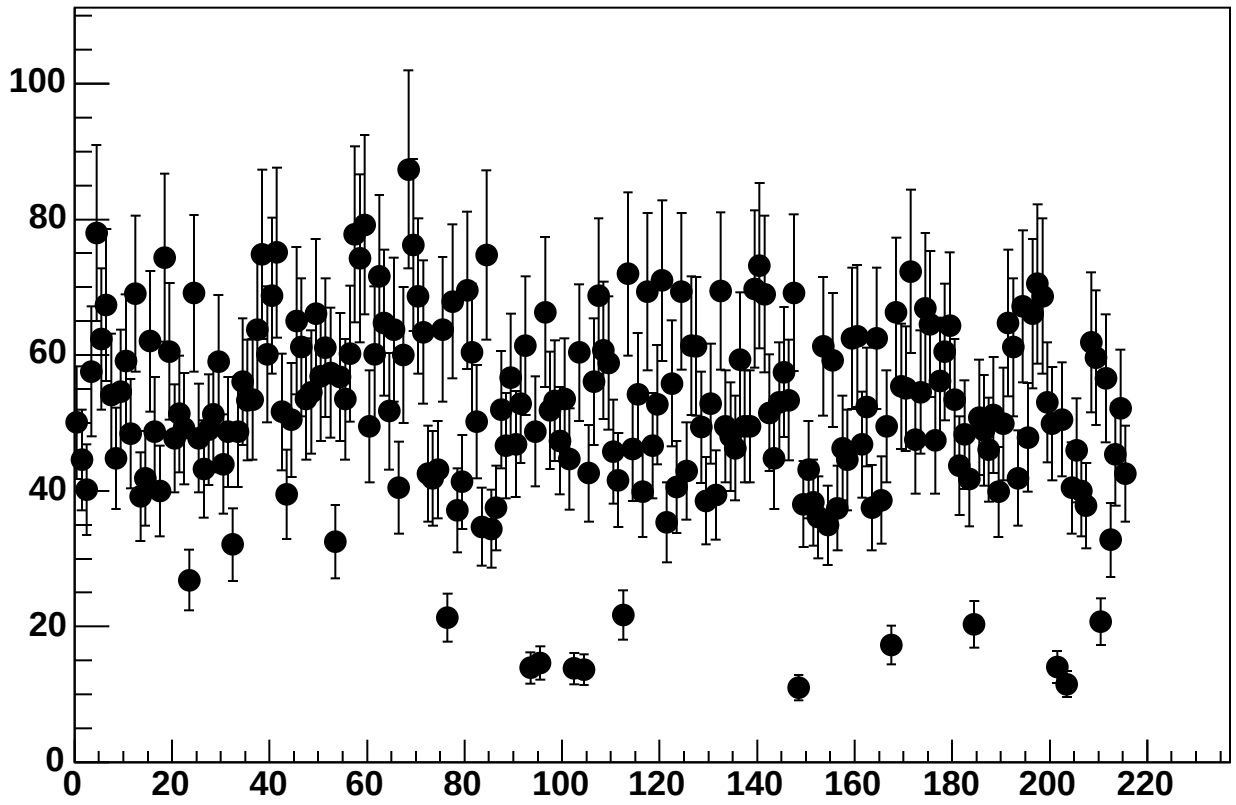
Enable 4, DAC=1600, Hold=20, ADC Noise vs 18*Chip+Chan



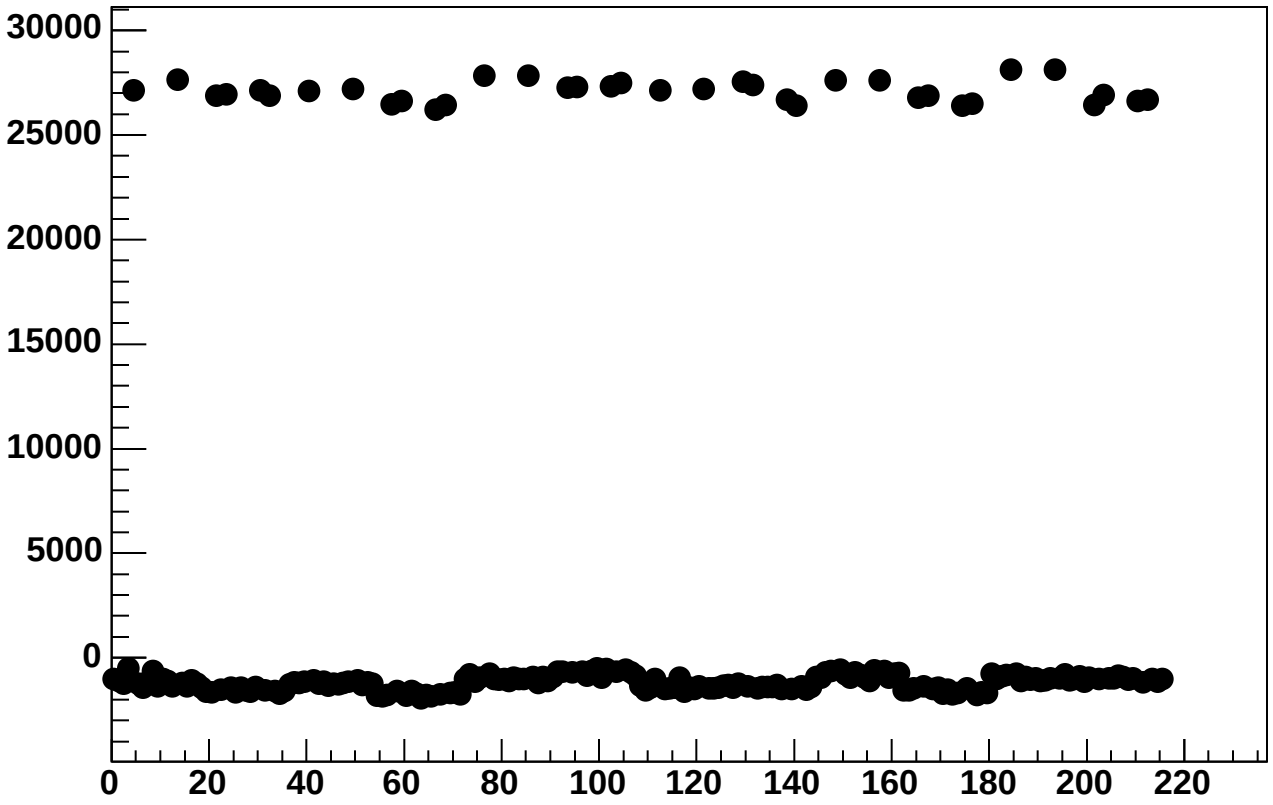
Enable 4, DAC=1600, Hold=25, ADC Mean vs 18*Chip+Chan



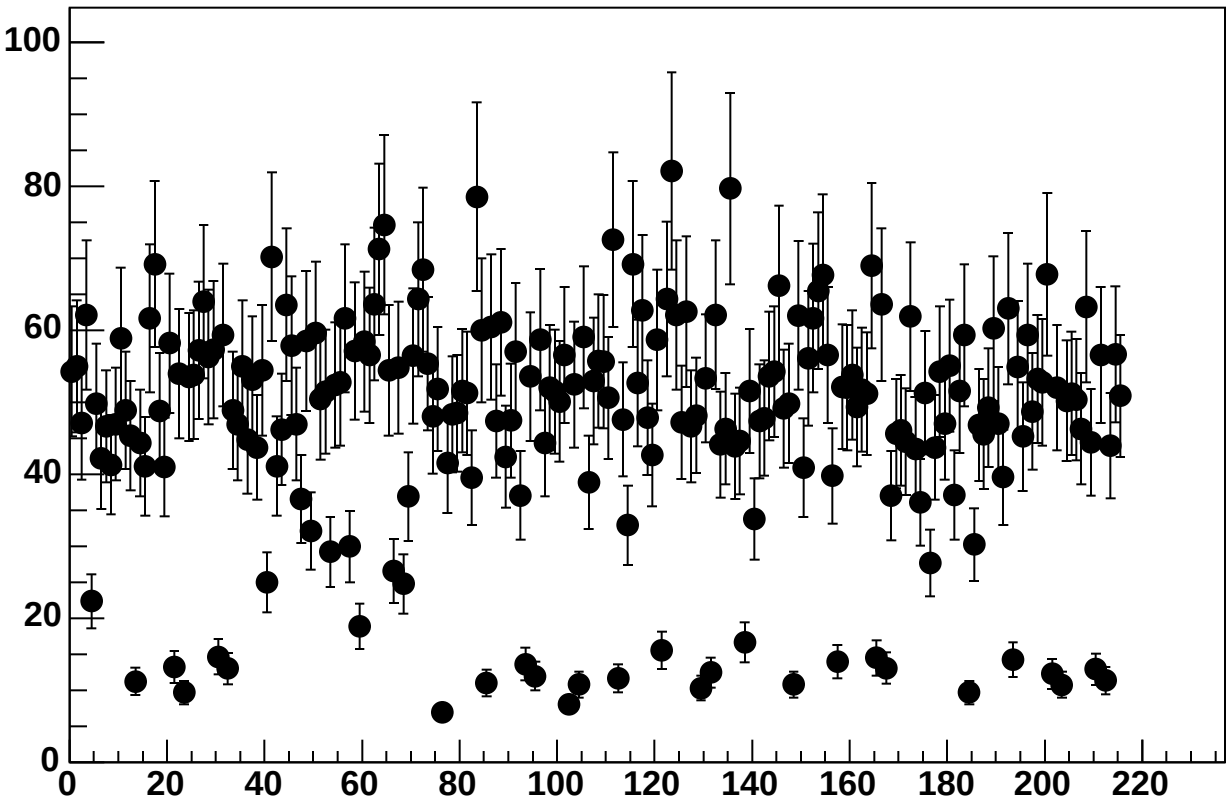
Enable 4, DAC=1600, Hold=25, ADC Noise vs 18*Chip+Chan



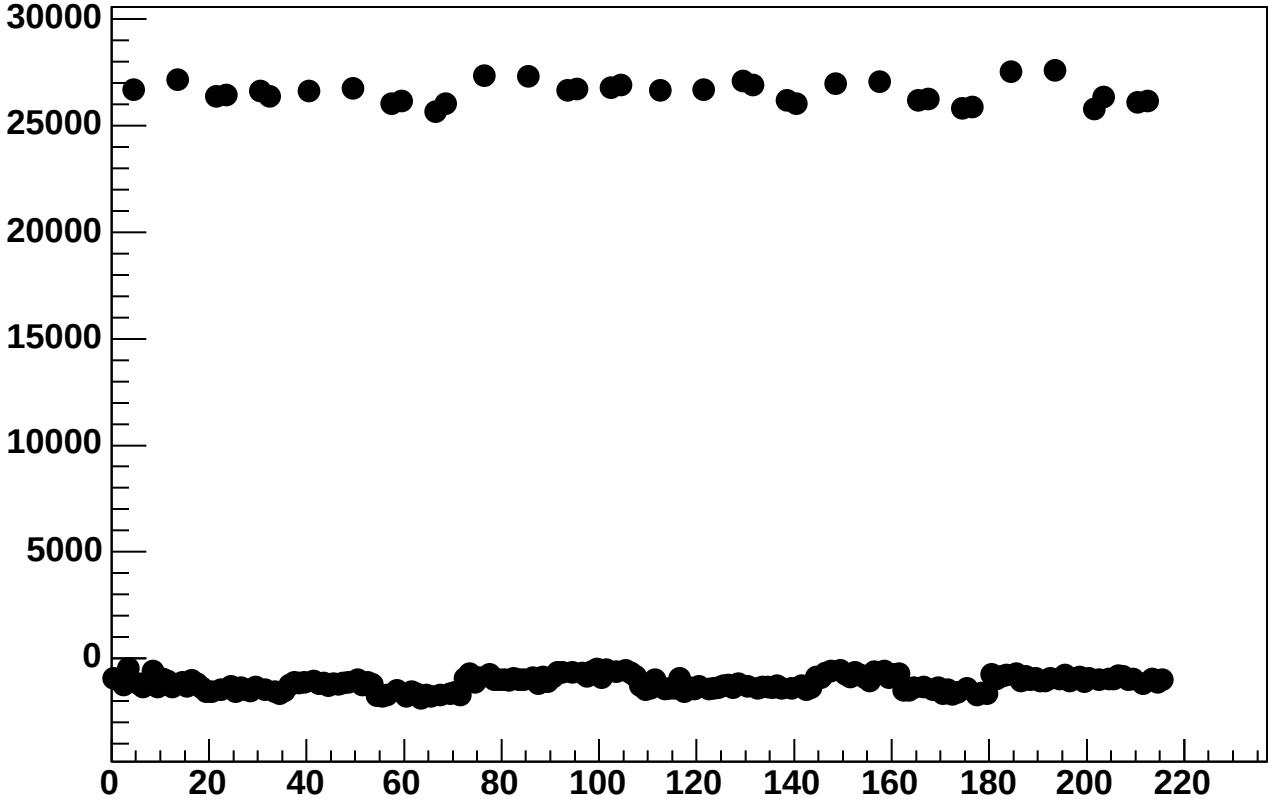
Enable 4, DAC=1600, Hold=30, ADC Mean vs 18*Chip+Chan



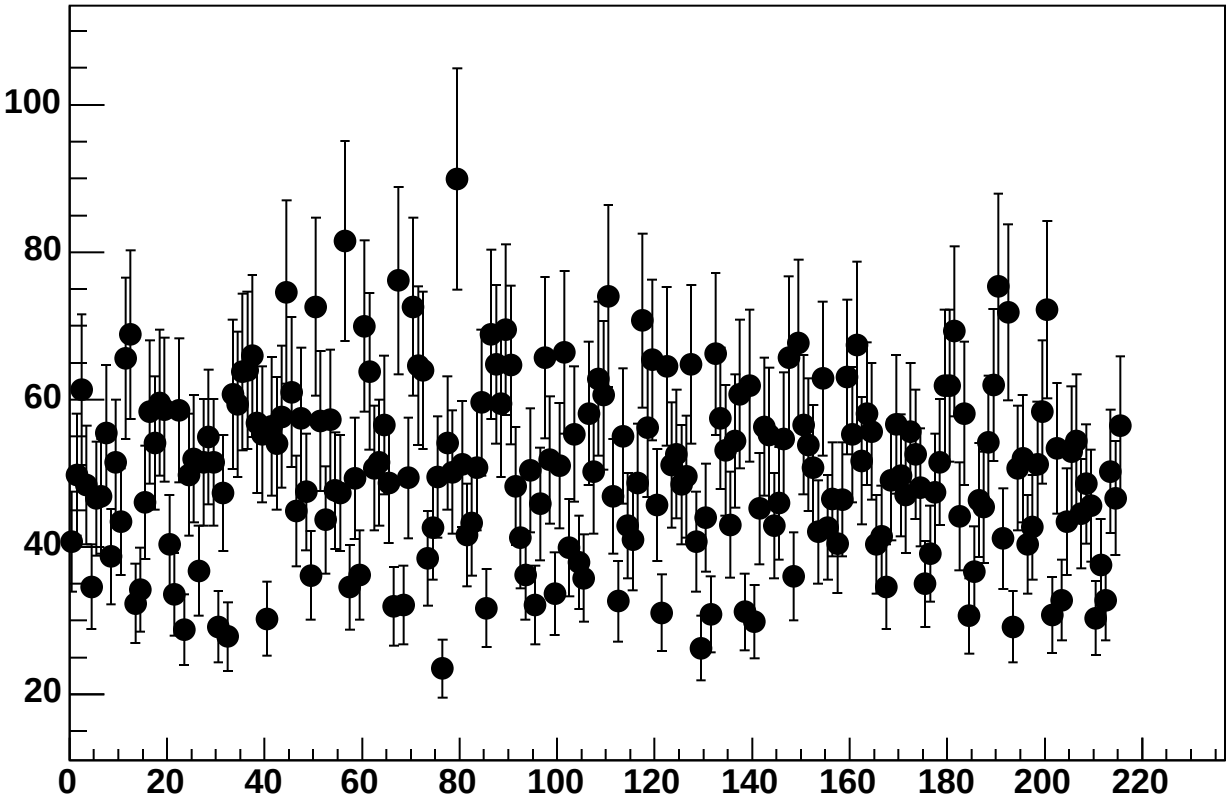
Enable 4, DAC=1600, Hold=30, ADC Noise vs 18*Chip+Chan



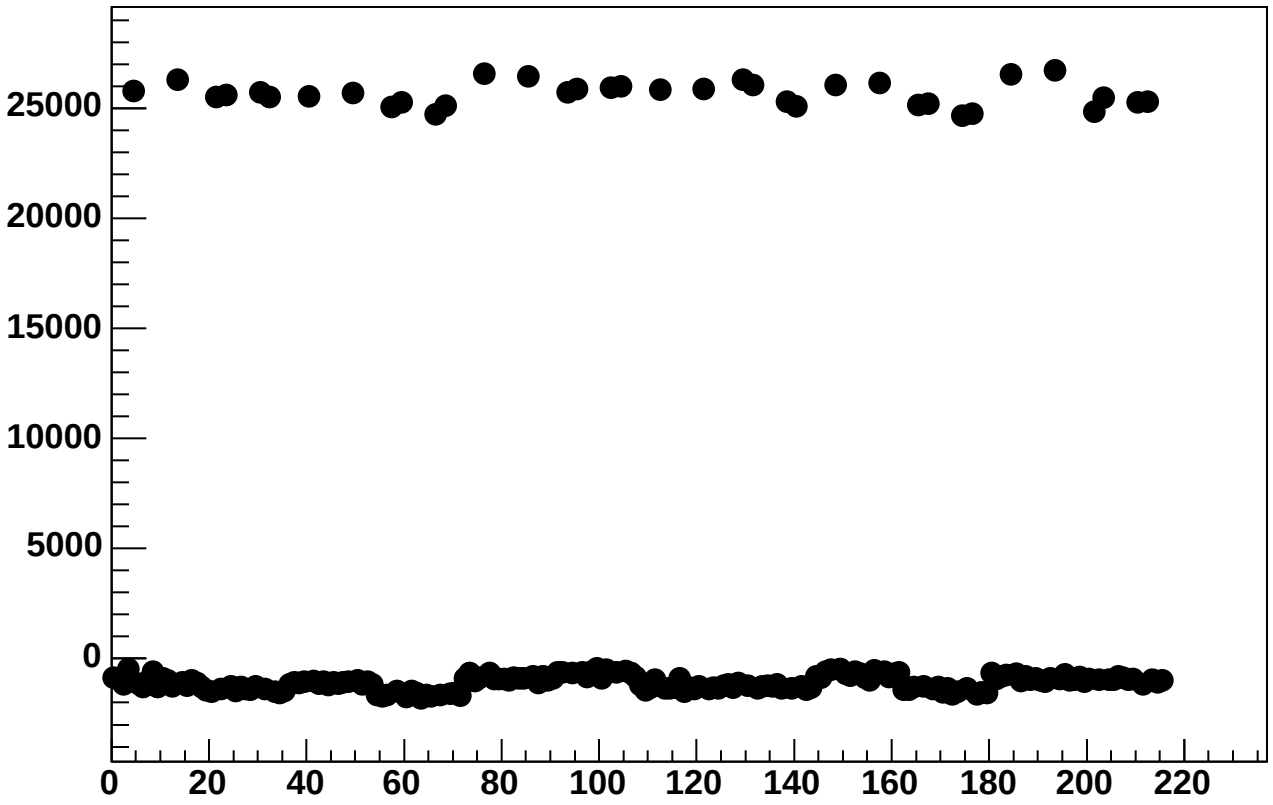
Enable 4, DAC=1600, Hold=35, ADC Mean vs 18*Chip+Chan



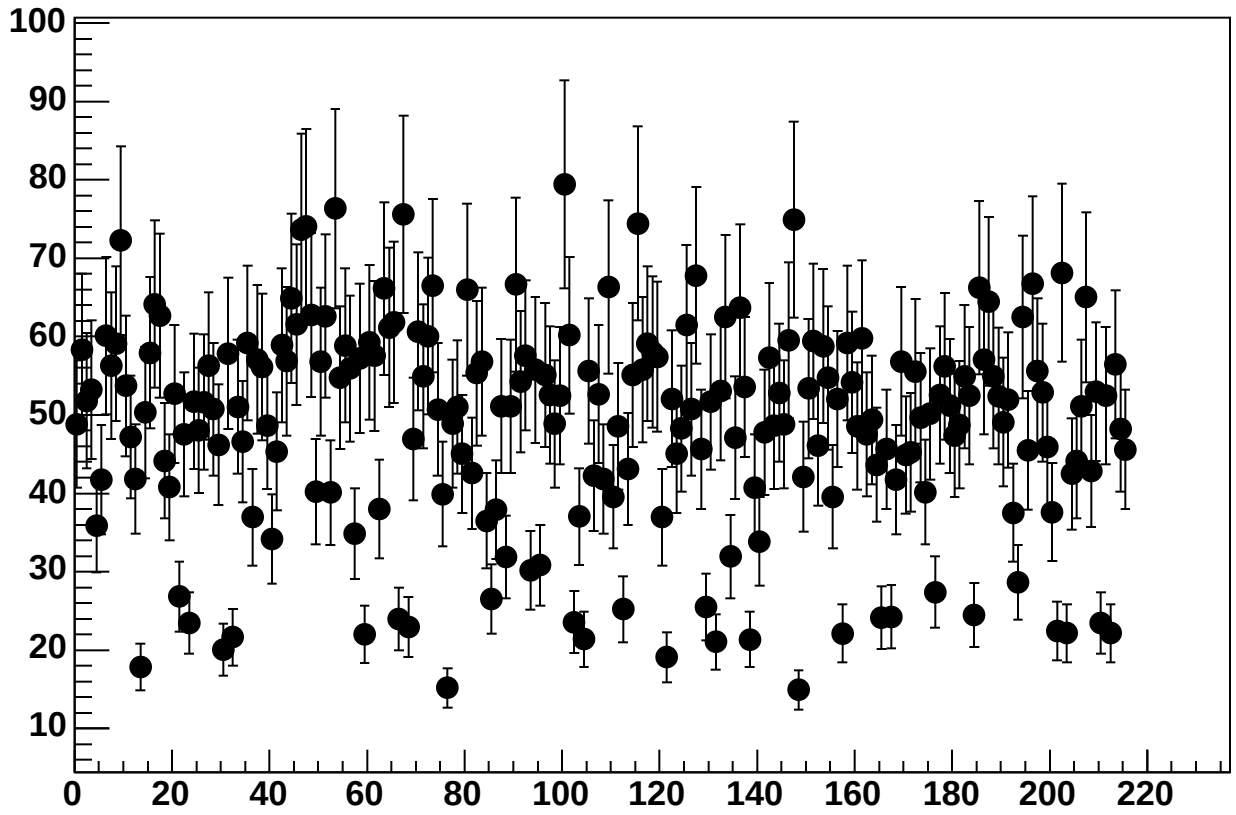
Enable 4, DAC=1600, Hold=35, ADC Noise vs 18*Chip+Chan



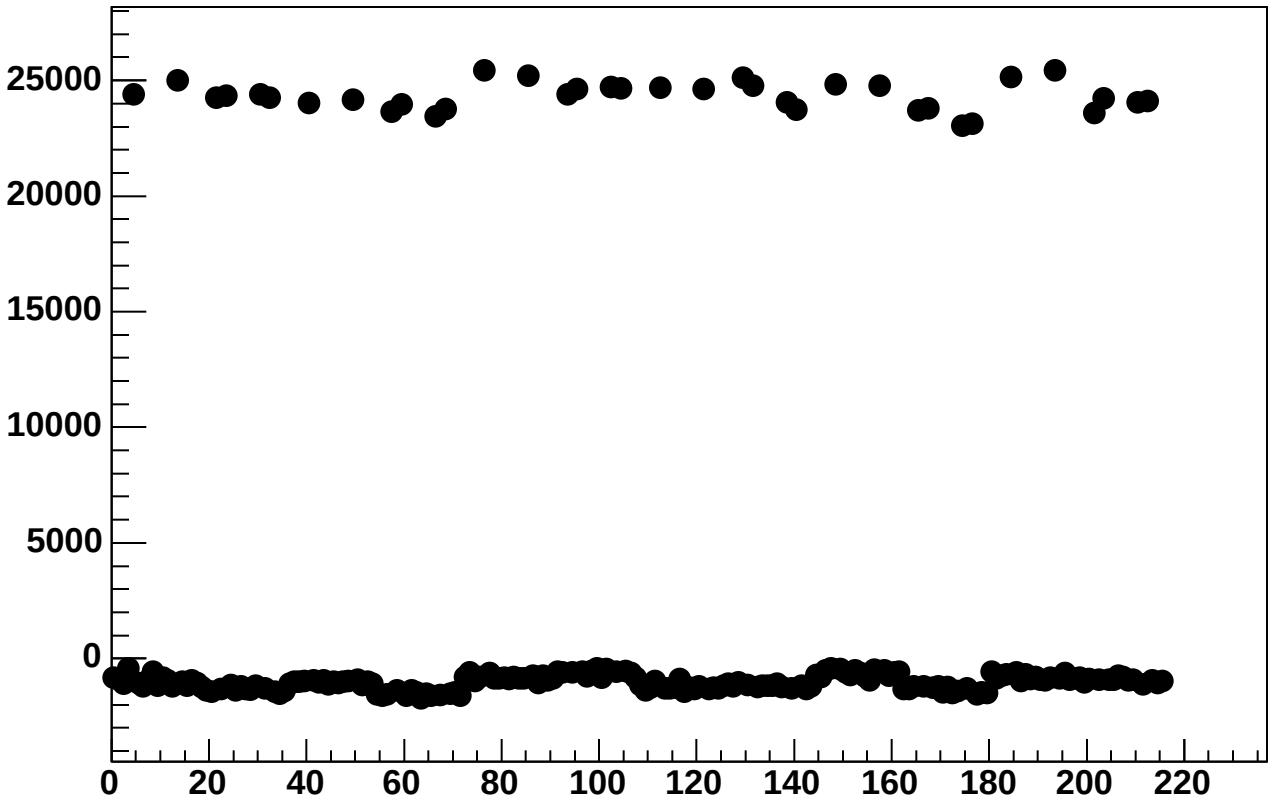
Enable 4, DAC=1600, Hold=40, ADC Mean vs 18*Chip+Chan



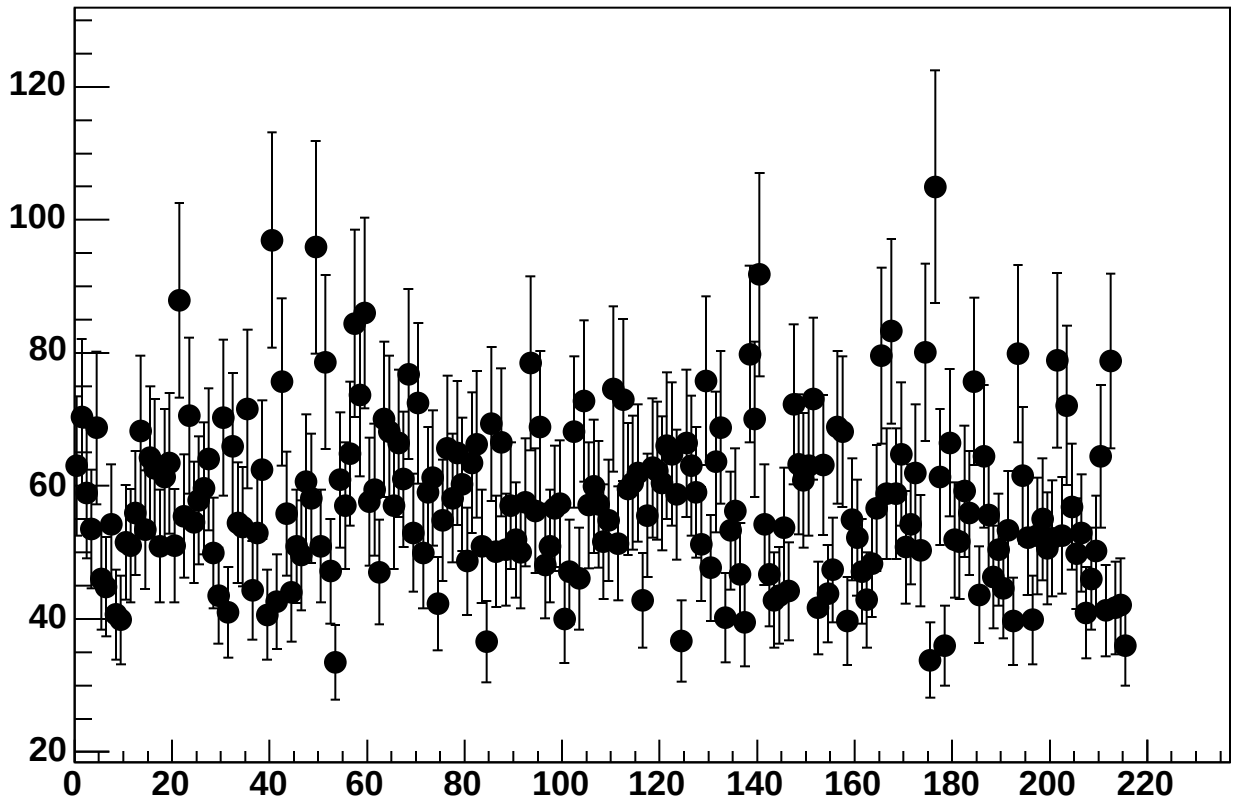
Enable 4, DAC=1600, Hold=40, ADC Noise vs 18*Chip+Chan



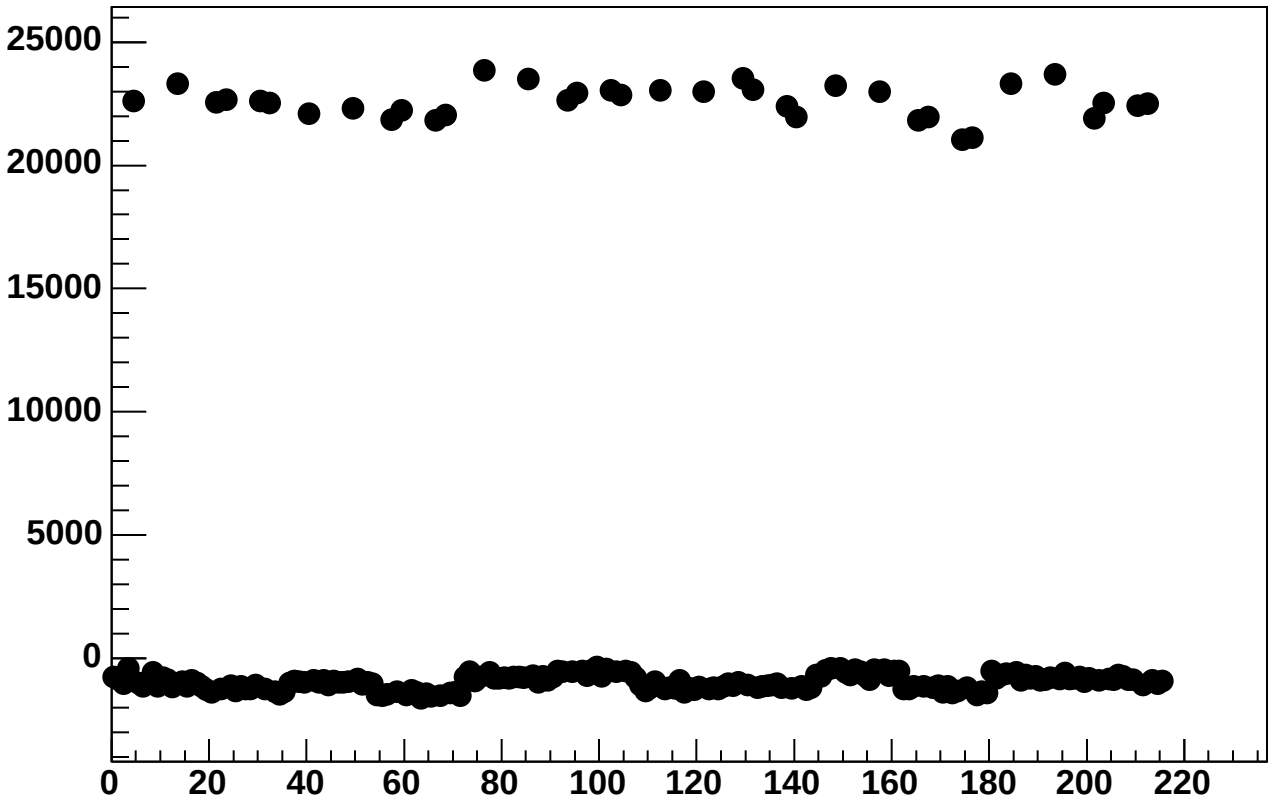
Enable 4, DAC=1600, Hold=45, ADC Mean vs 18*Chip+Chan



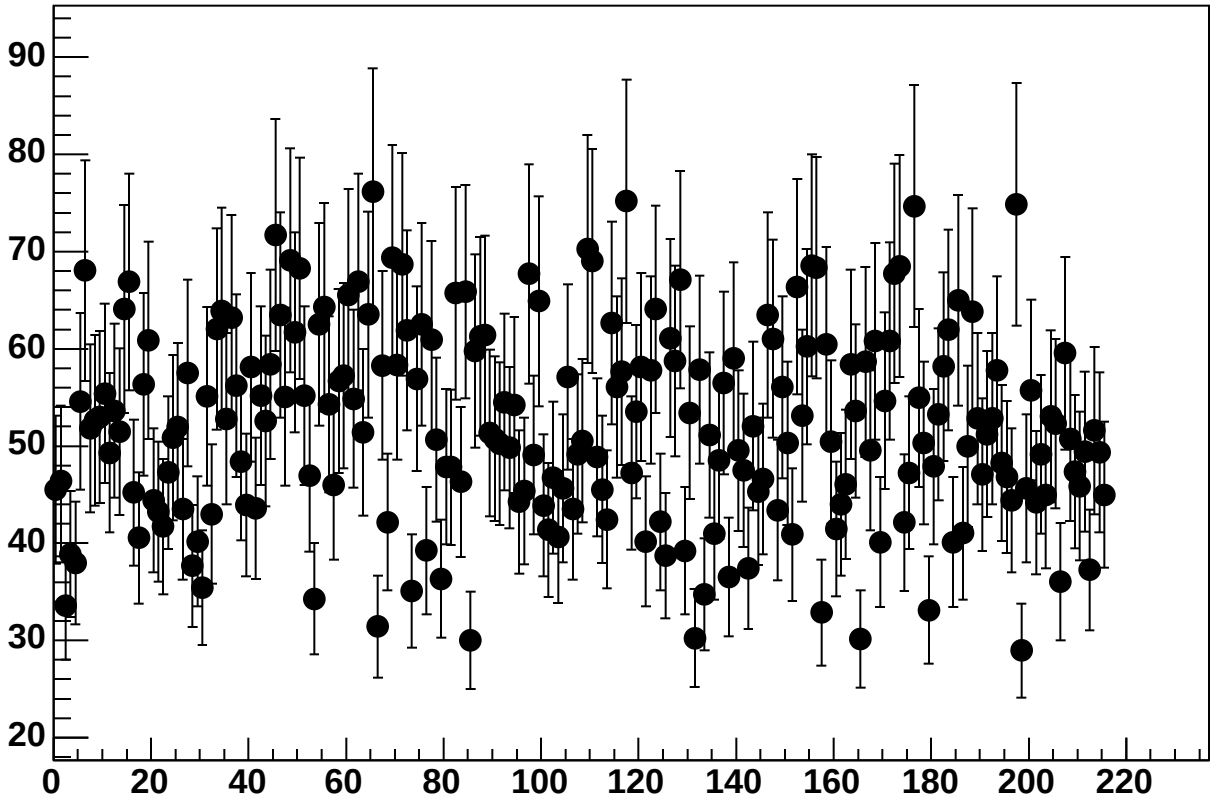
Enable 4, DAC=1600, Hold=45, ADC Noise vs 18*Chip+Chan



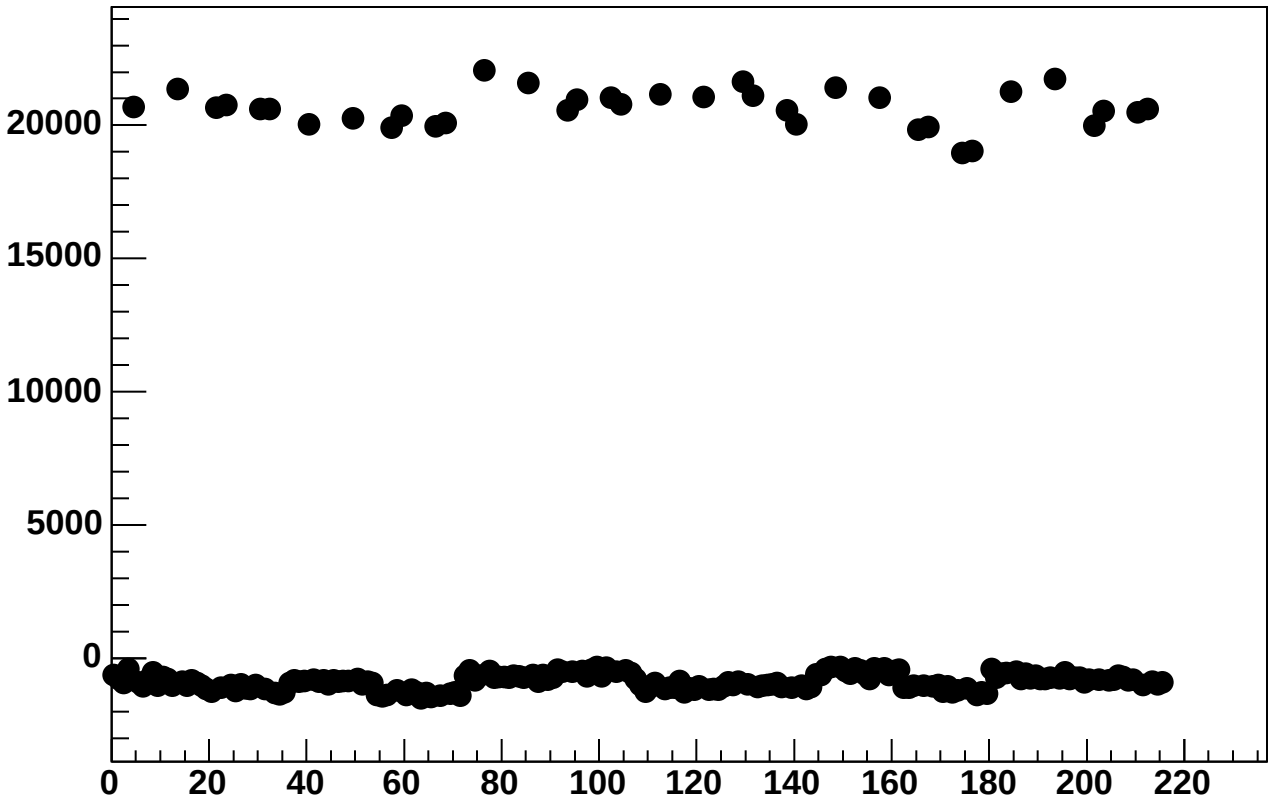
Enable 4, DAC=1600, Hold=50, ADC Mean vs 18*Chip+Chan



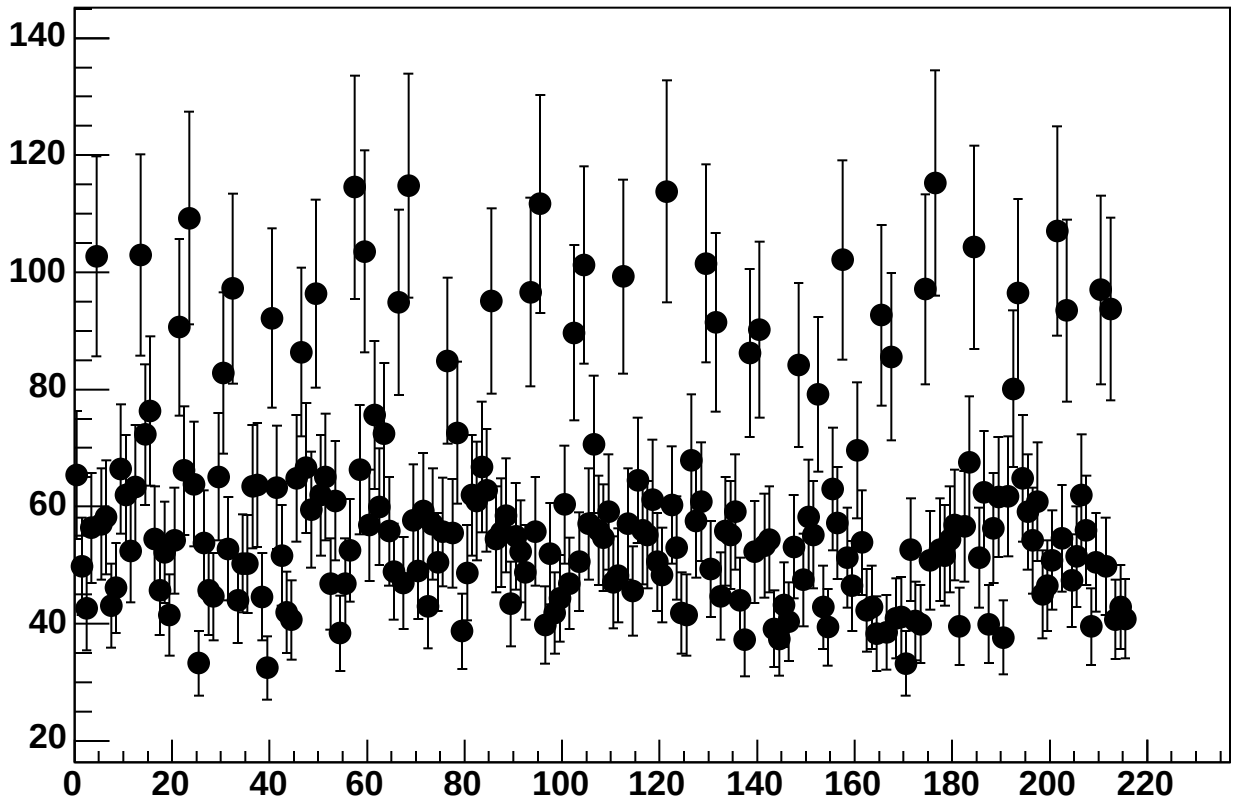
Enable 4, DAC=1600, Hold=50, ADC Noise vs 18*Chip+Chan



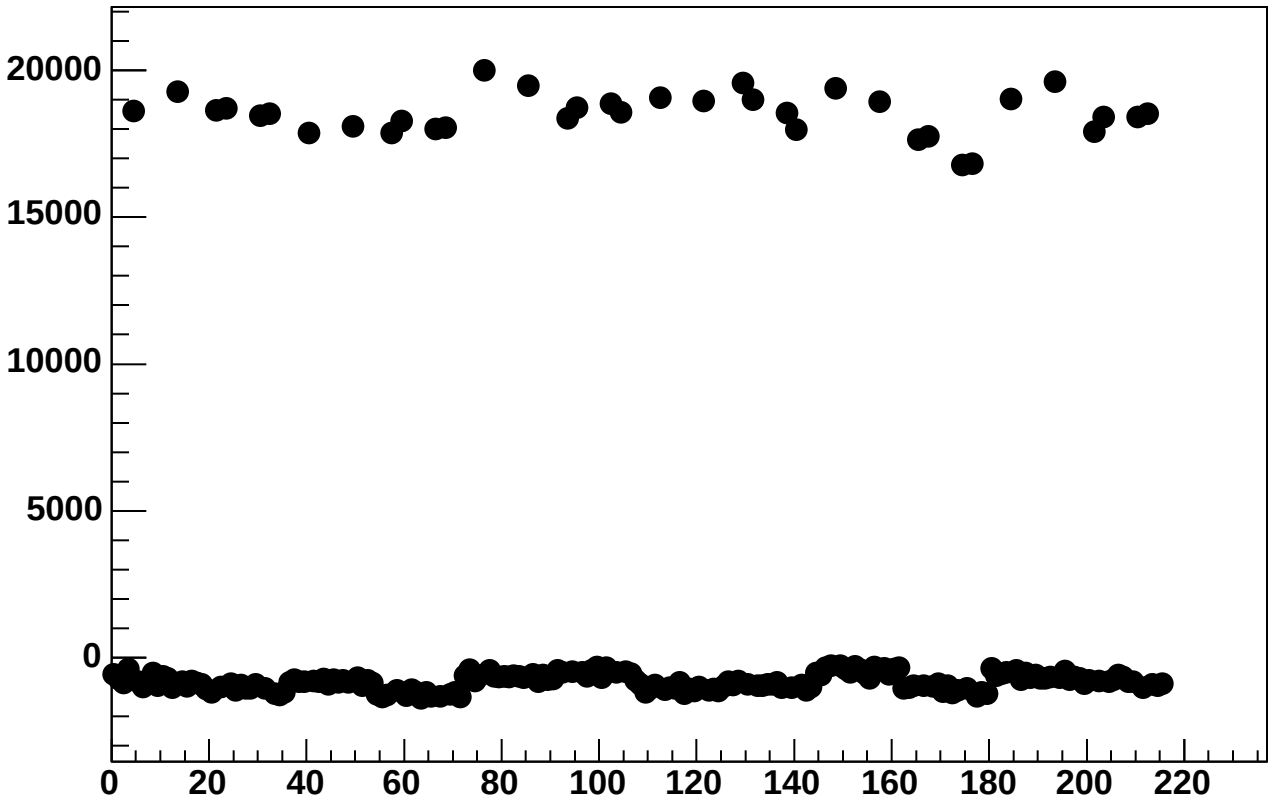
Enable 4, DAC=1600, Hold=55, ADC Mean vs 18*Chip+Chan



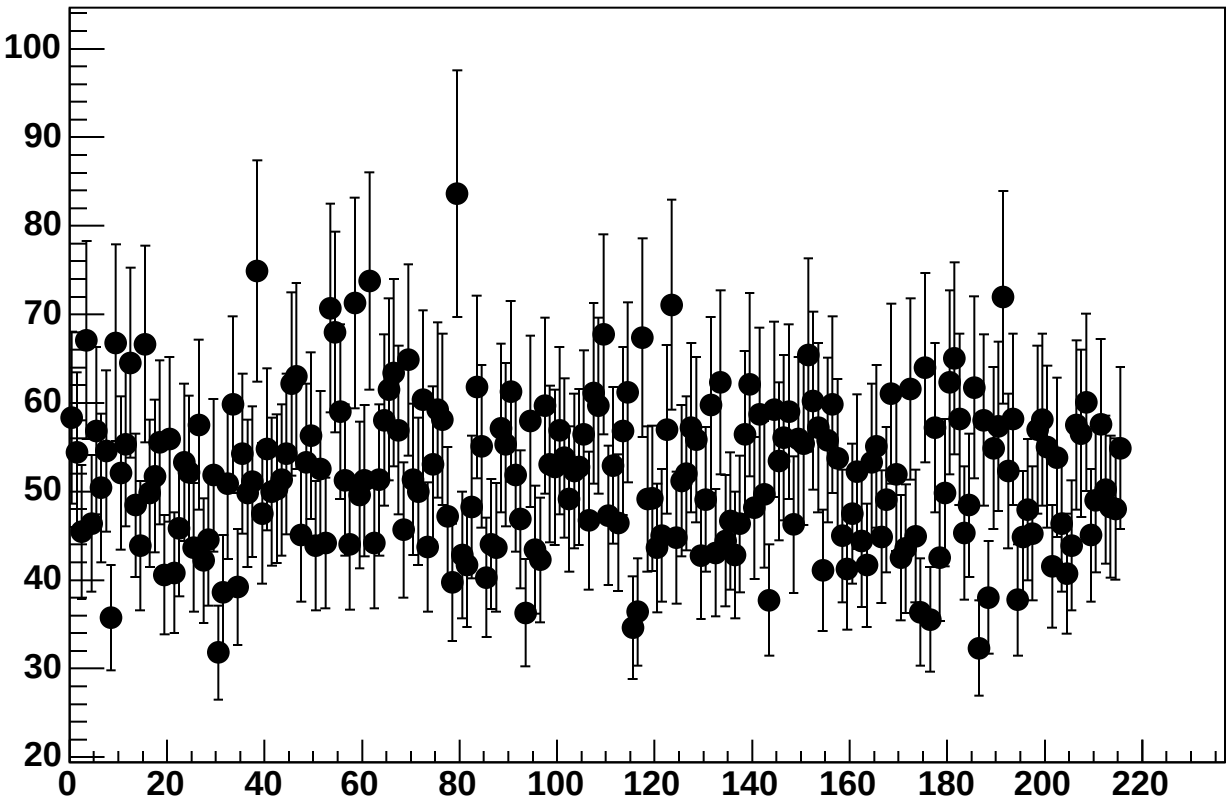
Enable 4, DAC=1600, Hold=55, ADC Noise vs 18*Chip+Chan



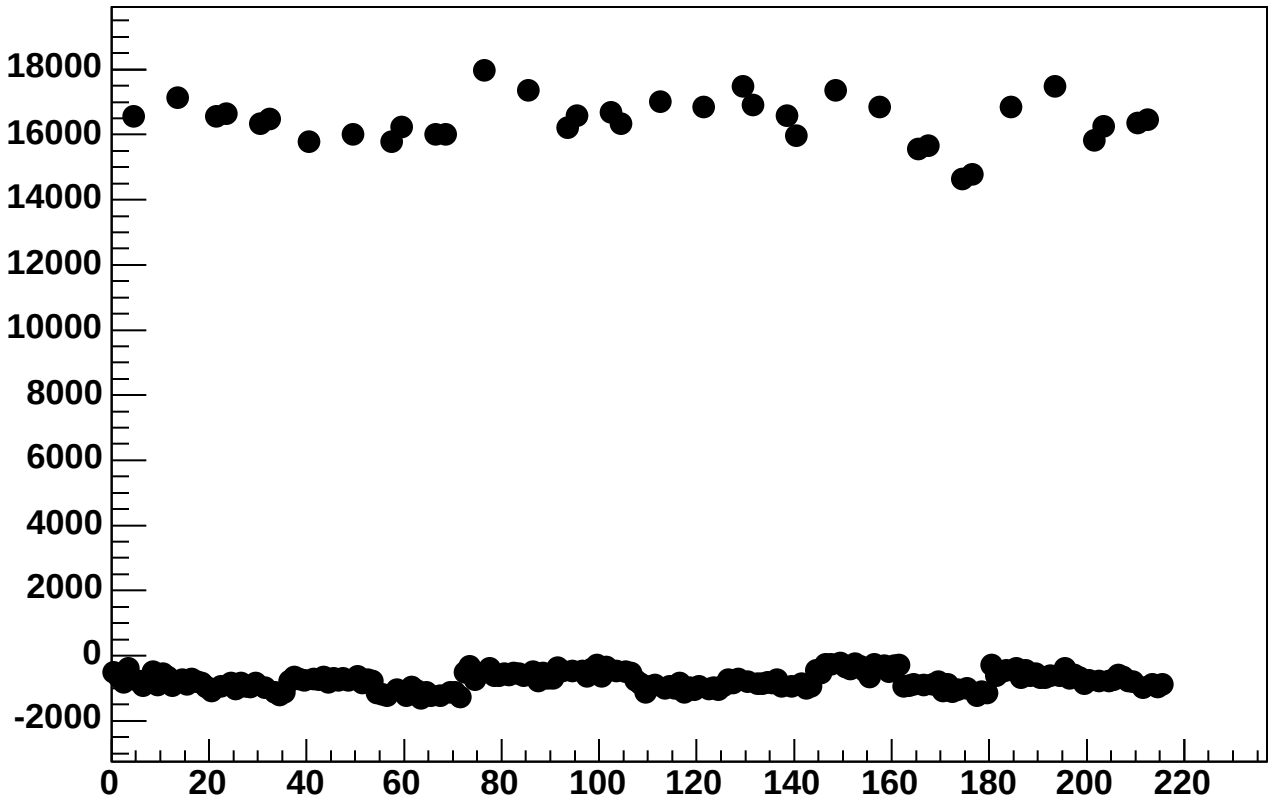
Enable 4, DAC=1600, Hold=60, ADC Mean vs 18*Chip+Chan



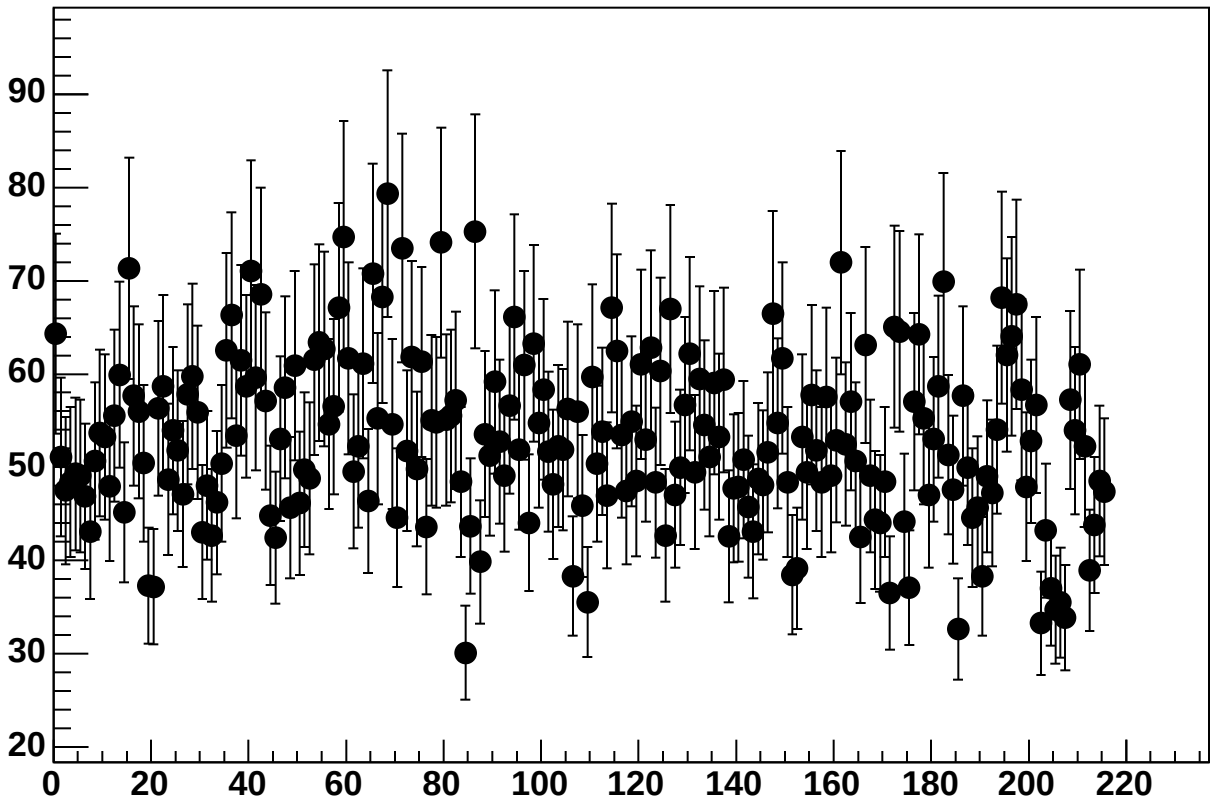
Enable 4, DAC=1600, Hold=60, ADC Noise vs 18*Chip+Chan



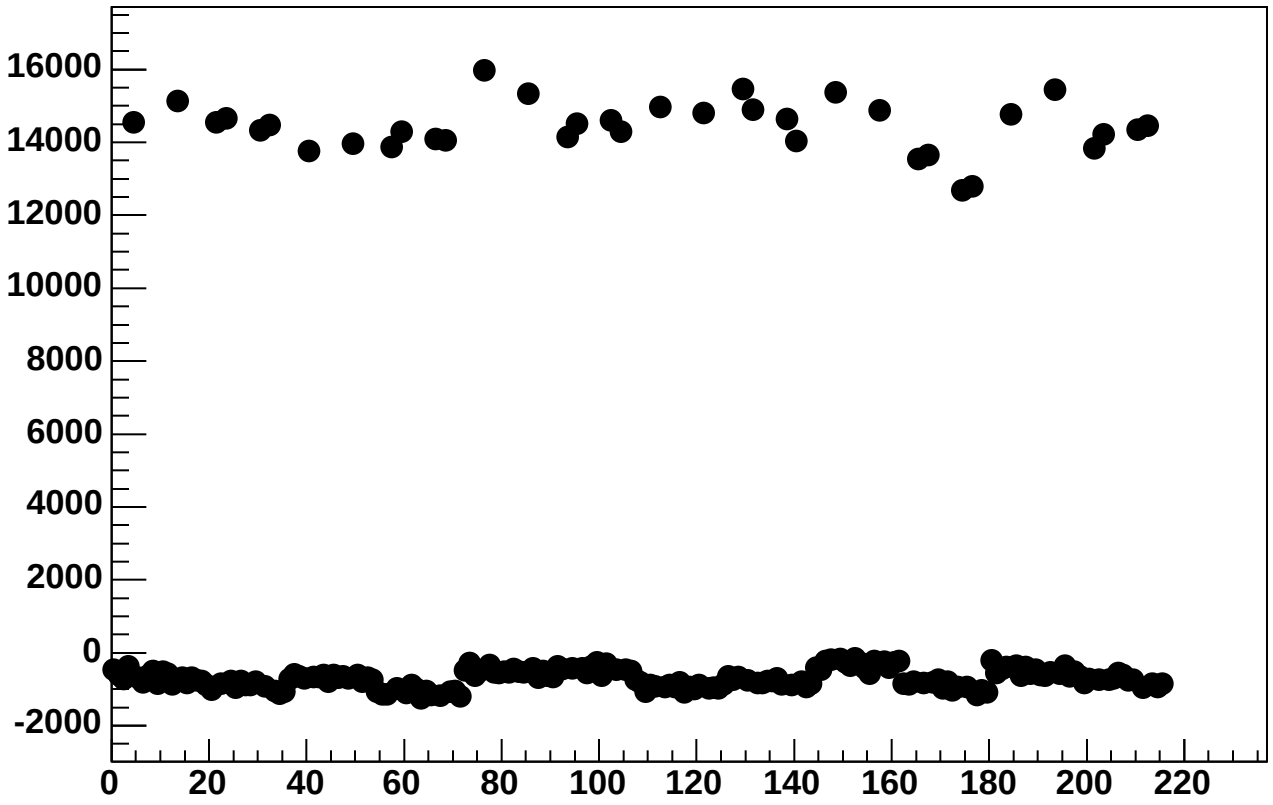
Enable 4, DAC=1600, Hold=65, ADC Mean vs 18*Chip+Chan



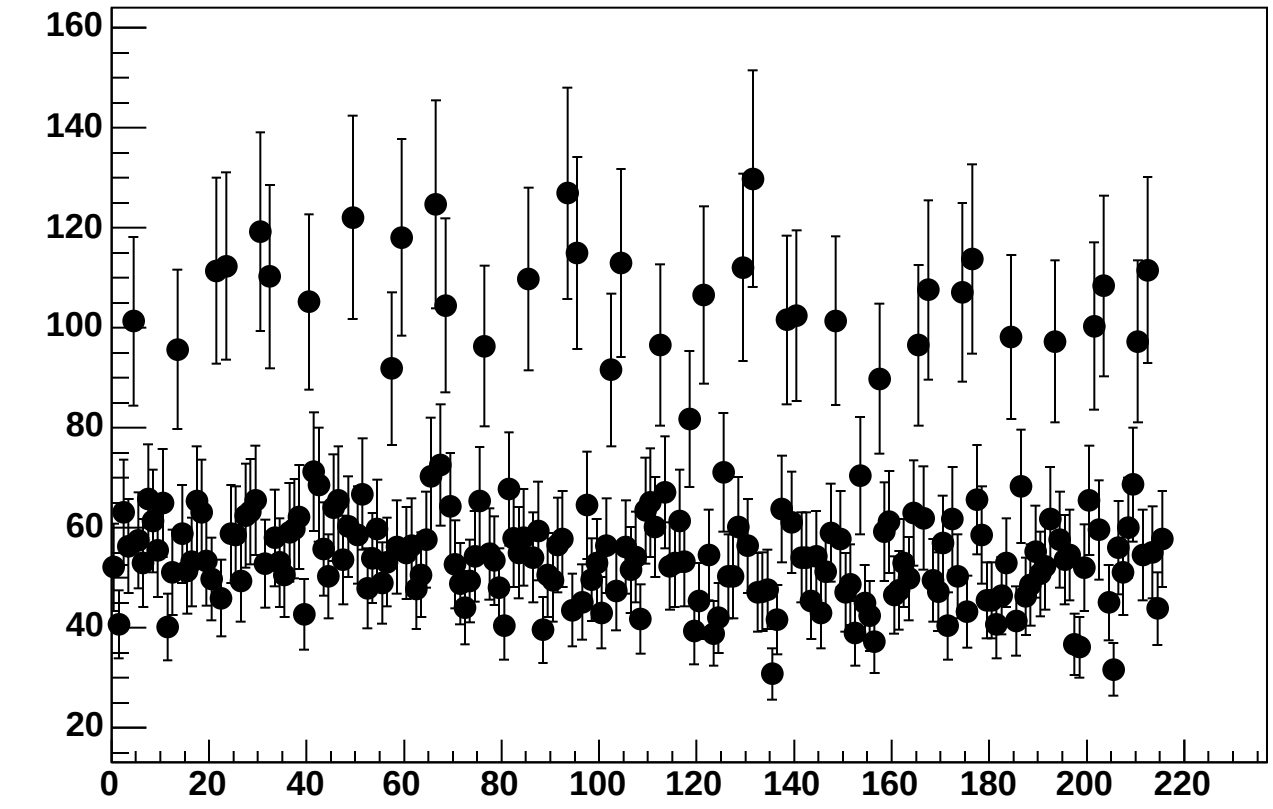
Enable 4, DAC=1600, Hold=65, ADC Noise vs 18*Chip+Chan



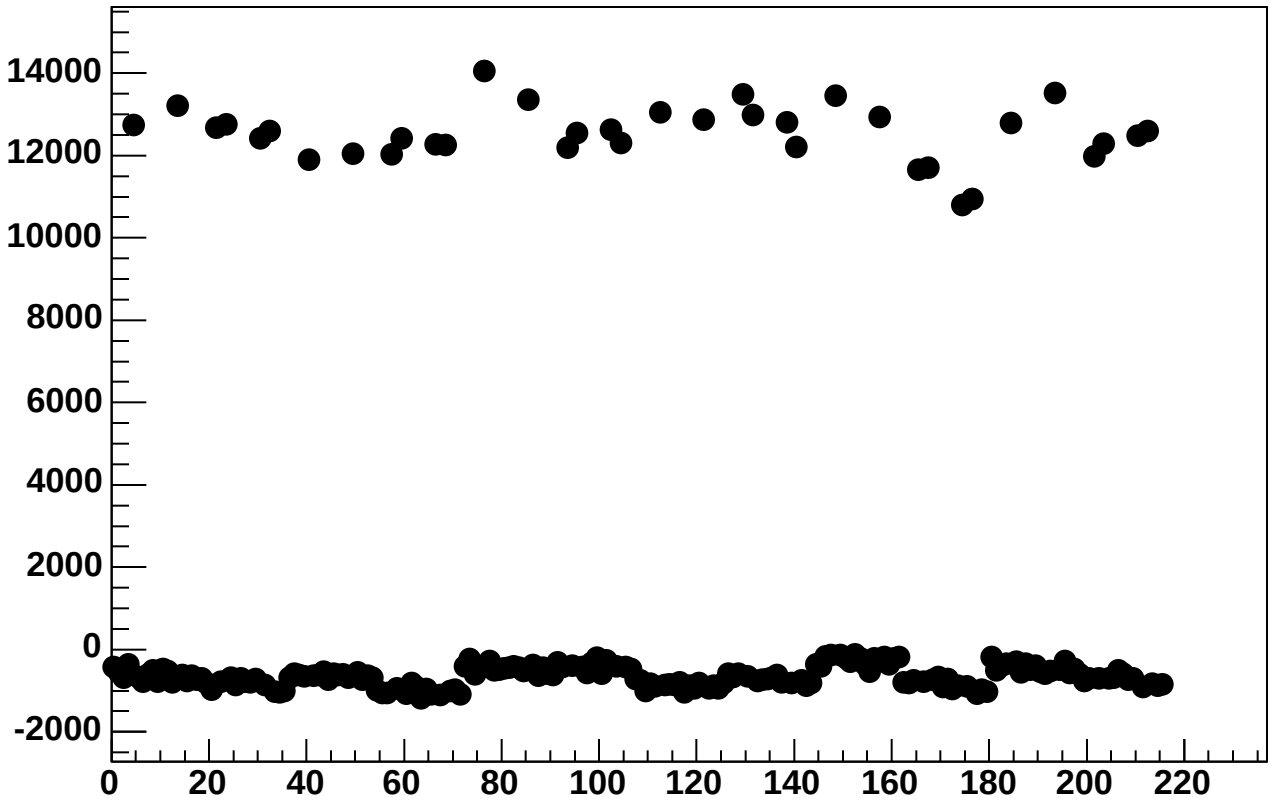
Enable 4, DAC=1600, Hold=70, ADC Mean vs 18*Chip+Chan



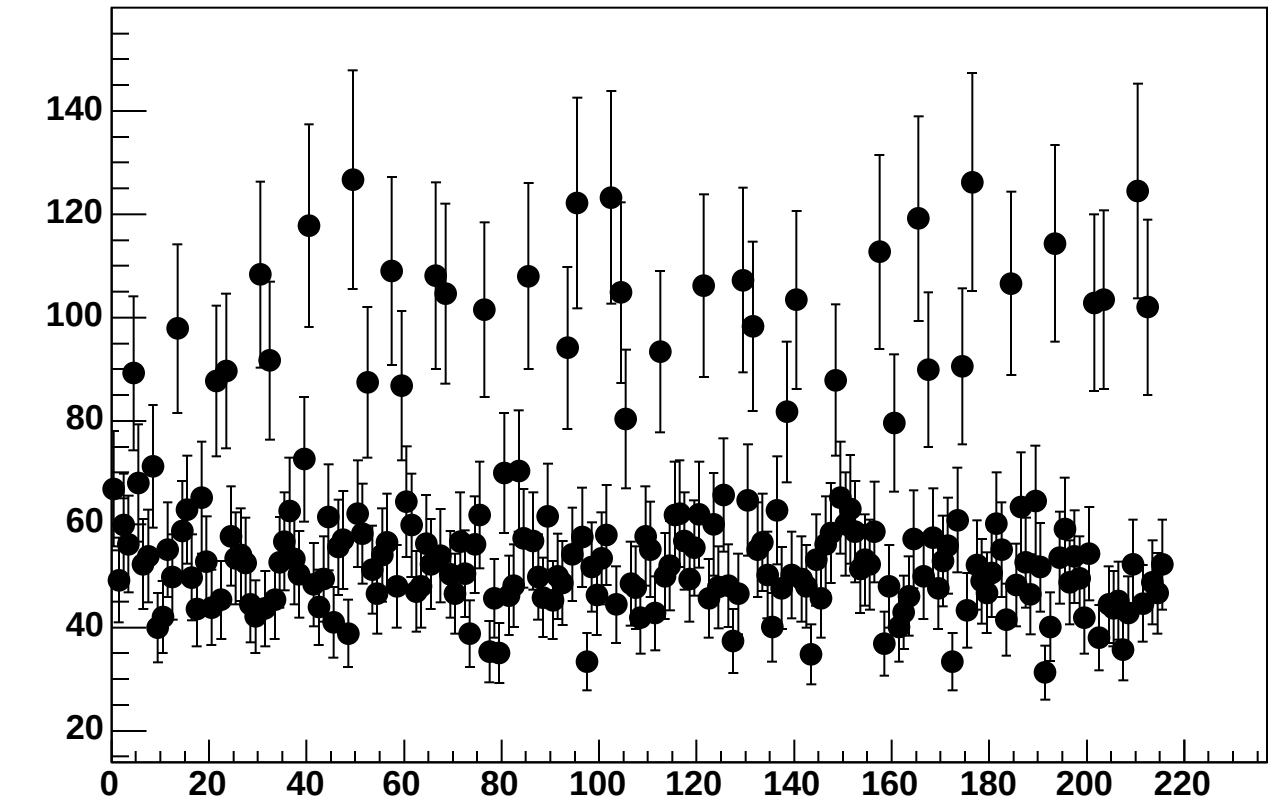
Enable 4, DAC=1600, Hold=70, ADC Noise vs 18*Chip+Chan



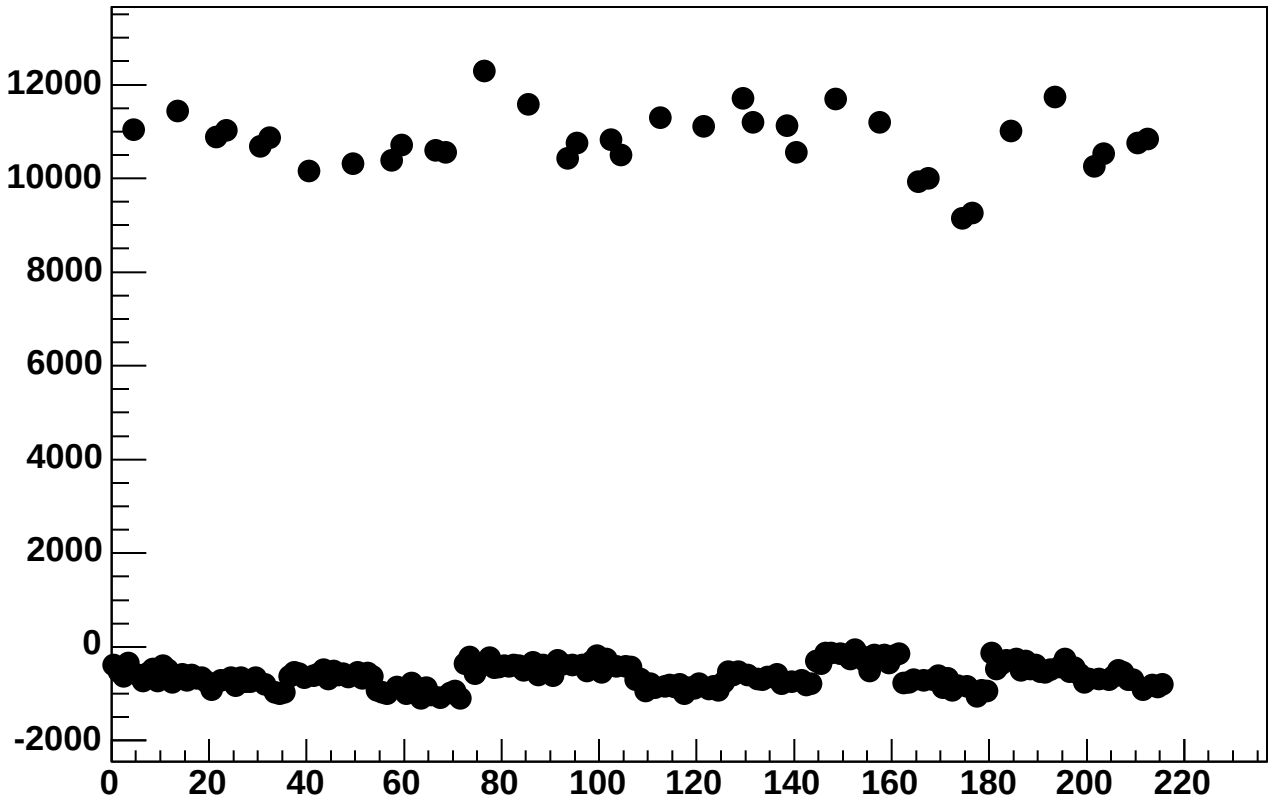
Enable 4, DAC=1600, Hold=75, ADC Mean vs 18*Chip+Chan



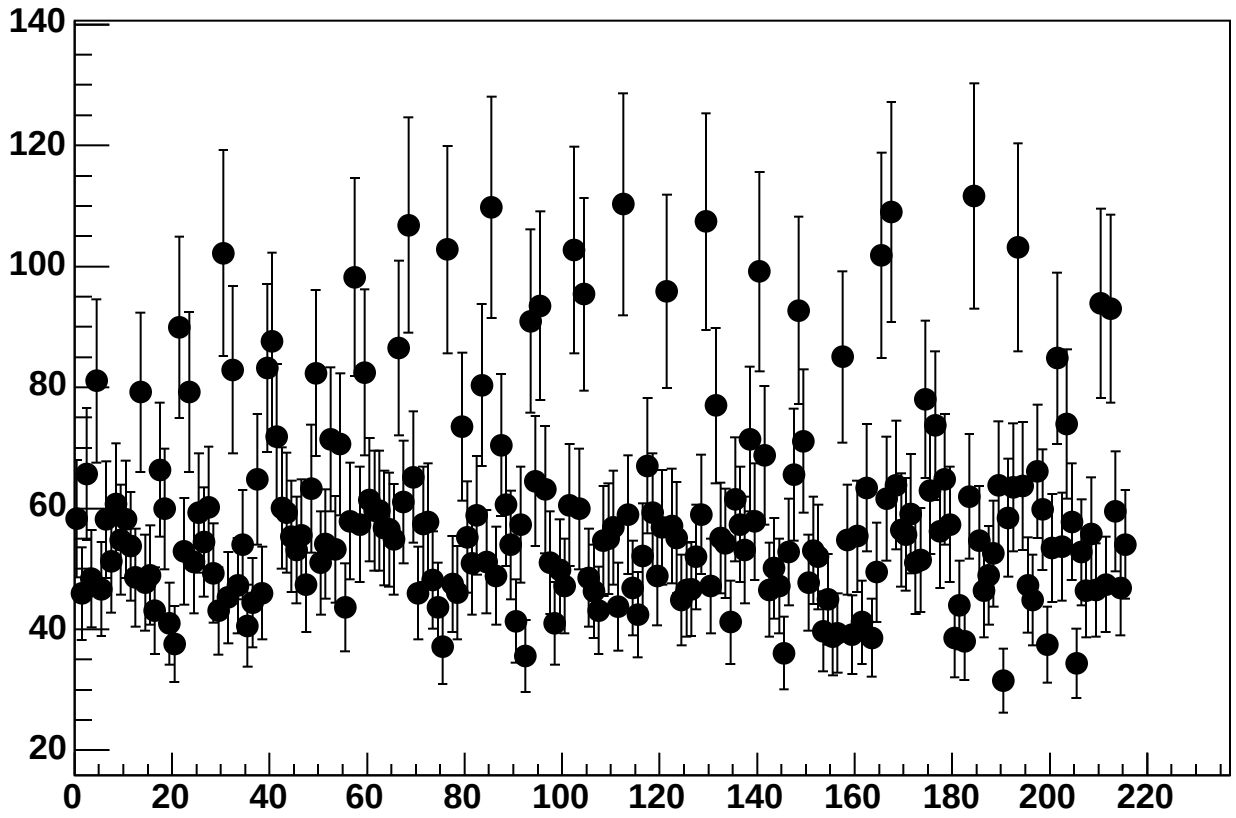
Enable 4, DAC=1600, Hold=75, ADC Noise vs 18*Chip+Chan



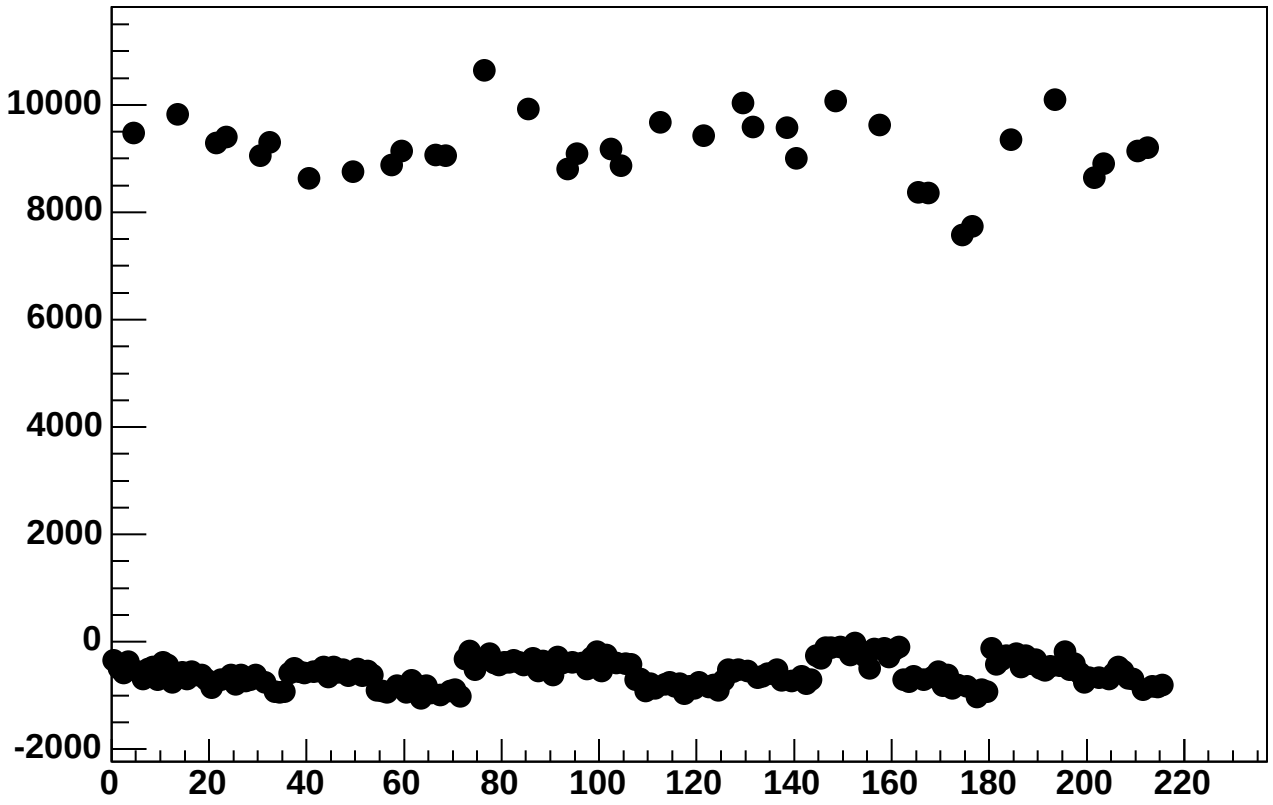
Enable 4, DAC=1600, Hold=80, ADC Mean vs 18*Chip+Chan



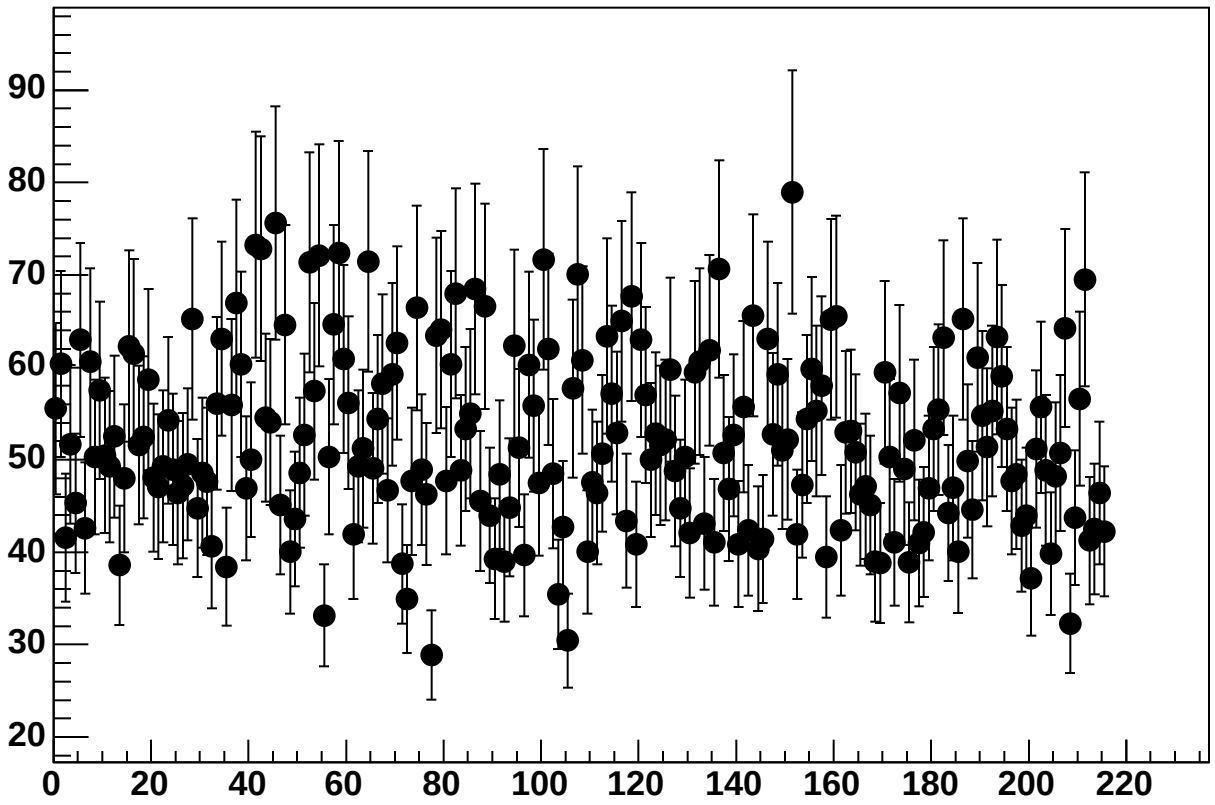
Enable 4, DAC=1600, Hold=80, ADC Noise vs 18*Chip+Chan



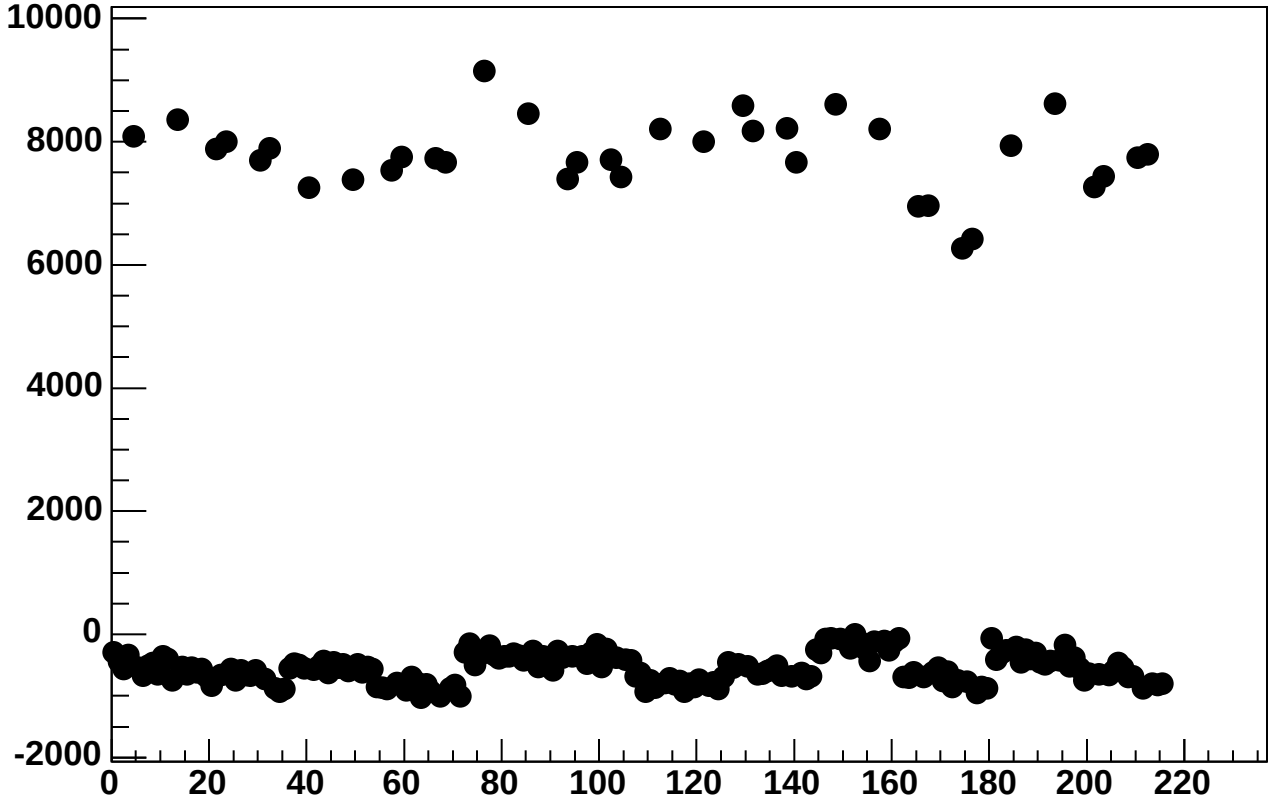
Enable 4, DAC=1600, Hold=85, ADC Mean vs 18*Chip+Chan



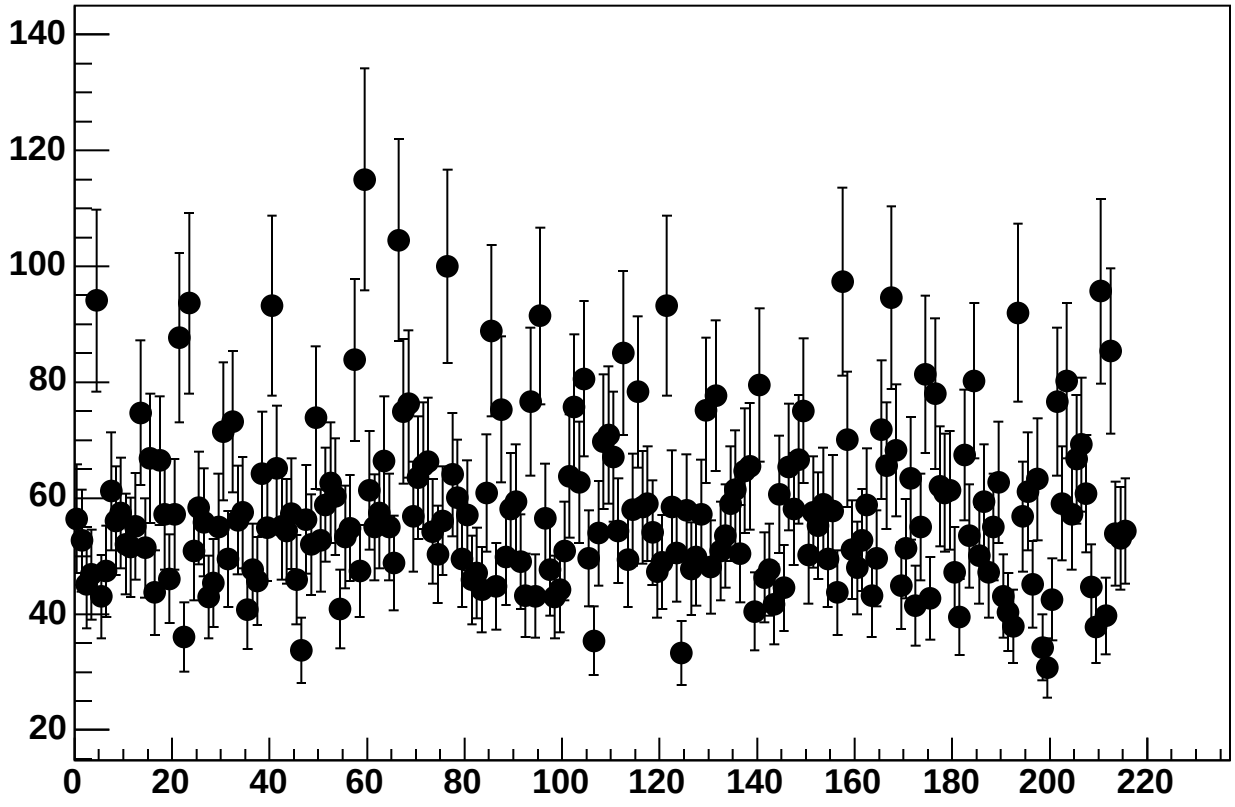
Enable 4, DAC=1600, Hold=85, ADC Noise vs 18*Chip+Chan



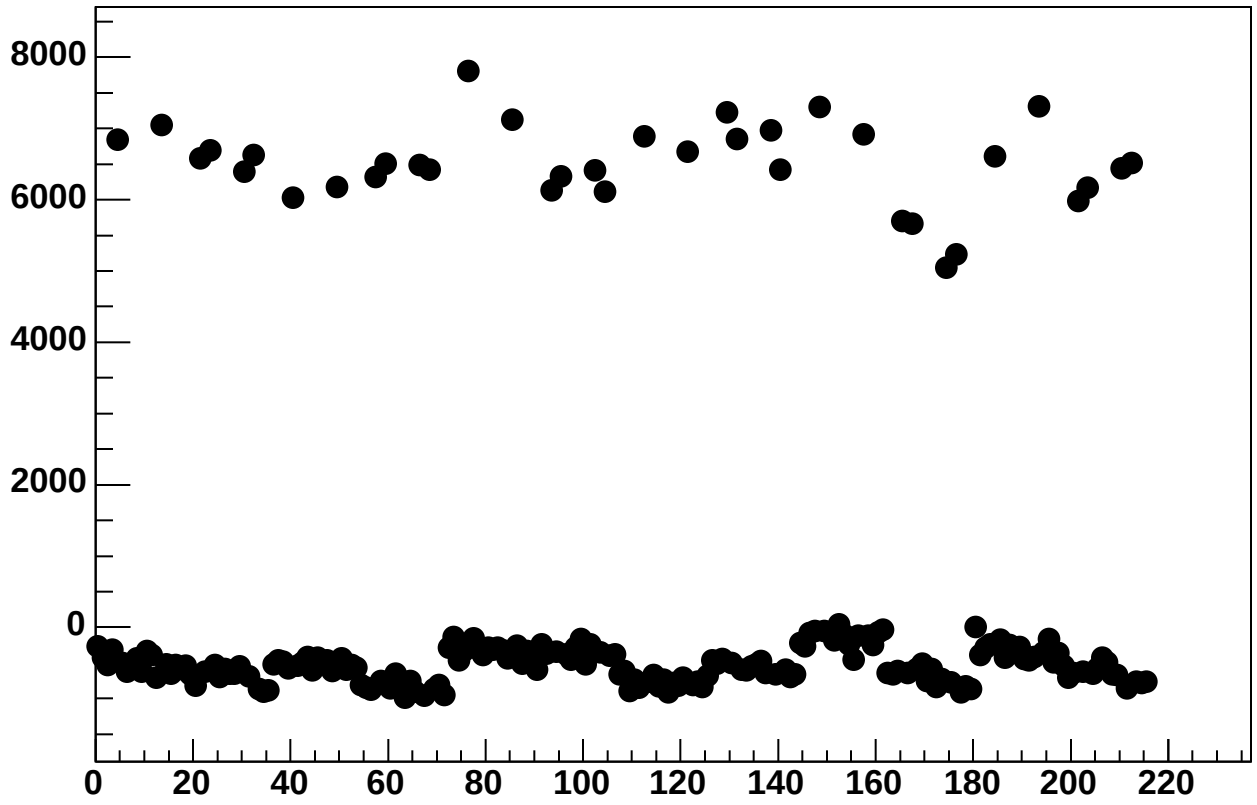
Enable 4, DAC=1600, Hold=90, ADC Mean vs 18*Chip+Chan



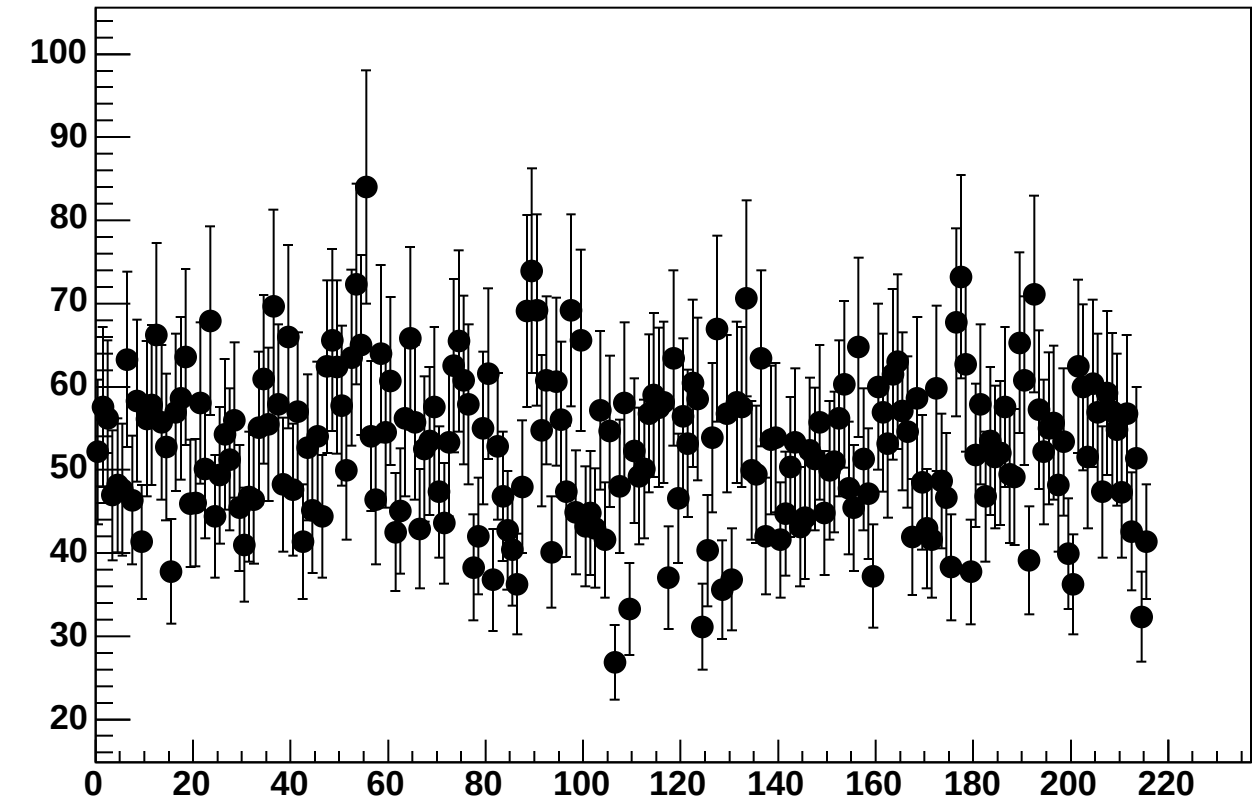
Enable 4, DAC=1600, Hold=90, ADC Noise vs 18*Chip+Chan



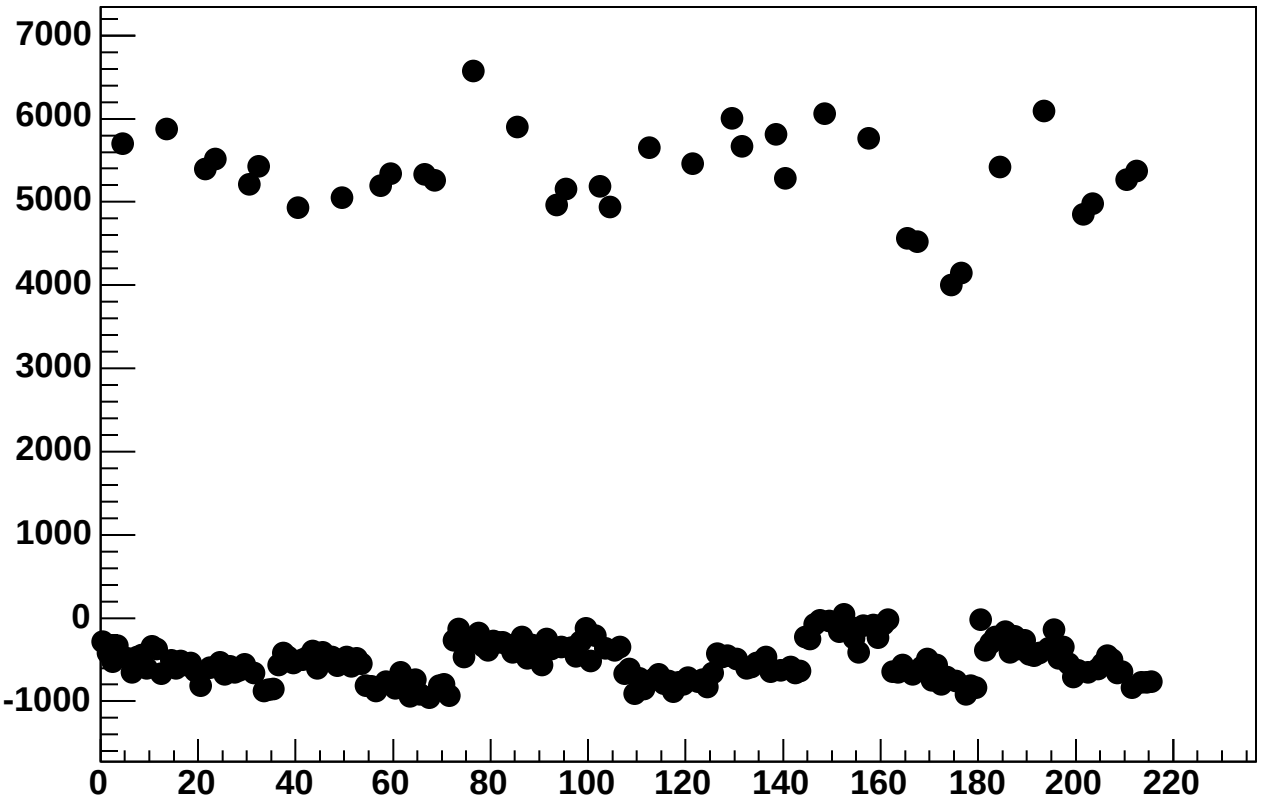
Enable 4, DAC=1600, Hold=95, ADC Mean vs 18*Chip+Chan



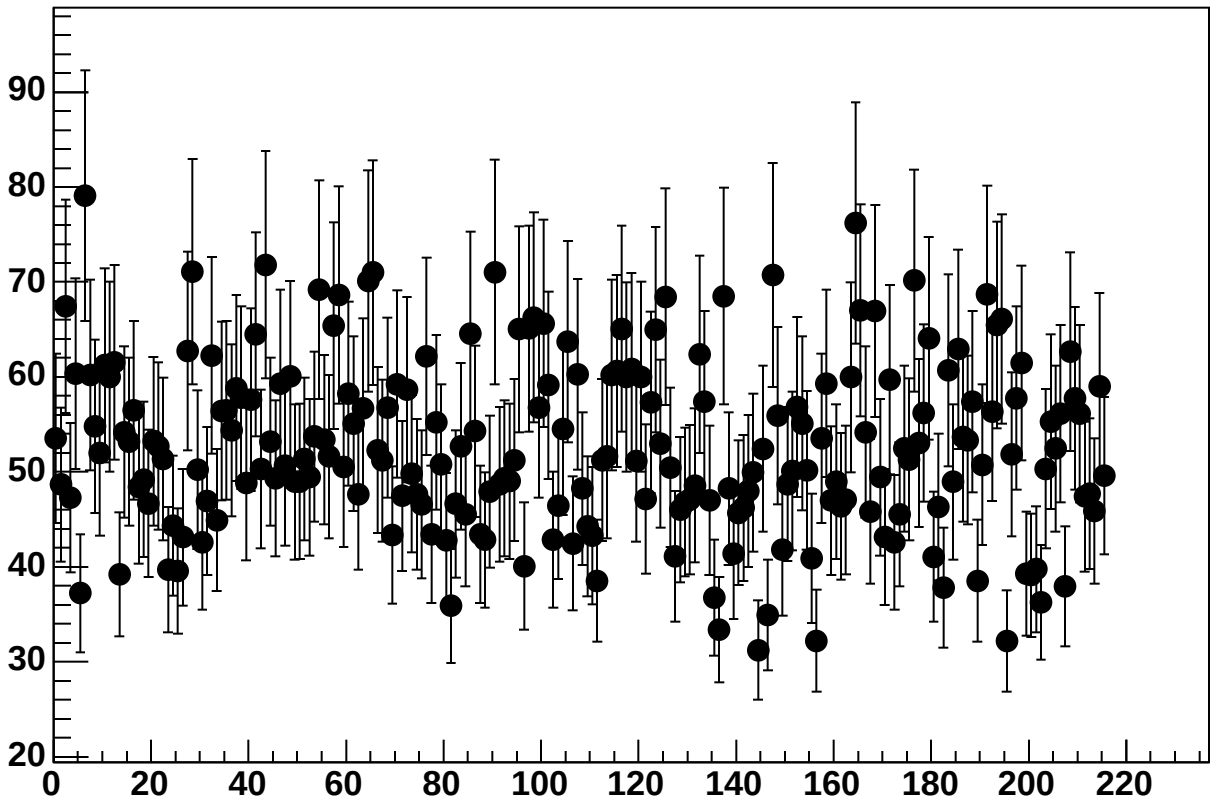
Enable 4, DAC=1600, Hold=95, ADC Noise vs 18*Chip+Chan



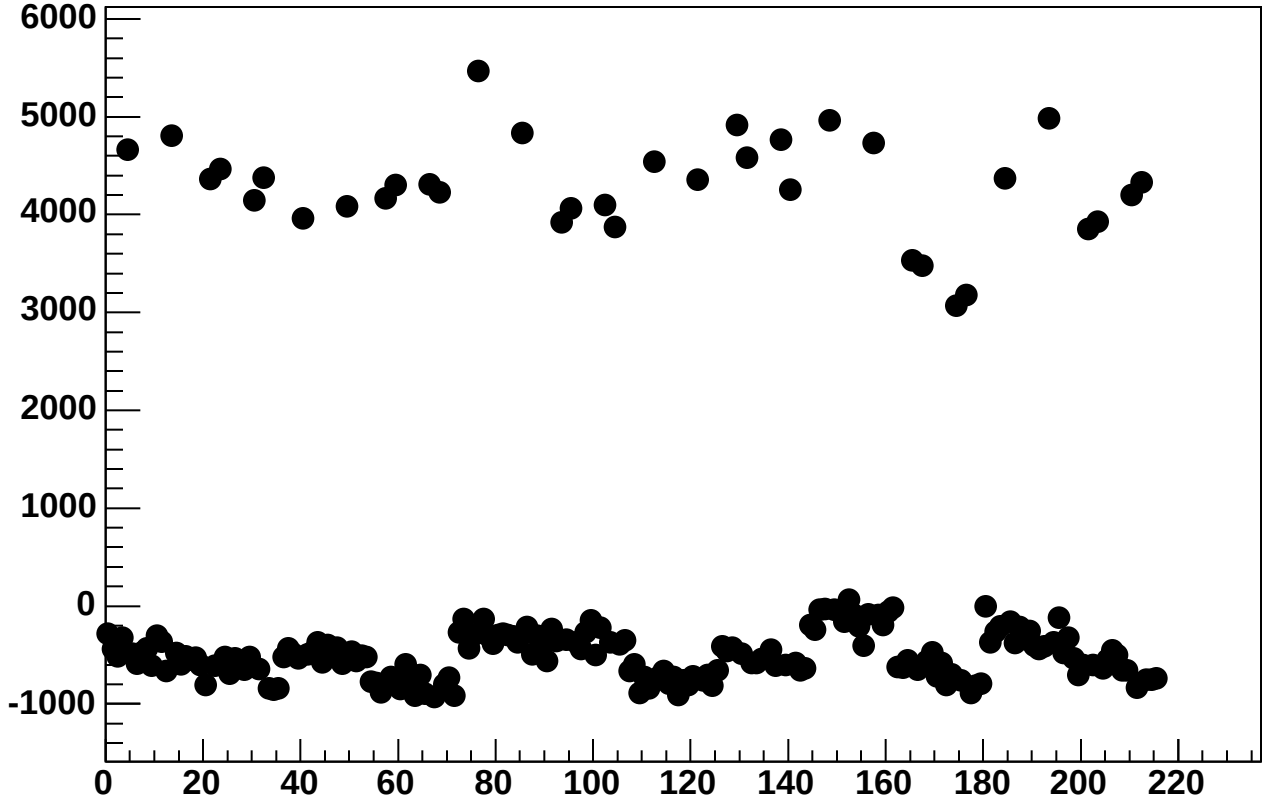
Enable 4, DAC=1600, Hold=100, ADC Mean vs 18*Chip+Chan



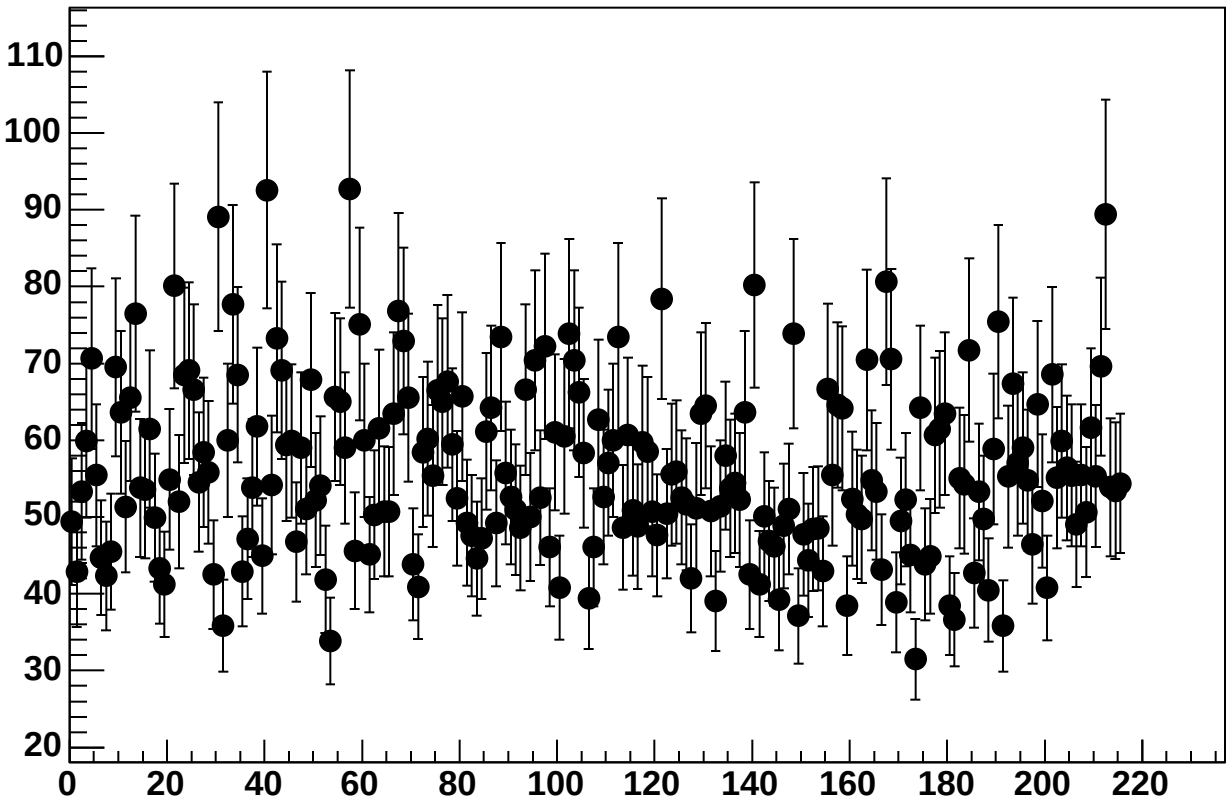
Enable 4, DAC=1600, Hold=100, ADC Noise vs 18*Chip+Chan



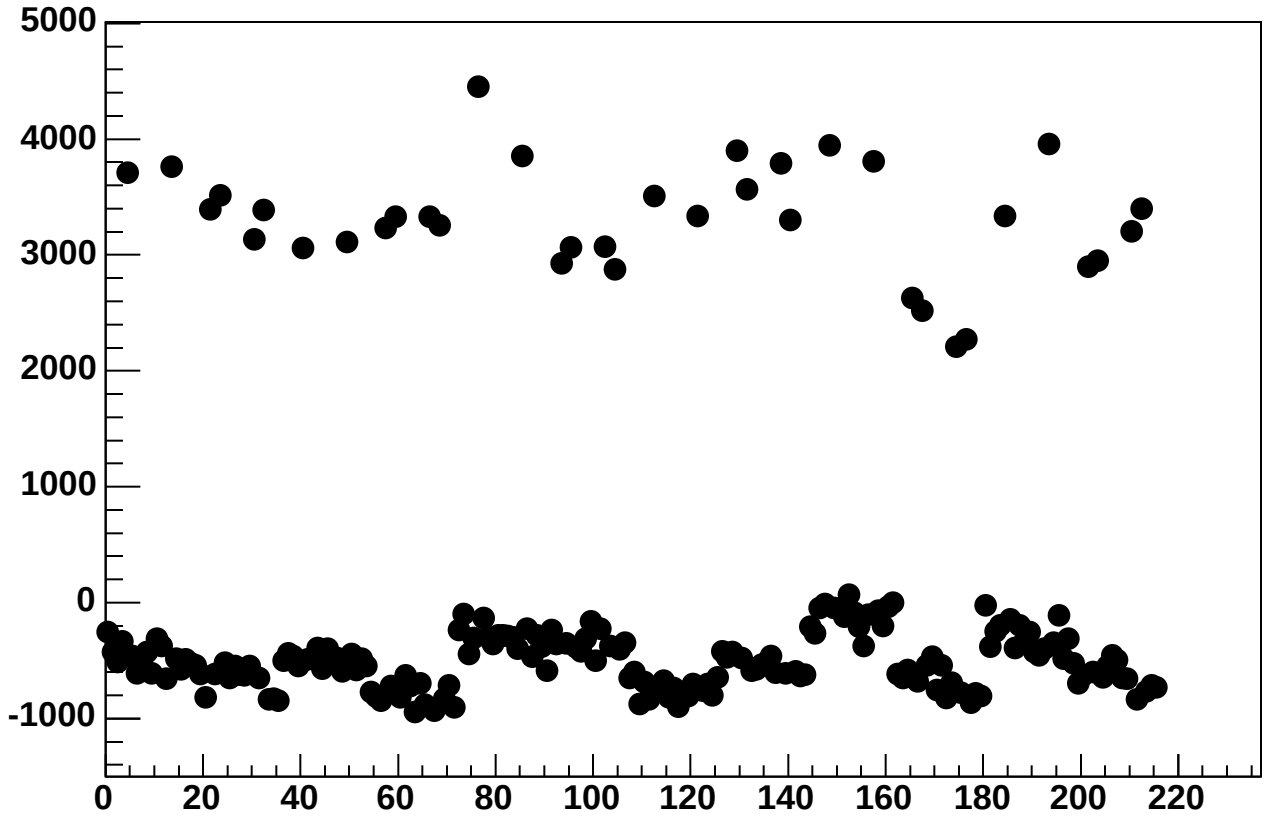
Enable 4, DAC=1600, Hold=105, ADC Mean vs 18*Chip+Chan



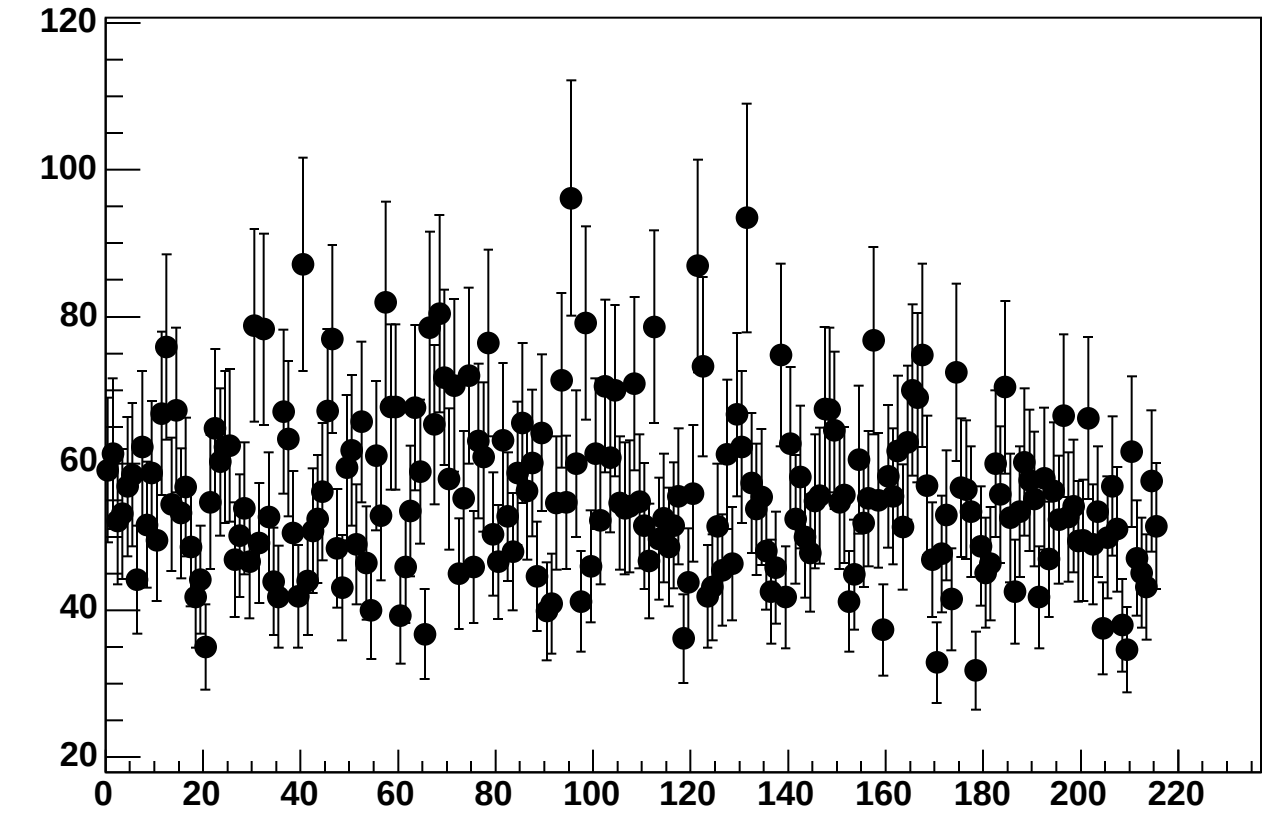
Enable 4, DAC=1600, Hold=105, ADC Noise vs 18*Chip+Chan



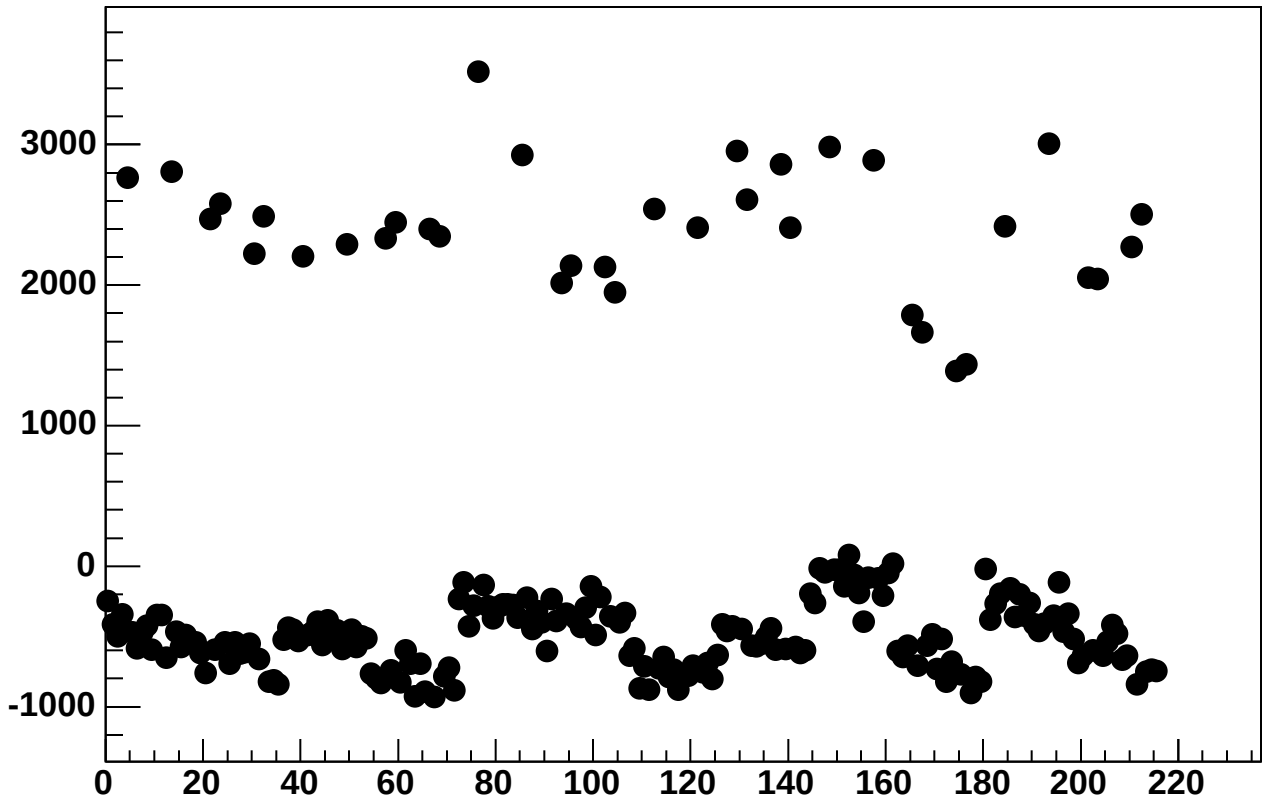
Enable 4, DAC=1600, Hold=110, ADC Mean vs 18*Chip+Chan



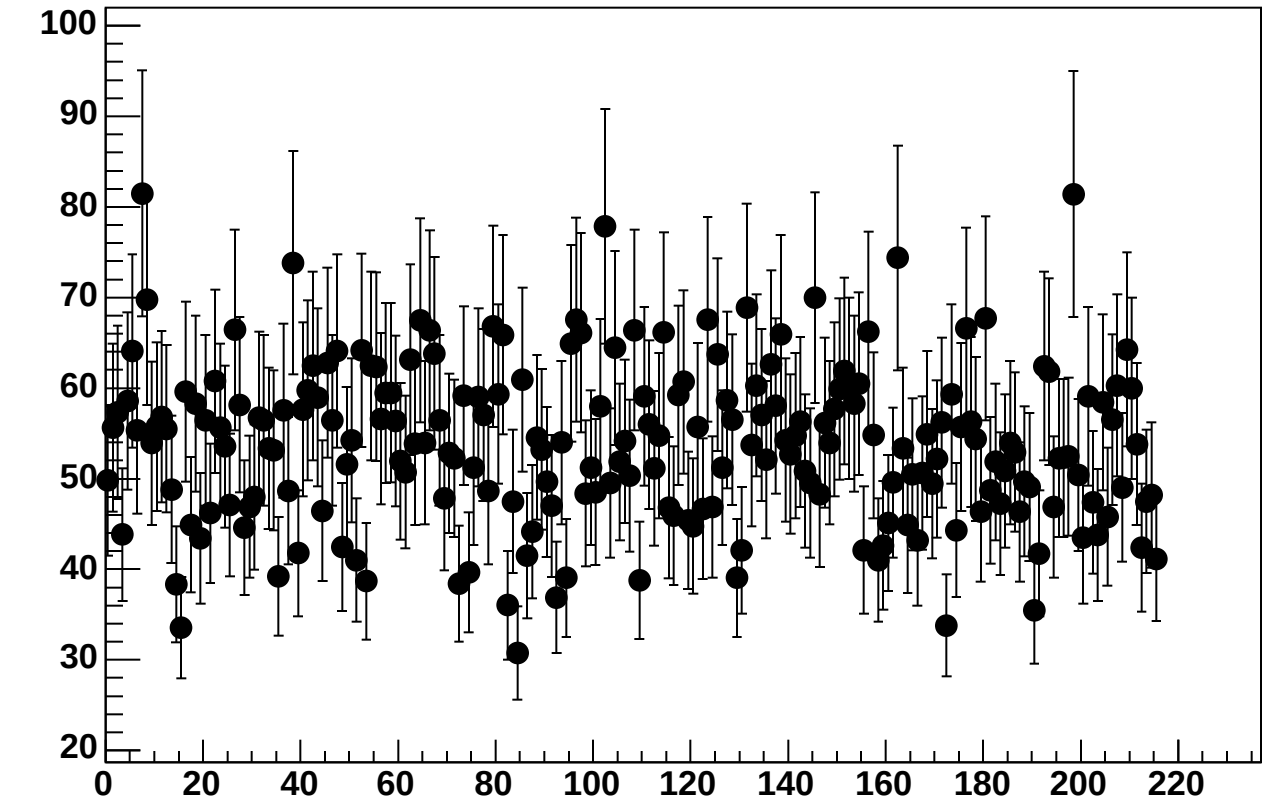
Enable 4, DAC=1600, Hold=110, ADC Noise vs 18*Chip+Chan



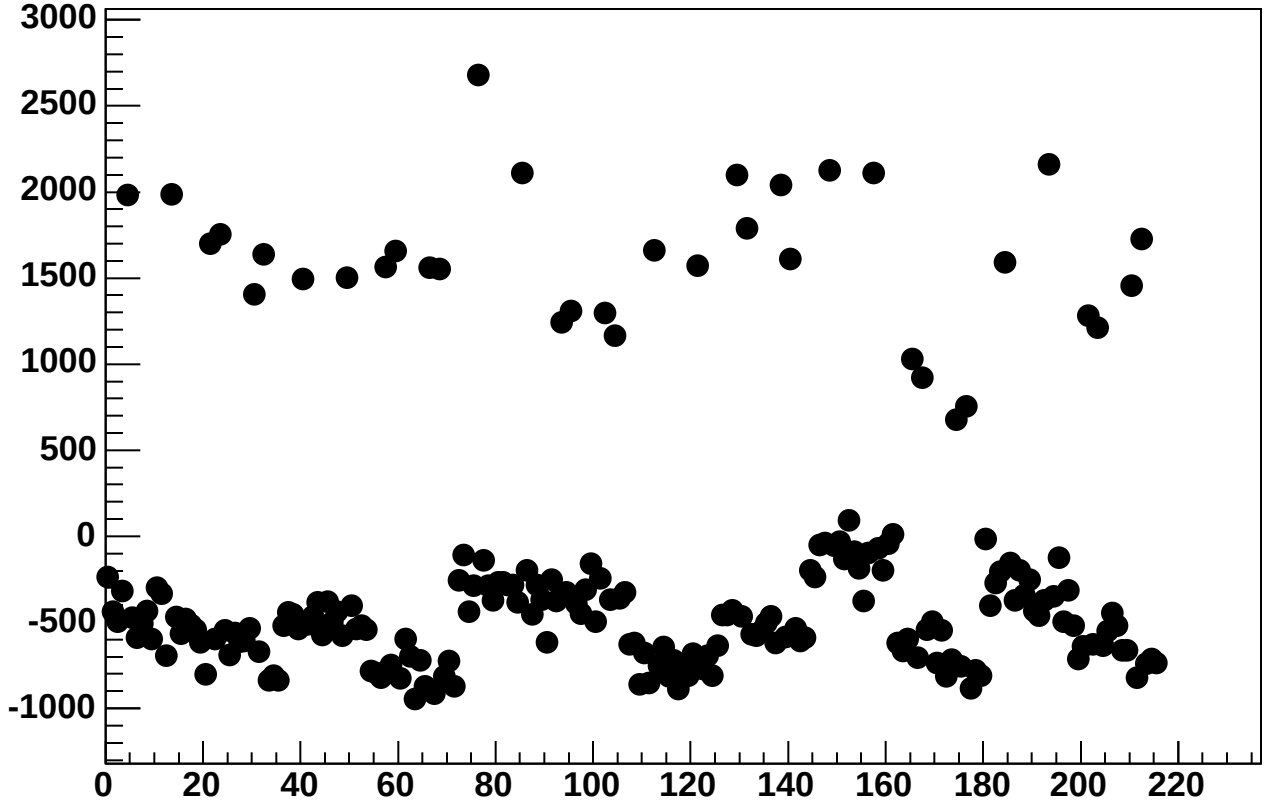
Enable 4, DAC=1600, Hold=115, ADC Mean vs 18*Chip+Chan



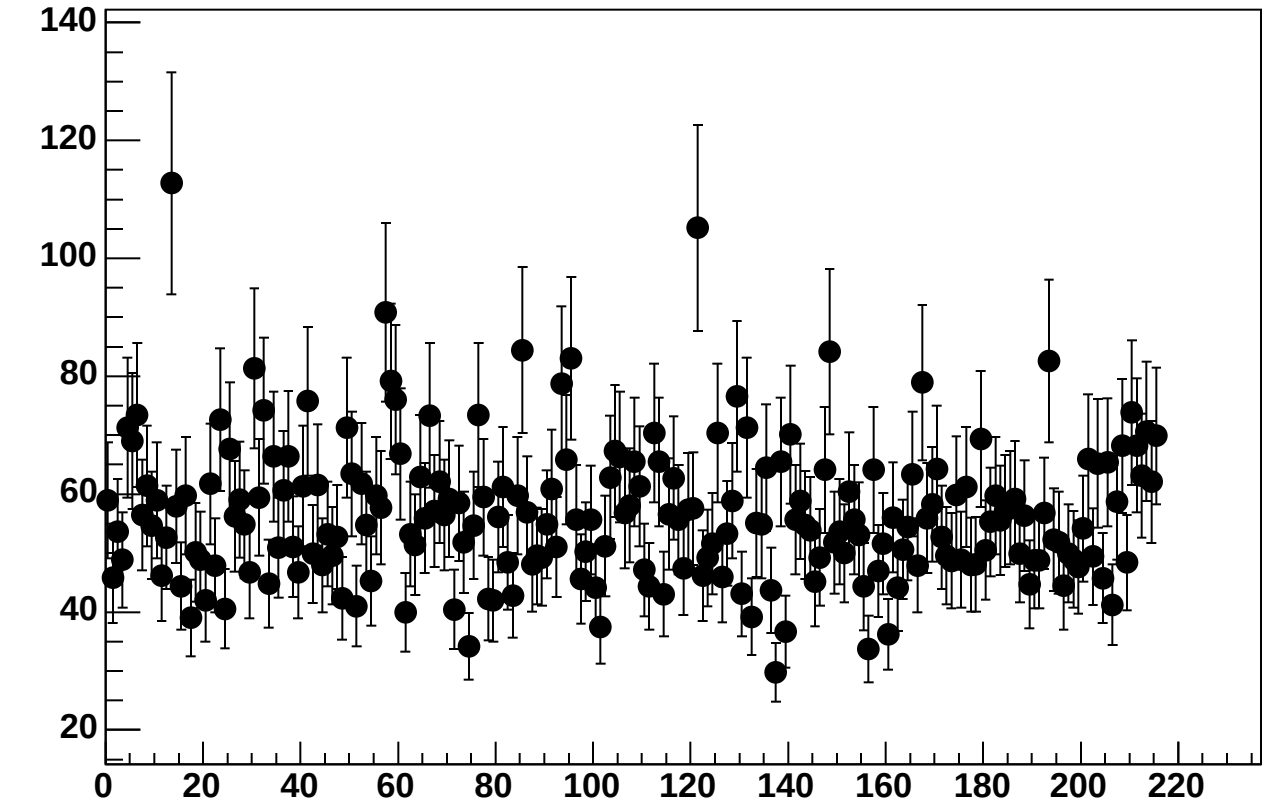
Enable 4, DAC=1600, Hold=115, ADC Noise vs 18*Chip+Chan



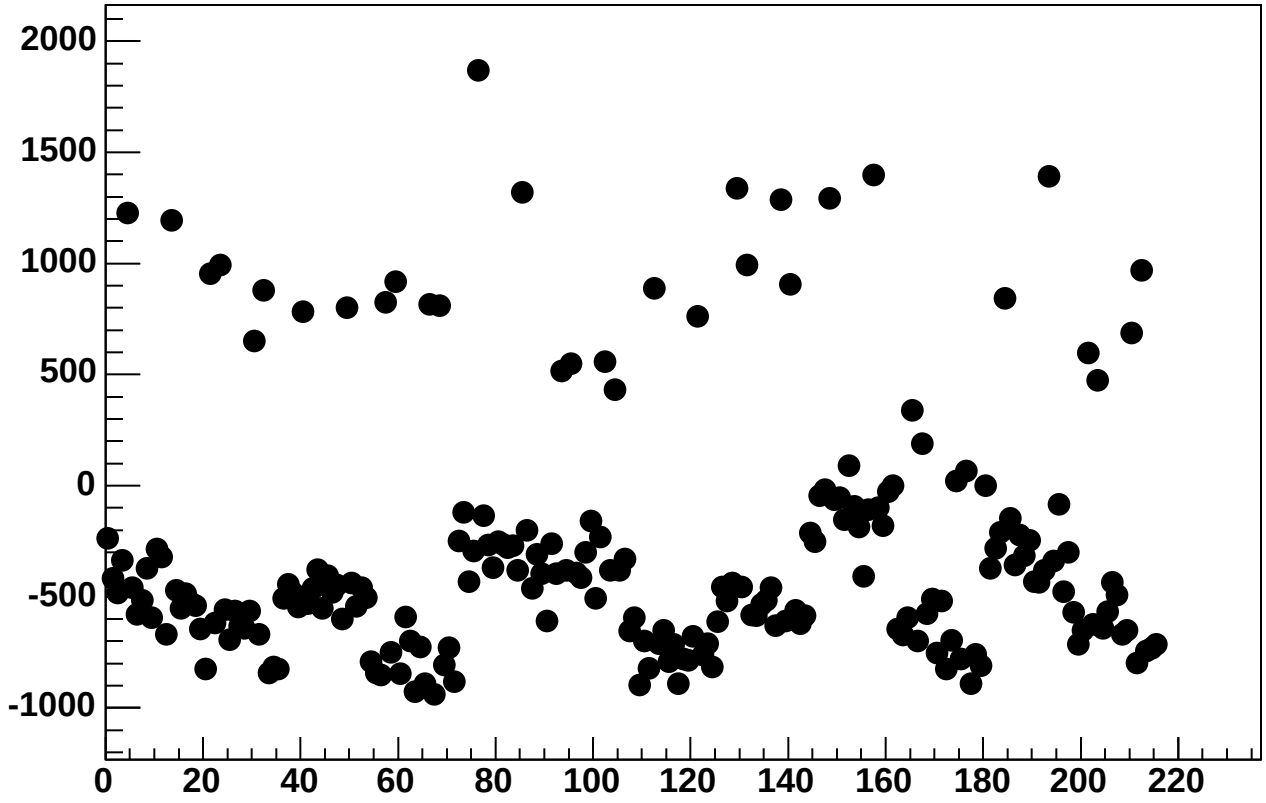
Enable 4, DAC=1600, Hold=120, ADC Mean vs 18*Chip+Chan



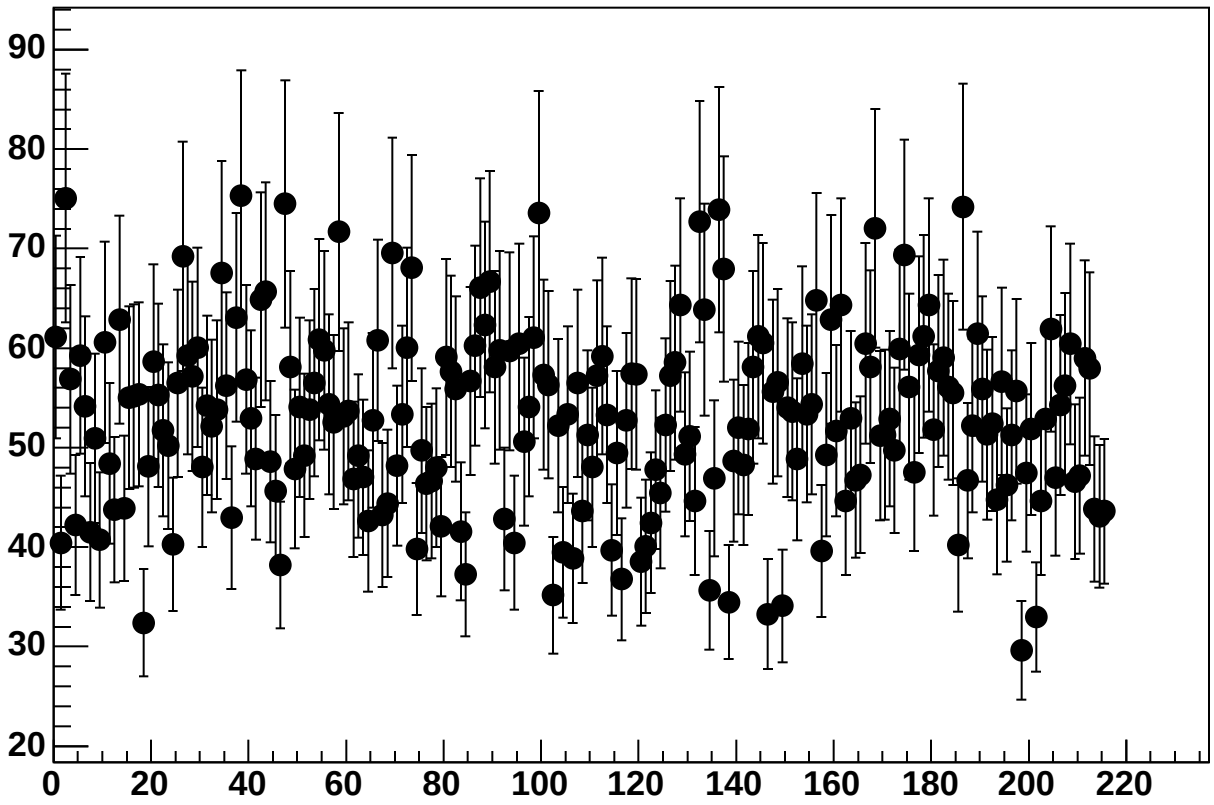
Enable 4, DAC=1600, Hold=120, ADC Noise vs 18*Chip+Chan



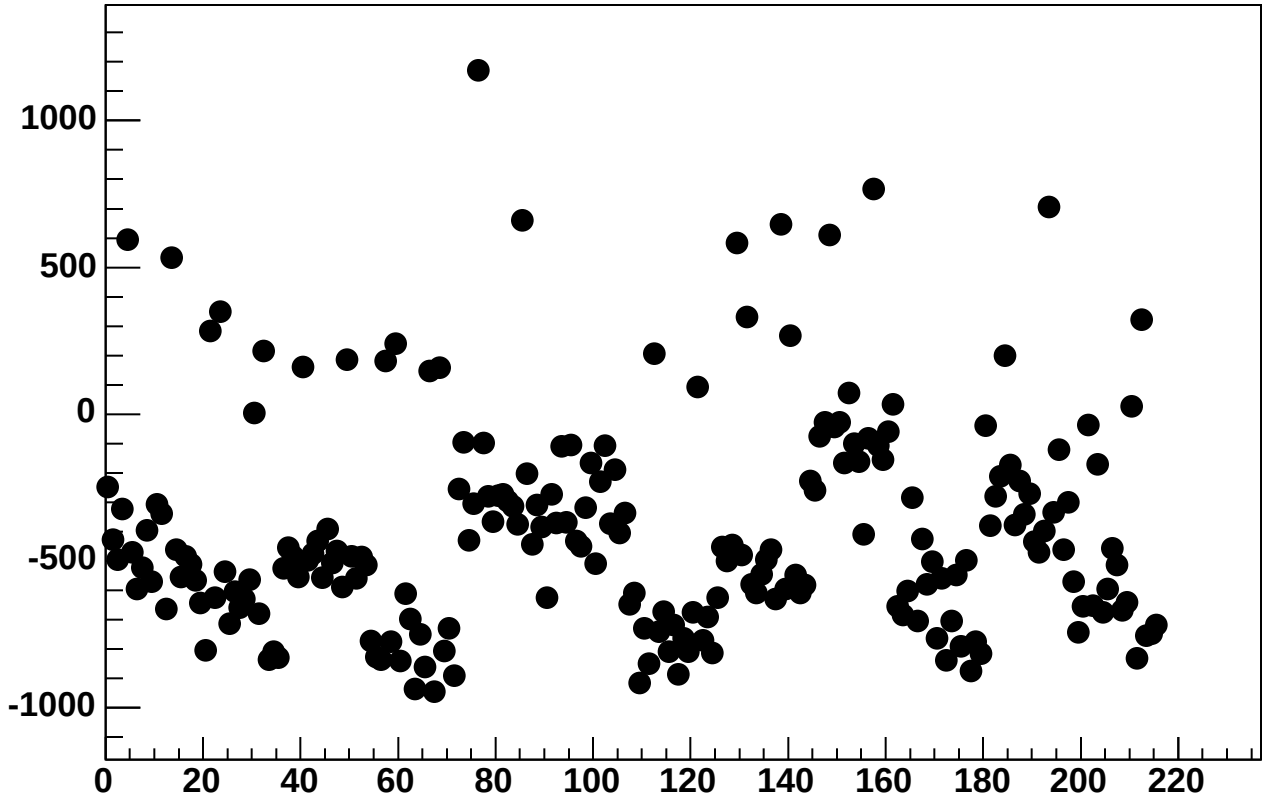
Enable 4, DAC=1600, Hold=125, ADC Mean vs 18*Chip+Chan



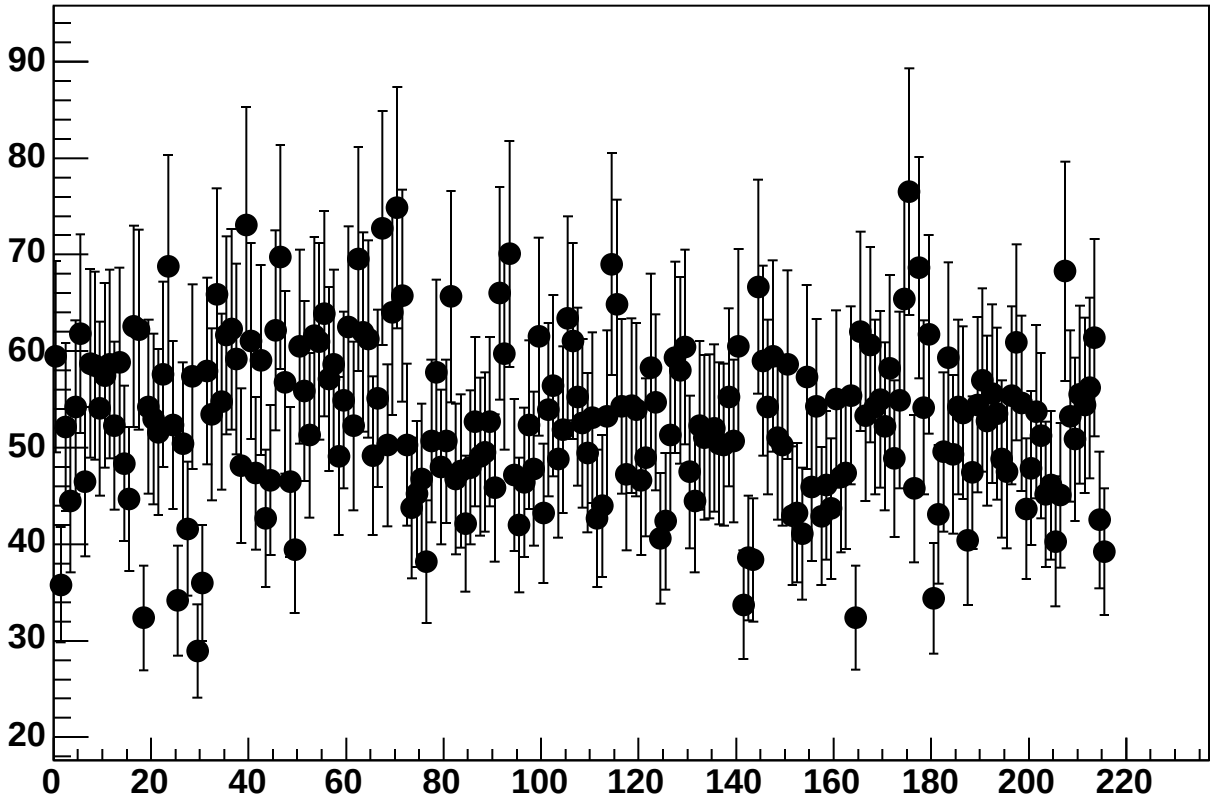
Enable 4, DAC=1600, Hold=125, ADC Noise vs 18*Chip+Chan



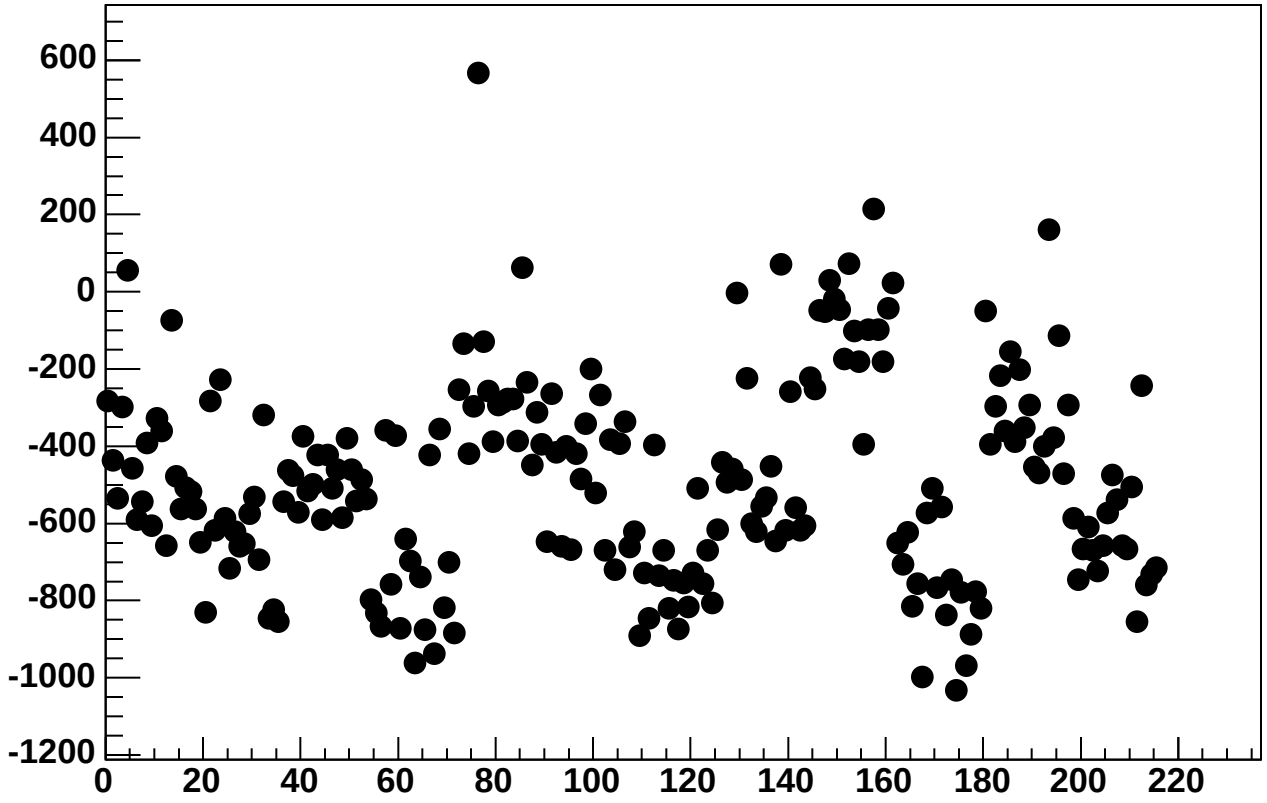
Enable 4, DAC=1600, Hold=130, ADC Mean vs 18*Chip+Chan



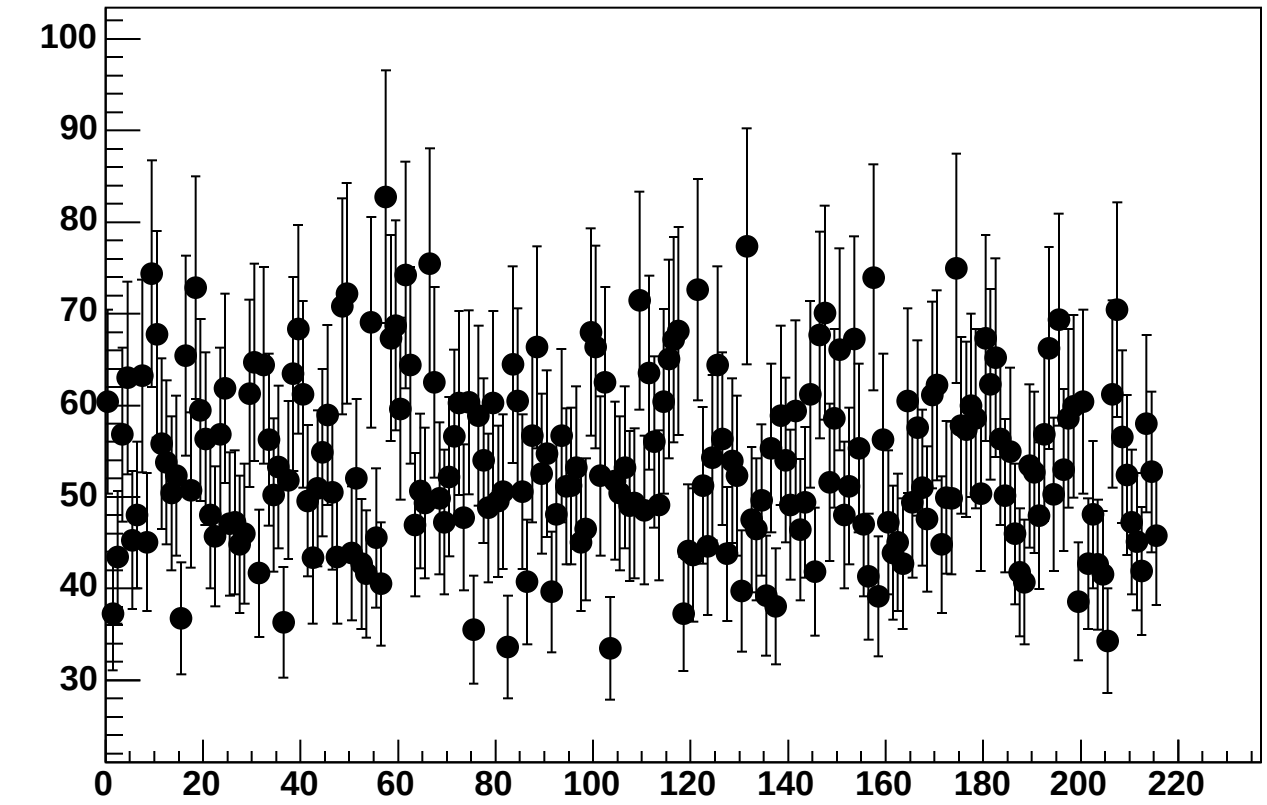
Enable 4, DAC=1600, Hold=130, ADC Noise vs 18*Chip+Chan



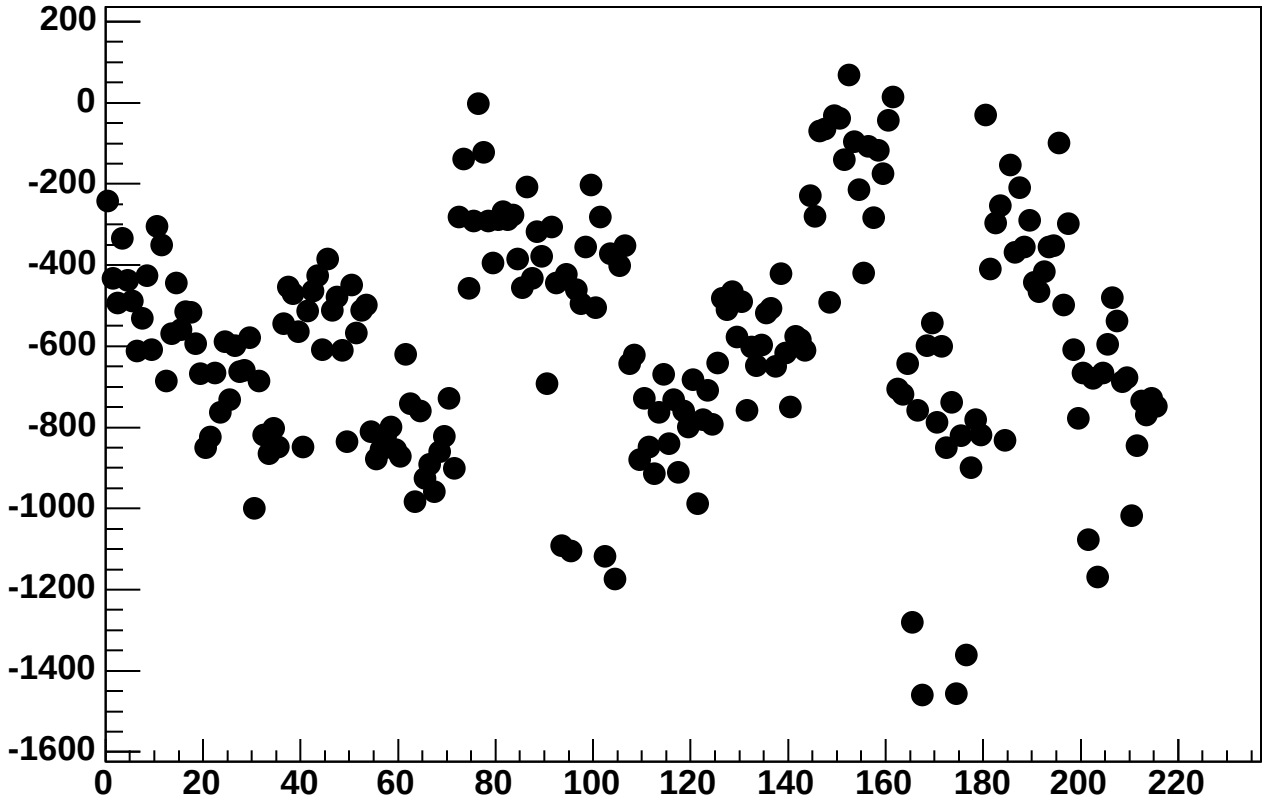
Enable 4, DAC=1600, Hold=135, ADC Mean vs 18*Chip+Chan



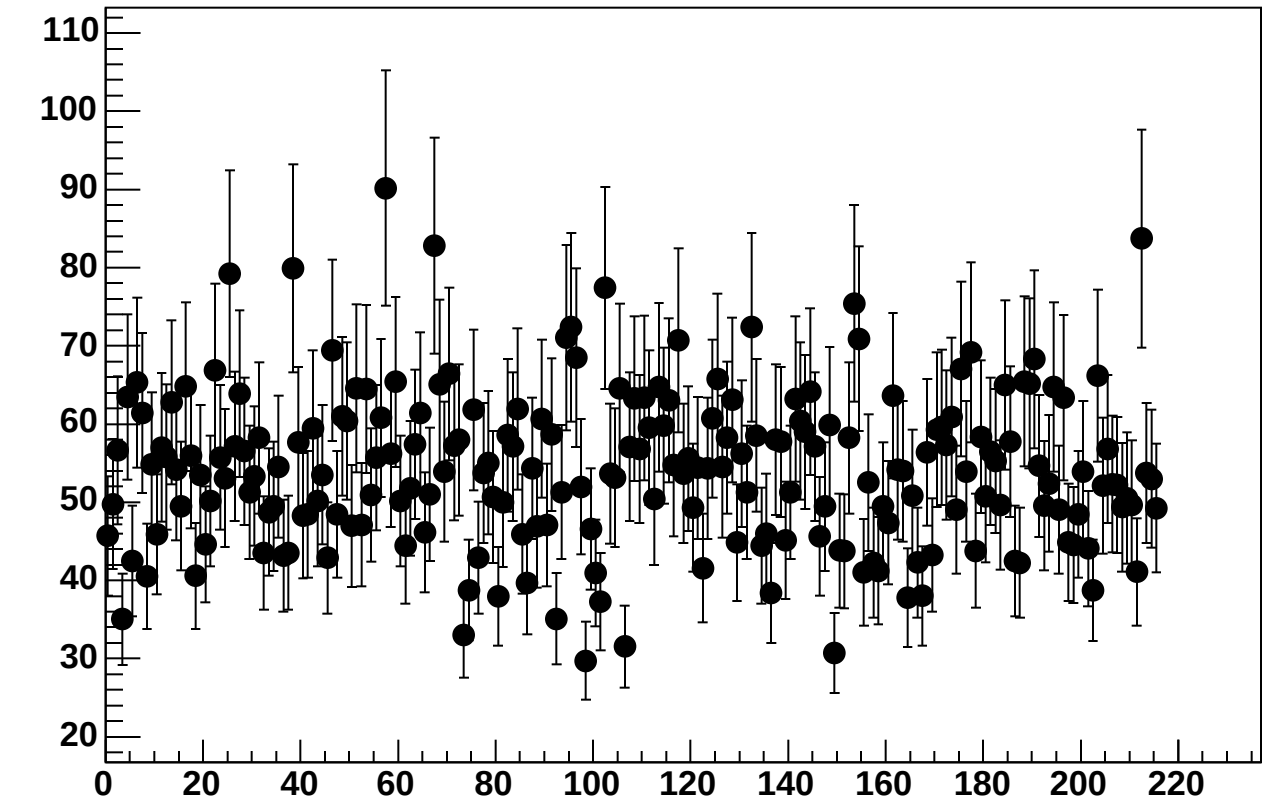
Enable 4, DAC=1600, Hold=135, ADC Noise vs 18*Chip+Chan



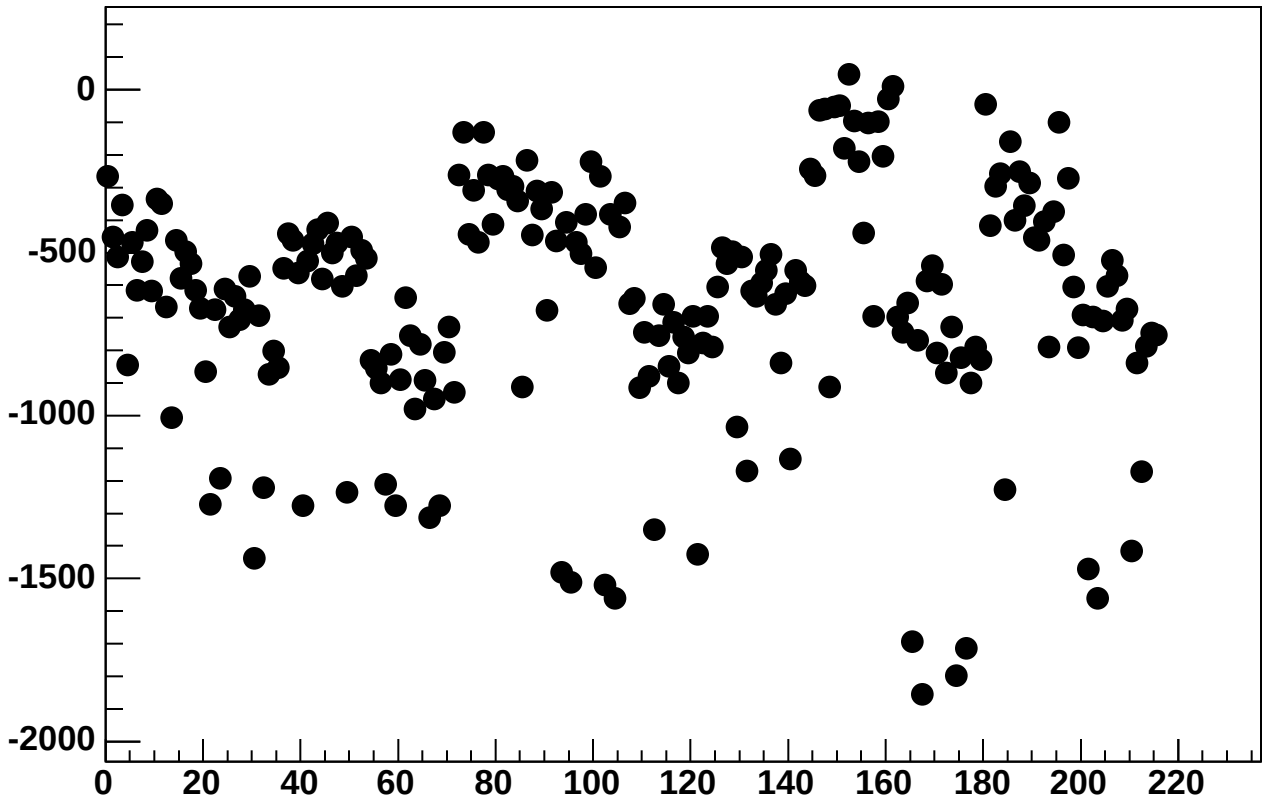
Enable 4, DAC=1600, Hold=140, ADC Mean vs 18*Chip+Chan



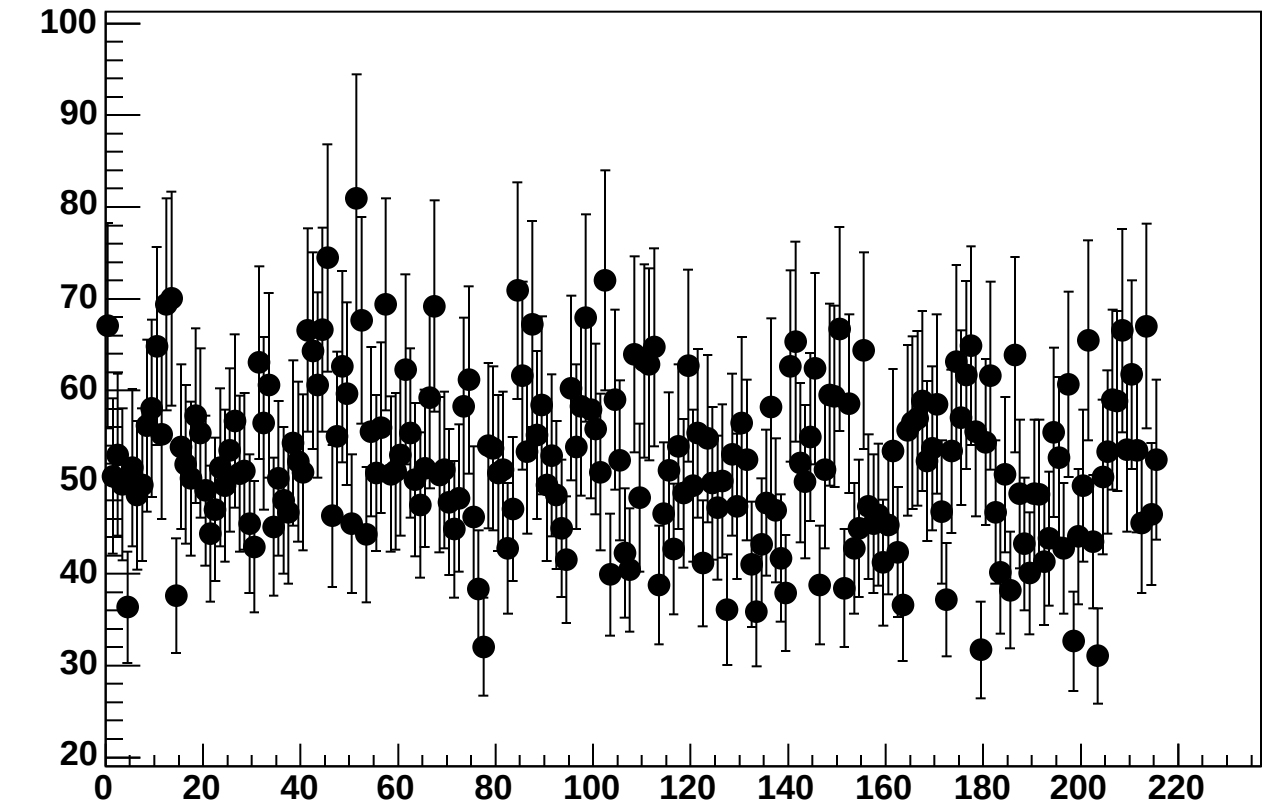
Enable 4, DAC=1600, Hold=140, ADC Noise vs 18*Chip+Chan



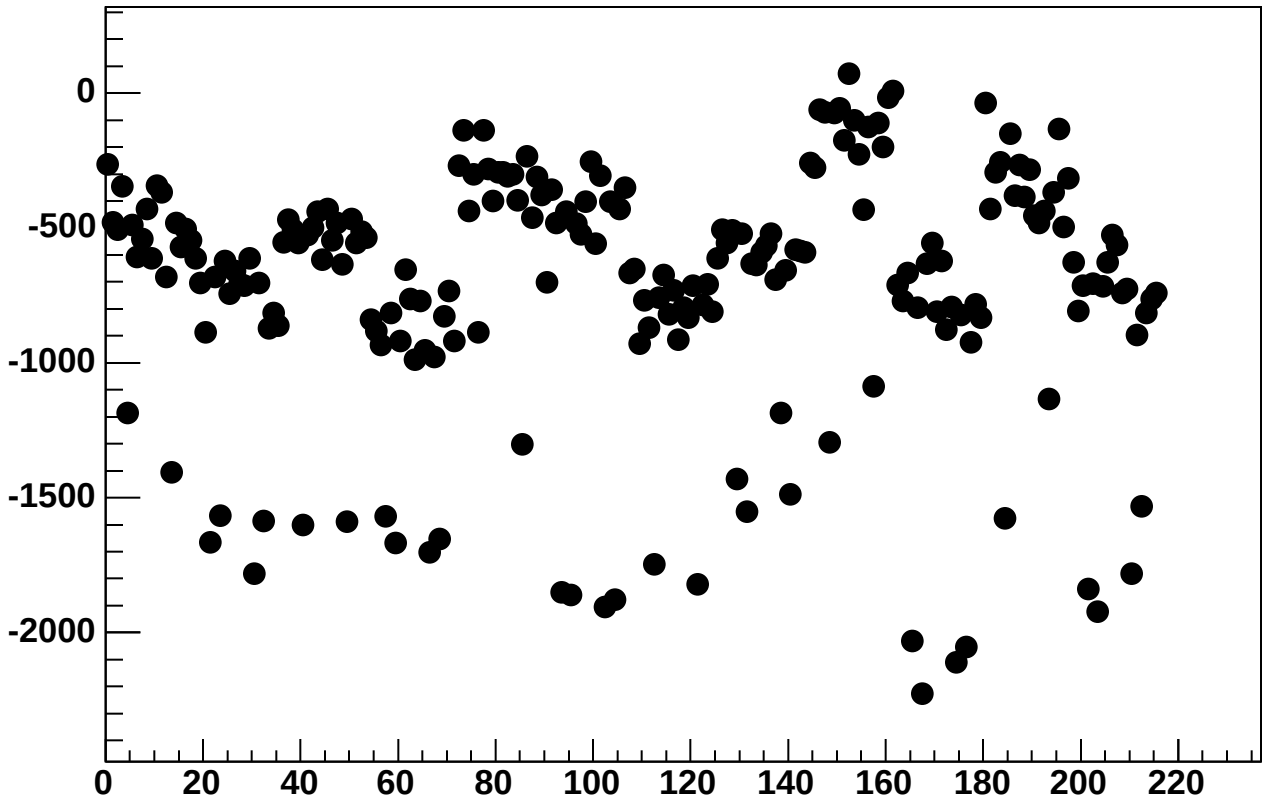
Enable 4, DAC=1600, Hold=145, ADC Mean vs 18*Chip+Chan



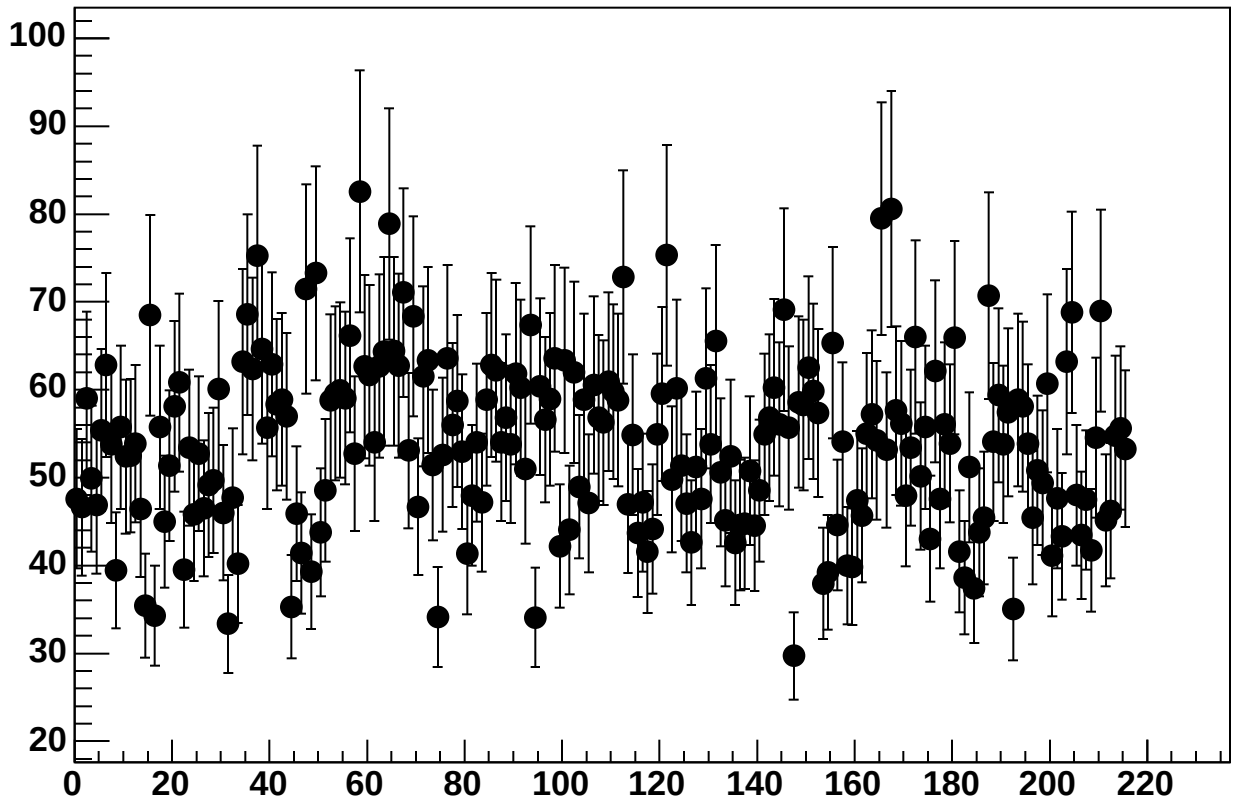
Enable 4, DAC=1600, Hold=145, ADC Noise vs 18*Chip+Chan



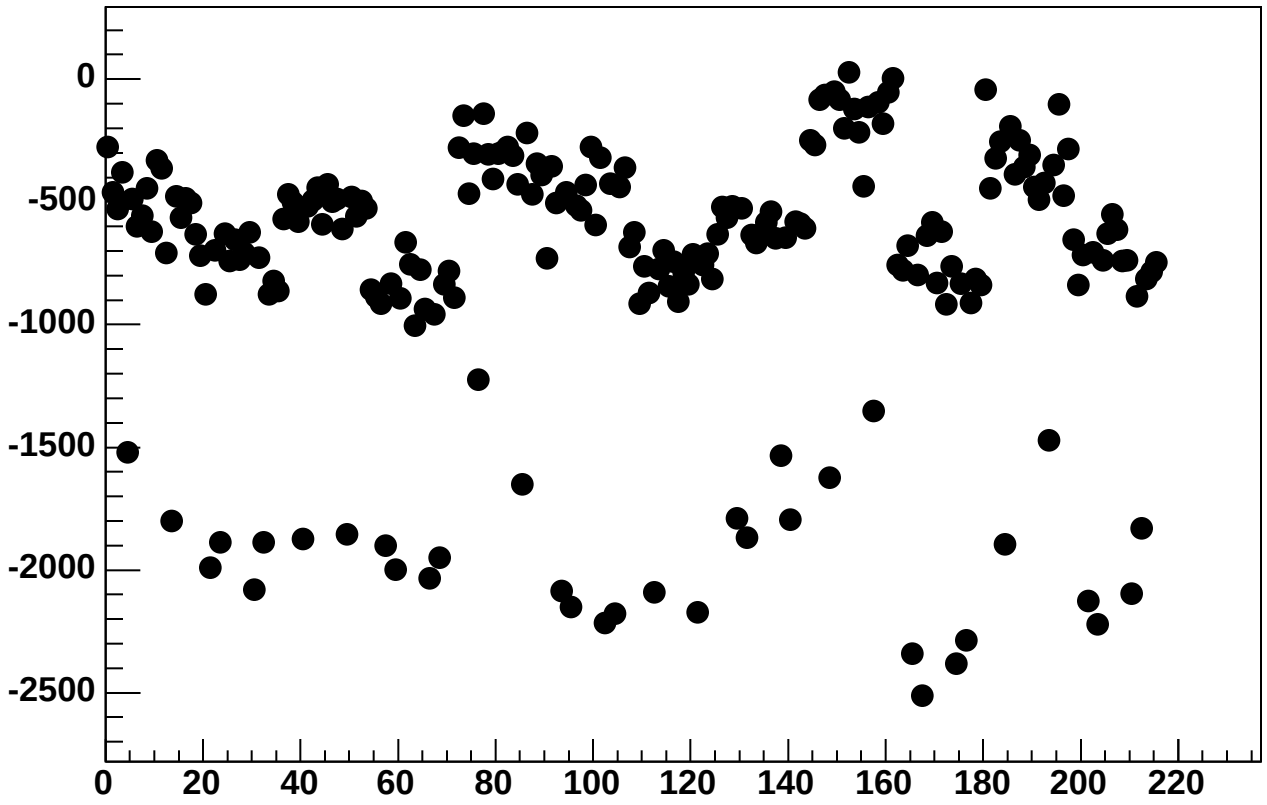
Enable 4, DAC=1600, Hold=150, ADC Mean vs 18*Chip+Chan



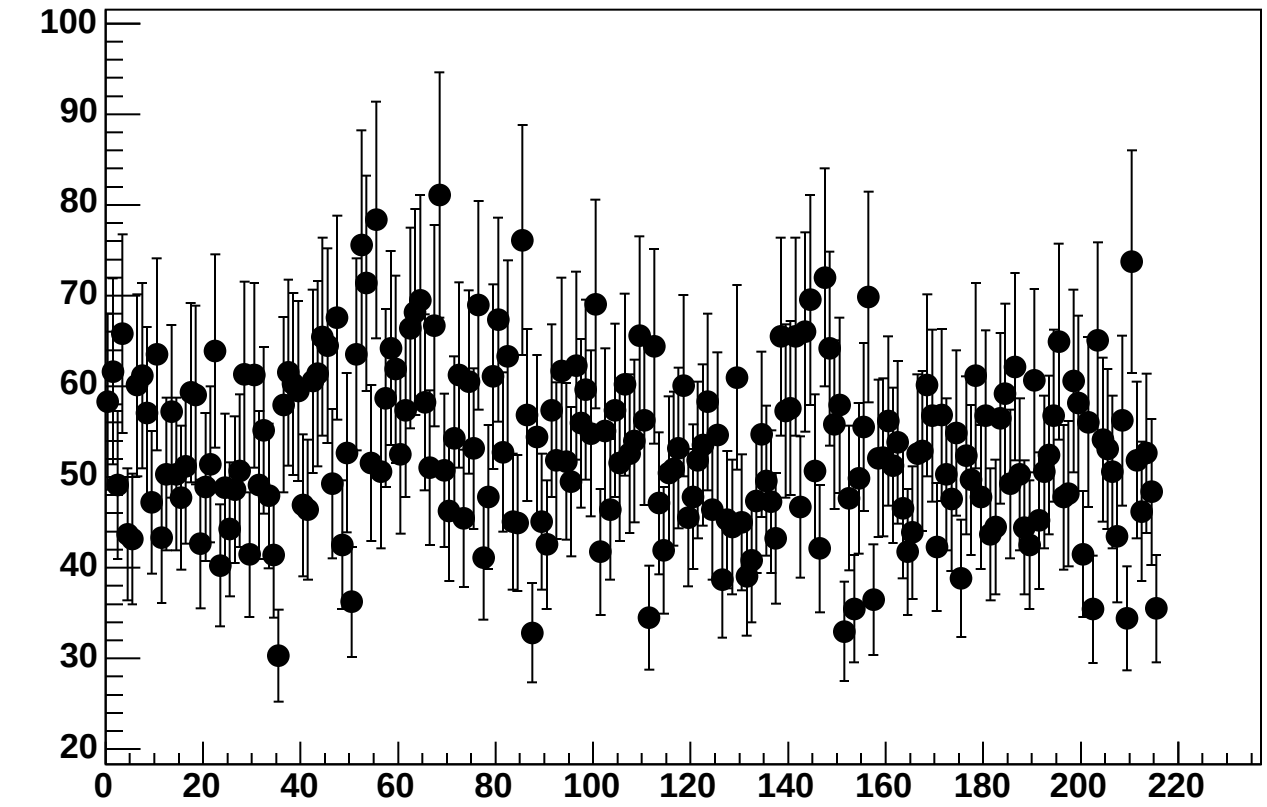
Enable 4, DAC=1600, Hold=150, ADC Noise vs 18*Chip+Chan



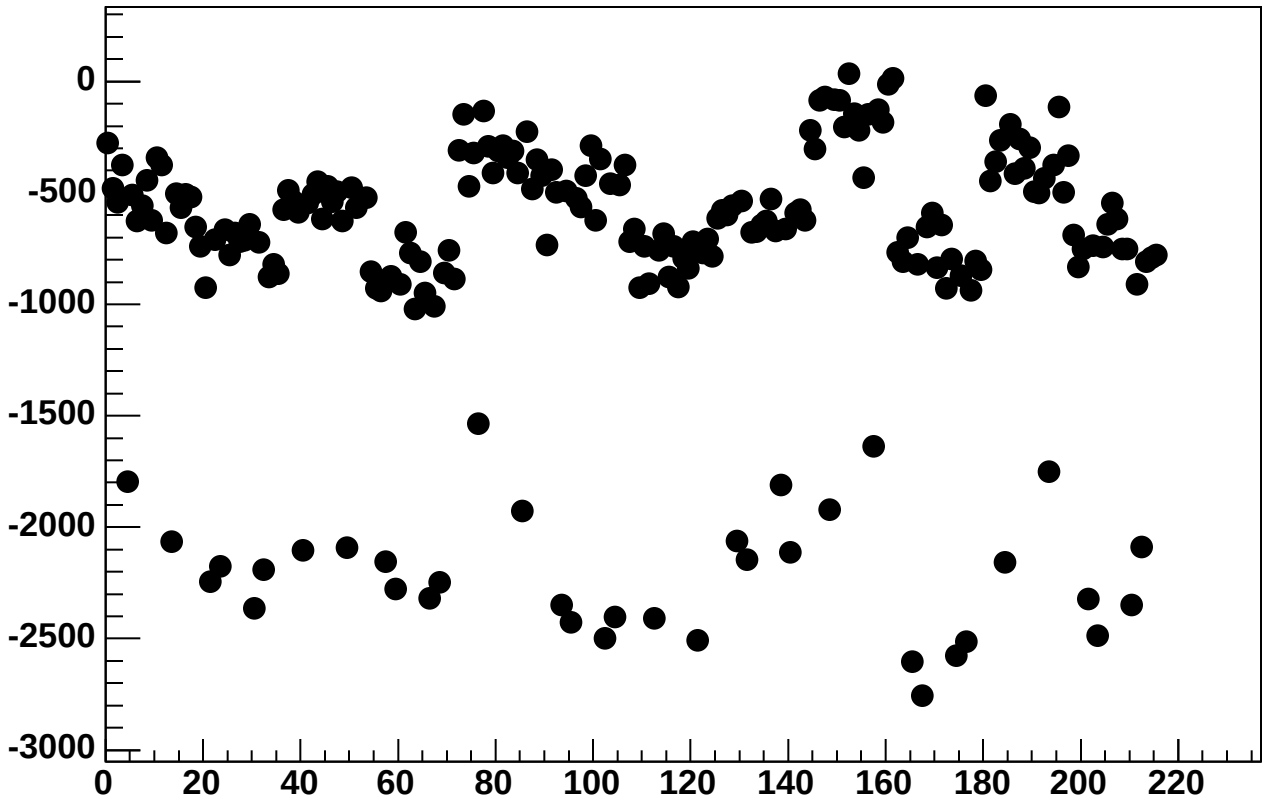
Enable 4, DAC=1600, Hold=155, ADC Mean vs 18*Chip+Chan



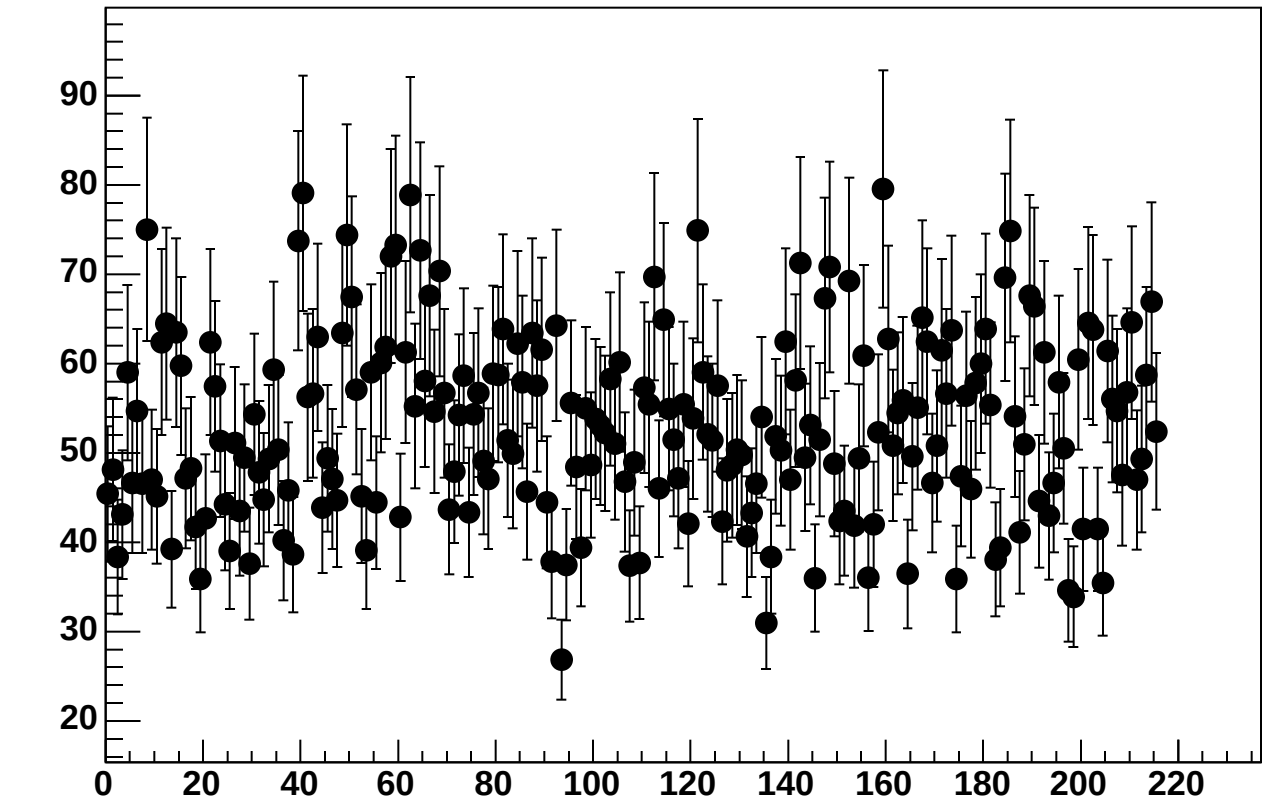
Enable 4, DAC=1600, Hold=155, ADC Noise vs 18*Chip+Chan



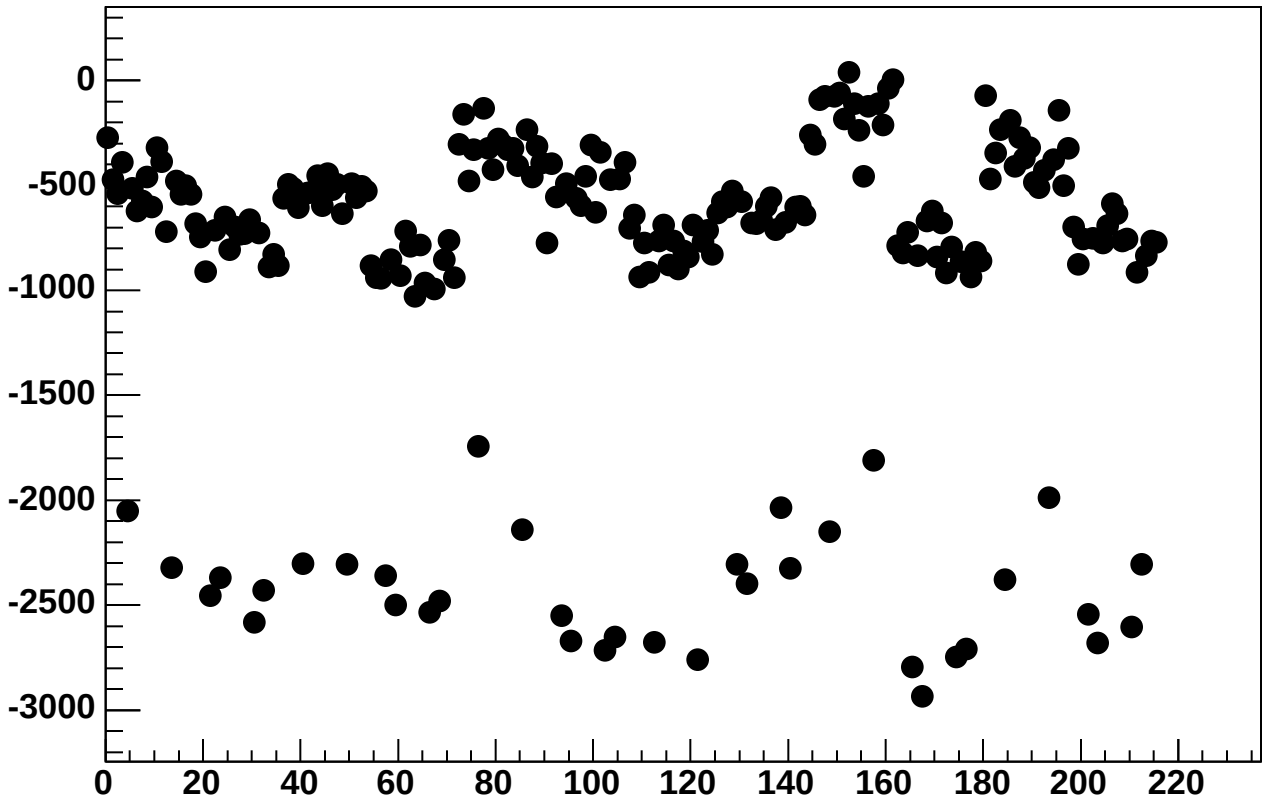
Enable 4, DAC=1600, Hold=160, ADC Mean vs 18*Chip+Chan



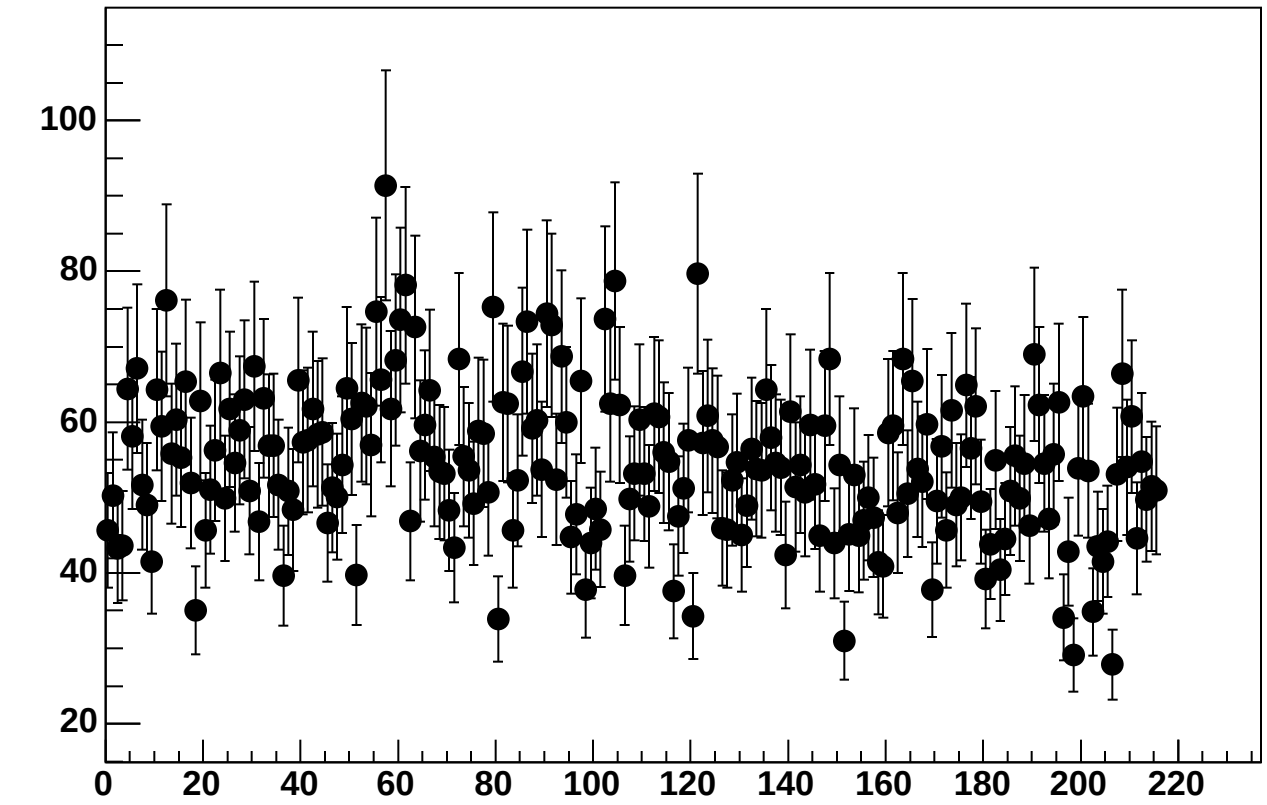
Enable 4, DAC=1600, Hold=160, ADC Noise vs 18*Chip+Chan



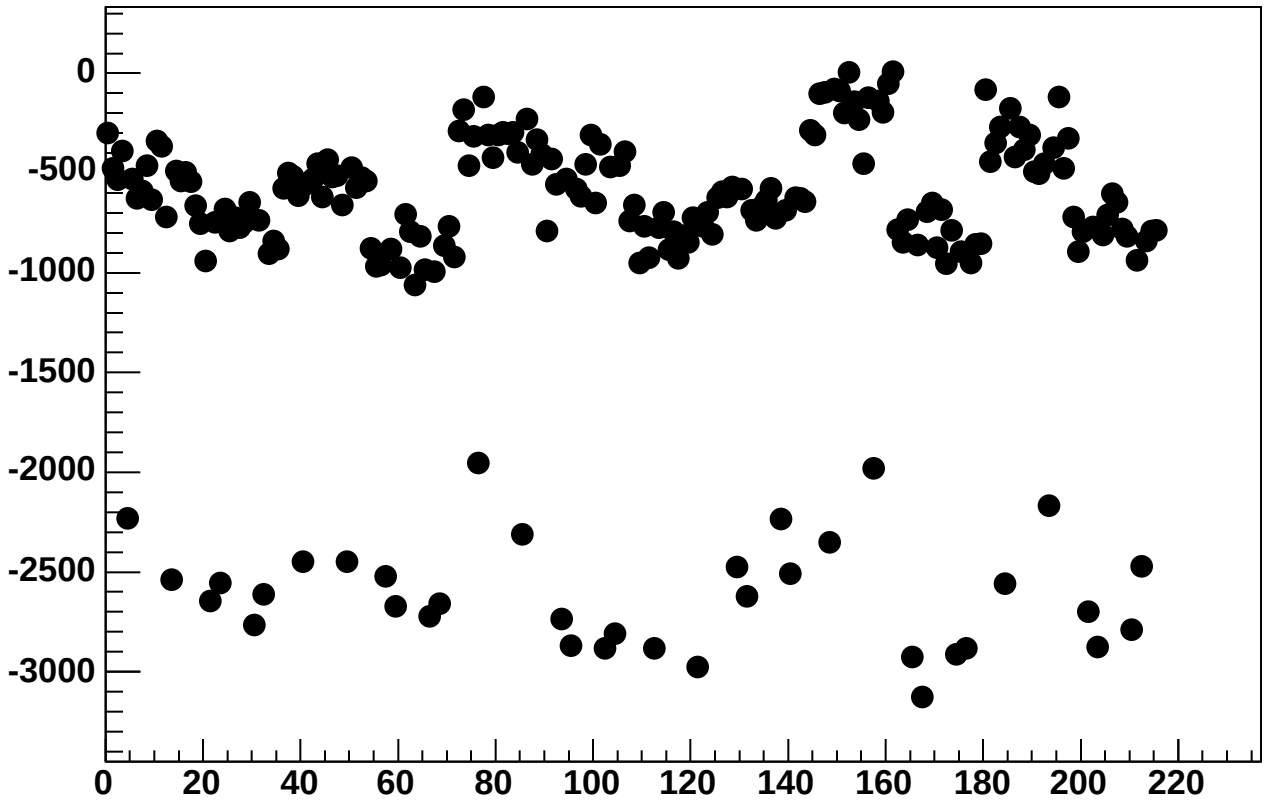
Enable 4, DAC=1600, Hold=165, ADC Mean vs 18*Chip+Chan



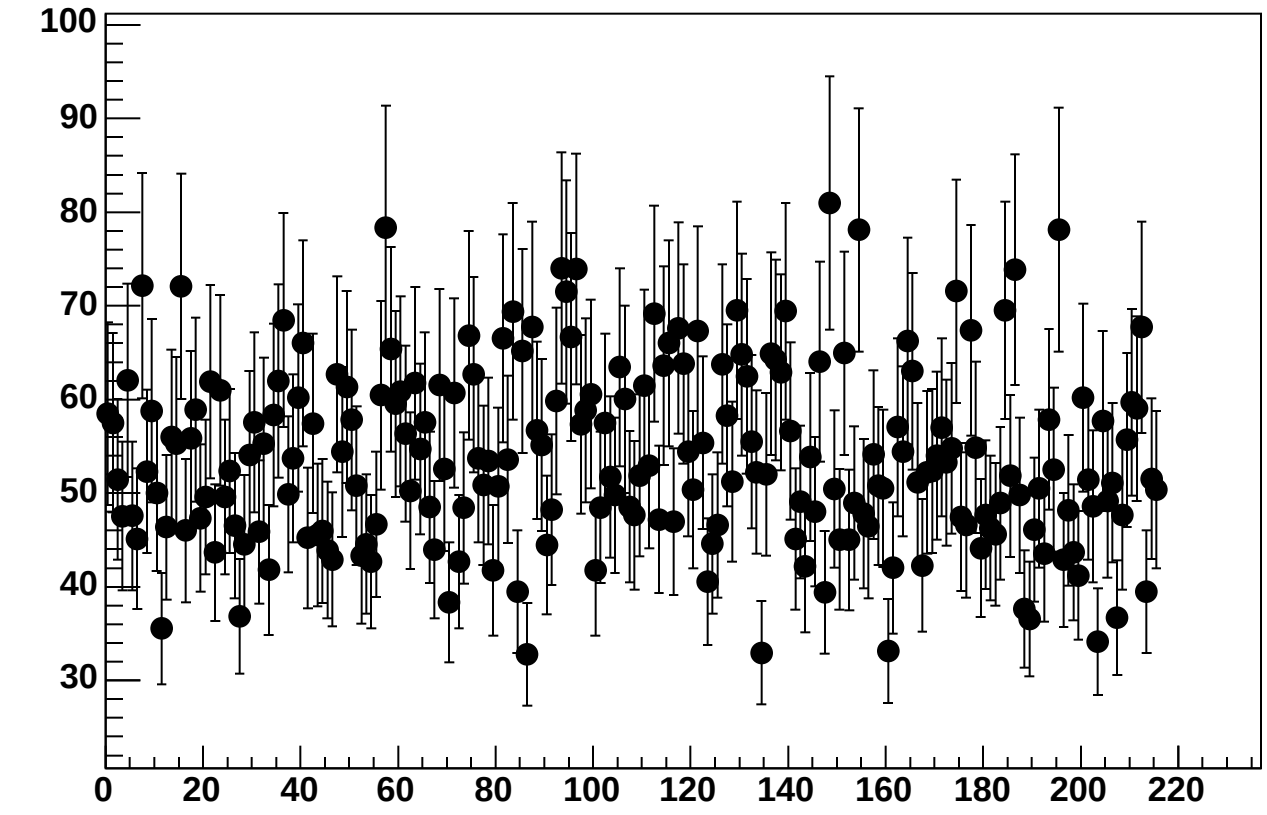
Enable 4, DAC=1600, Hold=165, ADC Noise vs 18*Chip+Chan



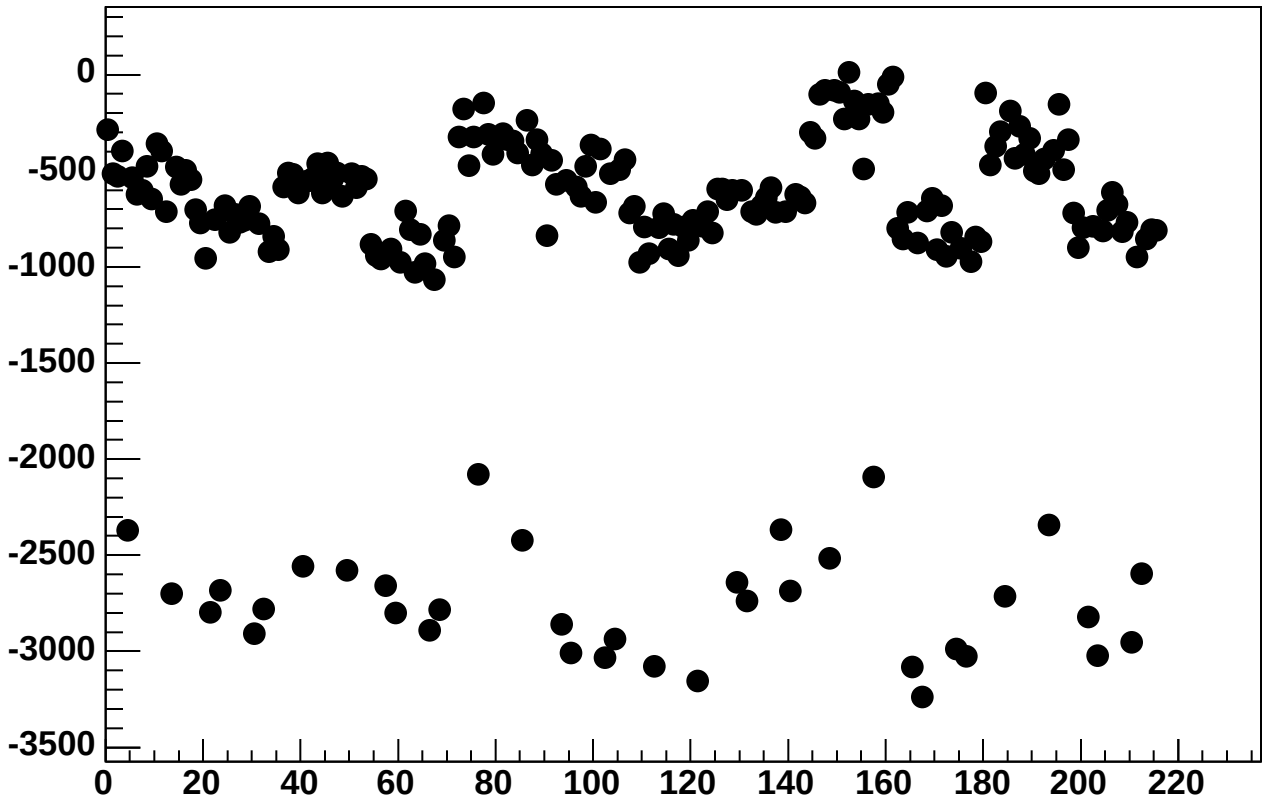
Enable 4, DAC=1600, Hold=170, ADC Mean vs 18*Chip+Chan



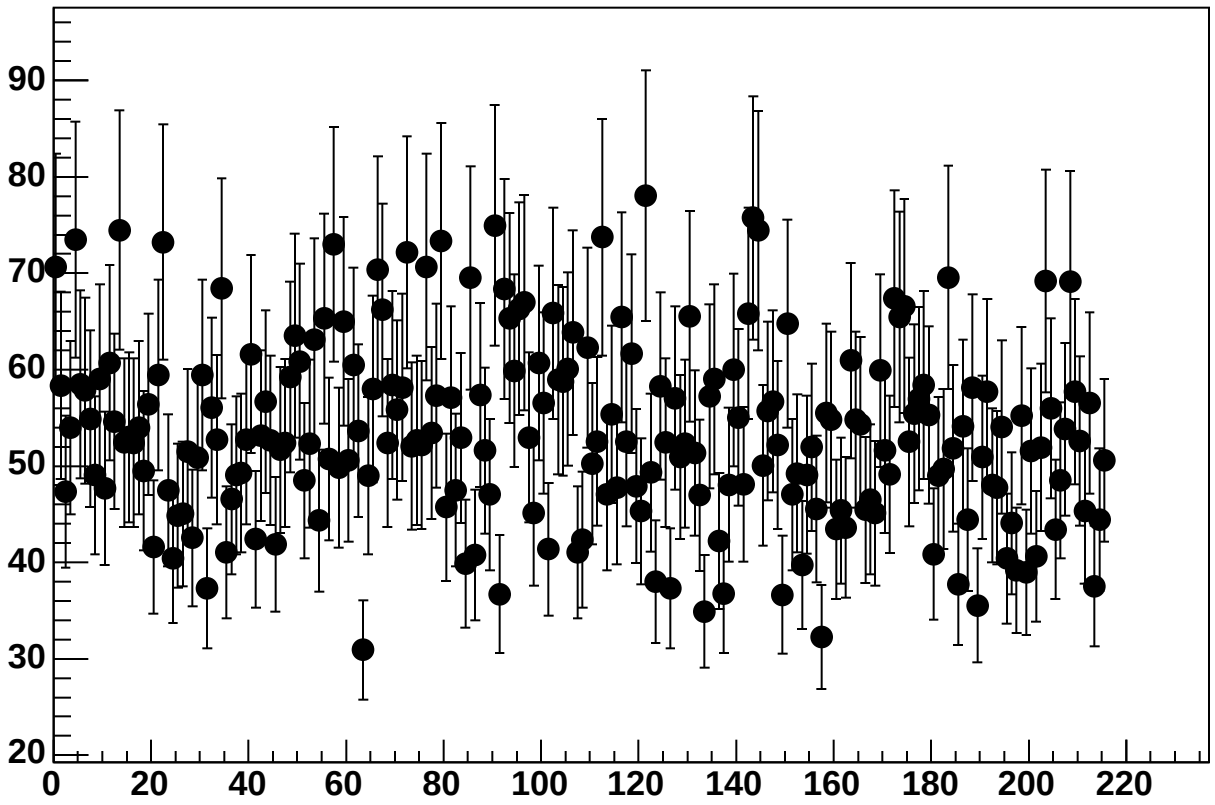
Enable 4, DAC=1600, Hold=170, ADC Noise vs 18*Chip+Chan



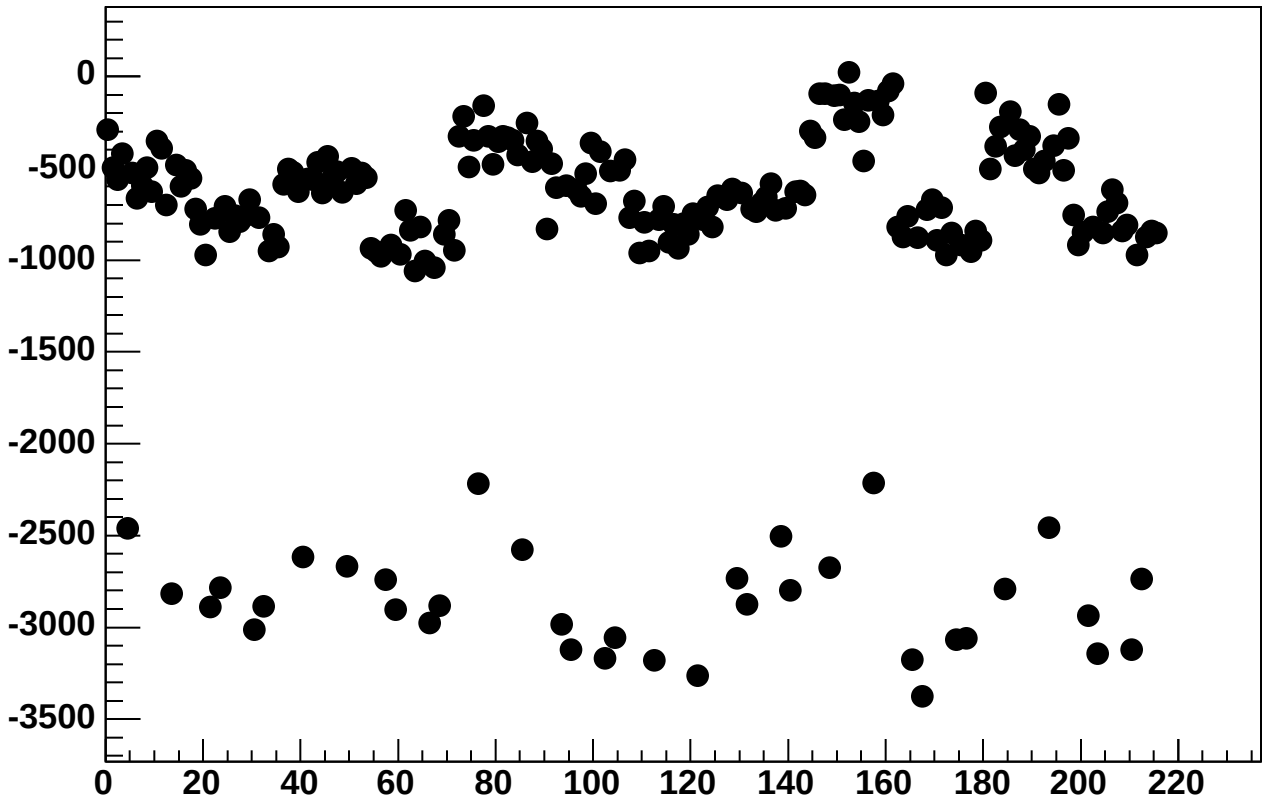
Enable 4, DAC=1600, Hold=175, ADC Mean vs 18*Chip+Chan



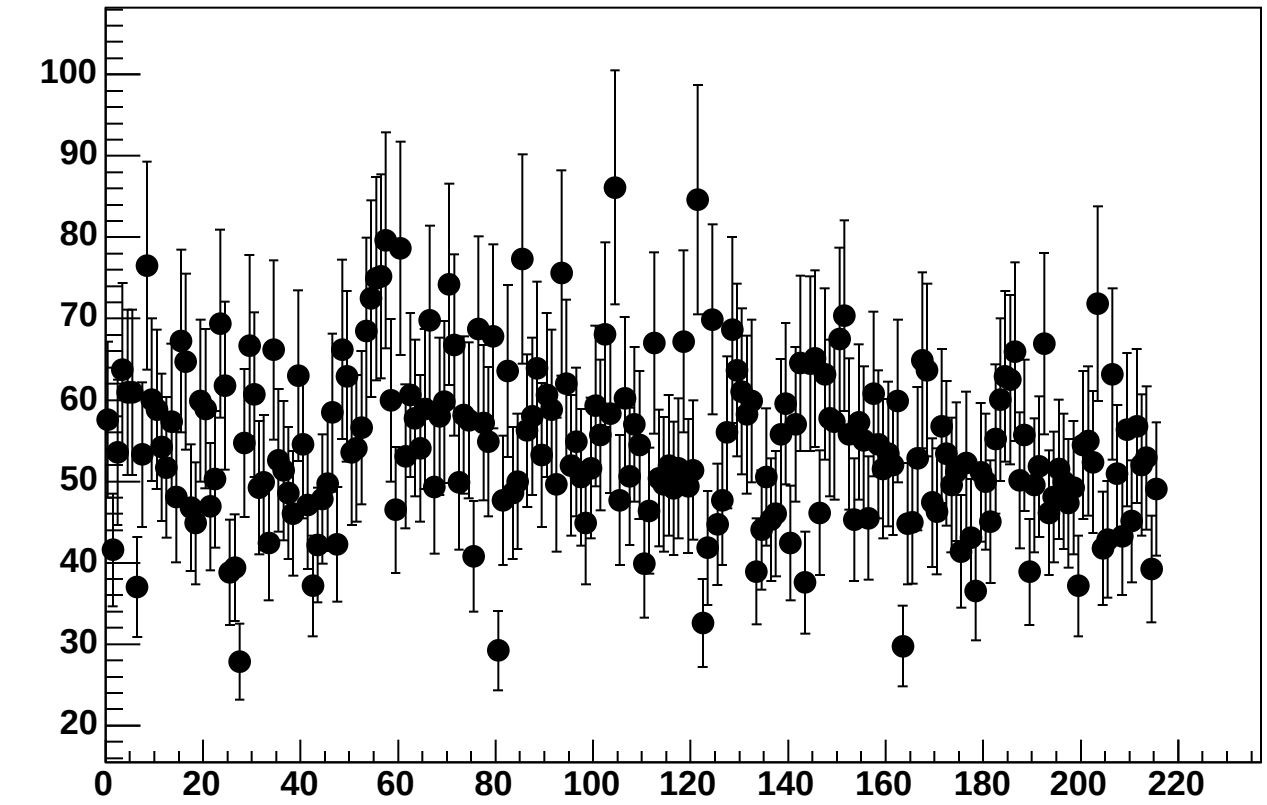
Enable 4, DAC=1600, Hold=175, ADC Noise vs 18*Chip+Chan



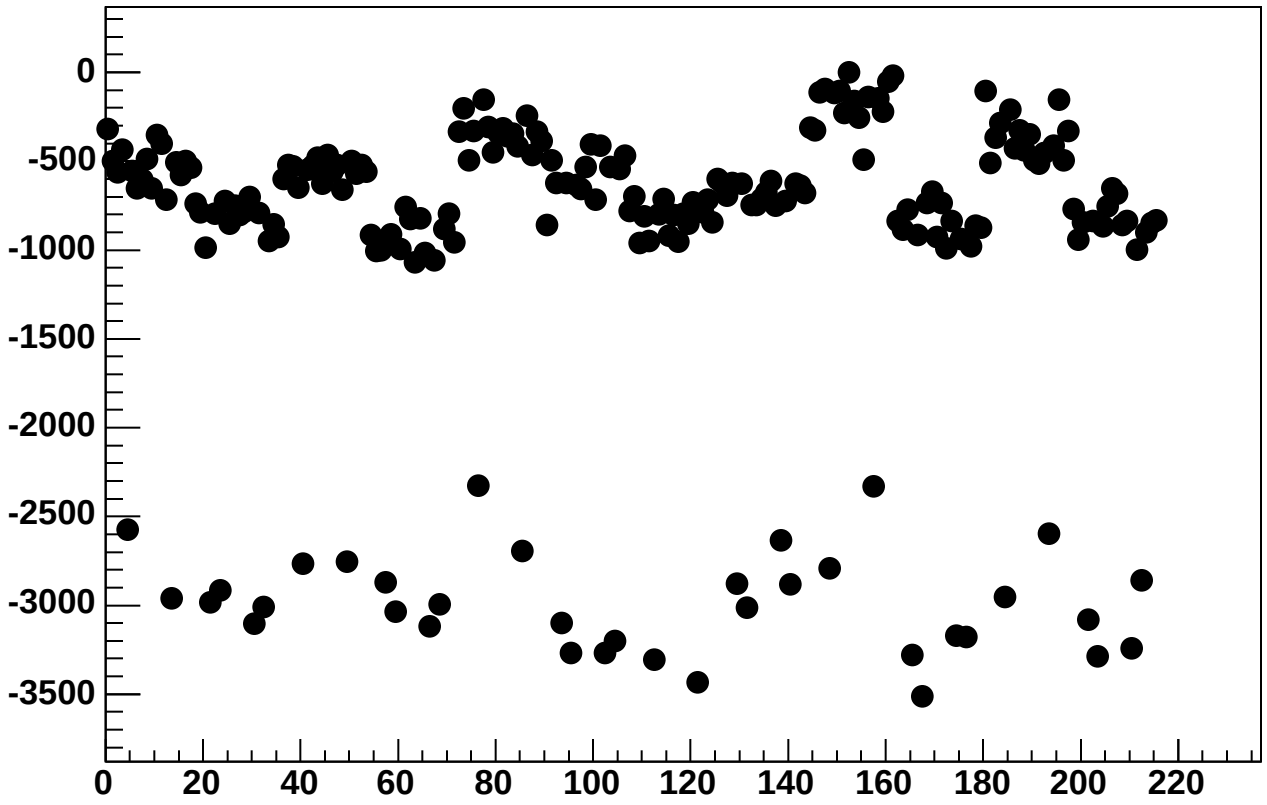
Enable 4, DAC=1600, Hold=180, ADC Mean vs 18*Chip+Chan



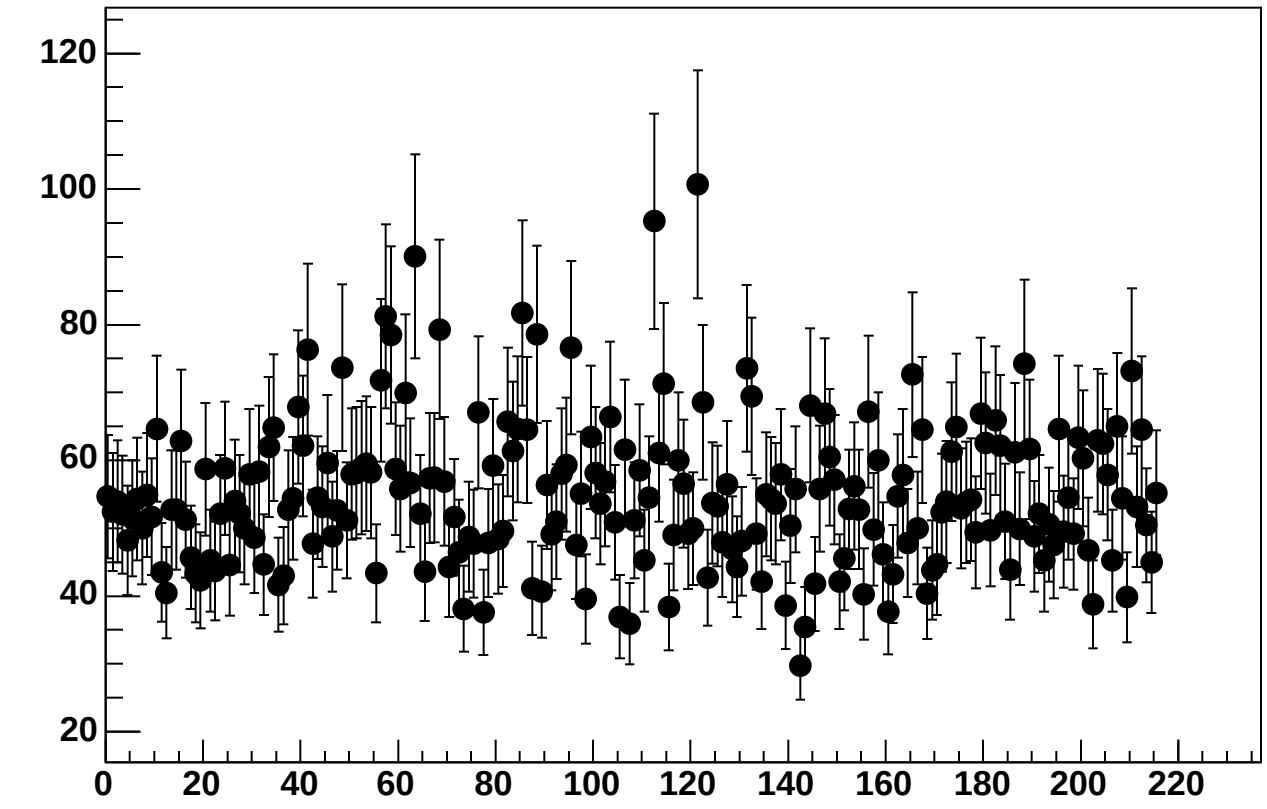
Enable 4, DAC=1600, Hold=180, ADC Noise vs 18*Chip+Chan



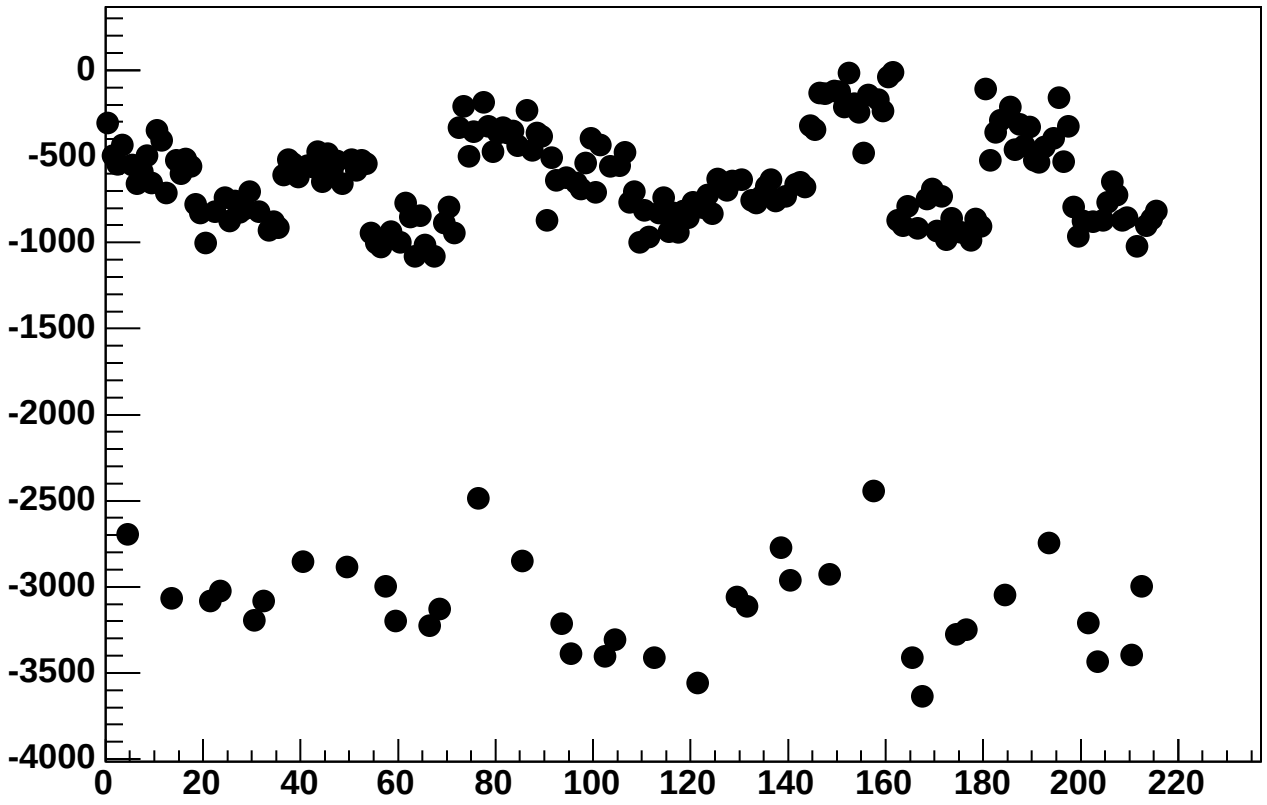
Enable 4, DAC=1600, Hold=185, ADC Mean vs 18*Chip+Chan



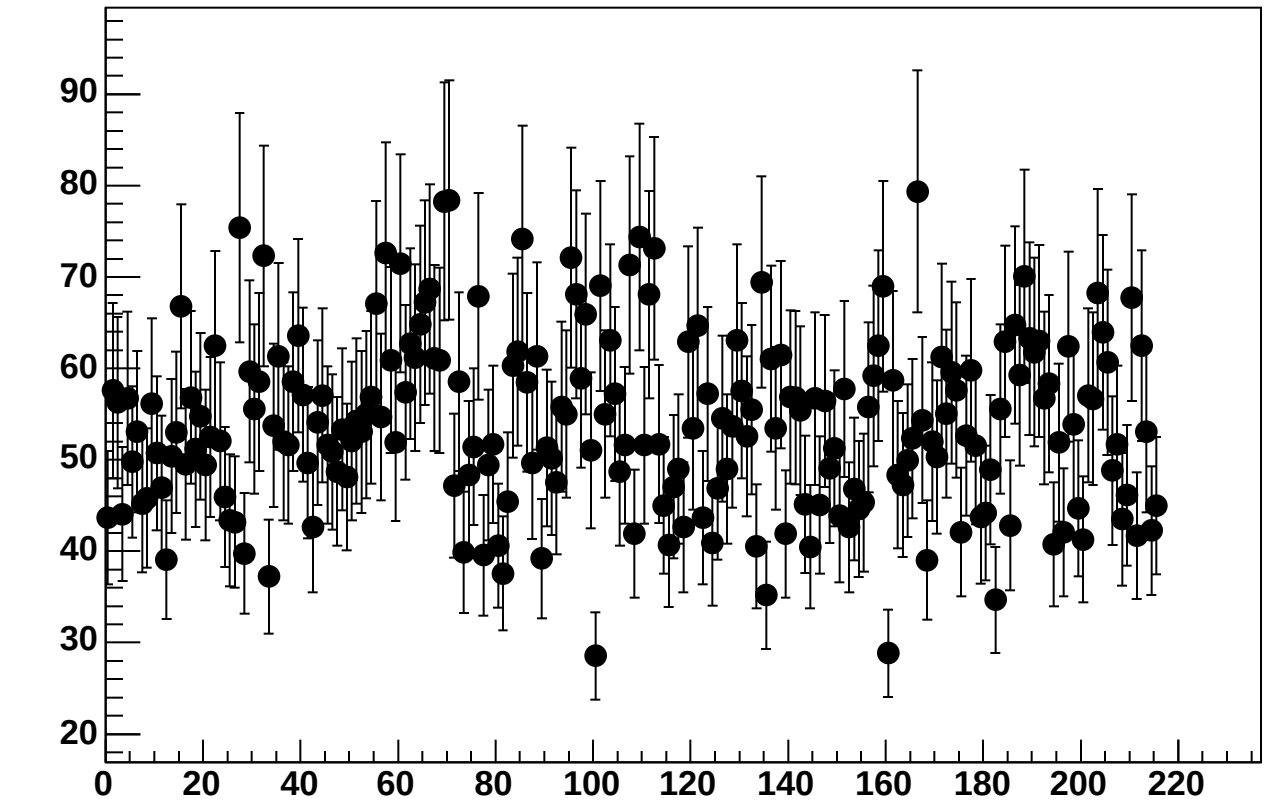
Enable 4, DAC=1600, Hold=185, ADC Noise vs 18*Chip+Chan



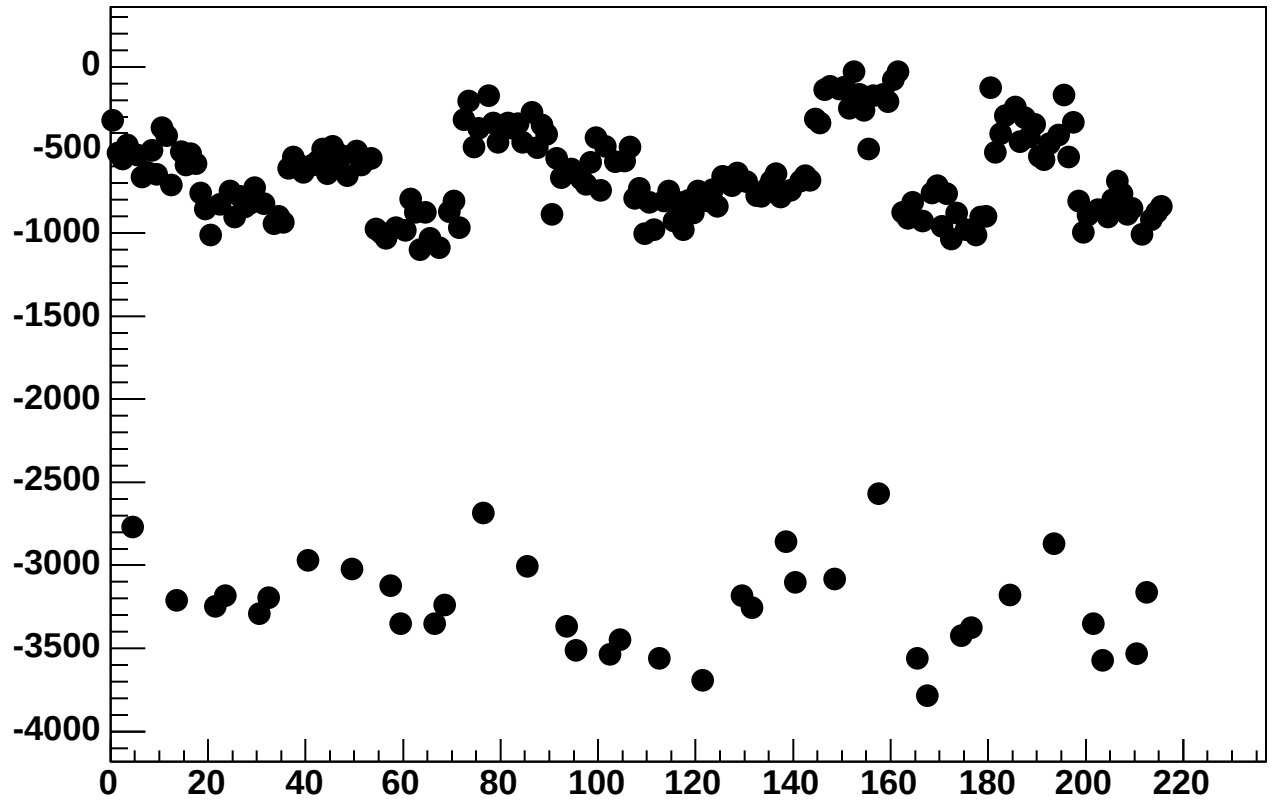
Enable 4, DAC=1600, Hold=190, ADC Mean vs 18*Chip+Chan



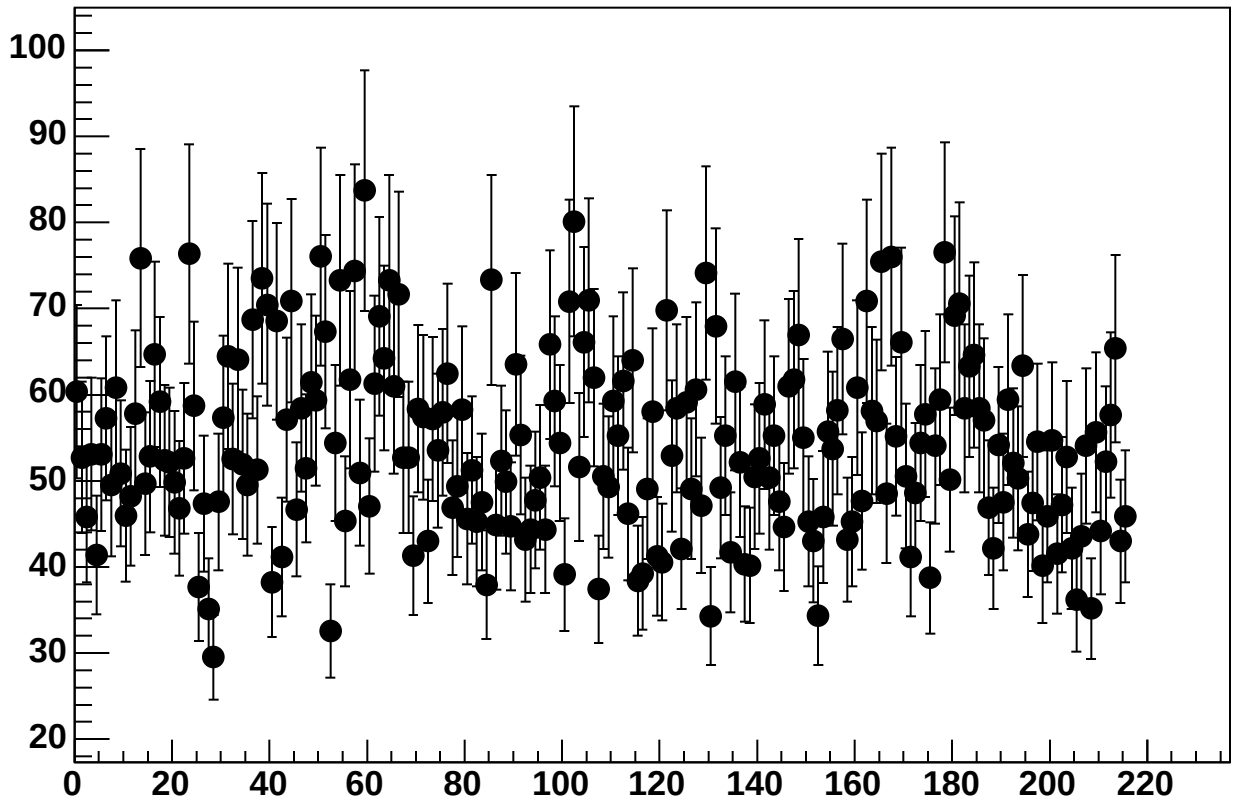
Enable 4, DAC=1600, Hold=190, ADC Noise vs 18*Chip+Chan



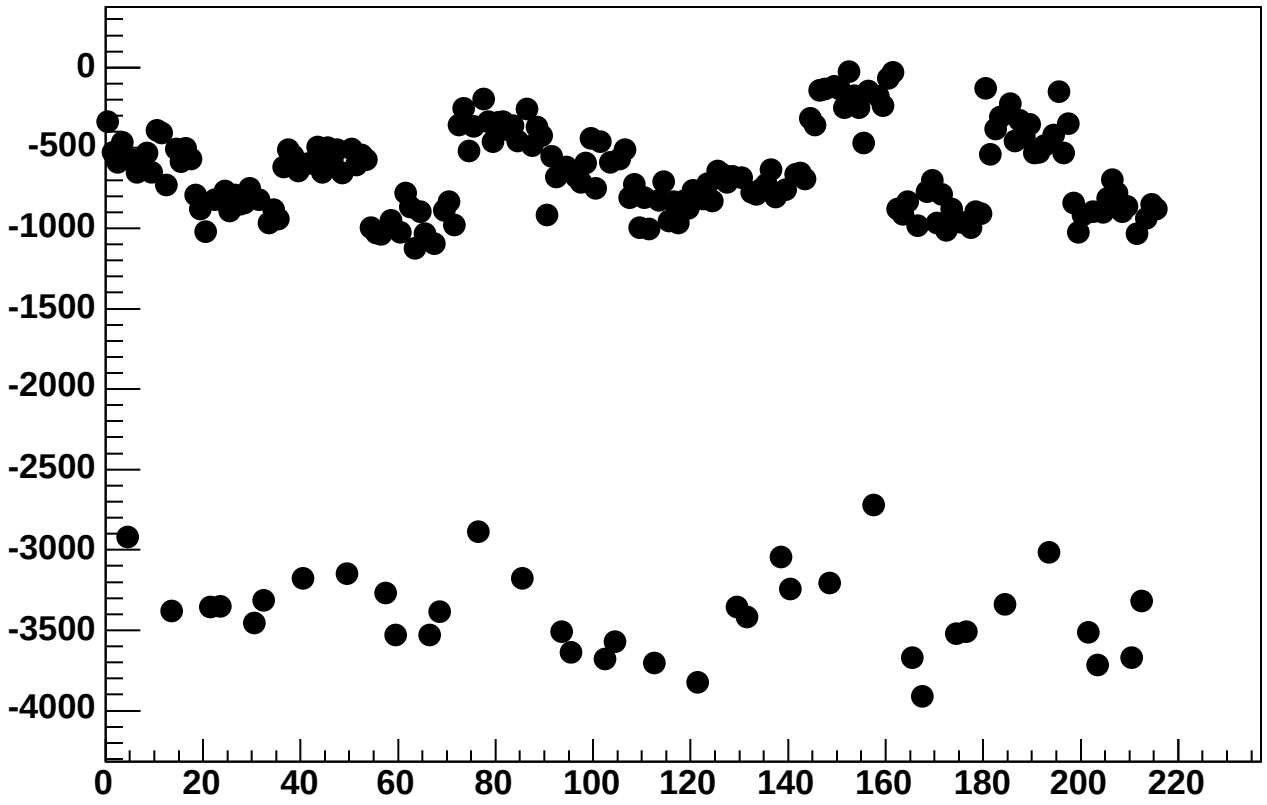
Enable 4, DAC=1600, Hold=195, ADC Mean vs 18*Chip+Chan



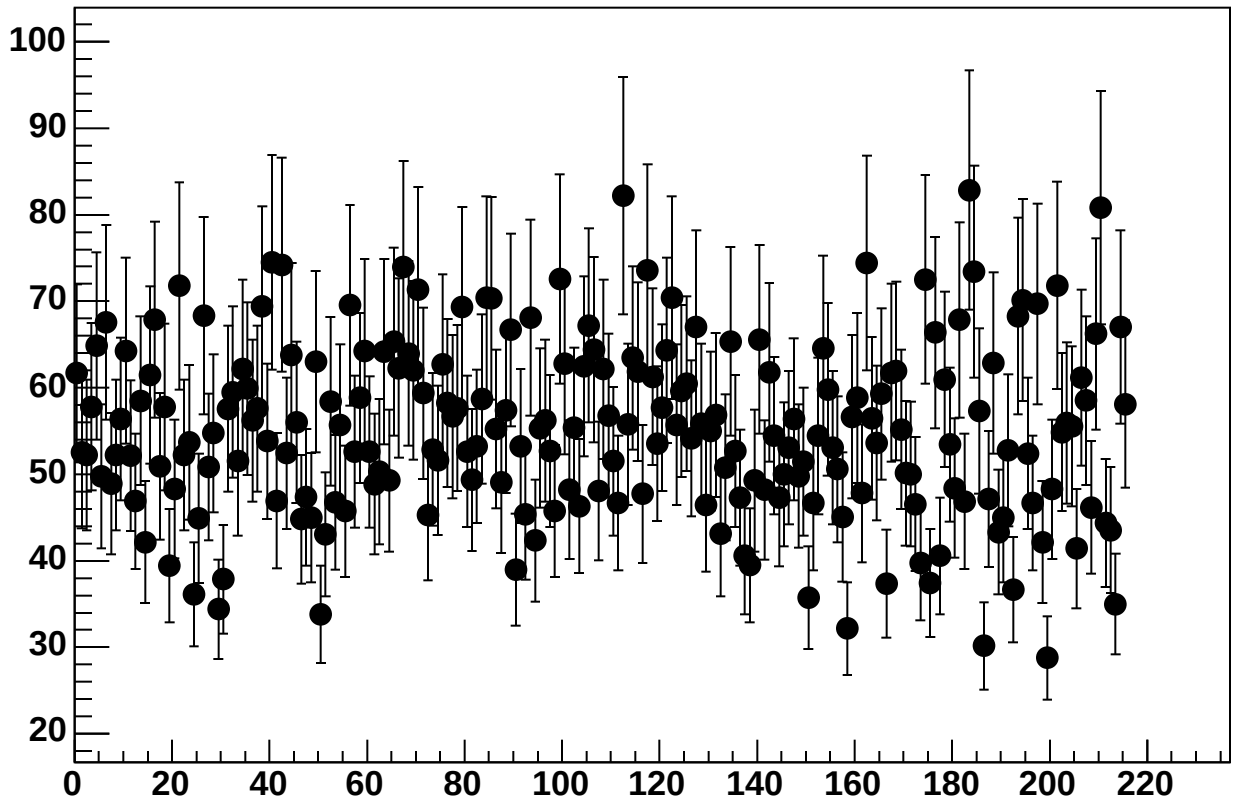
Enable 4, DAC=1600, Hold=195, ADC Noise vs 18*Chip+Chan



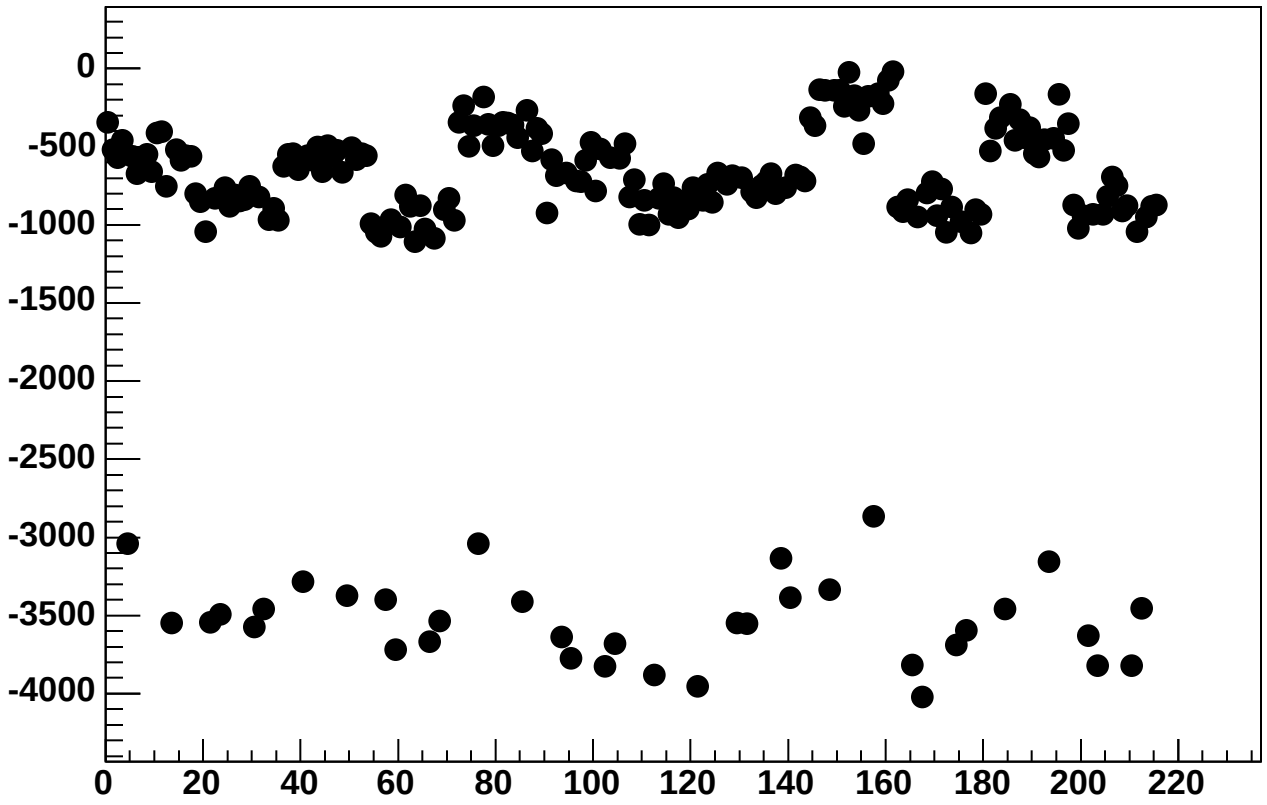
Enable 4, DAC=1600, Hold=200, ADC Mean vs 18*Chip+Chan



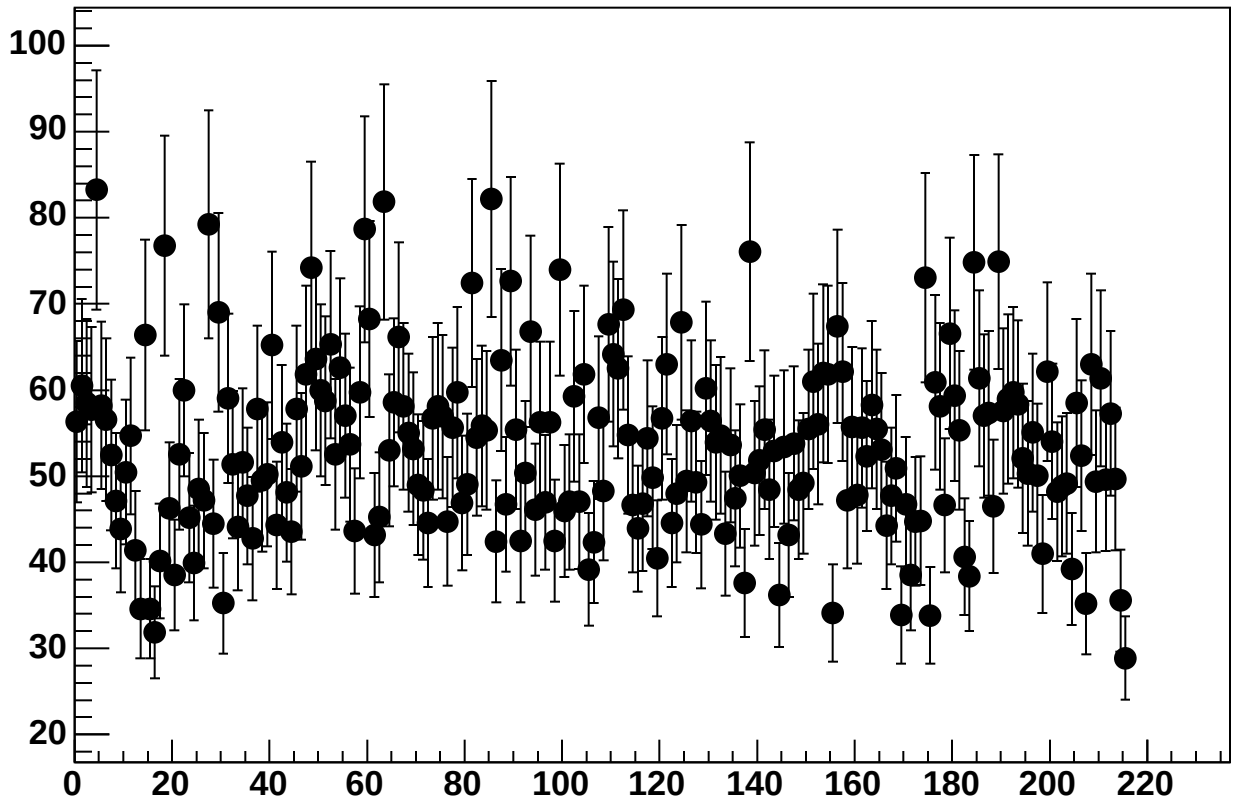
Enable 4, DAC=1600, Hold=200, ADC Noise vs 18*Chip+Chan



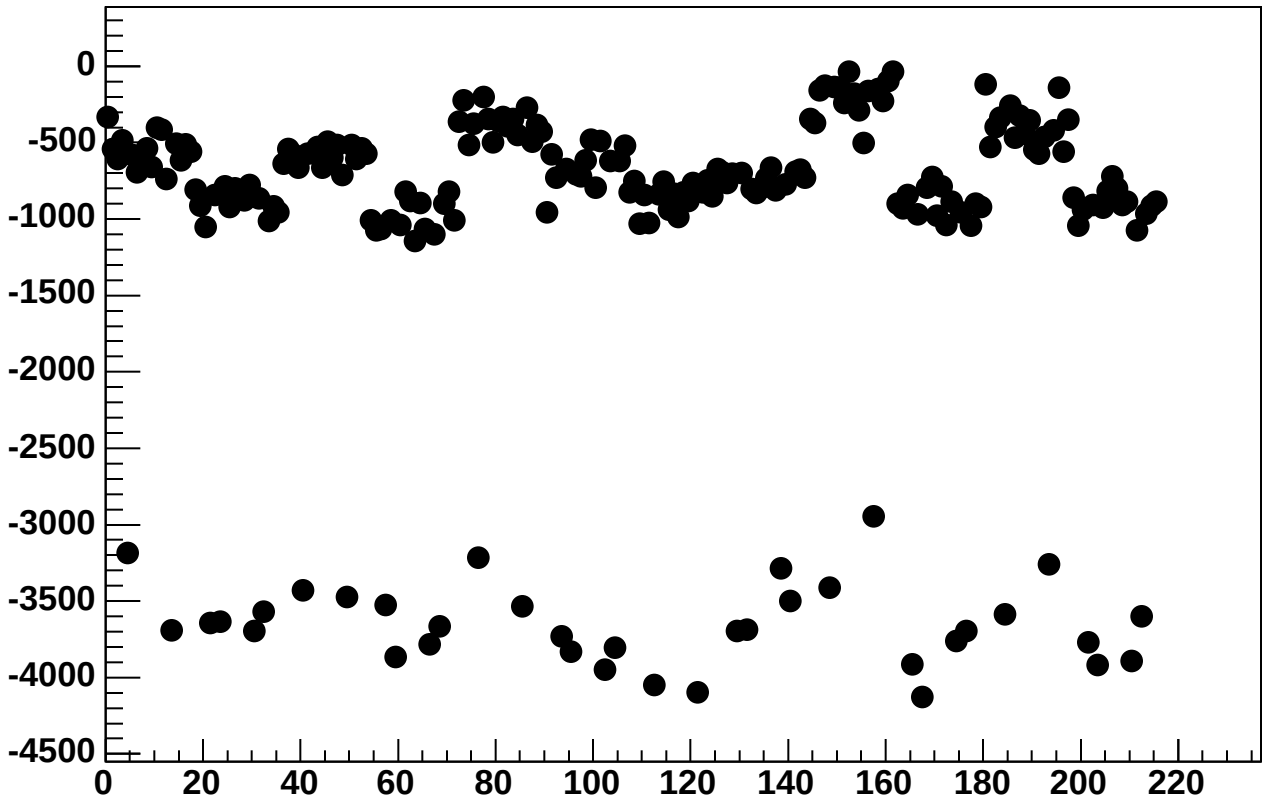
Enable 4, DAC=1600, Hold=205, ADC Mean vs 18*Chip+Chan



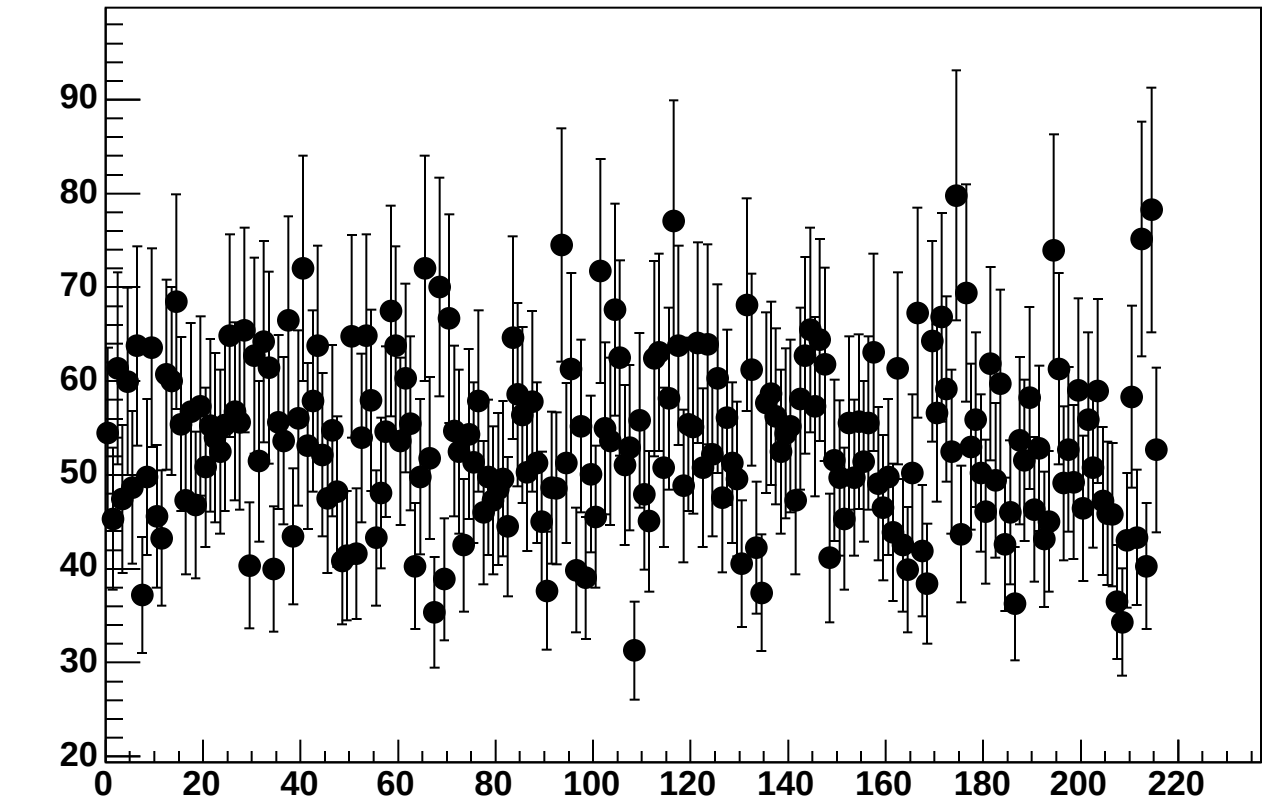
Enable 4, DAC=1600, Hold=205, ADC Noise vs 18*Chip+Chan



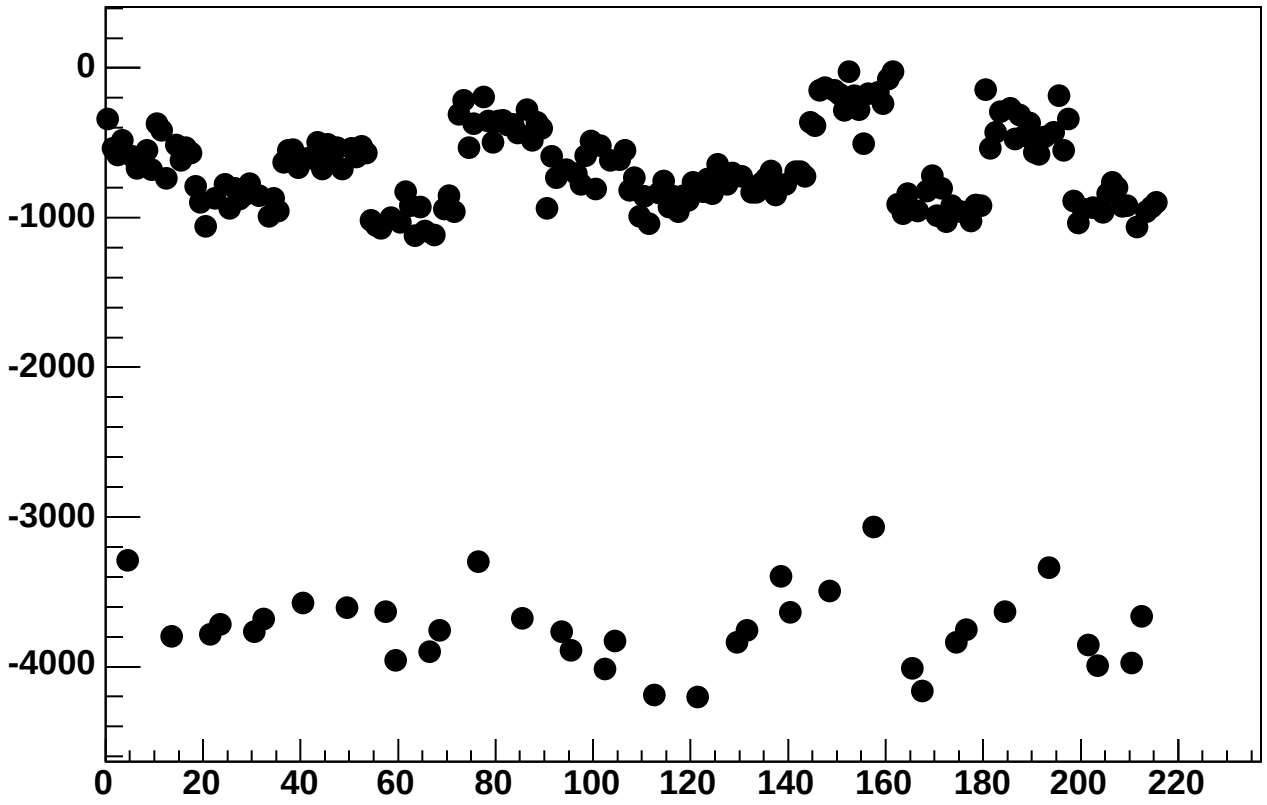
Enable 4, DAC=1600, Hold=210, ADC Mean vs 18*Chip+Chan



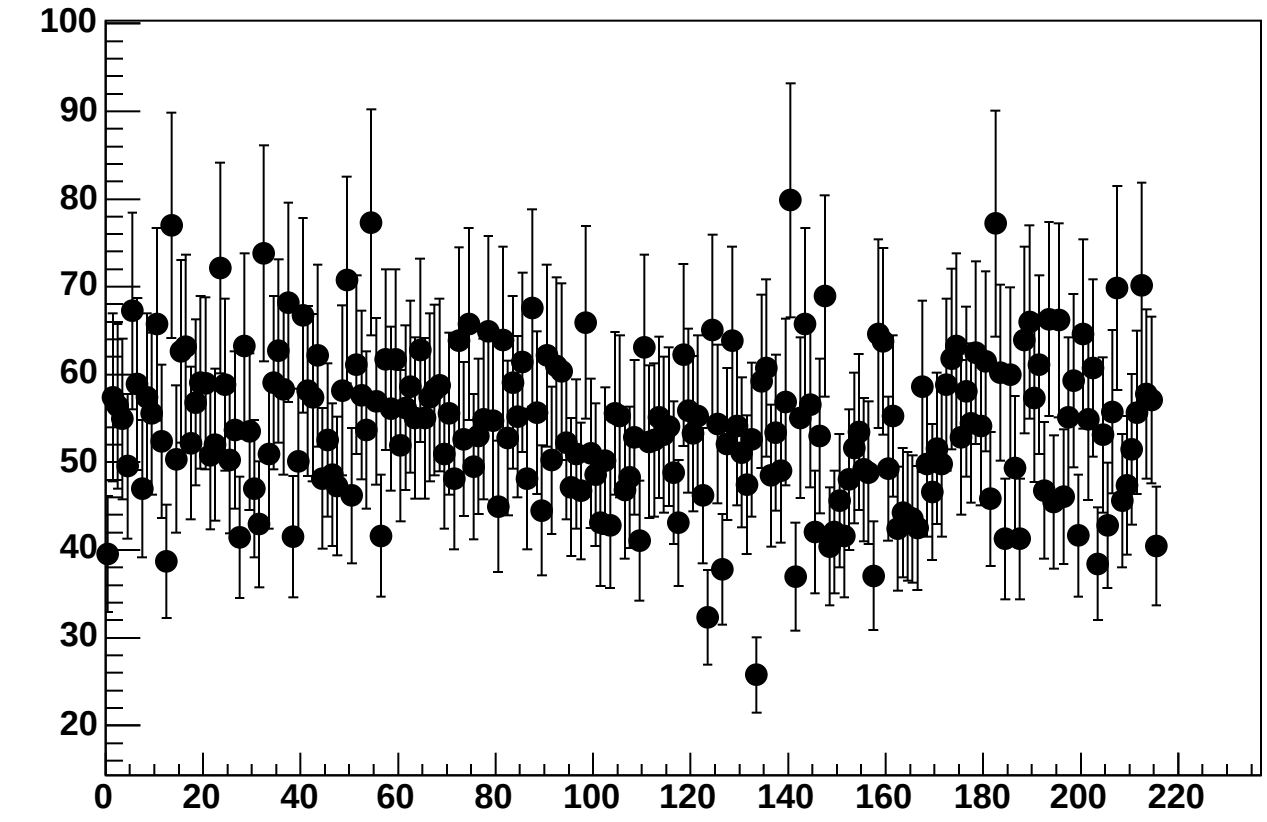
Enable 4, DAC=1600, Hold=210, ADC Noise vs 18*Chip+Chan



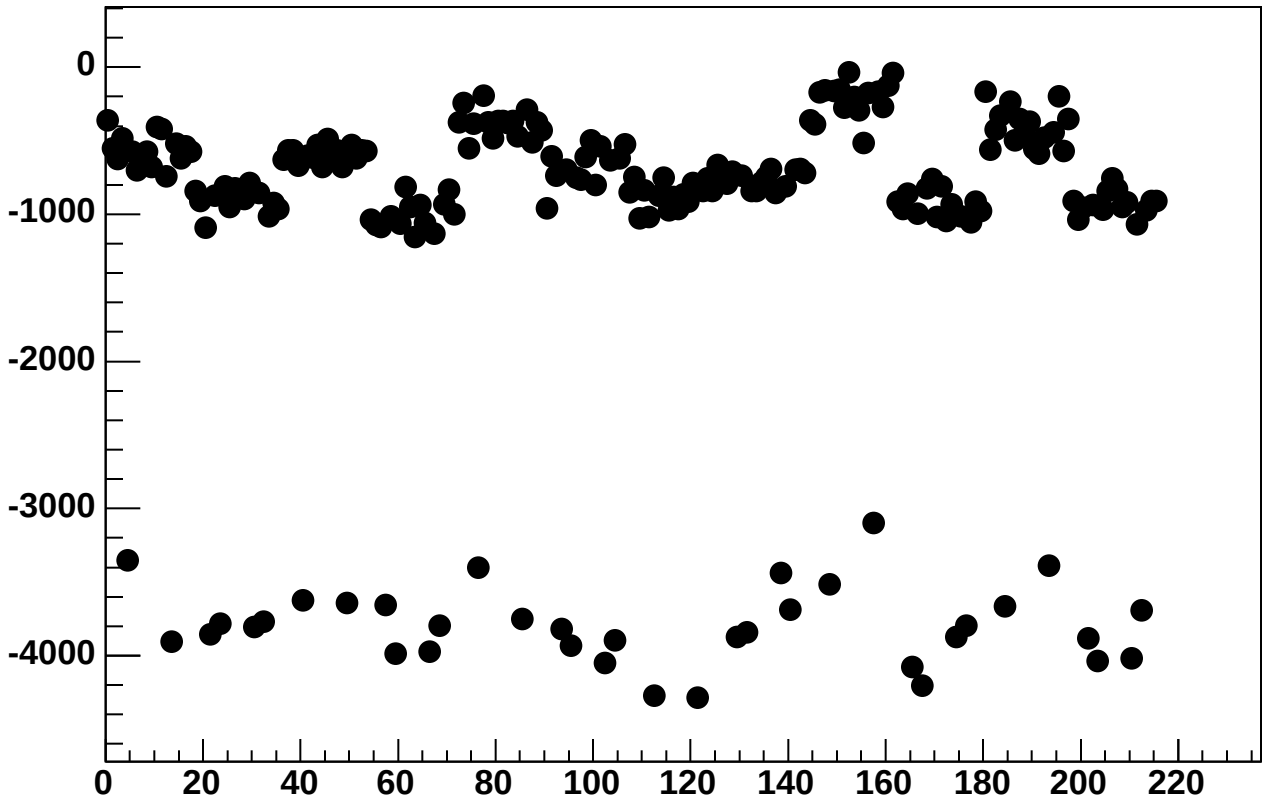
Enable 4, DAC=1600, Hold=215, ADC Mean vs 18*Chip+Chan



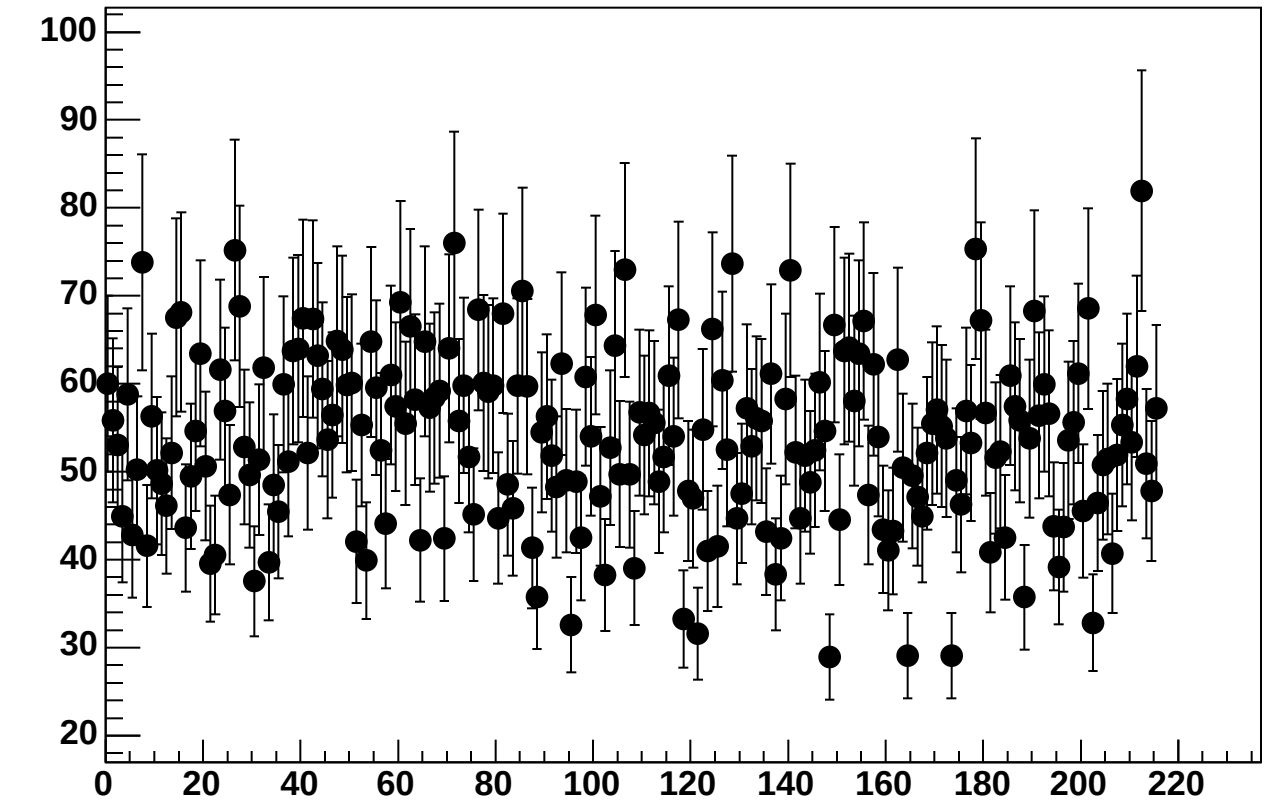
Enable 4, DAC=1600, Hold=215, ADC Noise vs 18*Chip+Chan



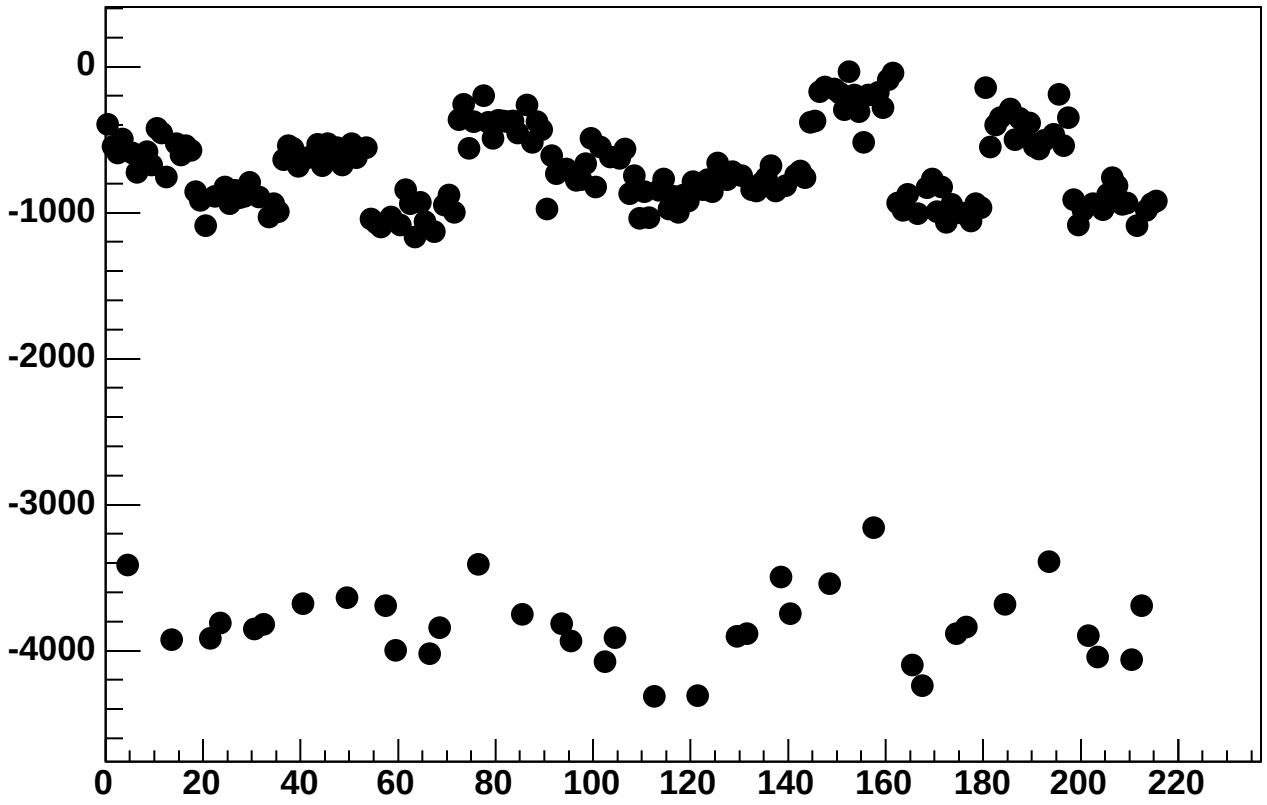
Enable 4, DAC=1600, Hold=220, ADC Mean vs 18*Chip+Chan



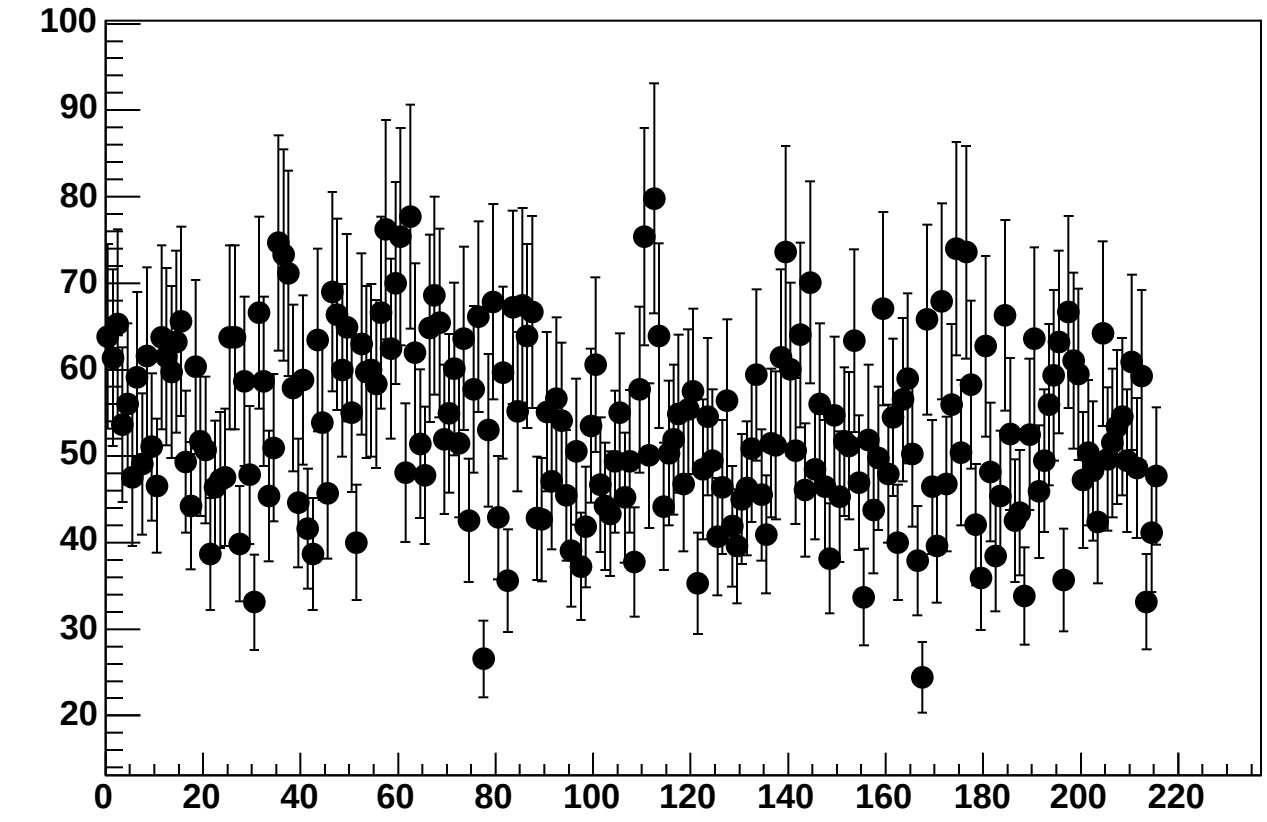
Enable 4, DAC=1600, Hold=220, ADC Noise vs 18*Chip+Chan



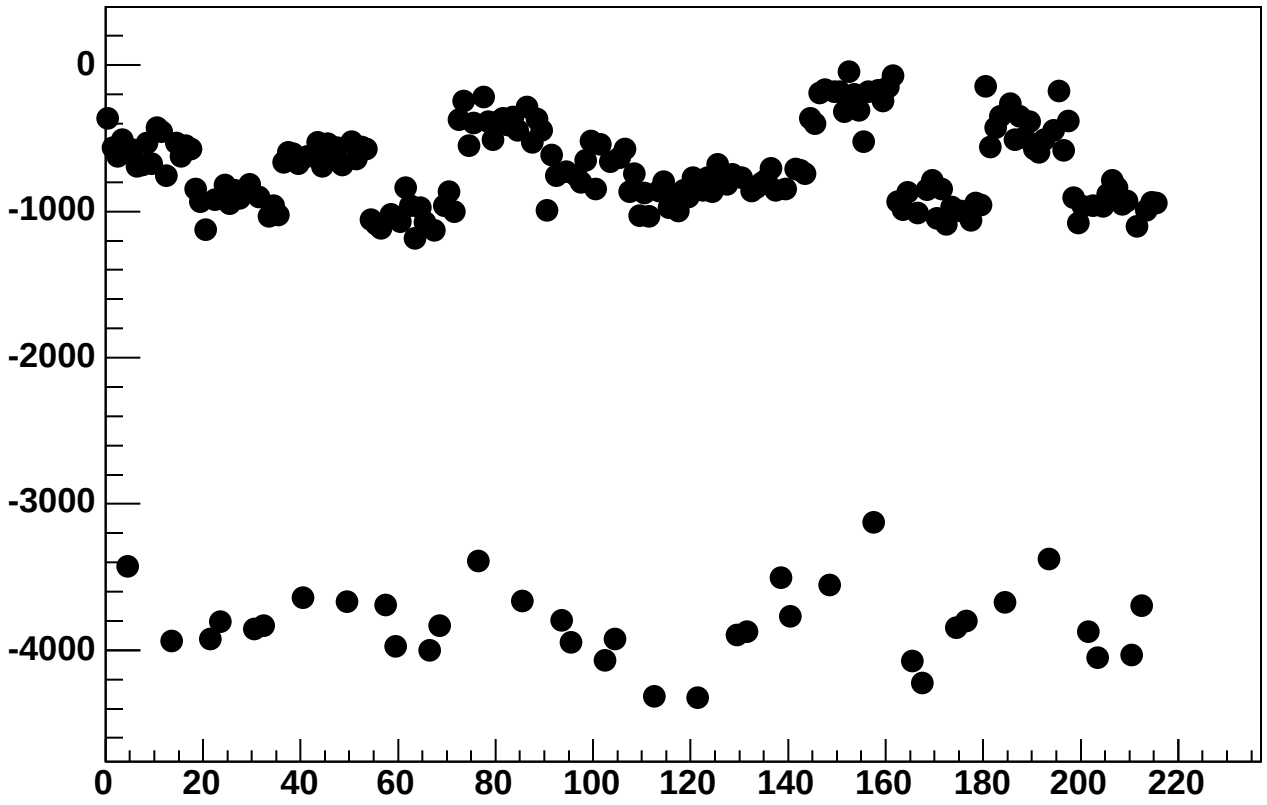
Enable 4, DAC=1600, Hold=225, ADC Mean vs 18*Chip+Chan



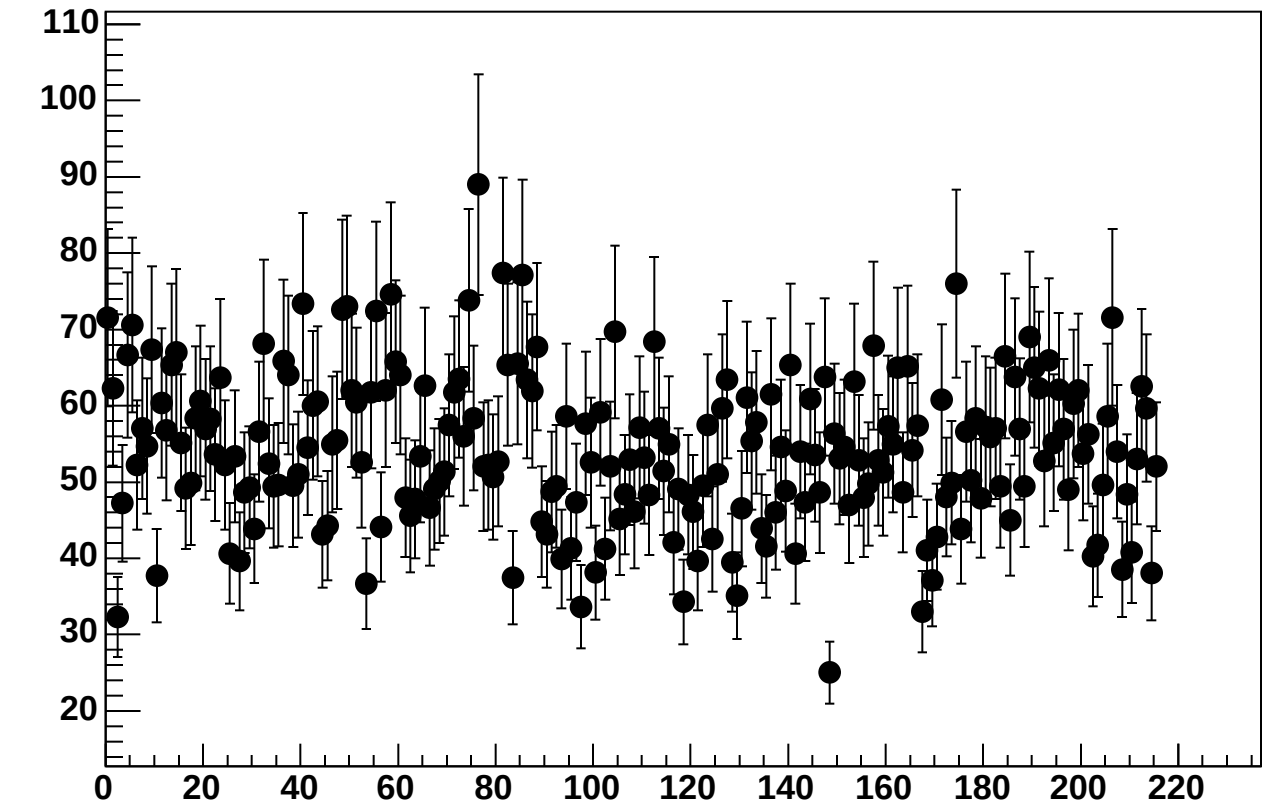
Enable 4, DAC=1600, Hold=225, ADC Noise vs 18*Chip+Chan



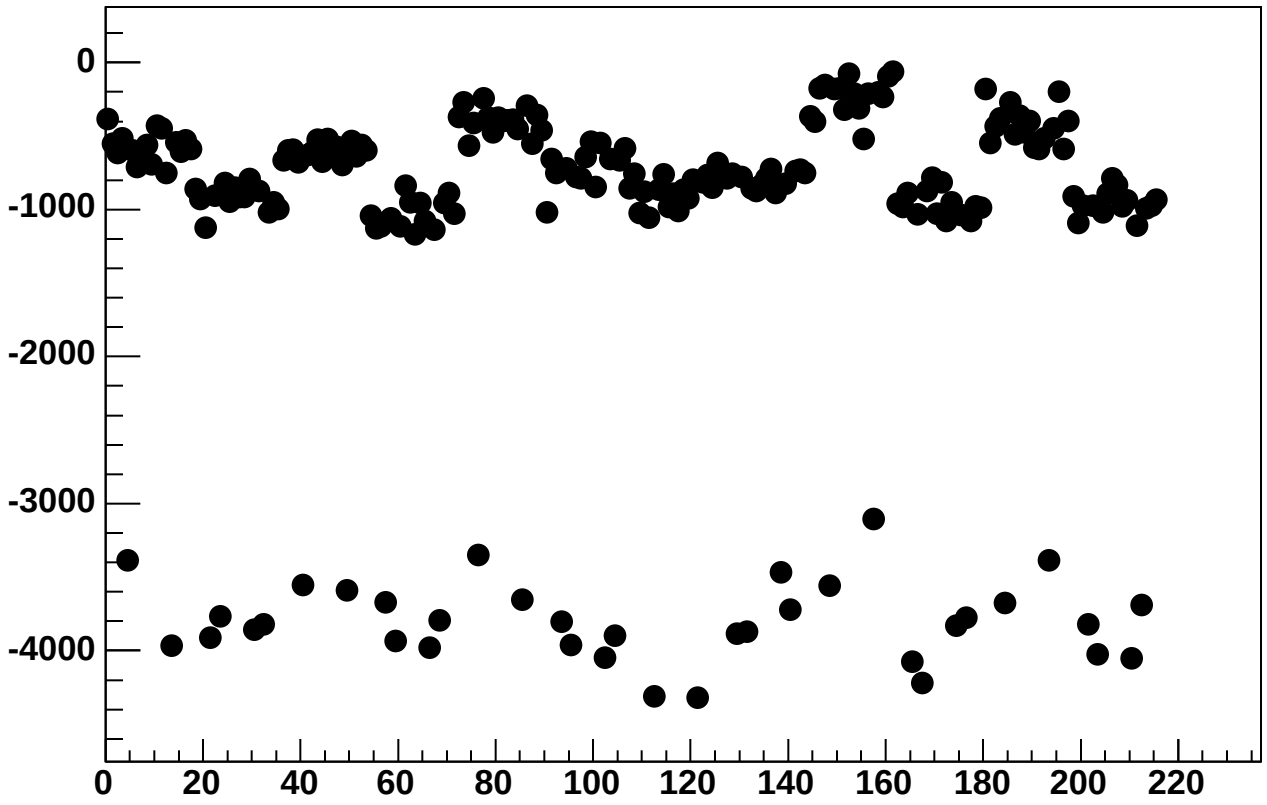
Enable 4, DAC=1600, Hold=230, ADC Mean vs 18*Chip+Chan



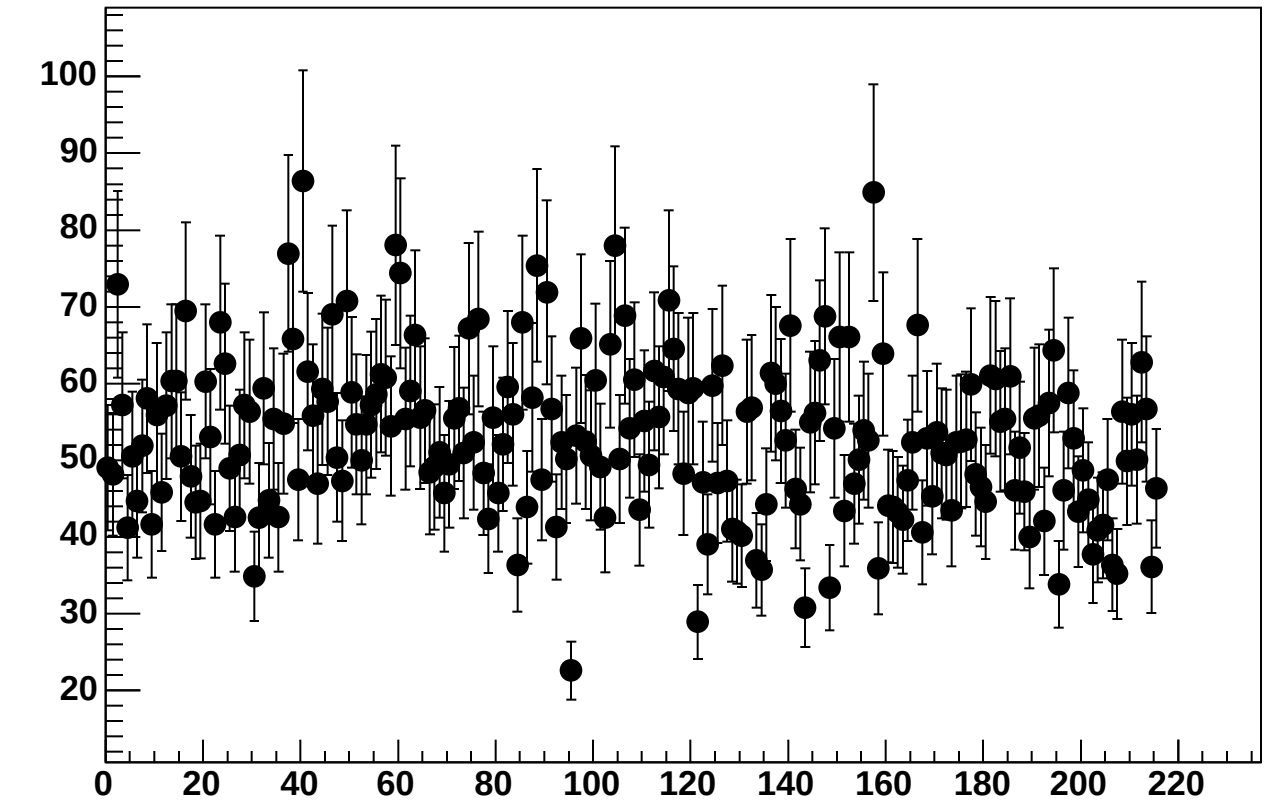
Enable 4, DAC=1600, Hold=230, ADC Noise vs 18*Chip+Chan



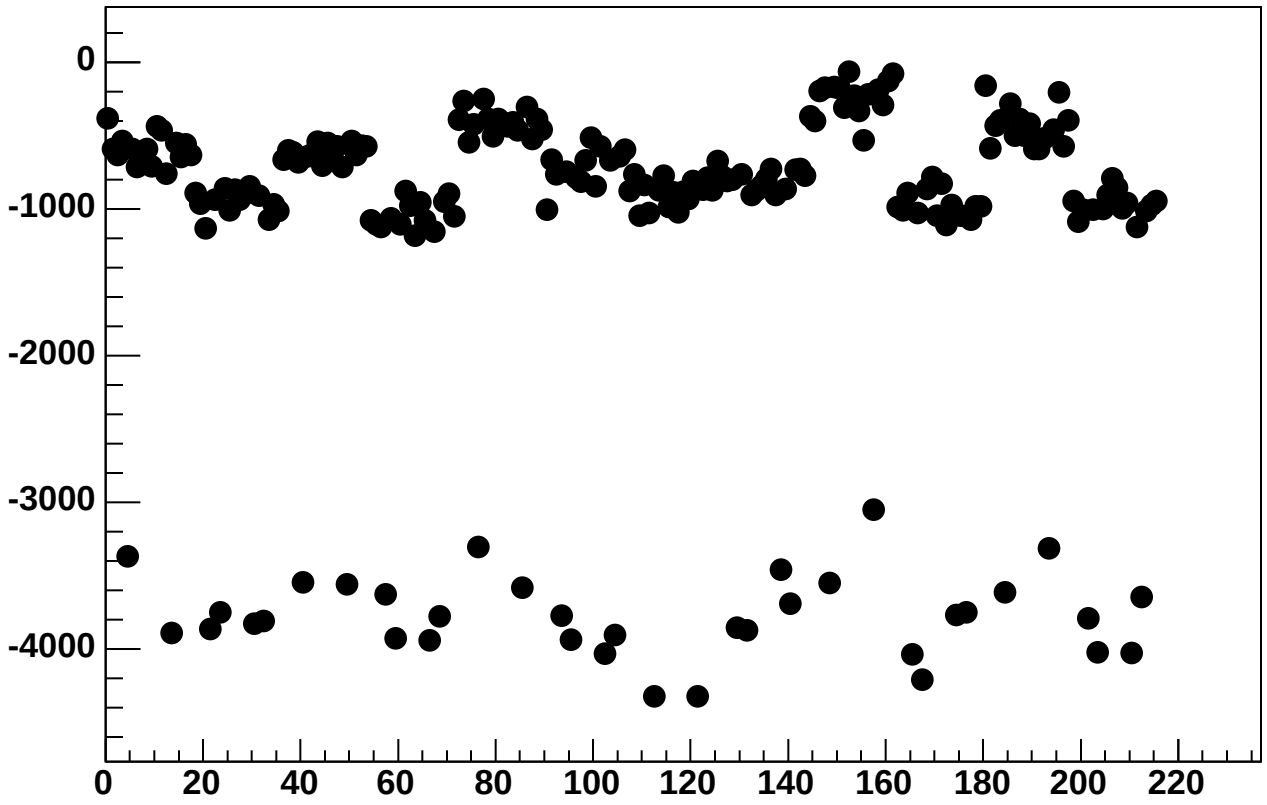
Enable 4, DAC=1600, Hold=235, ADC Mean vs 18*Chip+Chan



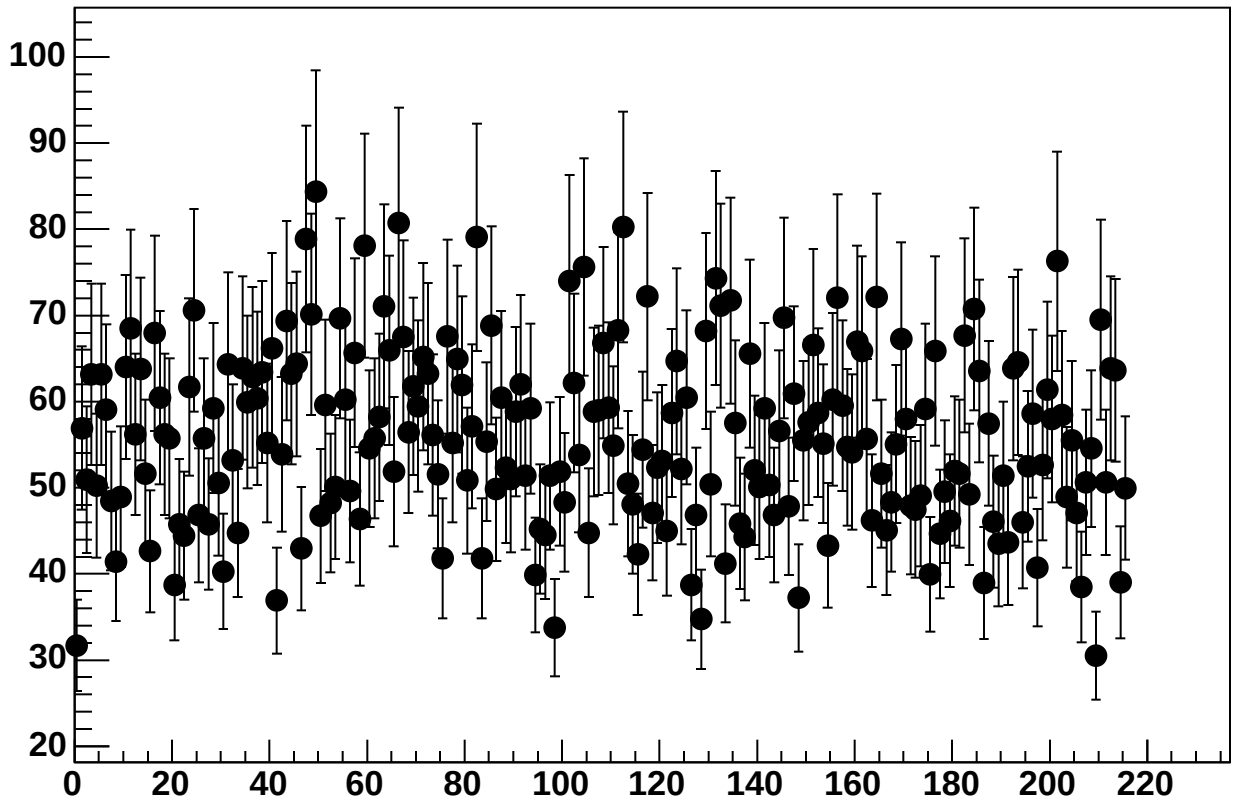
Enable 4, DAC=1600, Hold=235, ADC Noise vs 18*Chip+Chan



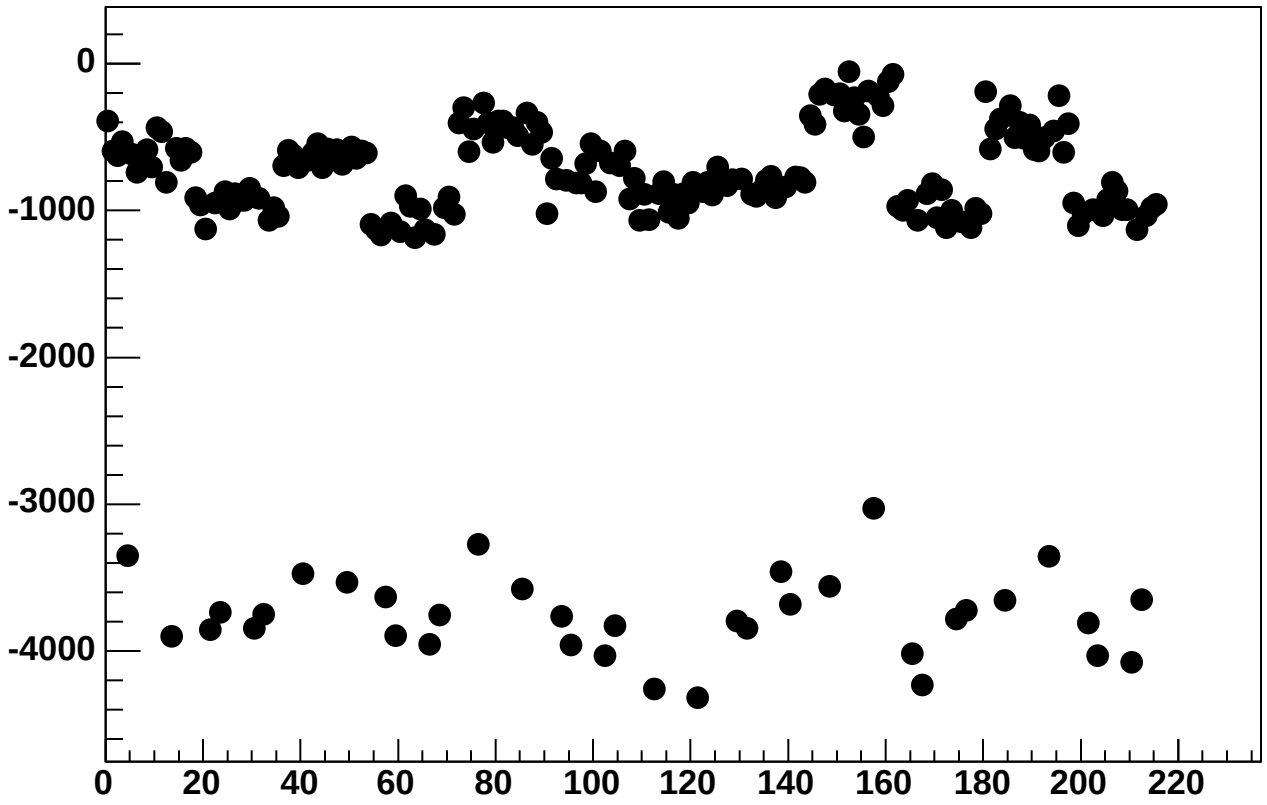
Enable 4, DAC=1600, Hold=240, ADC Mean vs 18*Chip+Chan



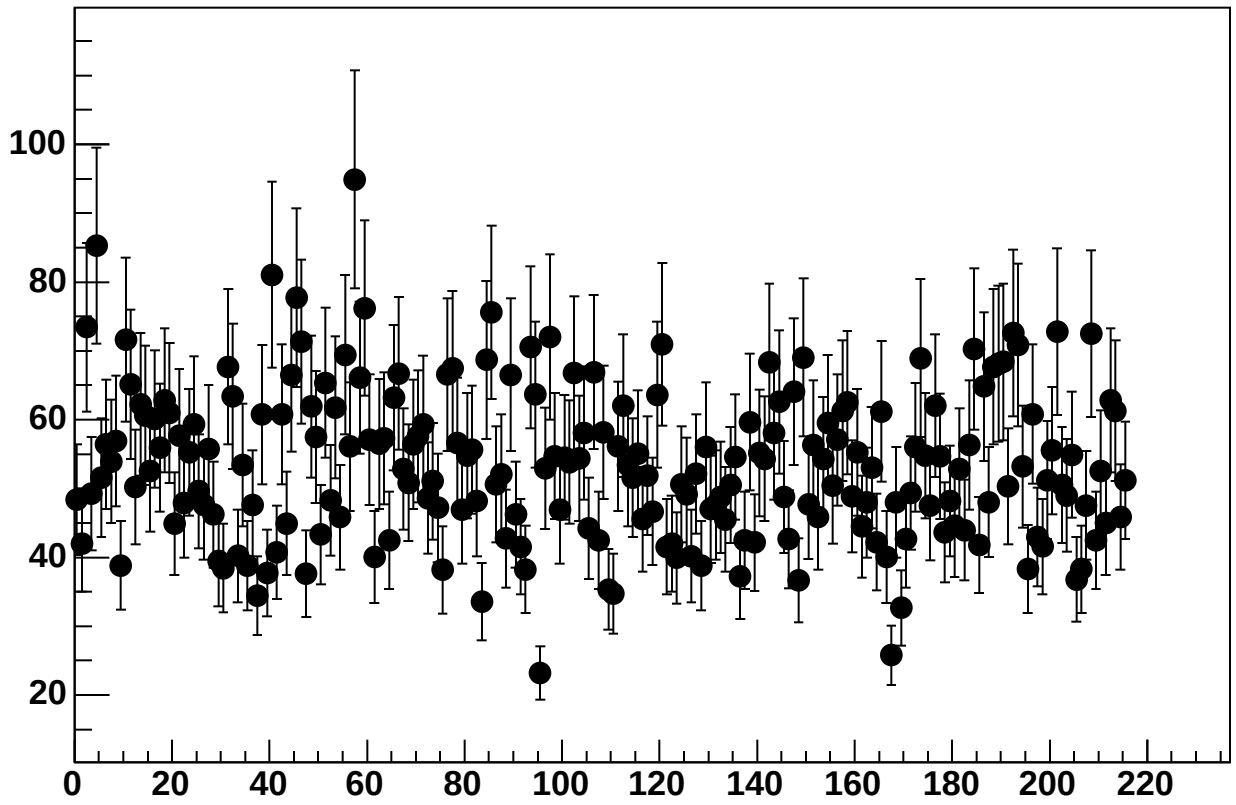
Enable 4, DAC=1600, Hold=240, ADC Noise vs 18*Chip+Chan



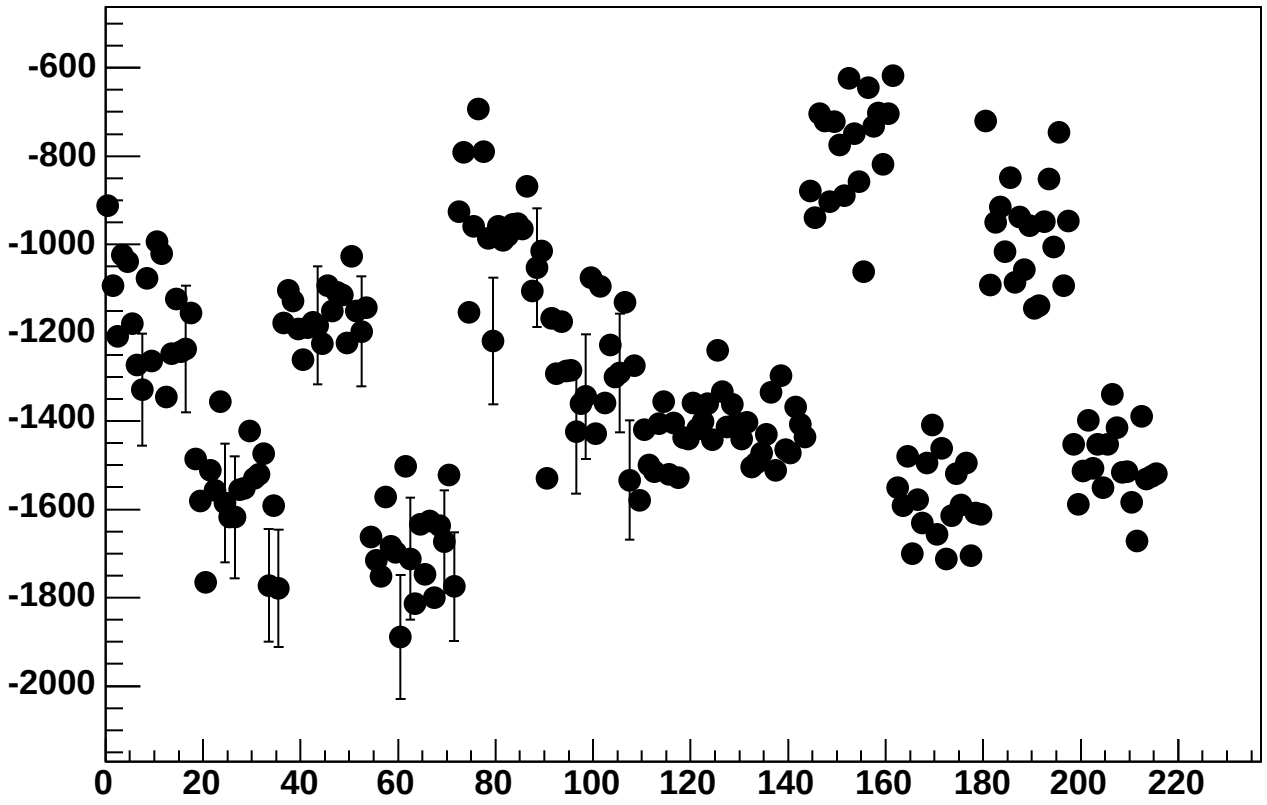
Enable 4, DAC=1600, Hold=245, ADC Mean vs 18*Chip+Chan



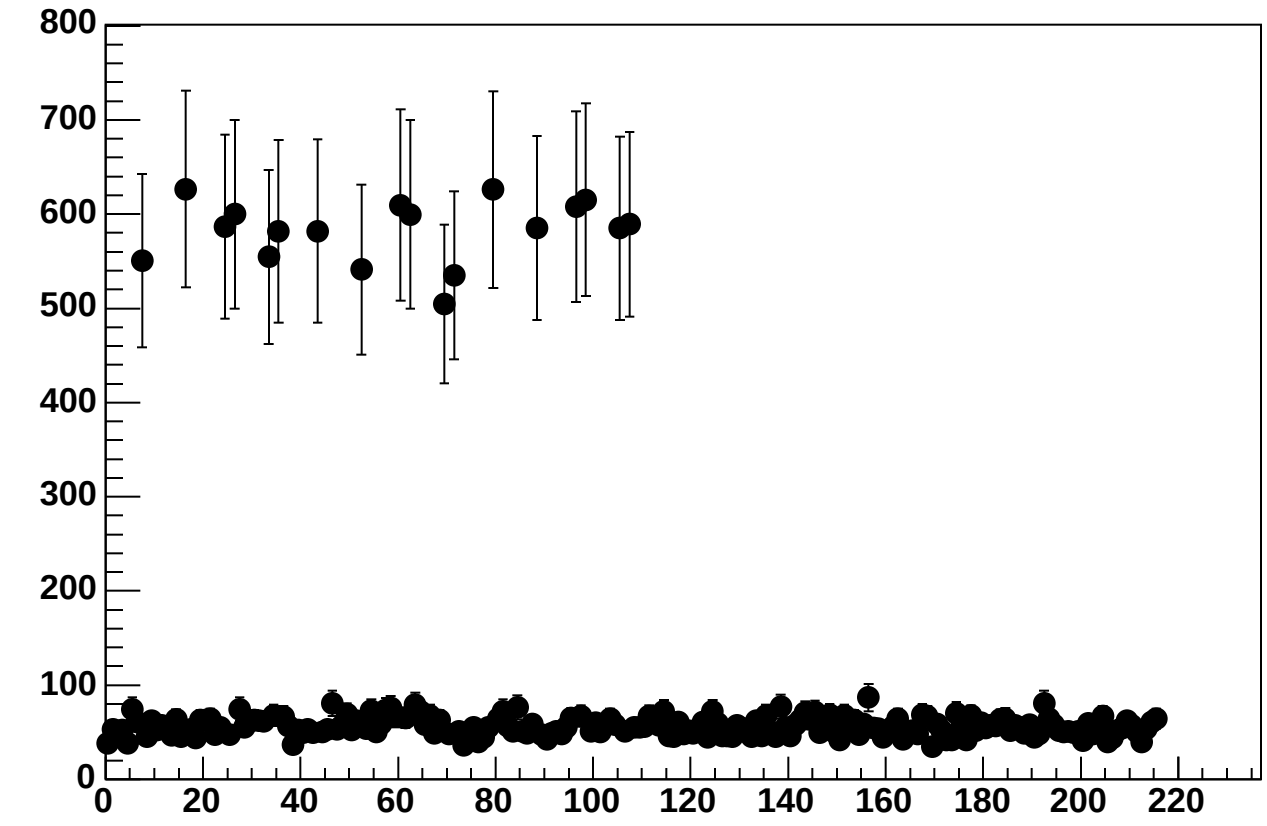
Enable 4, DAC=1600, Hold=245, ADC Noise vs 18*Chip+Chan



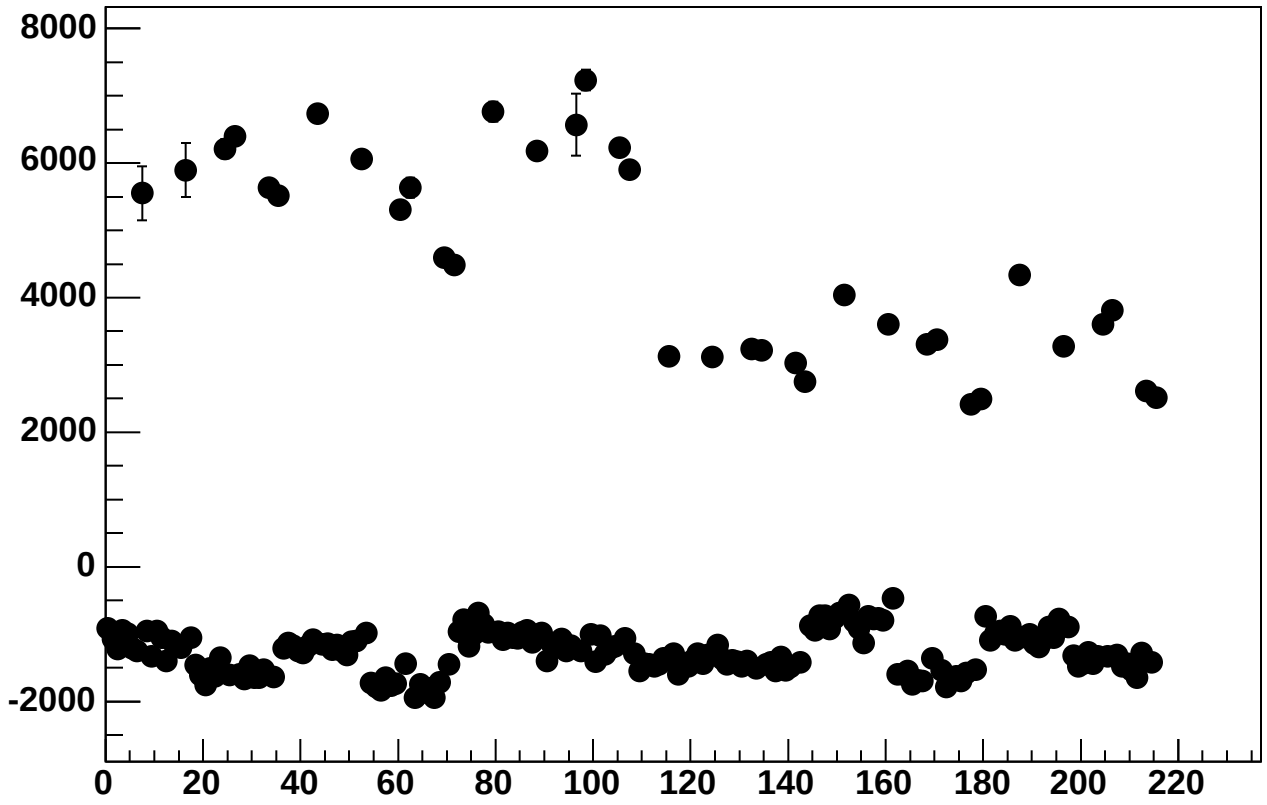
Enable 5, DAC=1600, Hold=0, ADC Mean vs 18*Chip+Chan



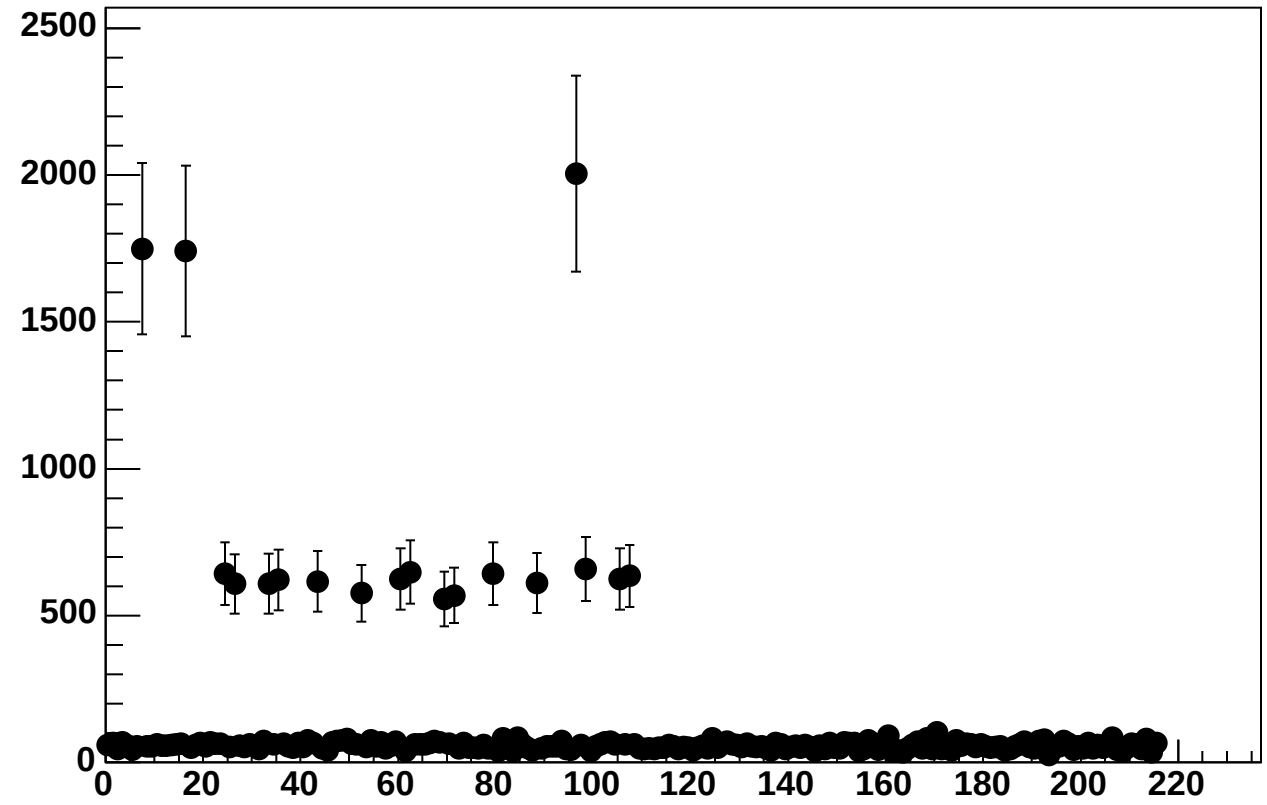
Enable 5, DAC=1600, Hold=0, ADC Noise vs 18*Chip+Chan



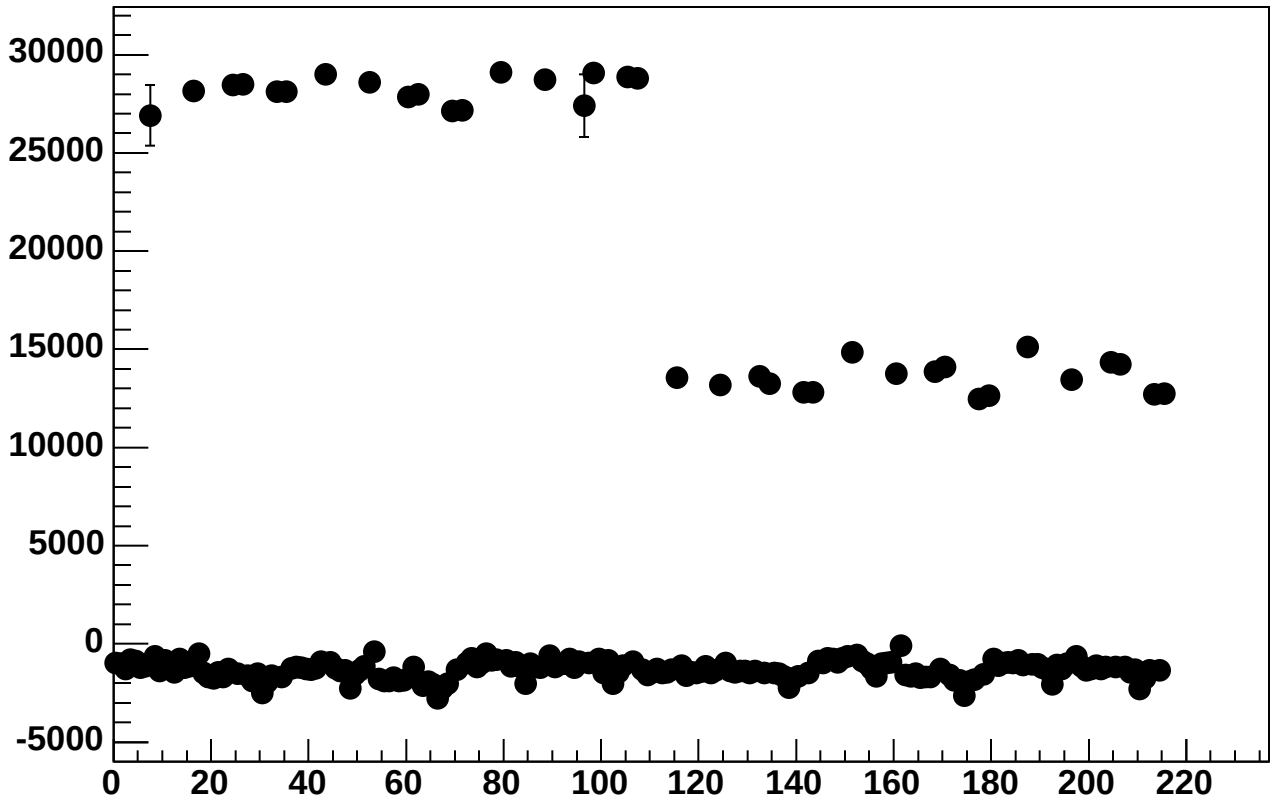
Enable 5, DAC=1600, Hold=5, ADC Mean vs 18*Chip+Chan



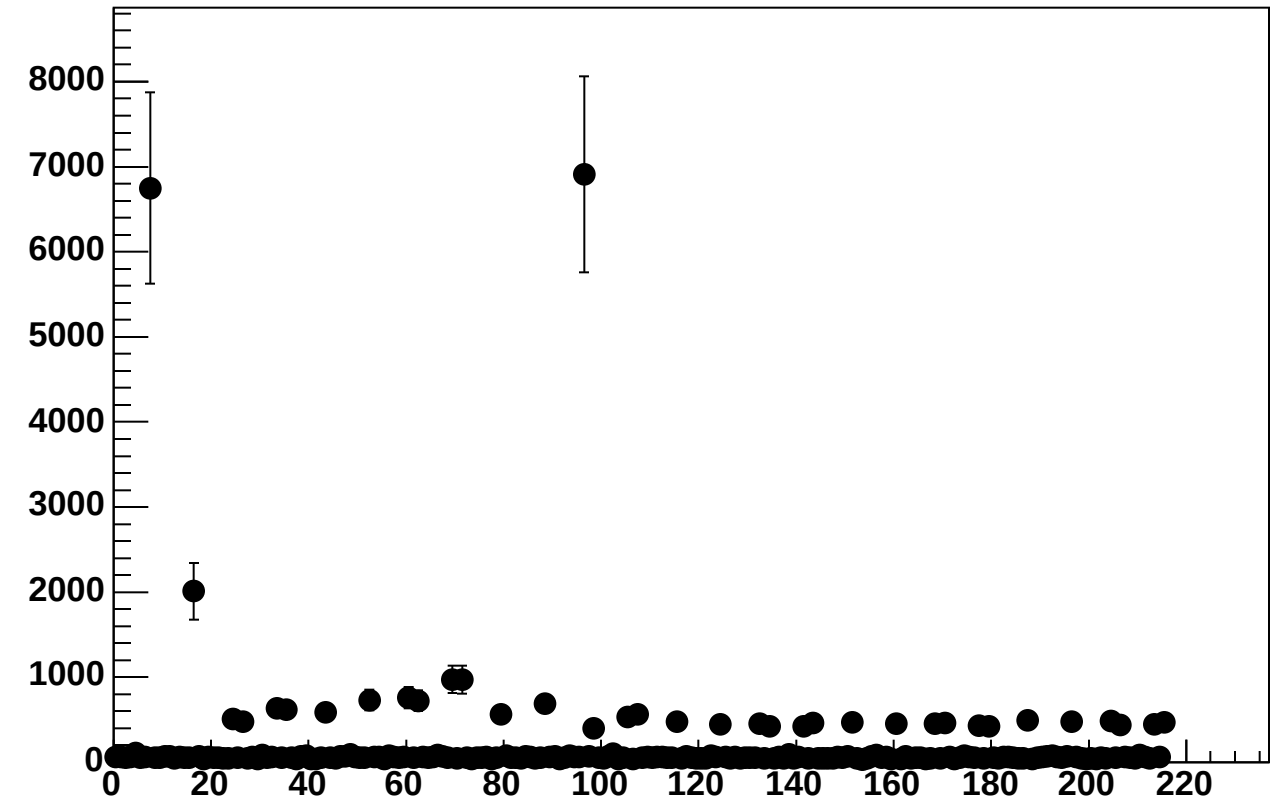
Enable 5, DAC=1600, Hold=5, ADC Noise vs 18*Chip+Chan



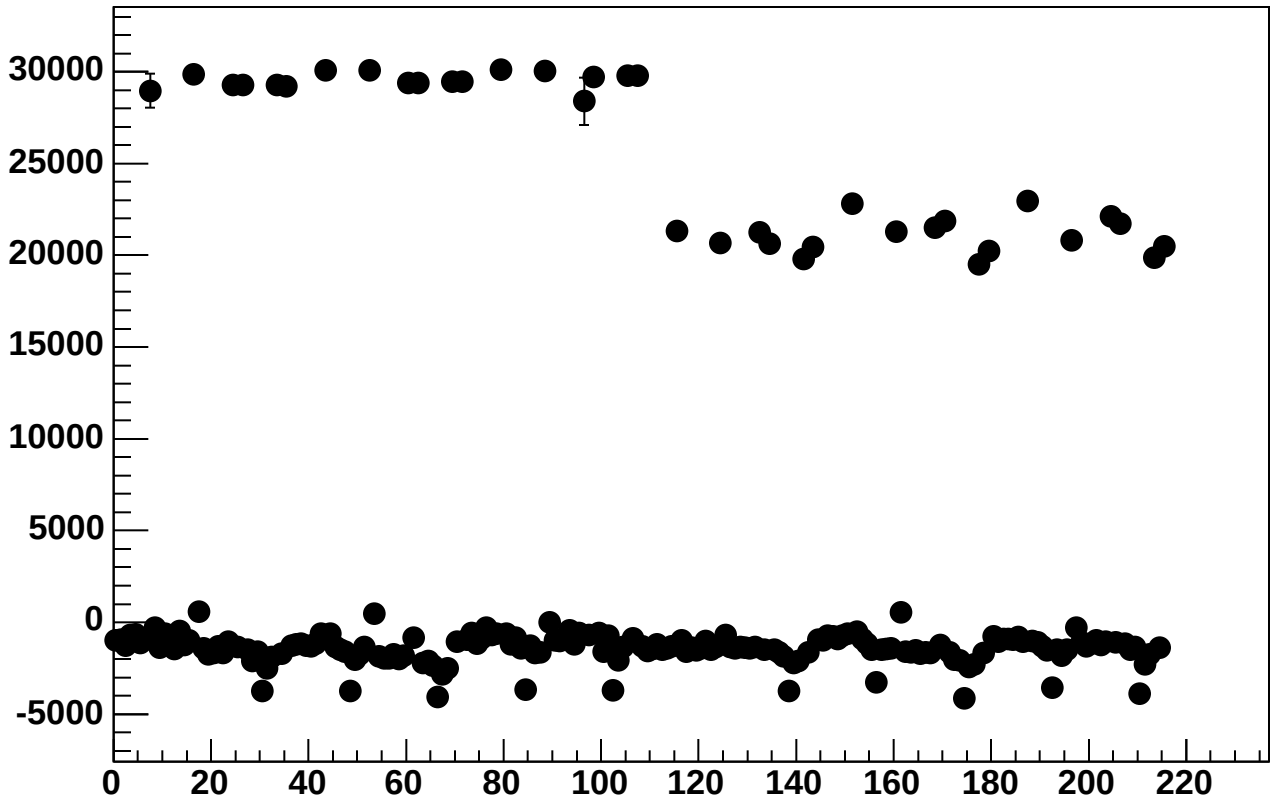
Enable 5, DAC=1600, Hold=10, ADC Mean vs 18*Chip+Chan



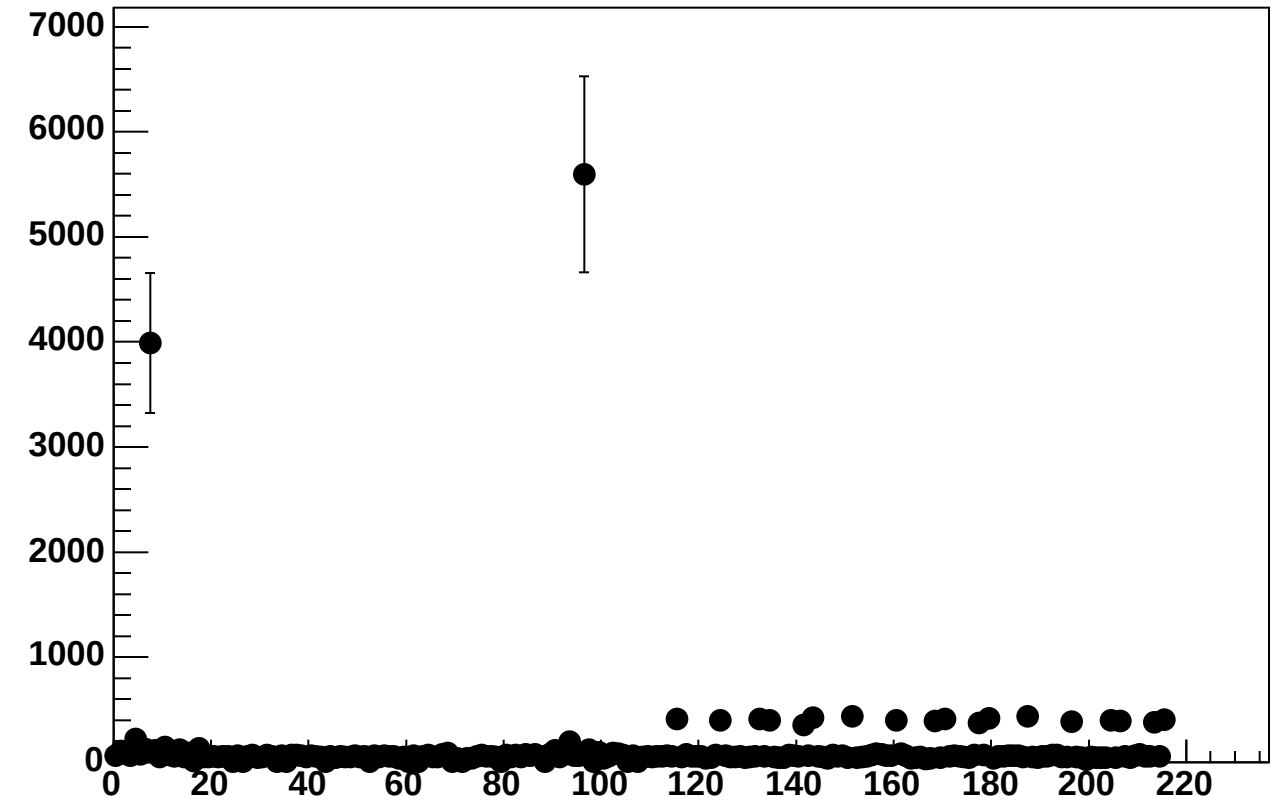
Enable 5, DAC=1600, Hold=10, ADC Noise vs 18*Chip+Chan



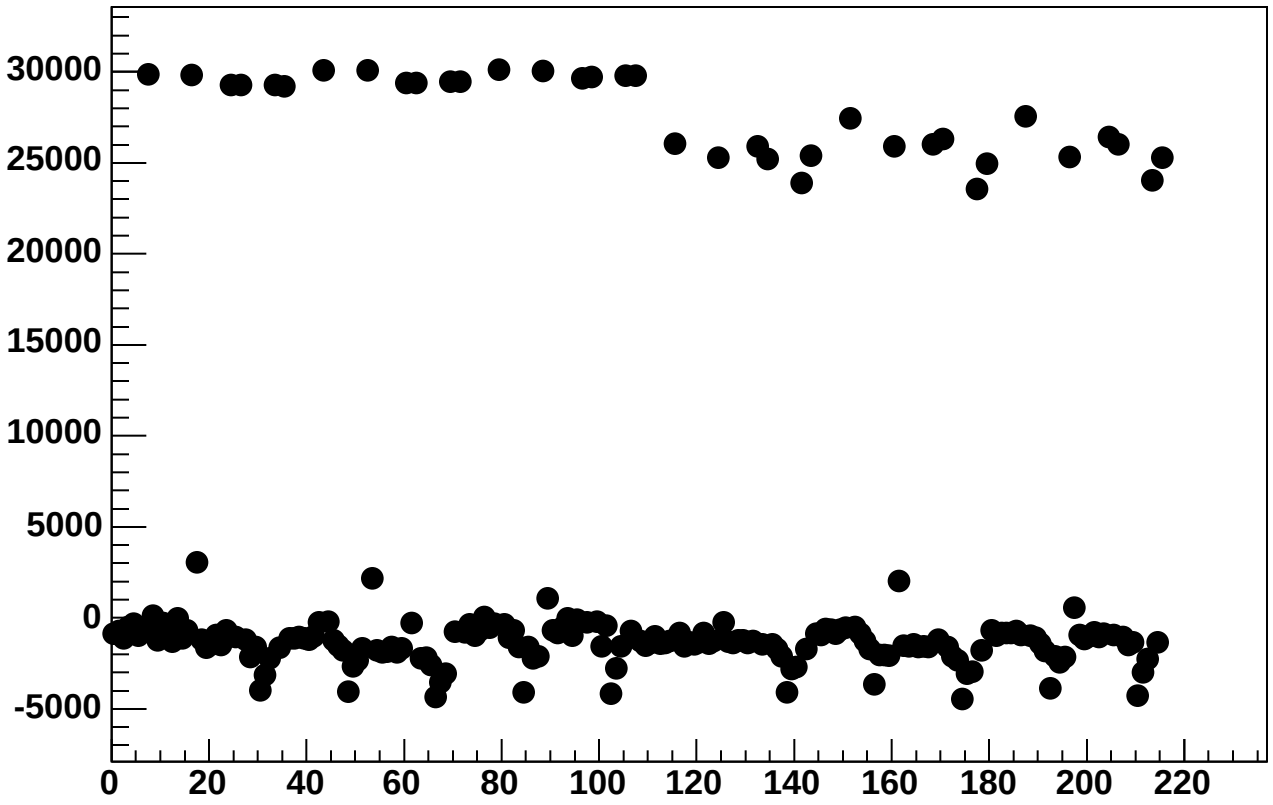
Enable 5, DAC=1600, Hold=15, ADC Mean vs 18*Chip+Chan



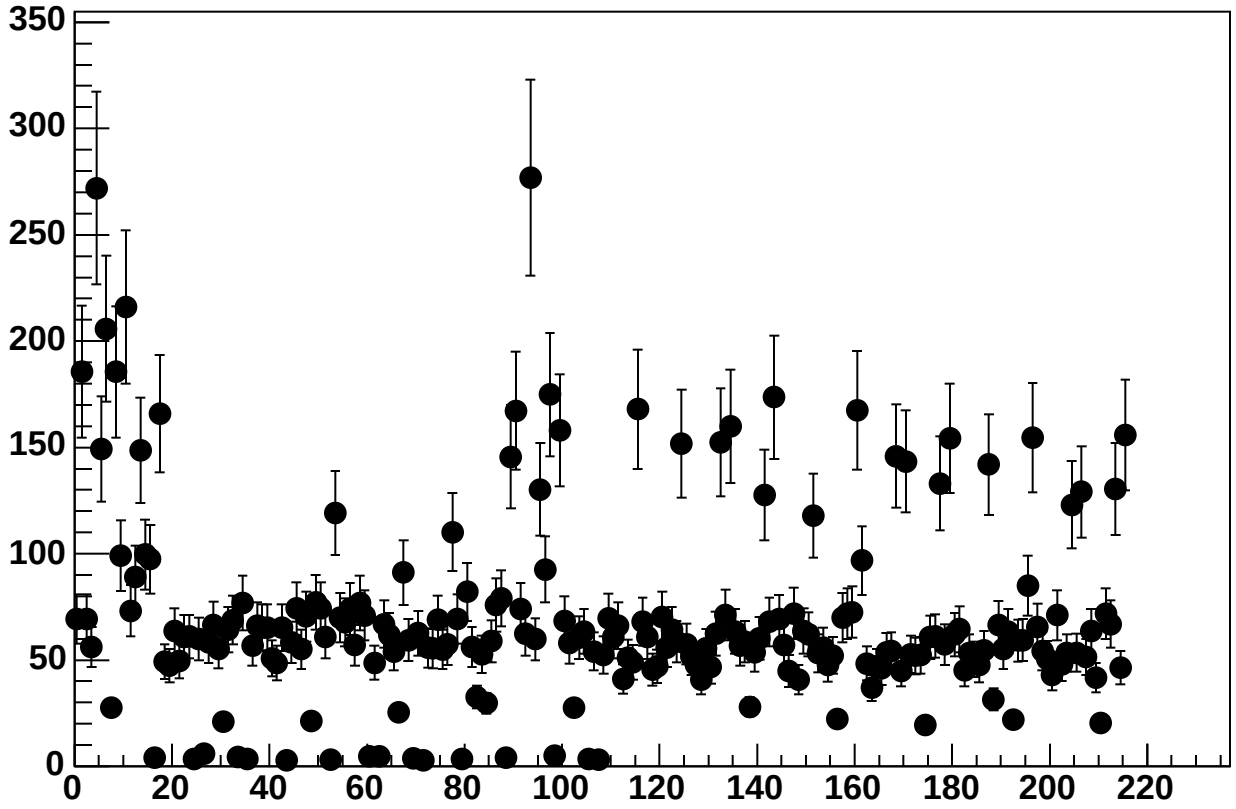
Enable 5, DAC=1600, Hold=15, ADC Noise vs 18*Chip+Chan



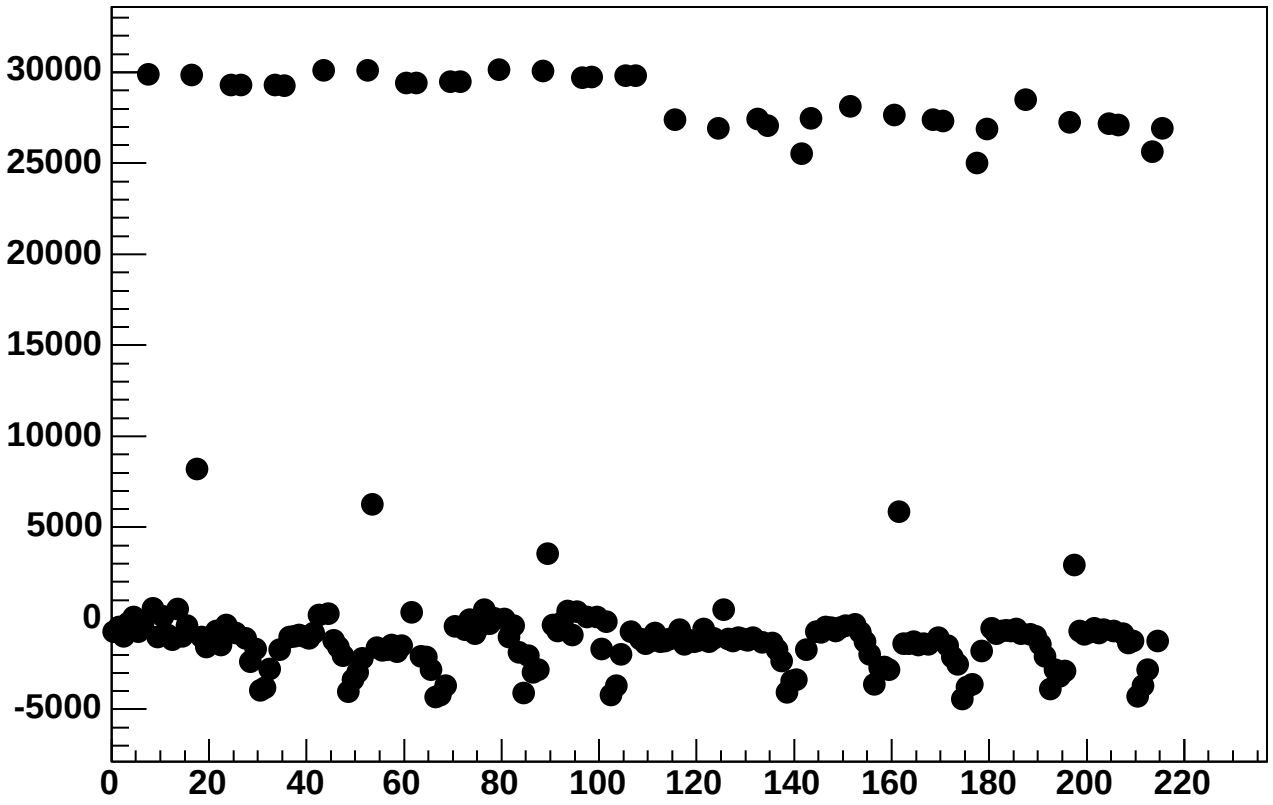
Enable 5, DAC=1600, Hold=20, ADC Mean vs 18*Chip+Chan



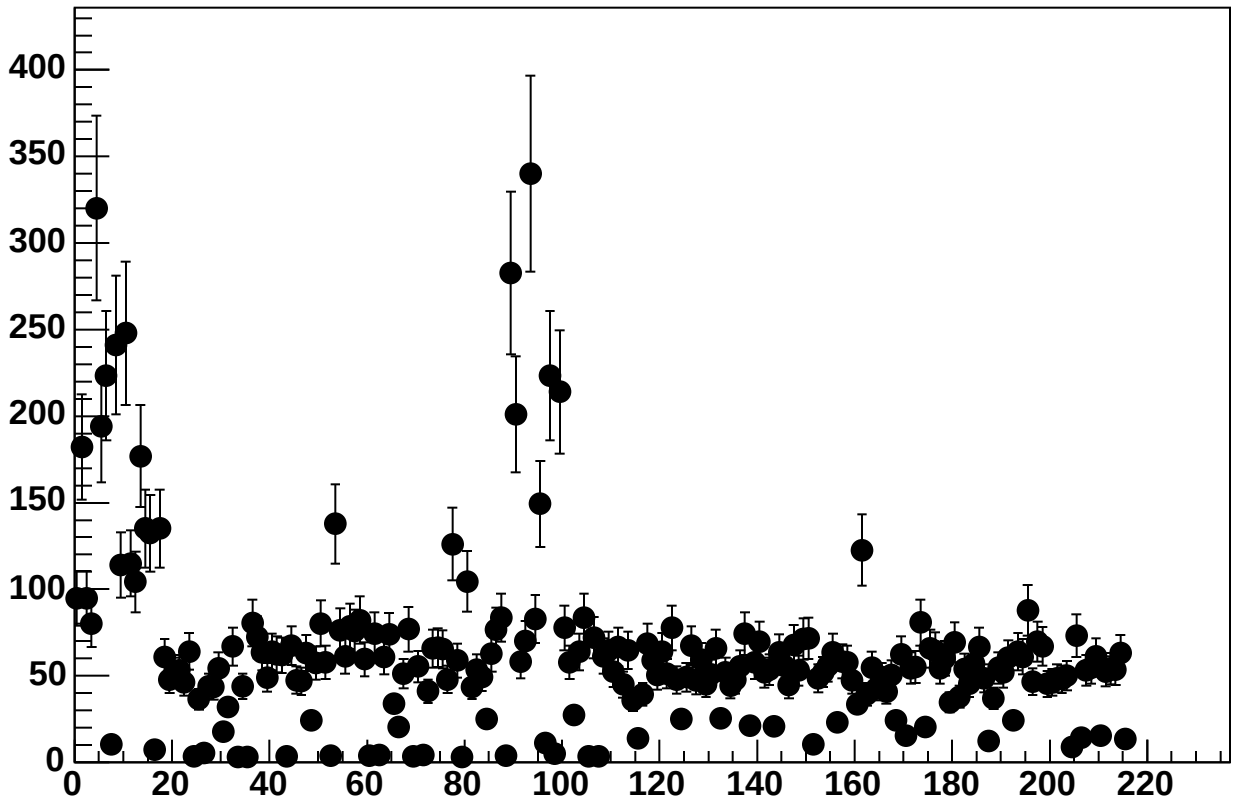
Enable 5, DAC=1600, Hold=20, ADC Noise vs 18*Chip+Chan



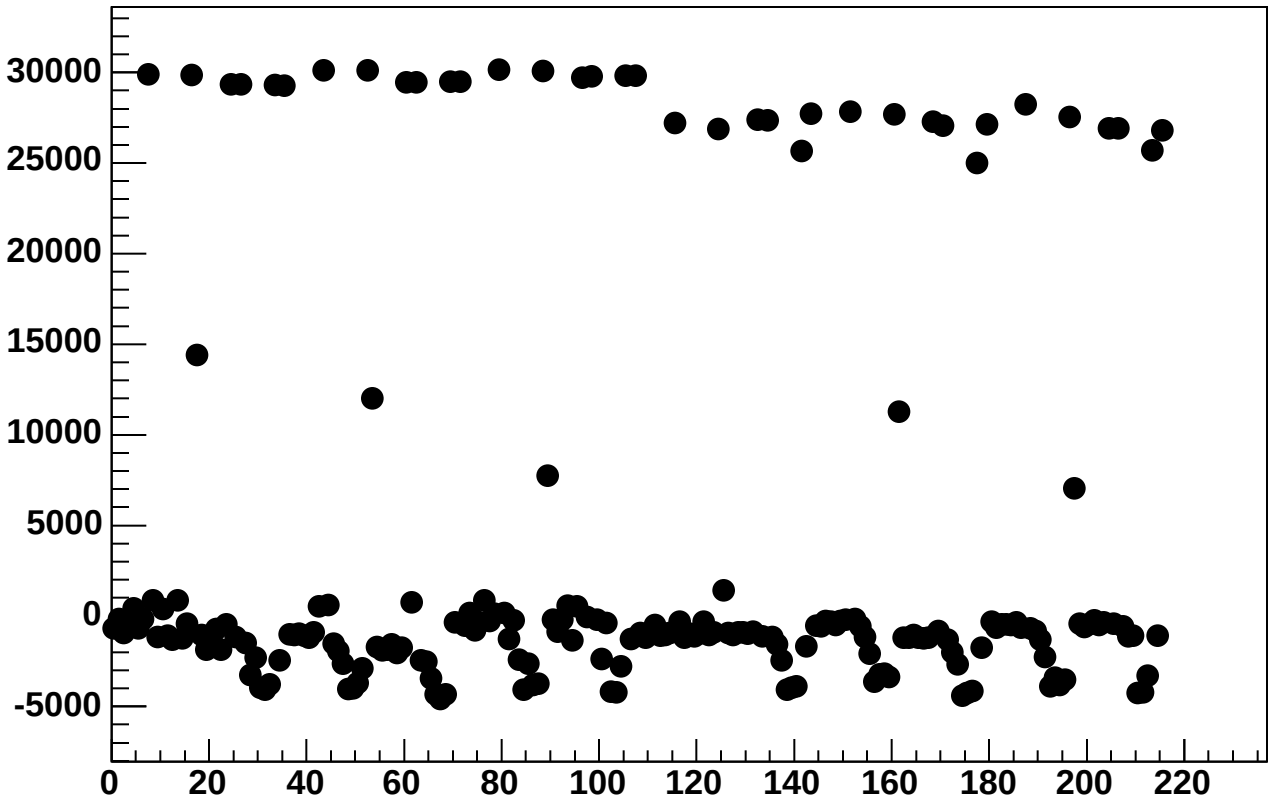
Enable 5, DAC=1600, Hold=25, ADC Mean vs 18*Chip+Chan



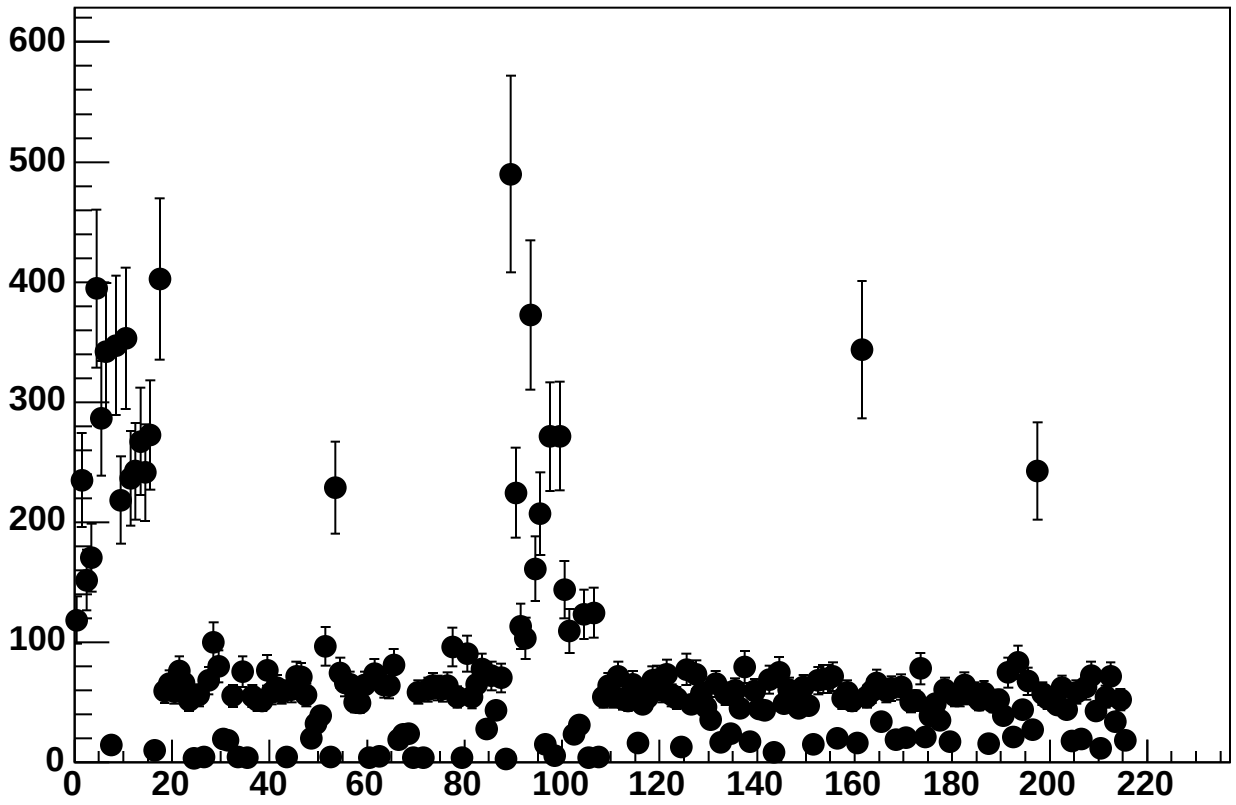
Enable 5, DAC=1600, Hold=25, ADC Noise vs 18*Chip+Chan



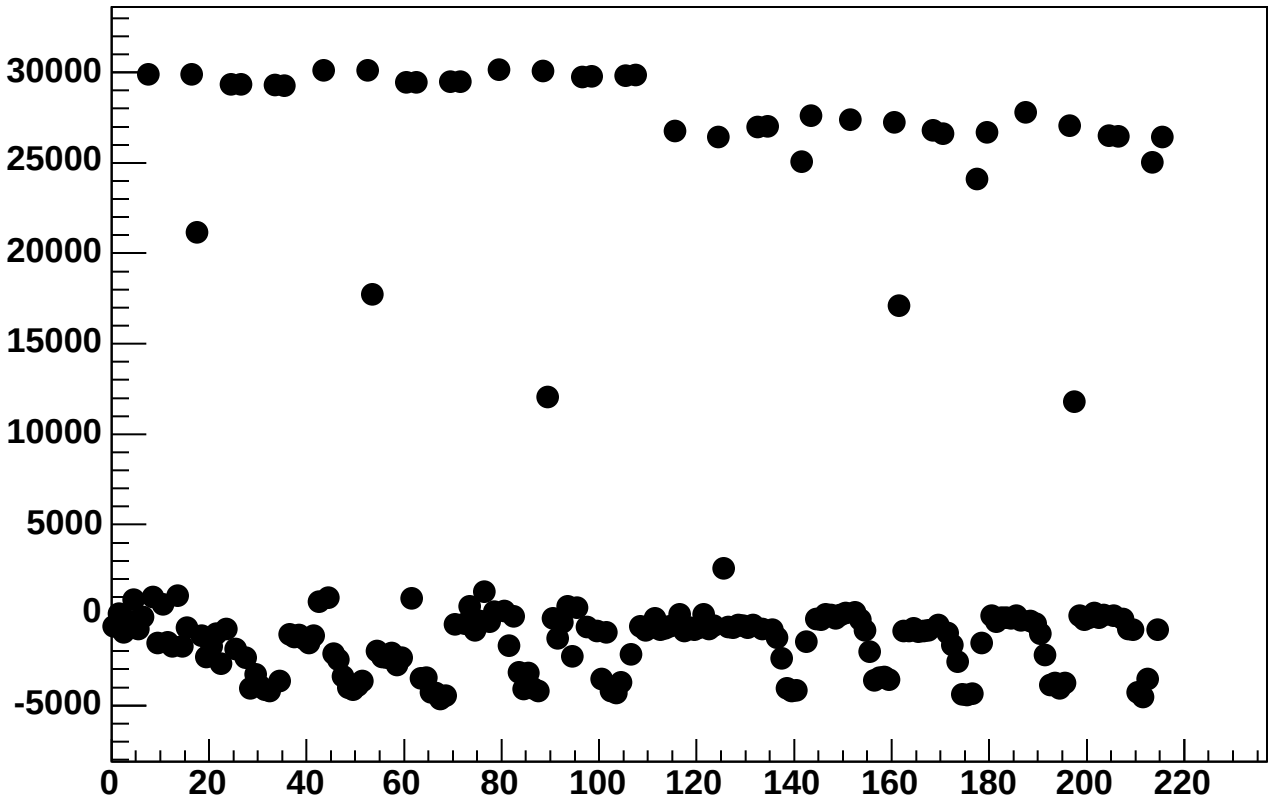
Enable 5, DAC=1600, Hold=30, ADC Mean vs 18*Chip+Chan



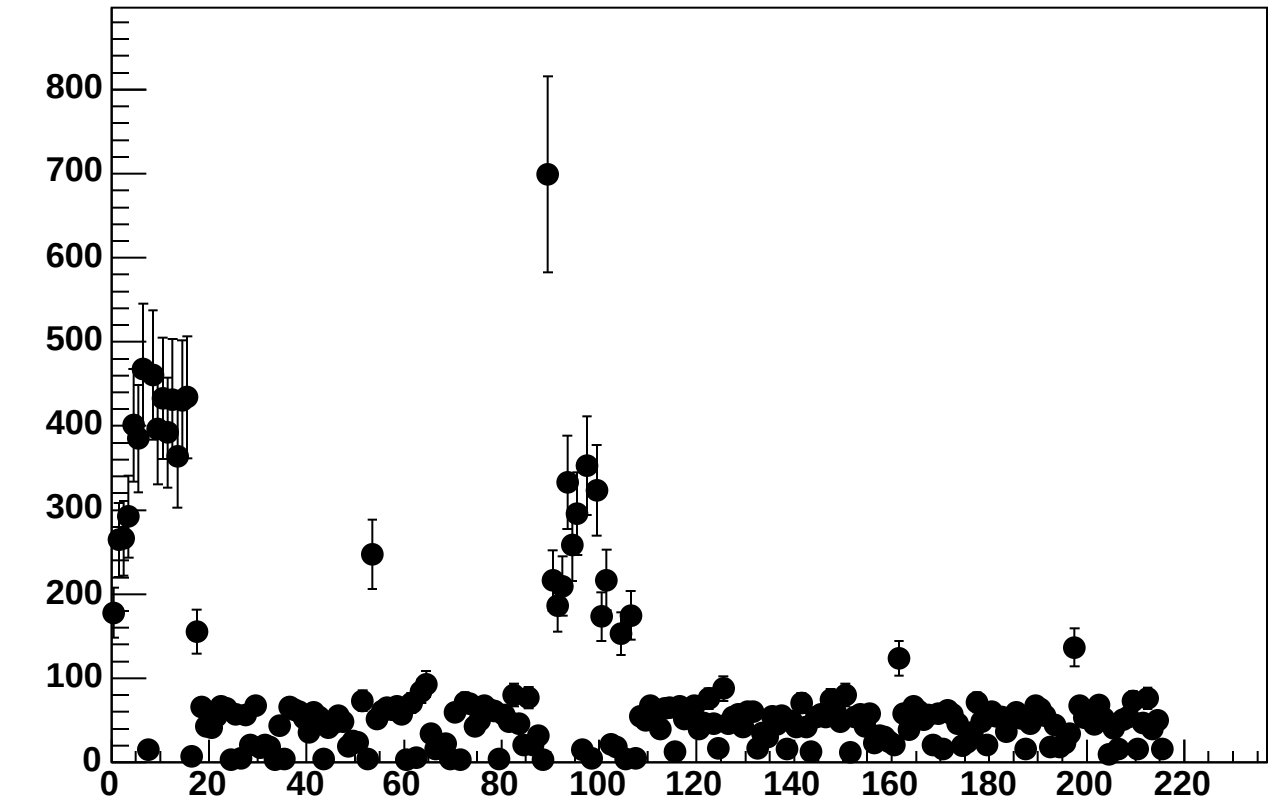
Enable 5, DAC=1600, Hold=30, ADC Noise vs 18*Chip+Chan



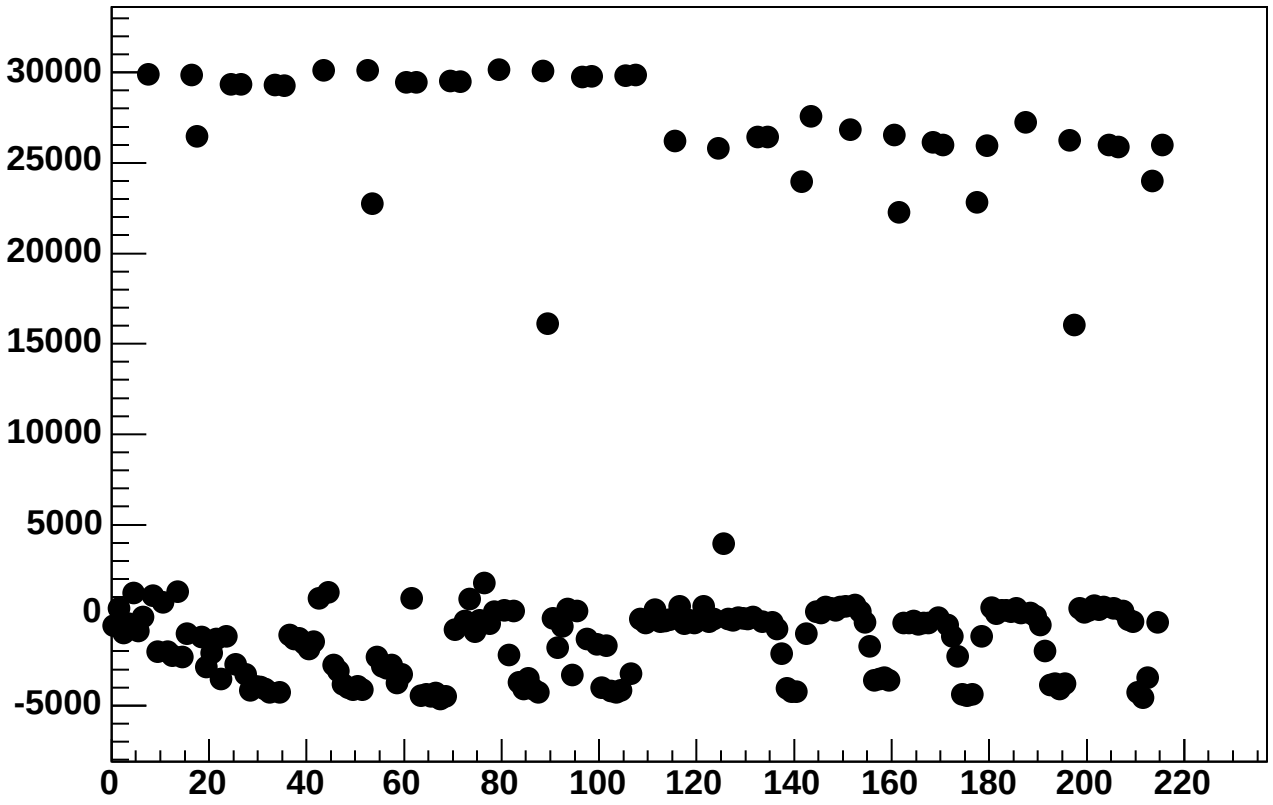
Enable 5, DAC=1600, Hold=35, ADC Mean vs 18*Chip+Chan



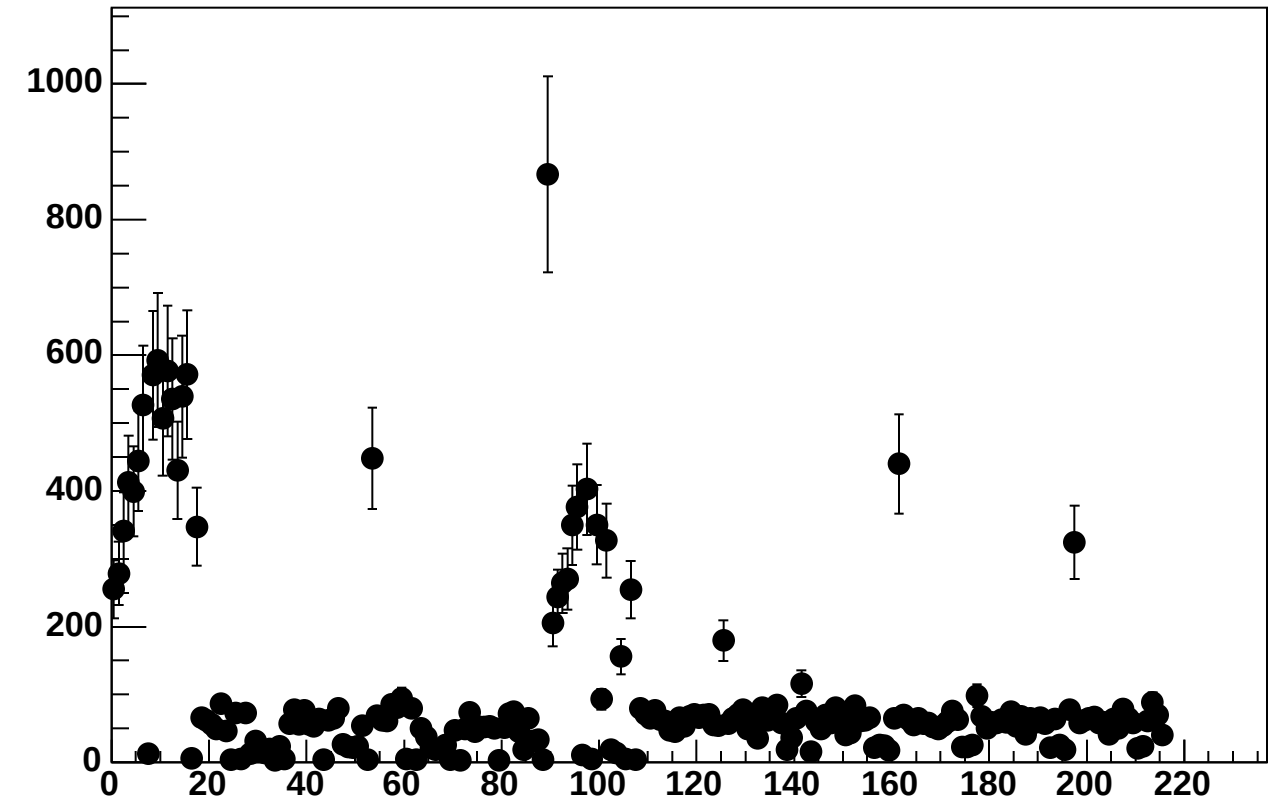
Enable 5, DAC=1600, Hold=35, ADC Noise vs 18*Chip+Chan



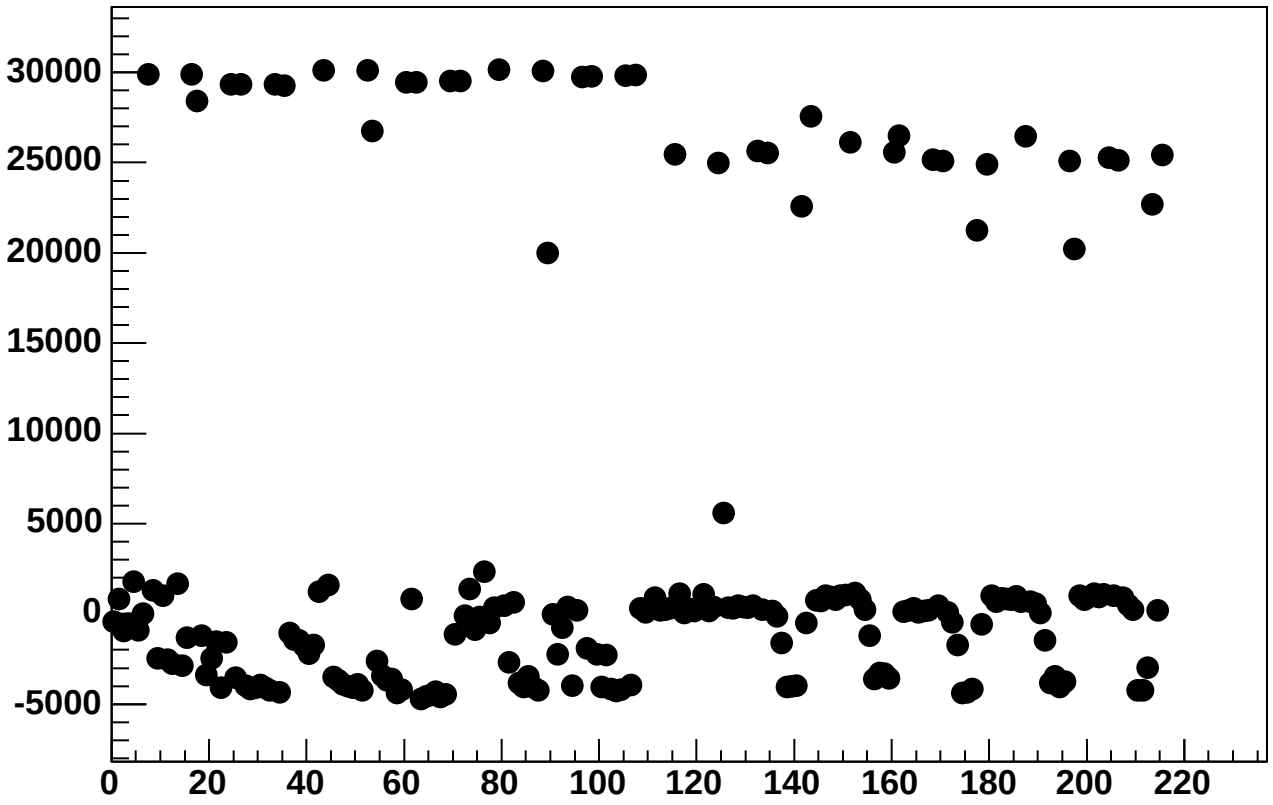
Enable 5, DAC=1600, Hold=40, ADC Mean vs 18*Chip+Chan



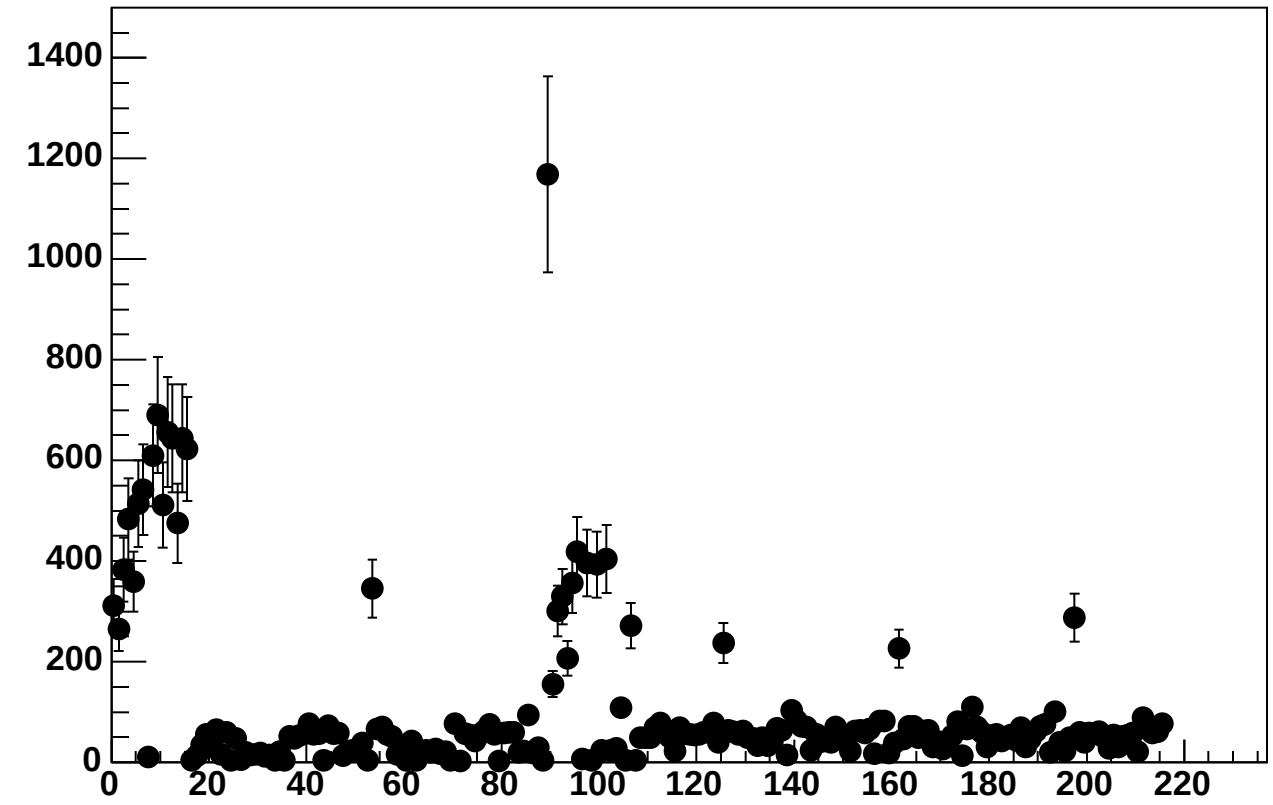
Enable 5, DAC=1600, Hold=40, ADC Noise vs 18*Chip+Chan



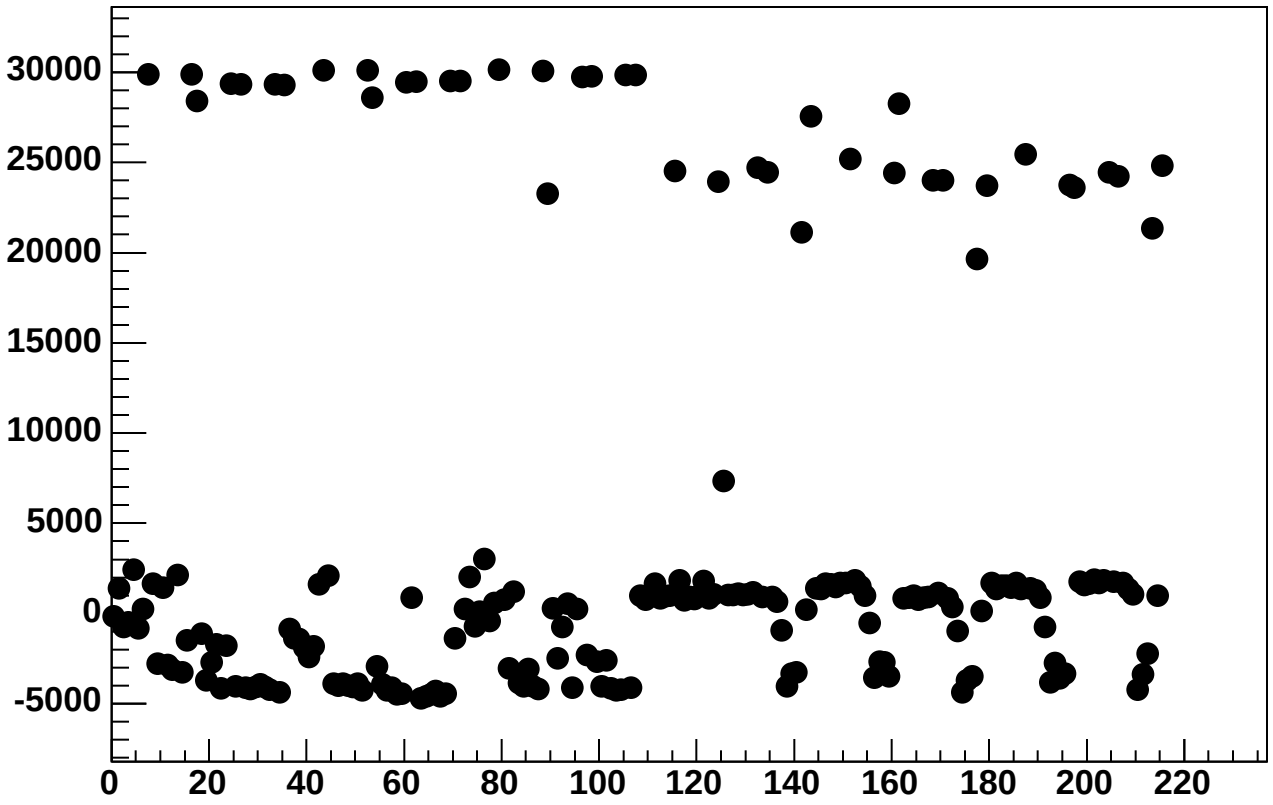
Enable 5, DAC=1600, Hold=45, ADC Mean vs 18*Chip+Chan



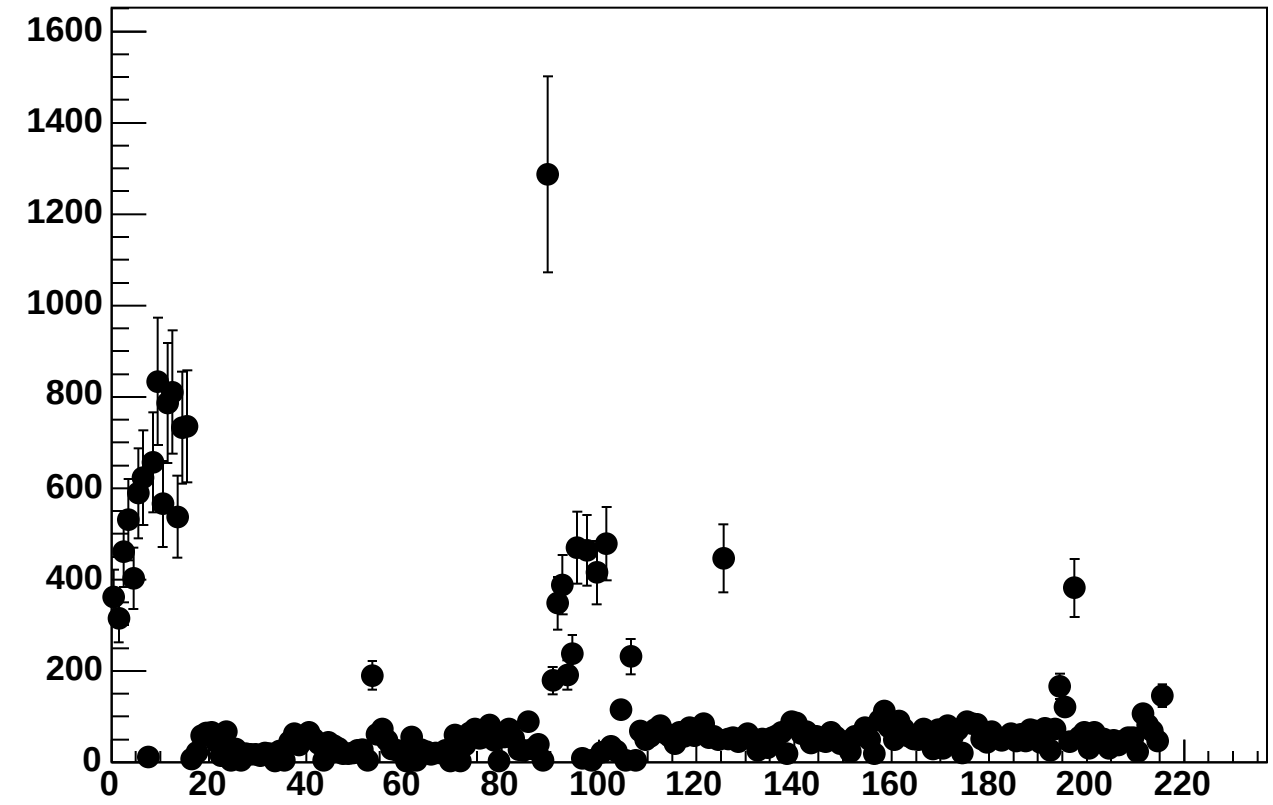
Enable 5, DAC=1600, Hold=45, ADC Noise vs 18*Chip+Chan



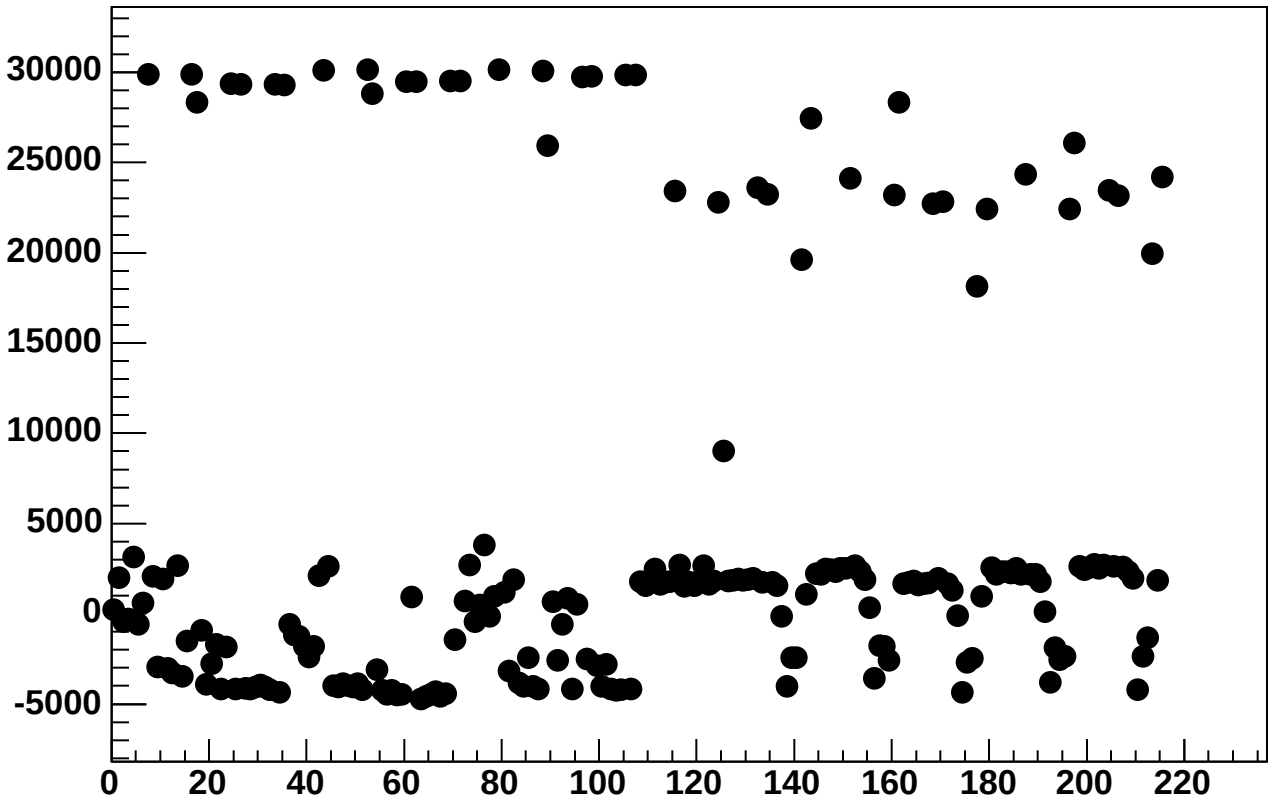
Enable 5, DAC=1600, Hold=50, ADC Mean vs 18*Chip+Chan



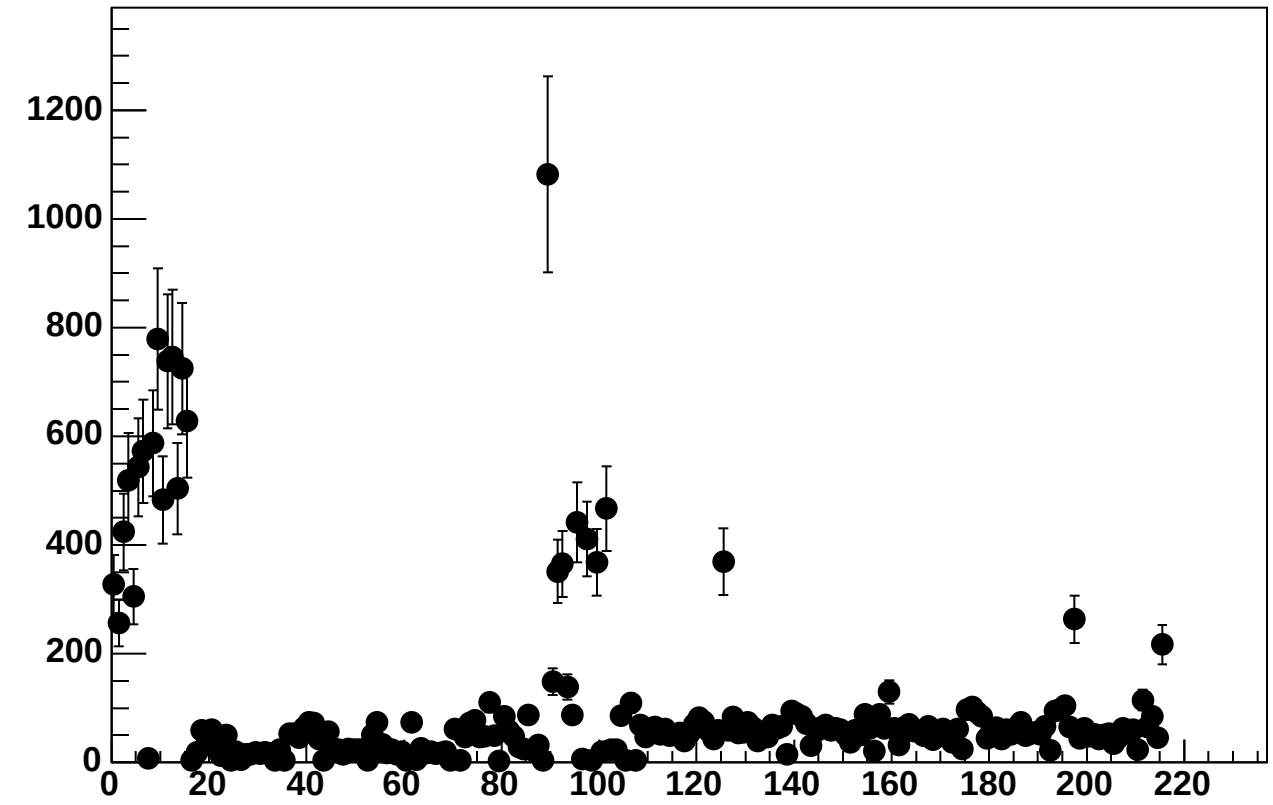
Enable 5, DAC=1600, Hold=50, ADC Noise vs 18*Chip+Chan



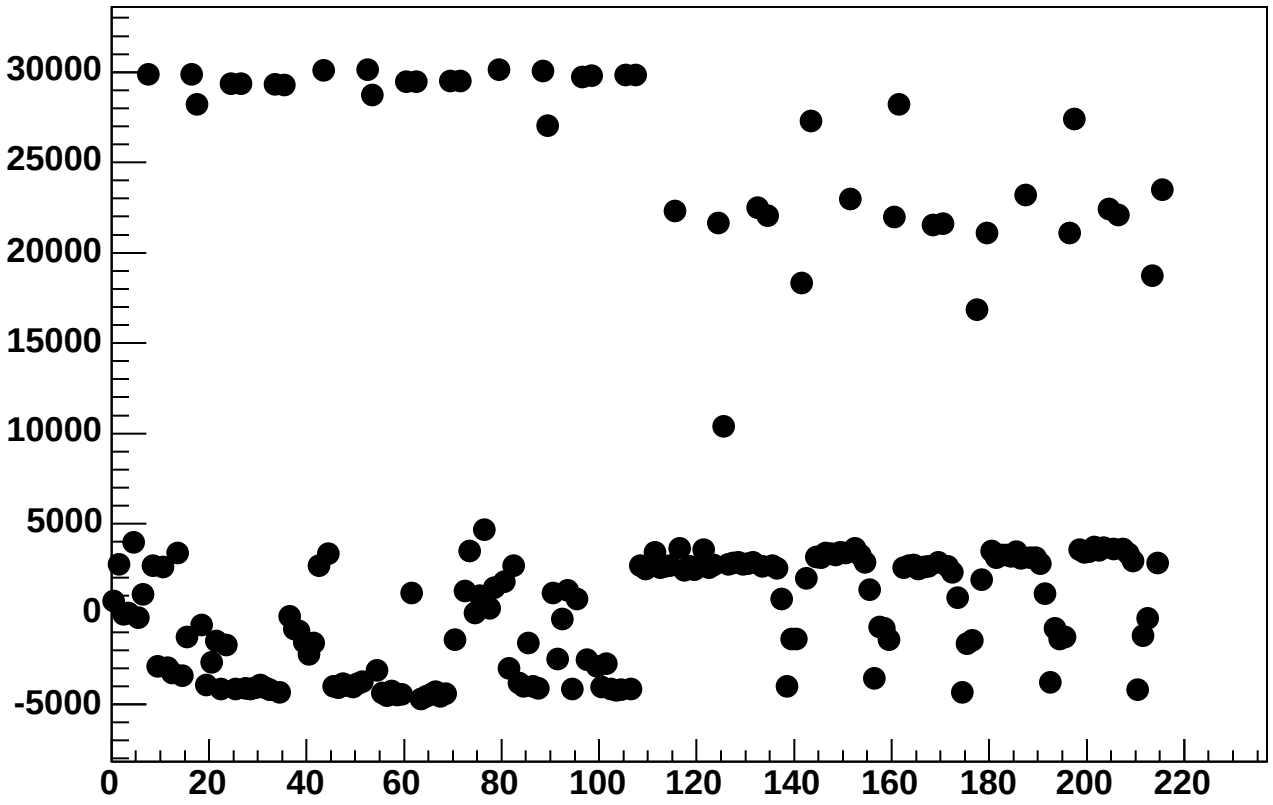
Enable 5, DAC=1600, Hold=55, ADC Mean vs 18*Chip+Chan



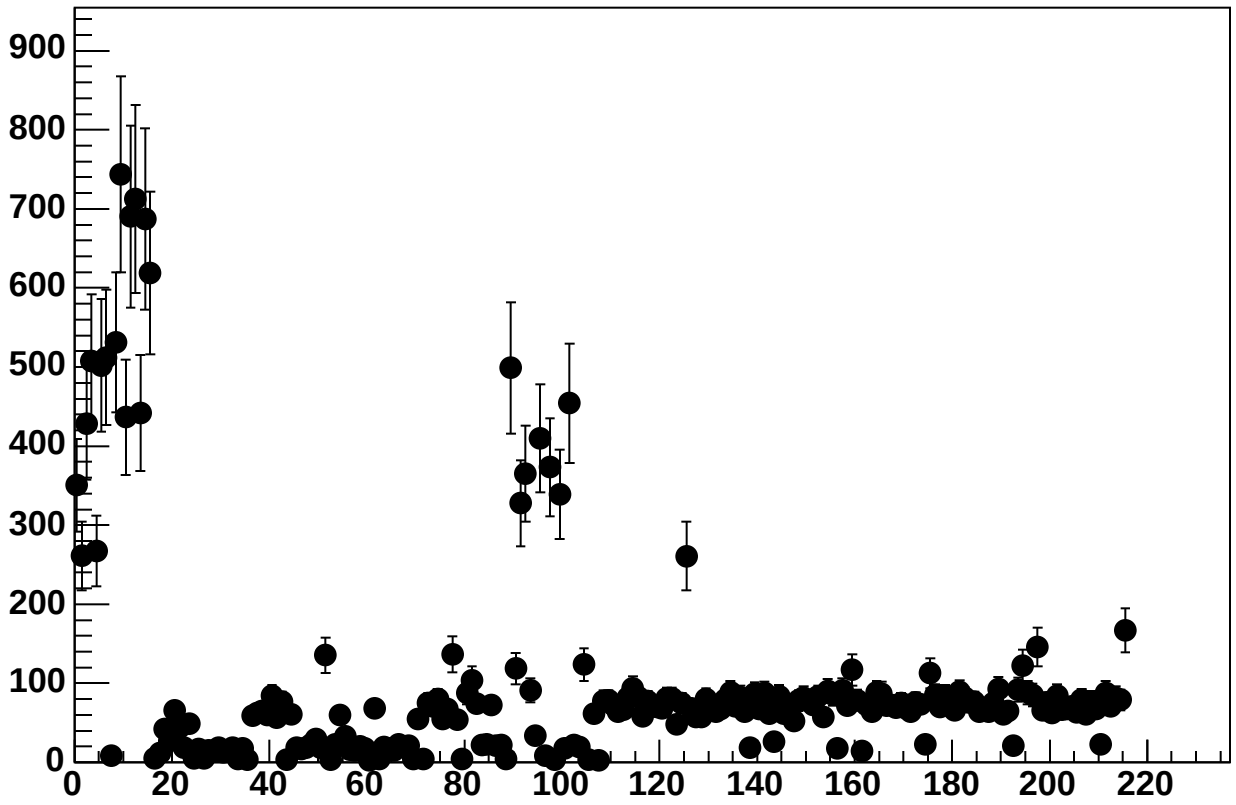
Enable 5, DAC=1600, Hold=55, ADC Noise vs 18*Chip+Chan



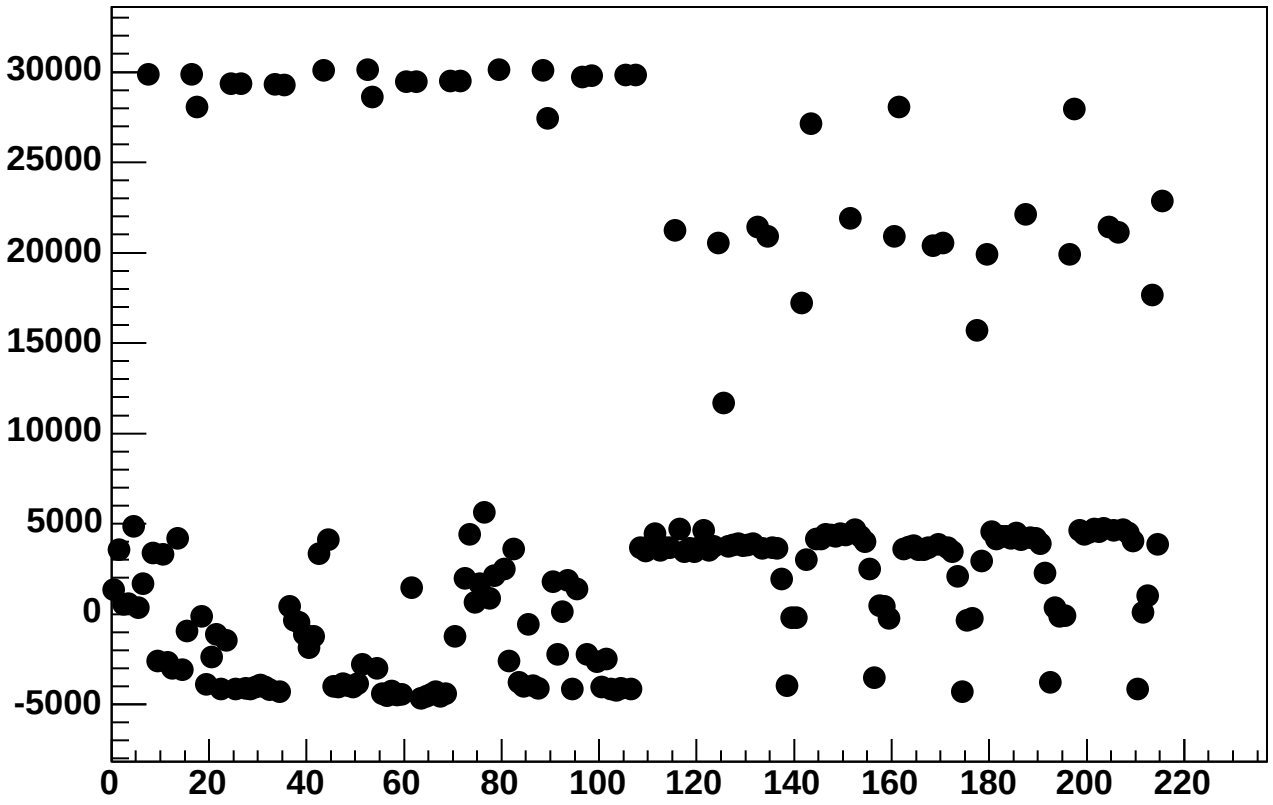
Enable 5, DAC=1600, Hold=60, ADC Mean vs 18*Chip+Chan



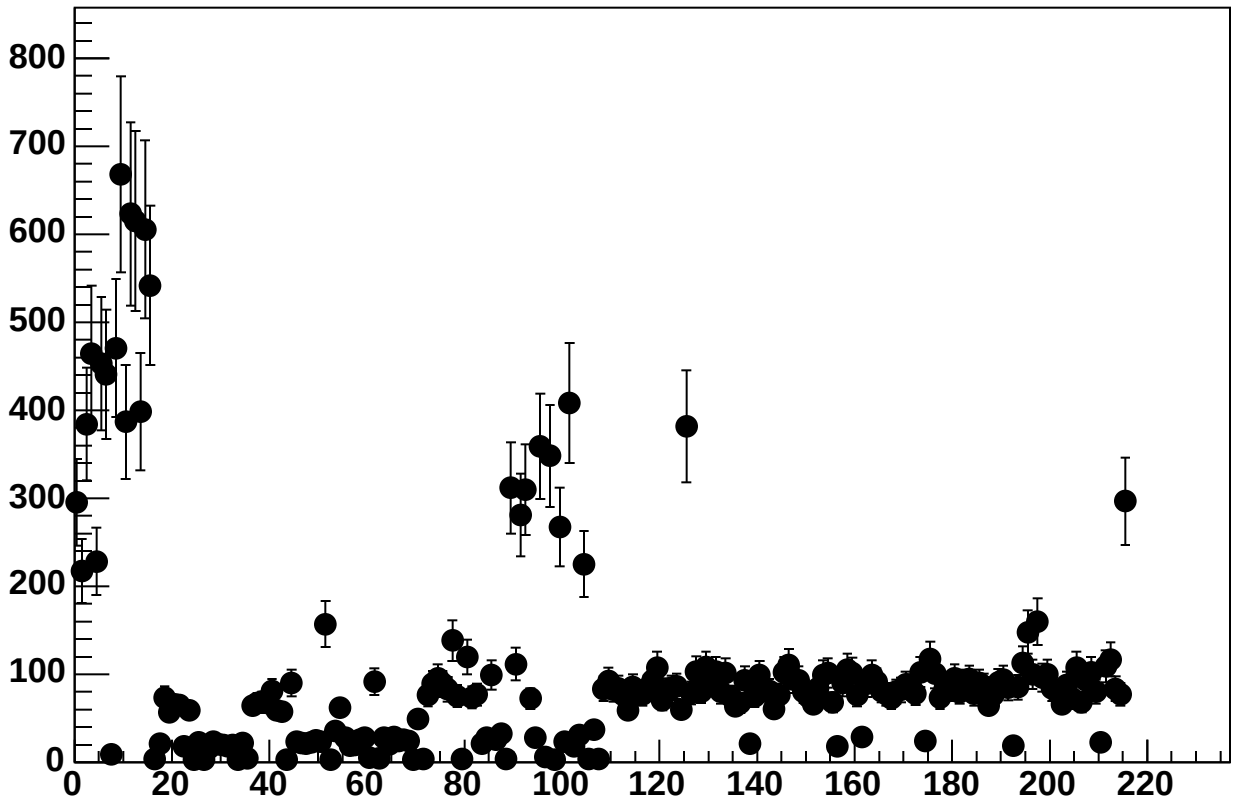
Enable 5, DAC=1600, Hold=60, ADC Noise vs 18*Chip+Chan



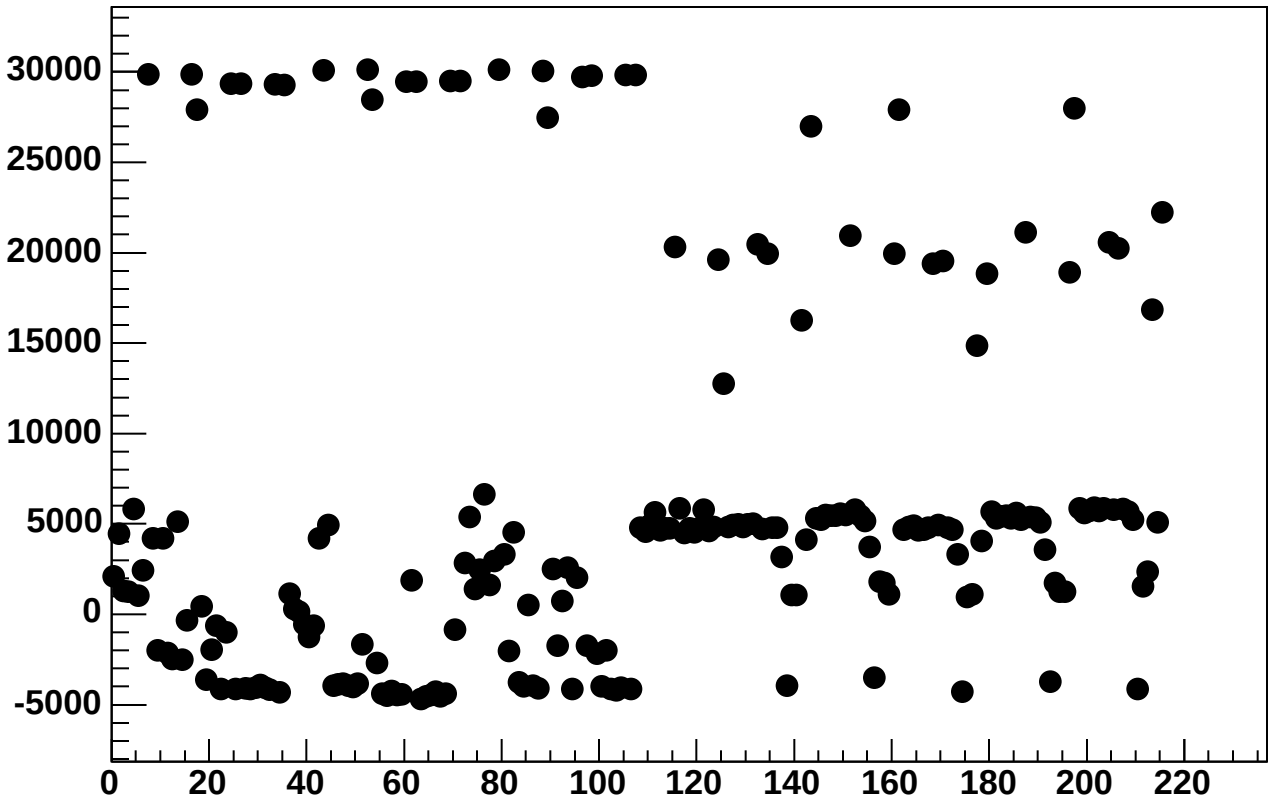
Enable 5, DAC=1600, Hold=65, ADC Mean vs 18*Chip+Chan



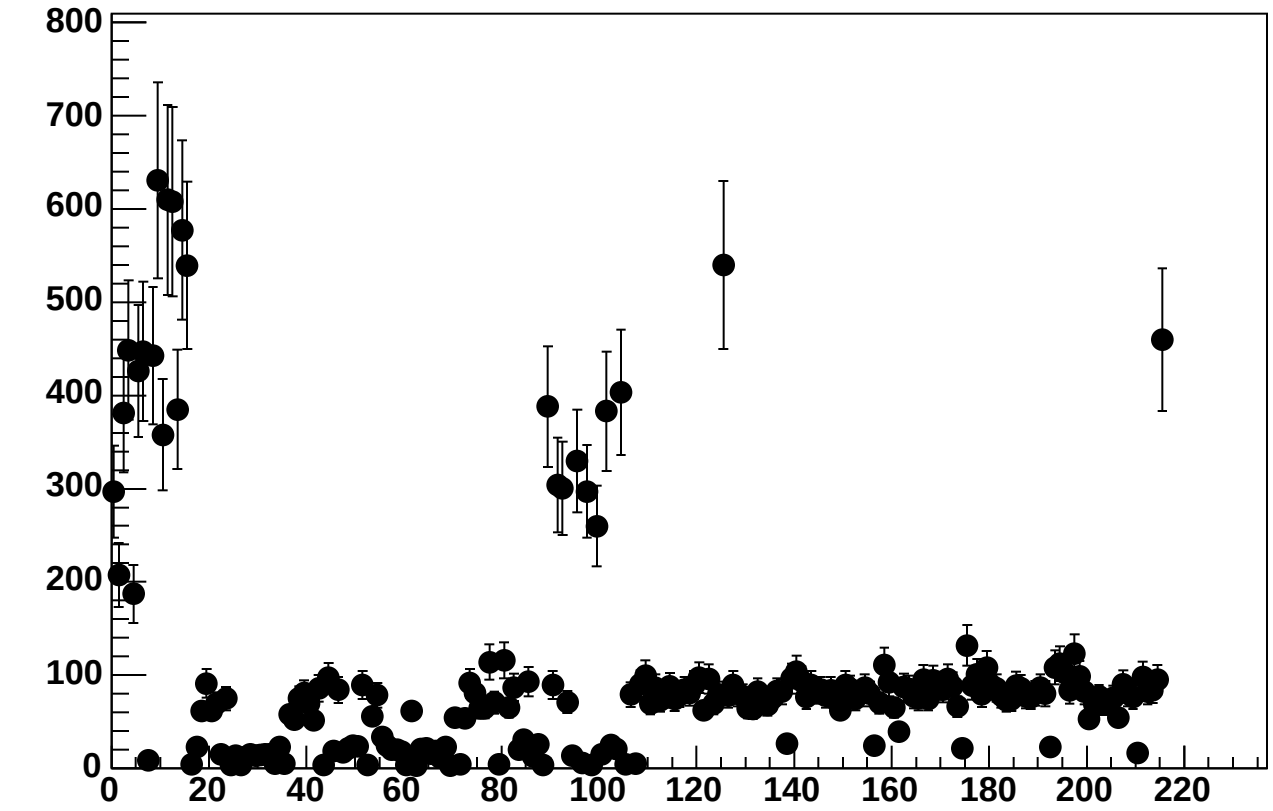
Enable 5, DAC=1600, Hold=65, ADC Noise vs 18*Chip+Chan



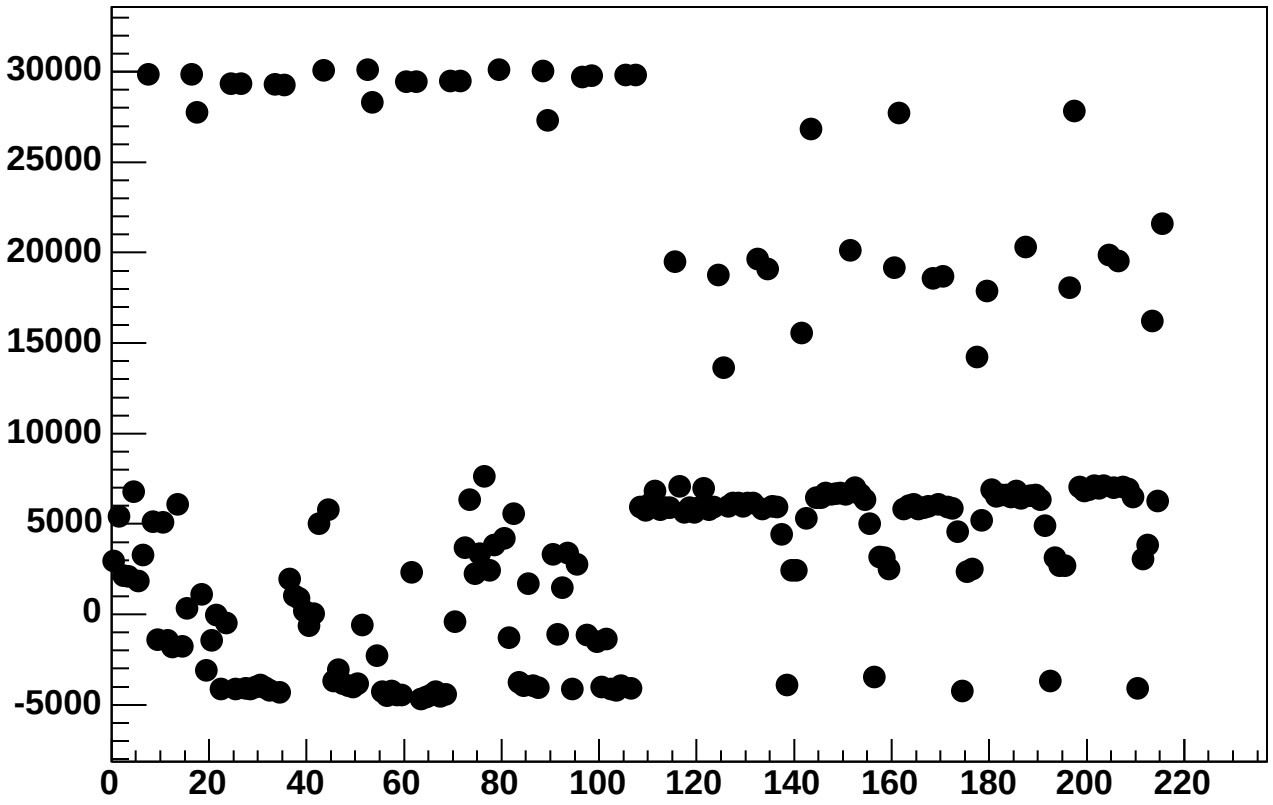
Enable 5, DAC=1600, Hold=70, ADC Mean vs 18*Chip+Chan



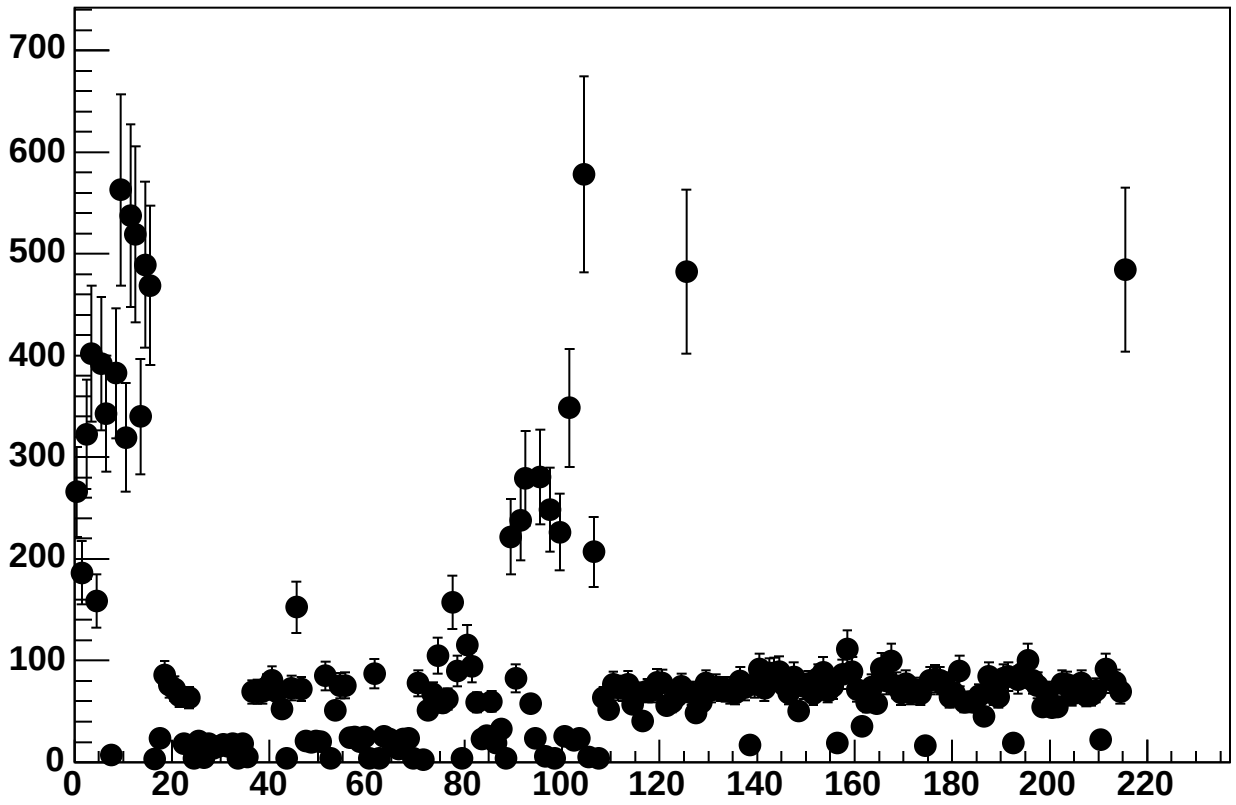
Enable 5, DAC=1600, Hold=70, ADC Noise vs 18*Chip+Chan



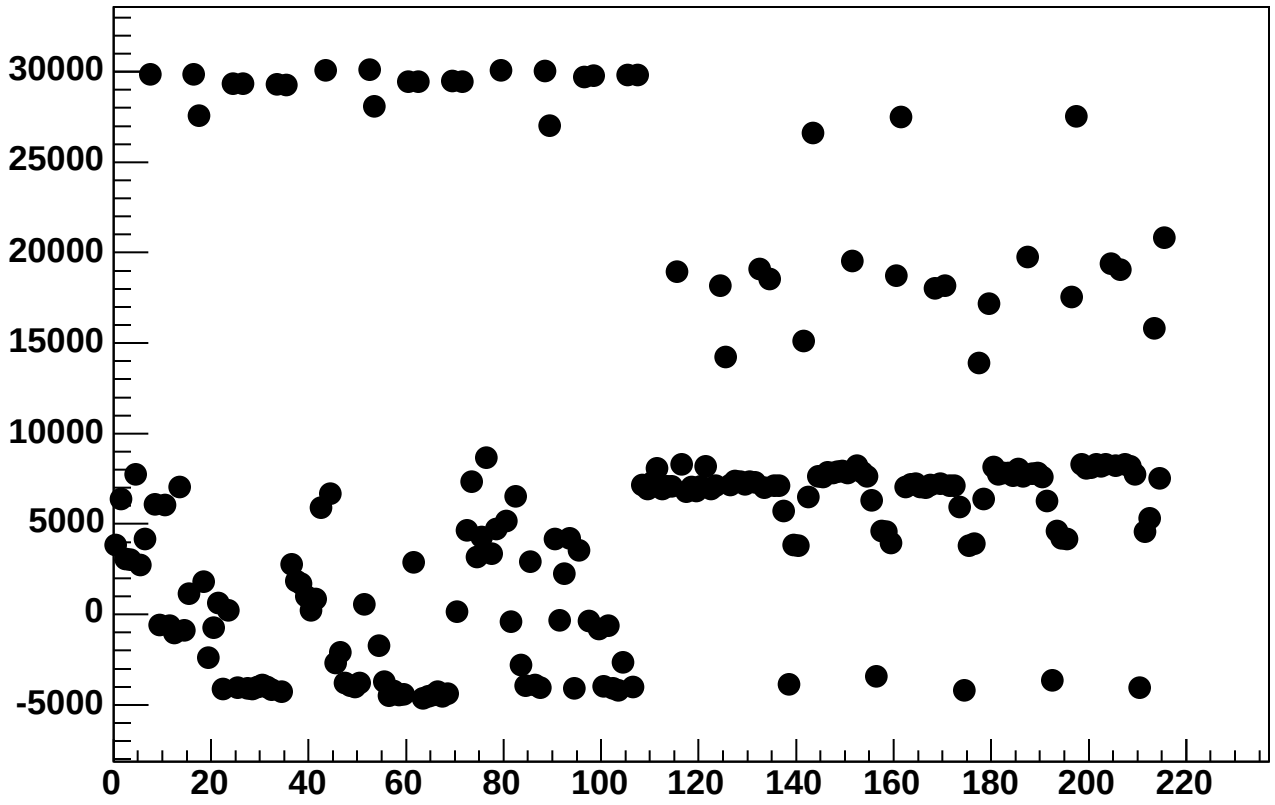
Enable 5, DAC=1600, Hold=75, ADC Mean vs 18*Chip+Chan



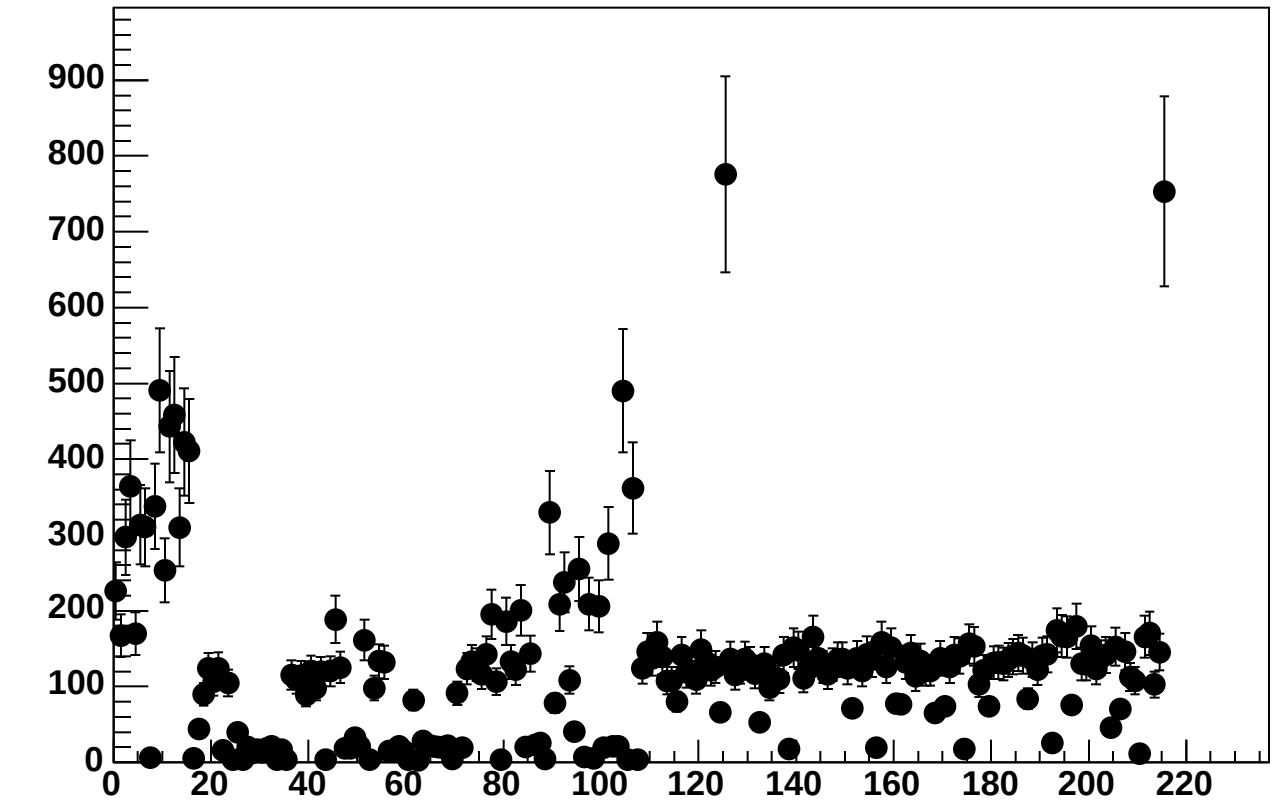
Enable 5, DAC=1600, Hold=75, ADC Noise vs 18*Chip+Chan



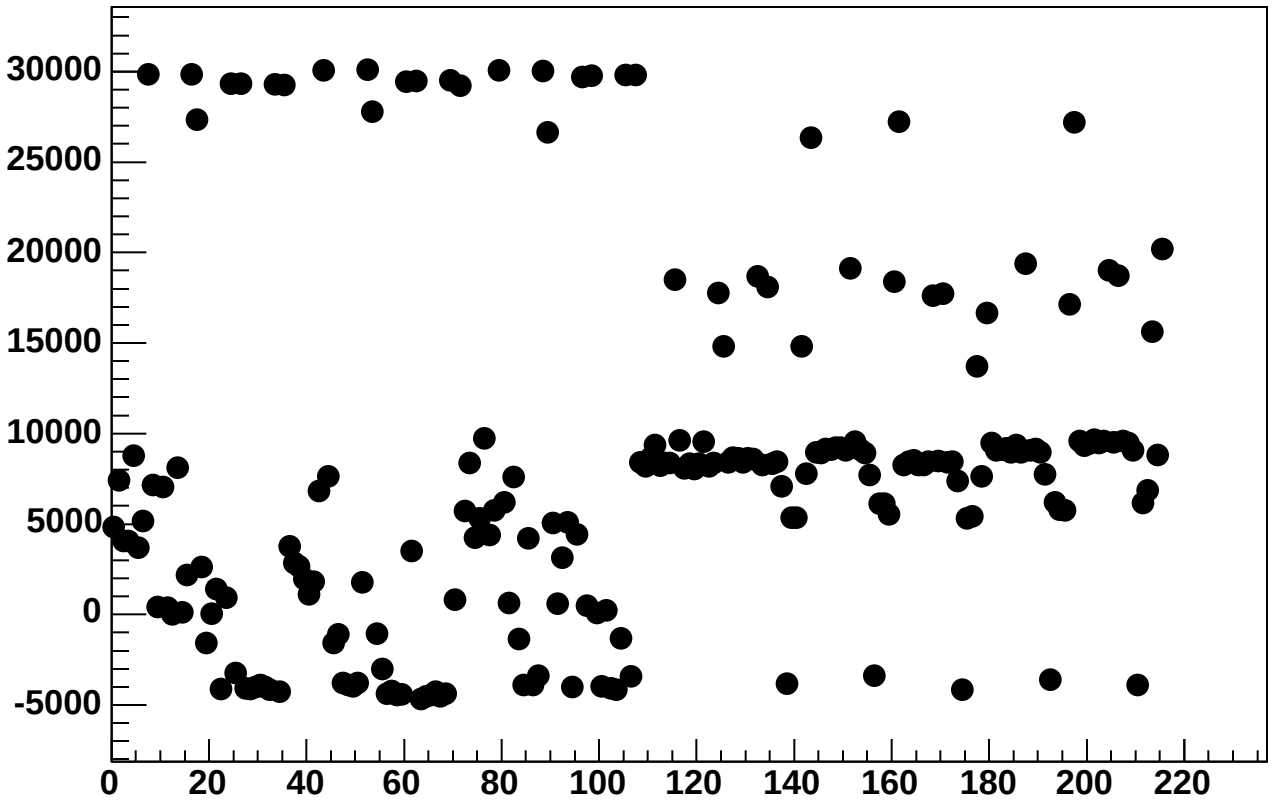
Enable 5, DAC=1600, Hold=80, ADC Mean vs 18*Chip+Chan



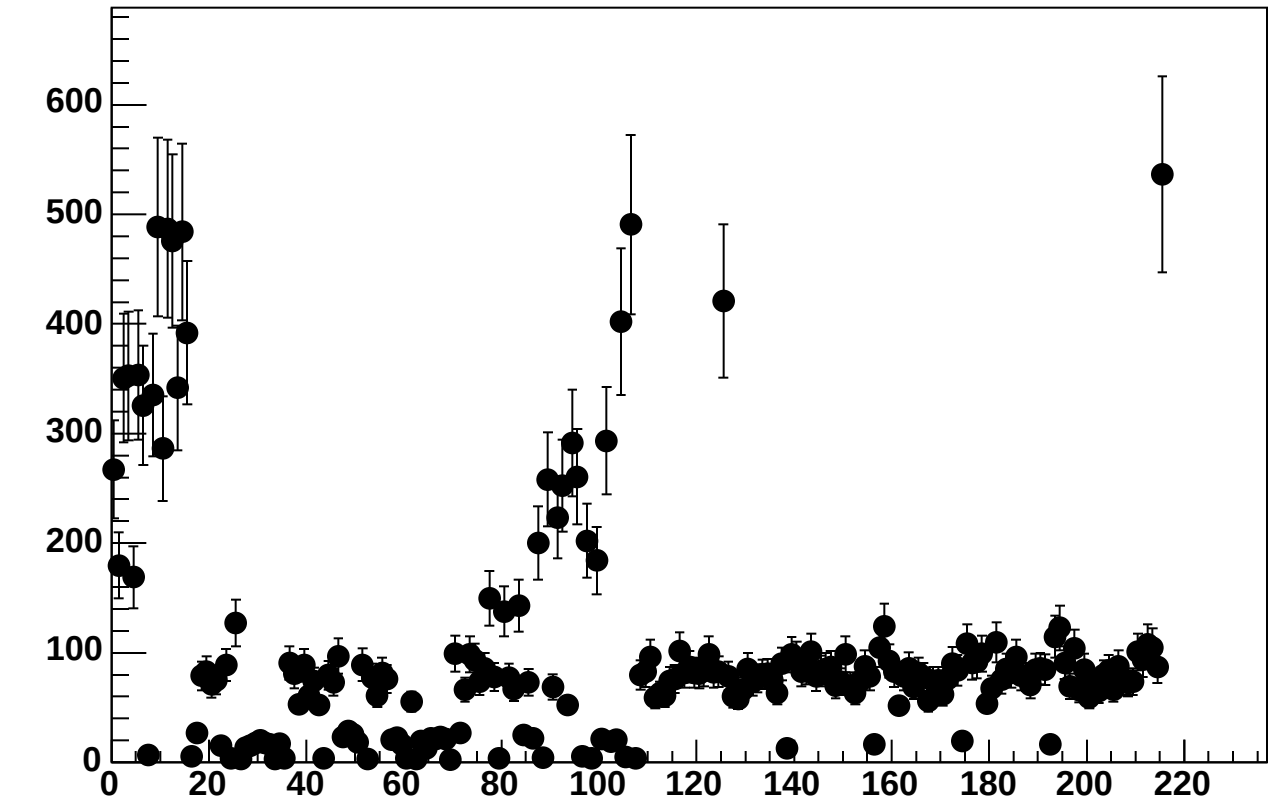
Enable 5, DAC=1600, Hold=80, ADC Noise vs 18*Chip+Chan



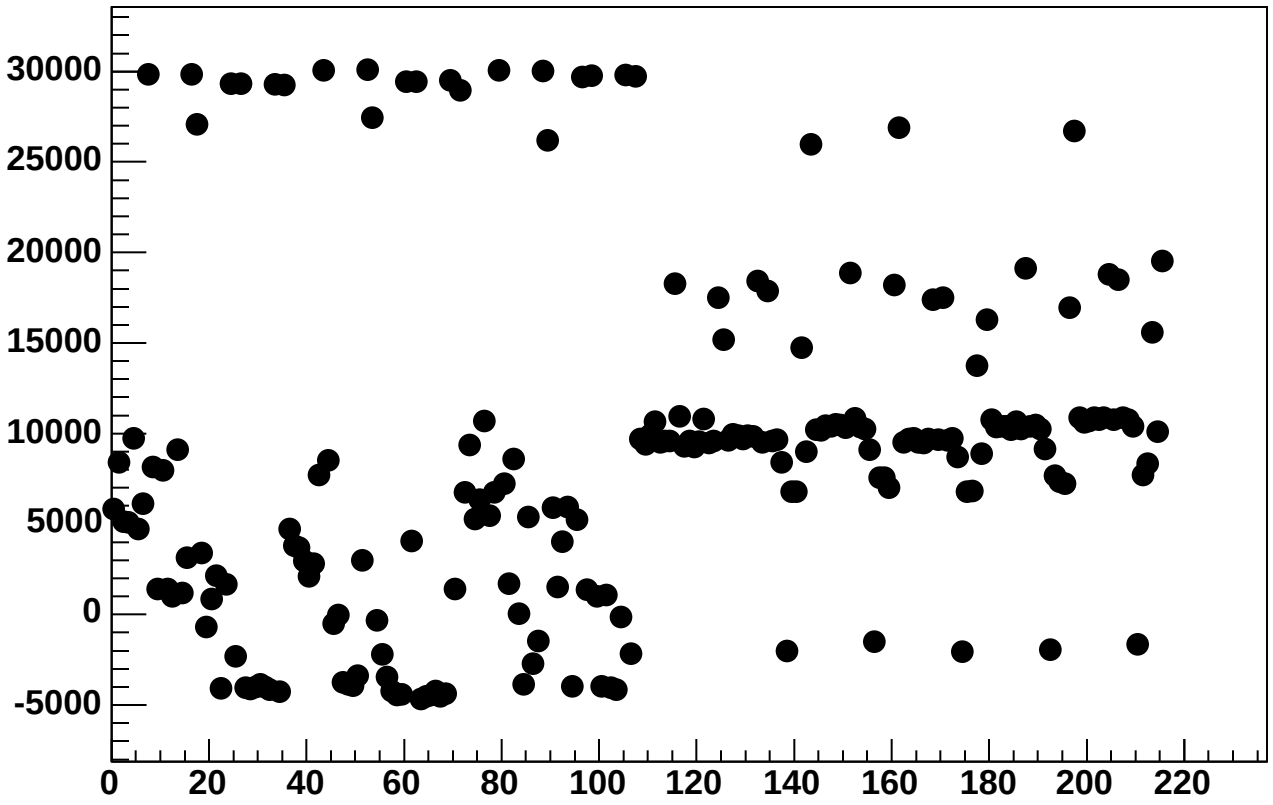
Enable 5, DAC=1600, Hold=85, ADC Mean vs 18*Chip+Chan



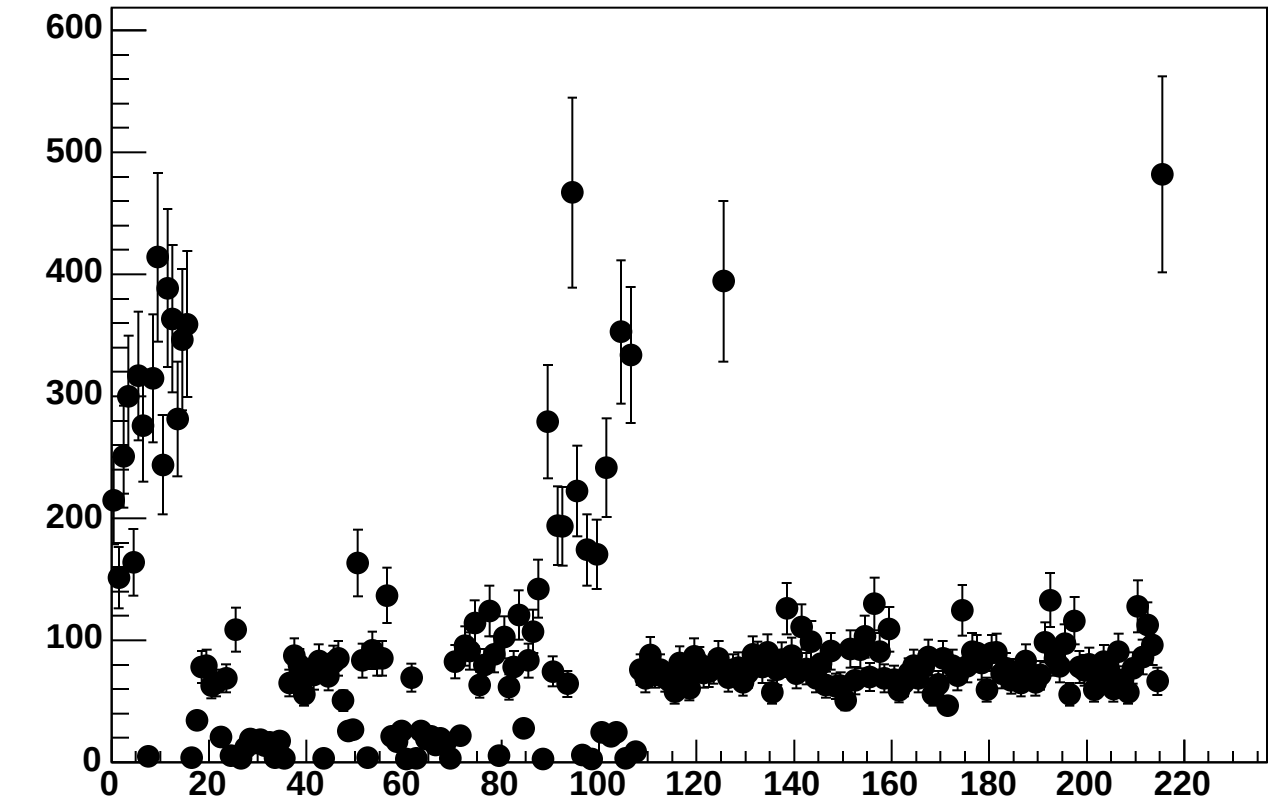
Enable 5, DAC=1600, Hold=85, ADC Noise vs 18*Chip+Chan



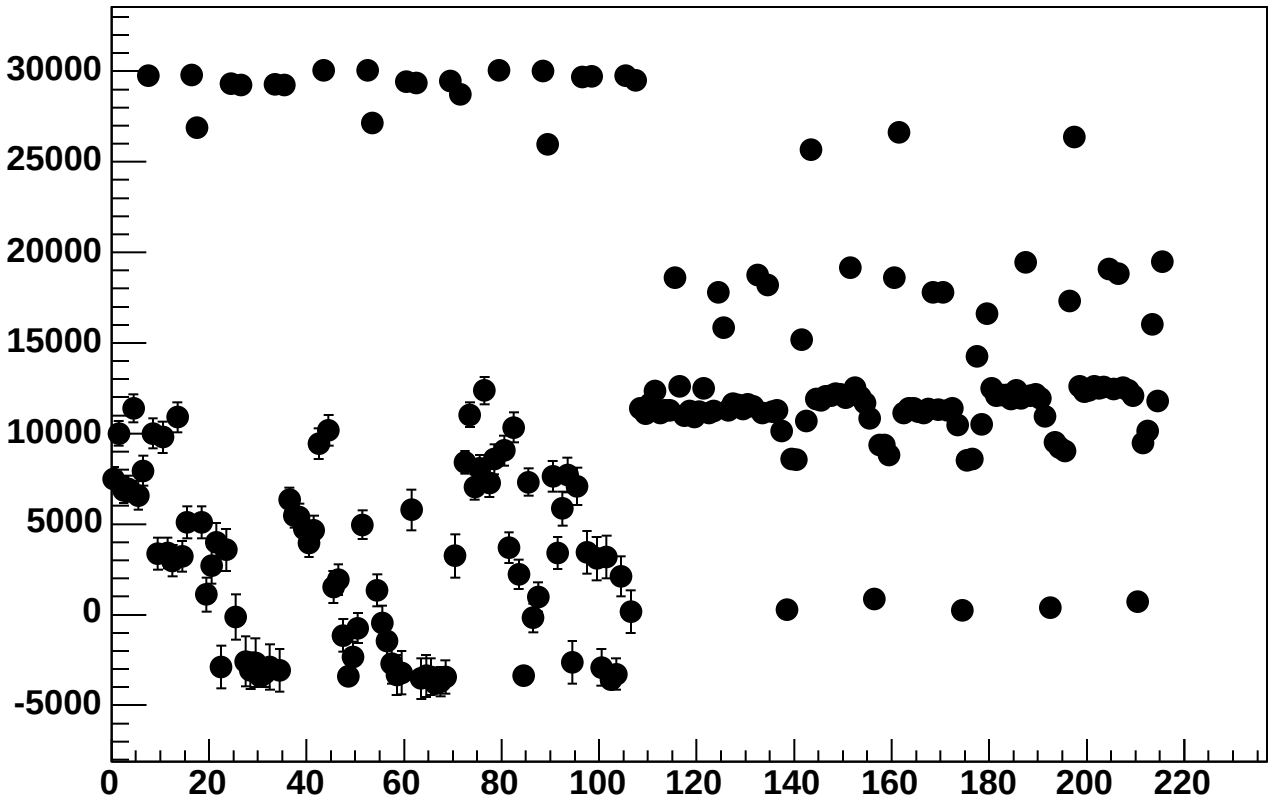
Enable 5, DAC=1600, Hold=90, ADC Mean vs 18*Chip+Chan



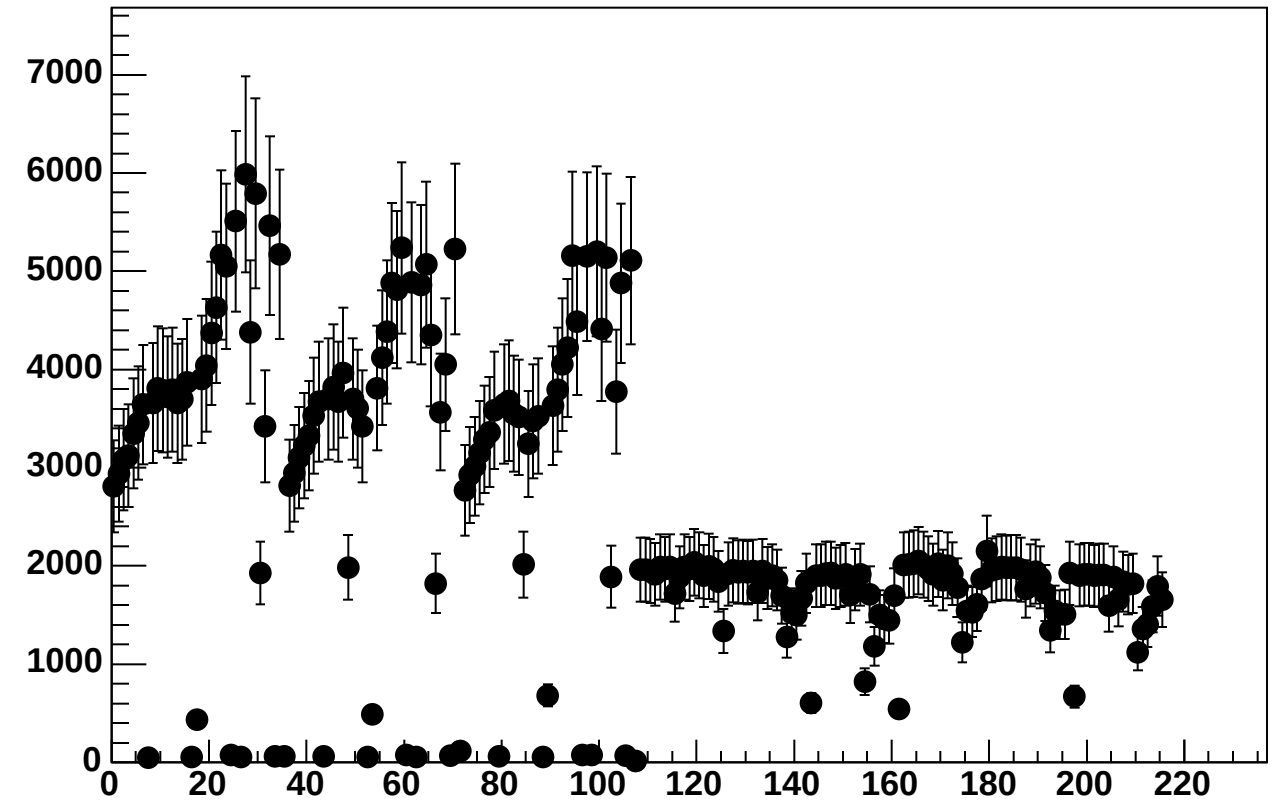
Enable 5, DAC=1600, Hold=90, ADC Noise vs 18*Chip+Chan



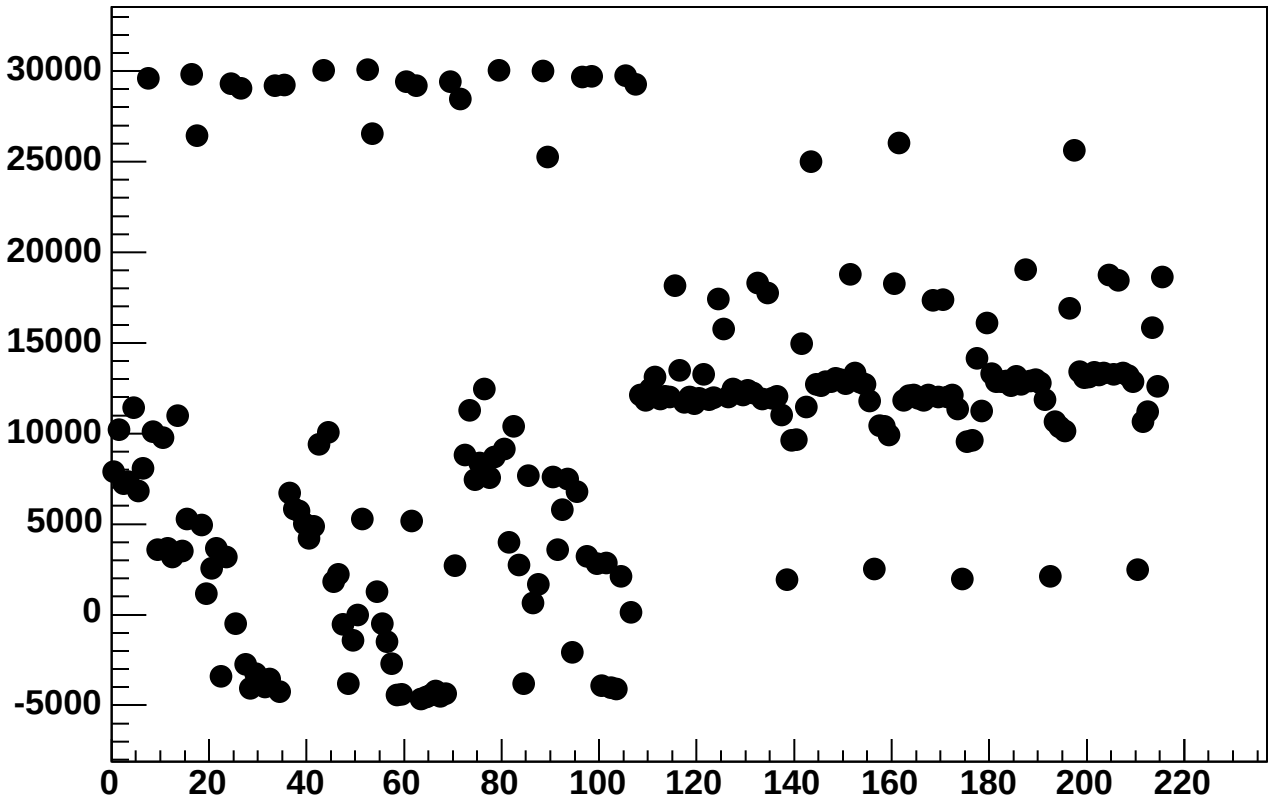
Enable 5, DAC=1600, Hold=95, ADC Mean vs 18*Chip+Chan



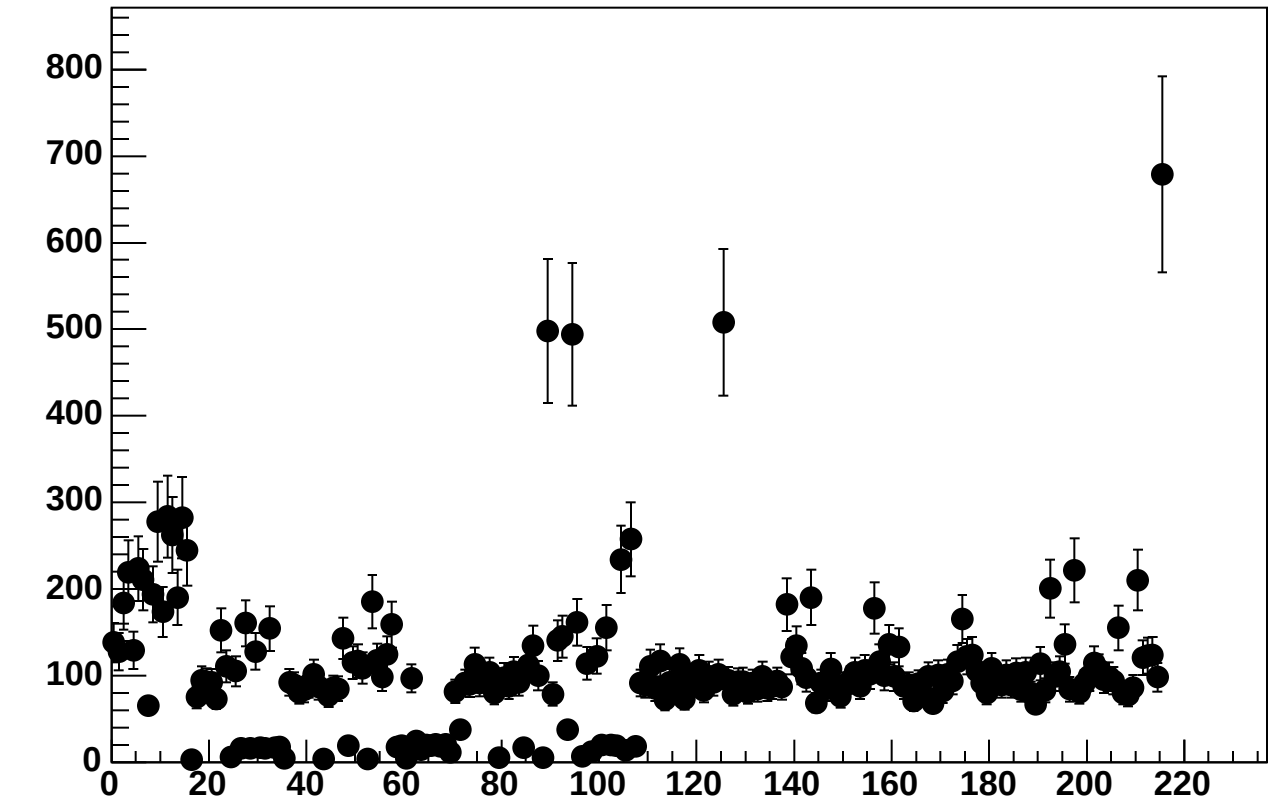
Enable 5, DAC=1600, Hold=95, ADC Noise vs 18*Chip+Chan



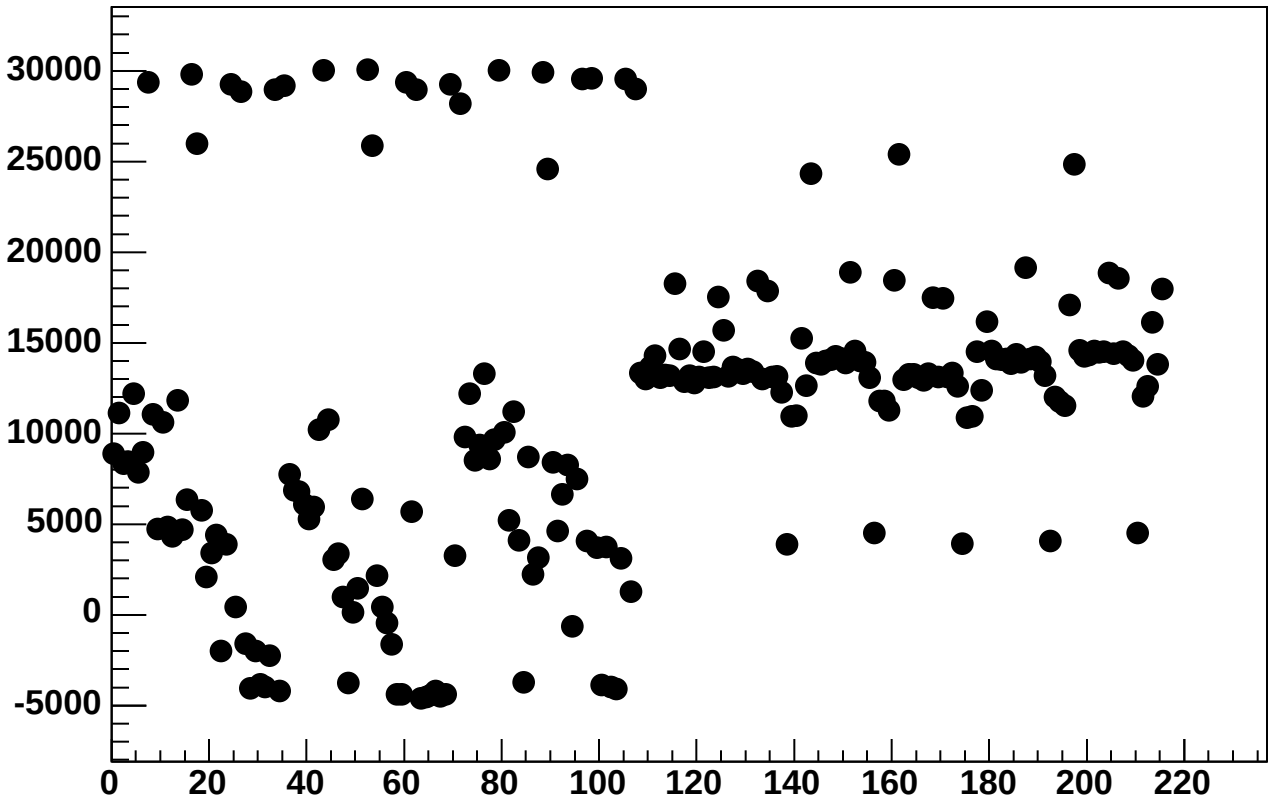
Enable 5, DAC=1600, Hold=100, ADC Mean vs 18*Chip+Chan



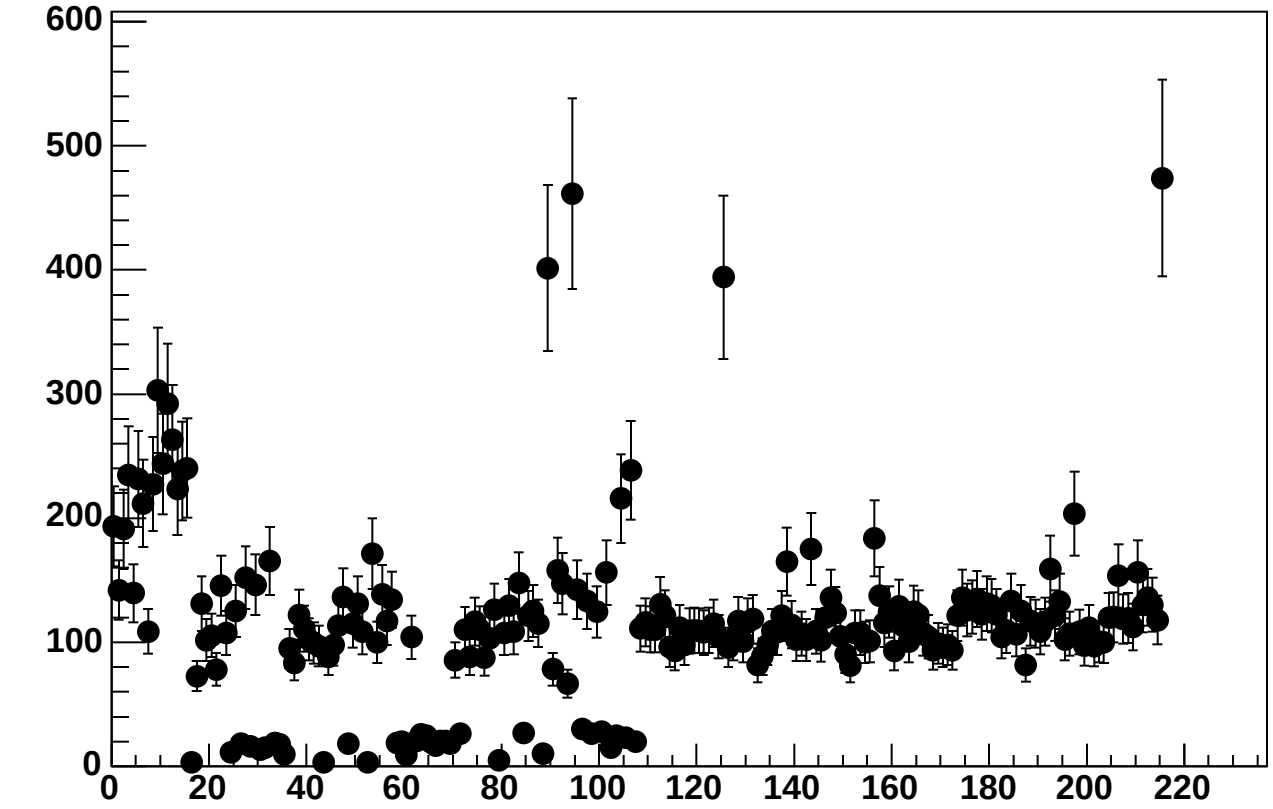
Enable 5, DAC=1600, Hold=100, ADC Noise vs 18*Chip+Chan



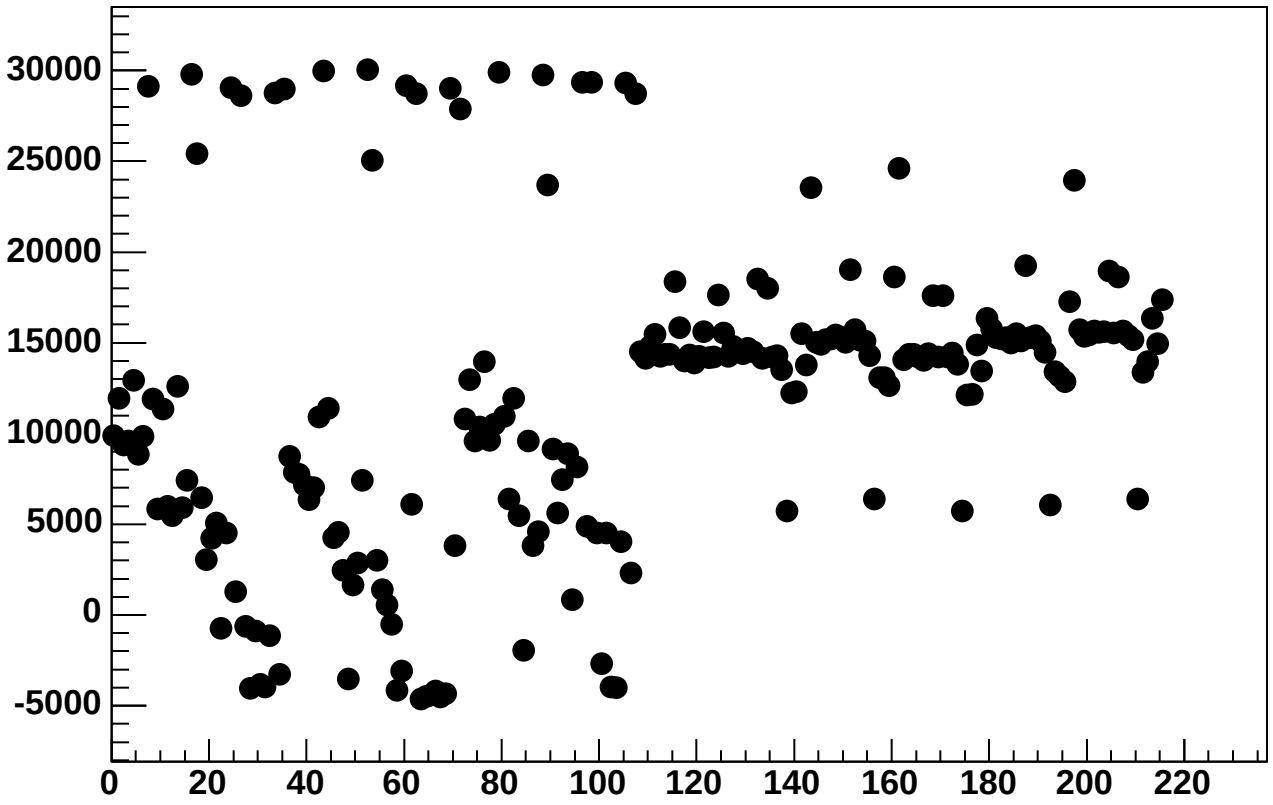
Enable 5, DAC=1600, Hold=105, ADC Mean vs 18*Chip+Chan



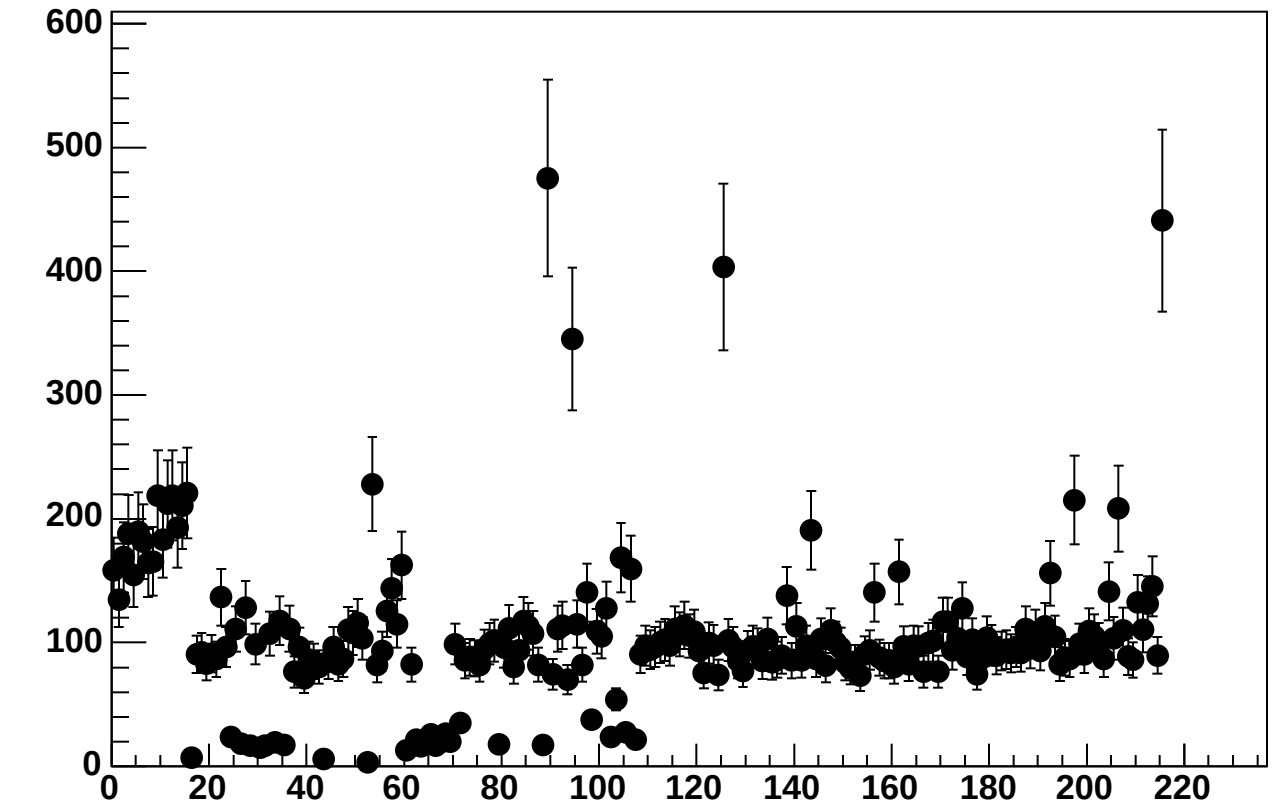
Enable 5, DAC=1600, Hold=105, ADC Noise vs 18*Chip+Chan



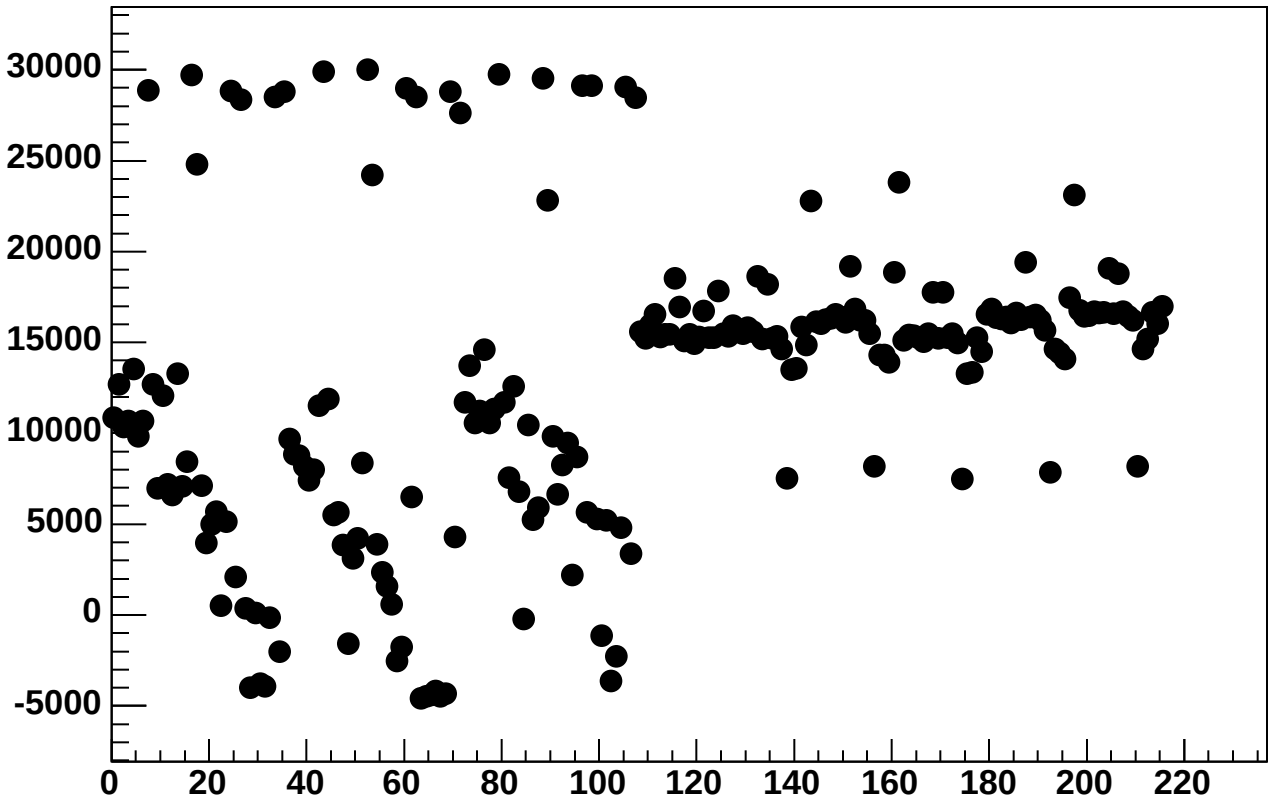
Enable 5, DAC=1600, Hold=110, ADC Mean vs 18*Chip+Chan



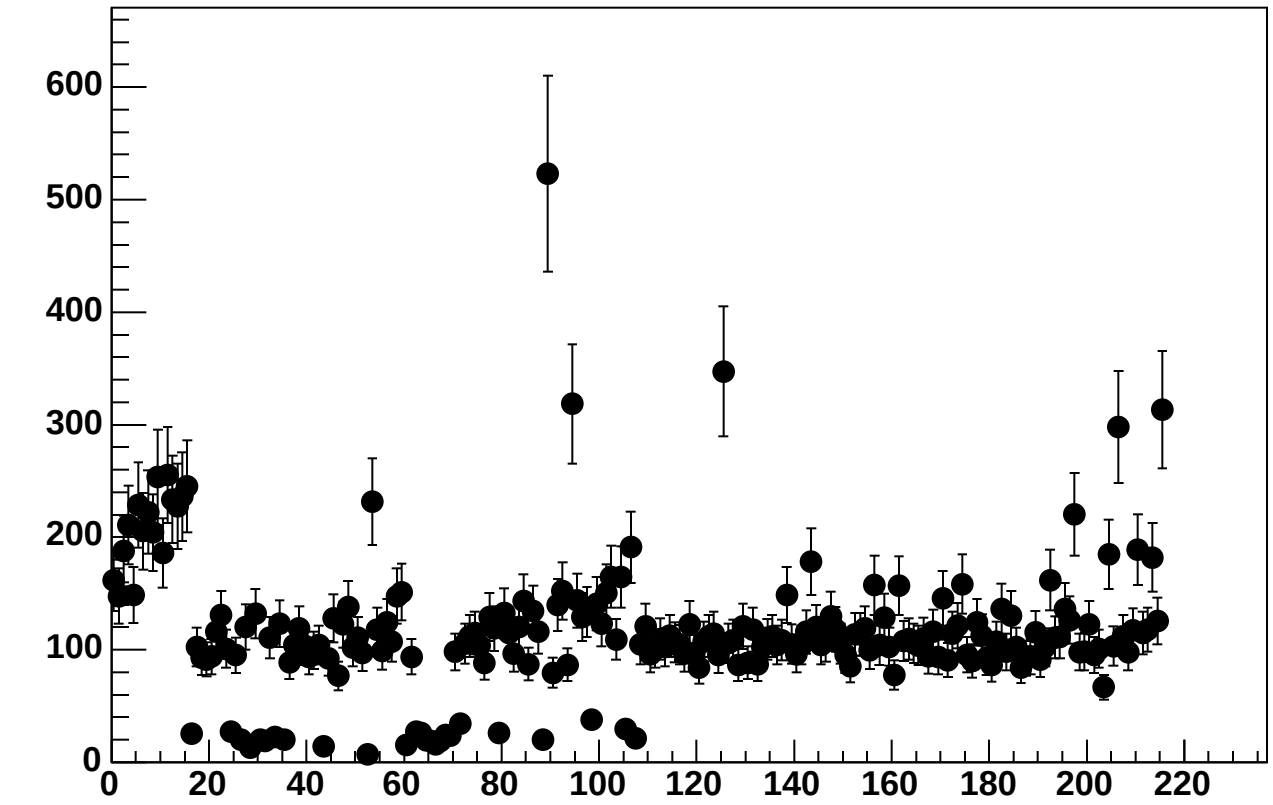
Enable 5, DAC=1600, Hold=110, ADC Noise vs 18*Chip+Chan



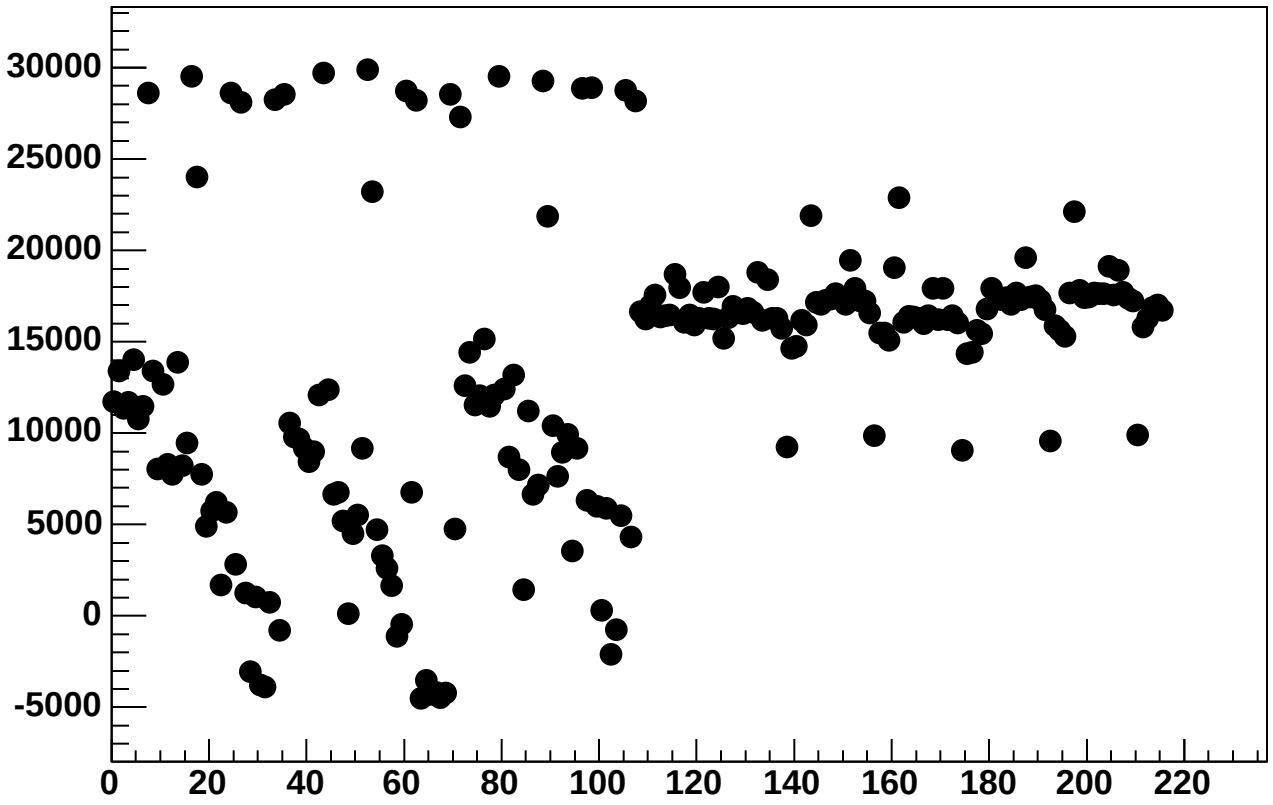
Enable 5, DAC=1600, Hold=115, ADC Mean vs 18*Chip+Chan



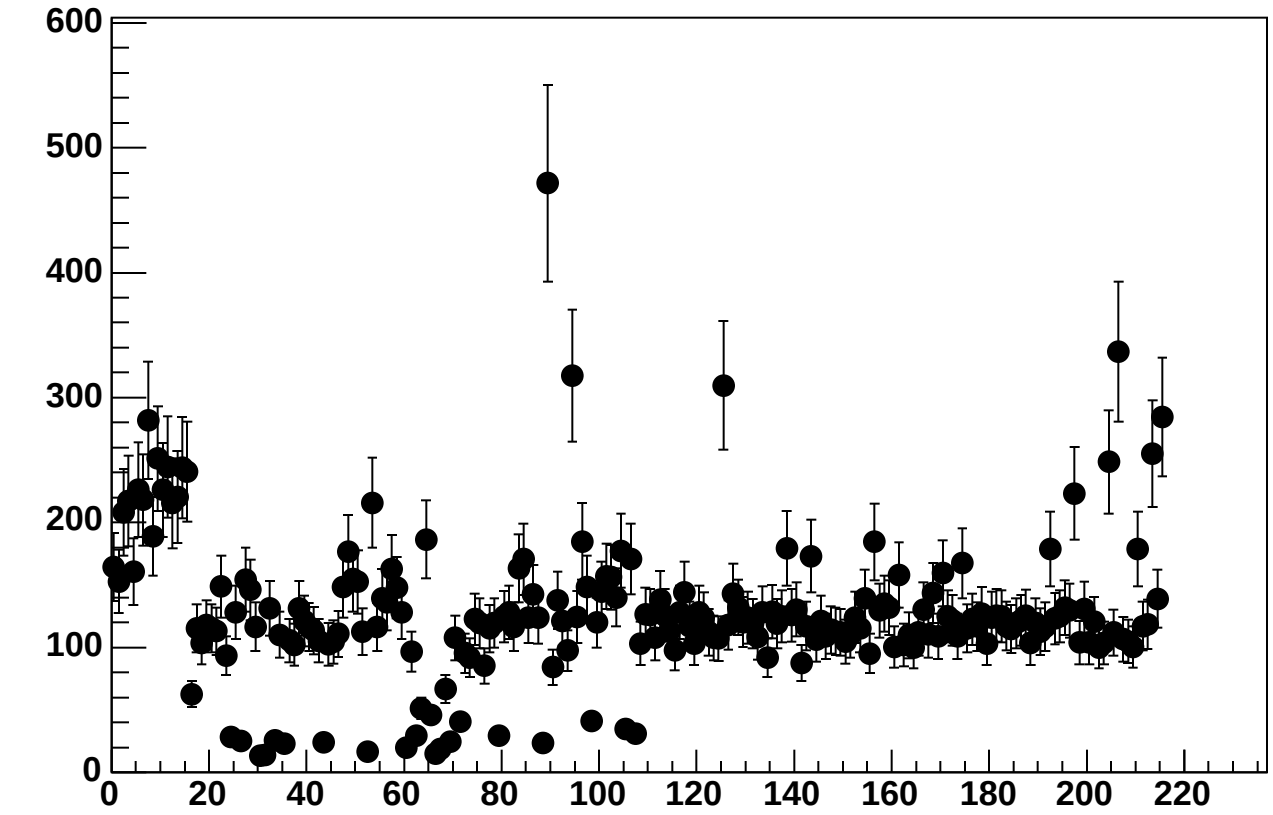
Enable 5, DAC=1600, Hold=115, ADC Noise vs 18*Chip+Chan



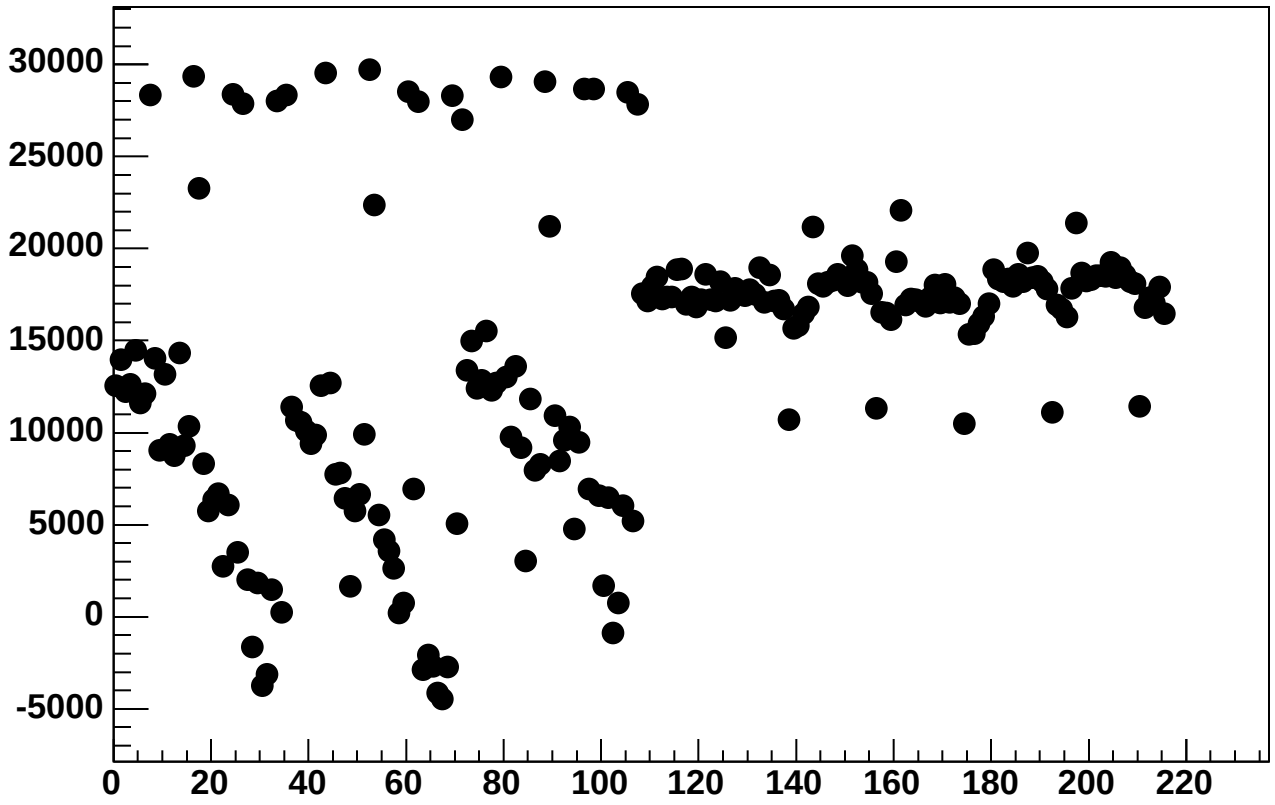
Enable 5, DAC=1600, Hold=120, ADC Mean vs 18*Chip+Chan



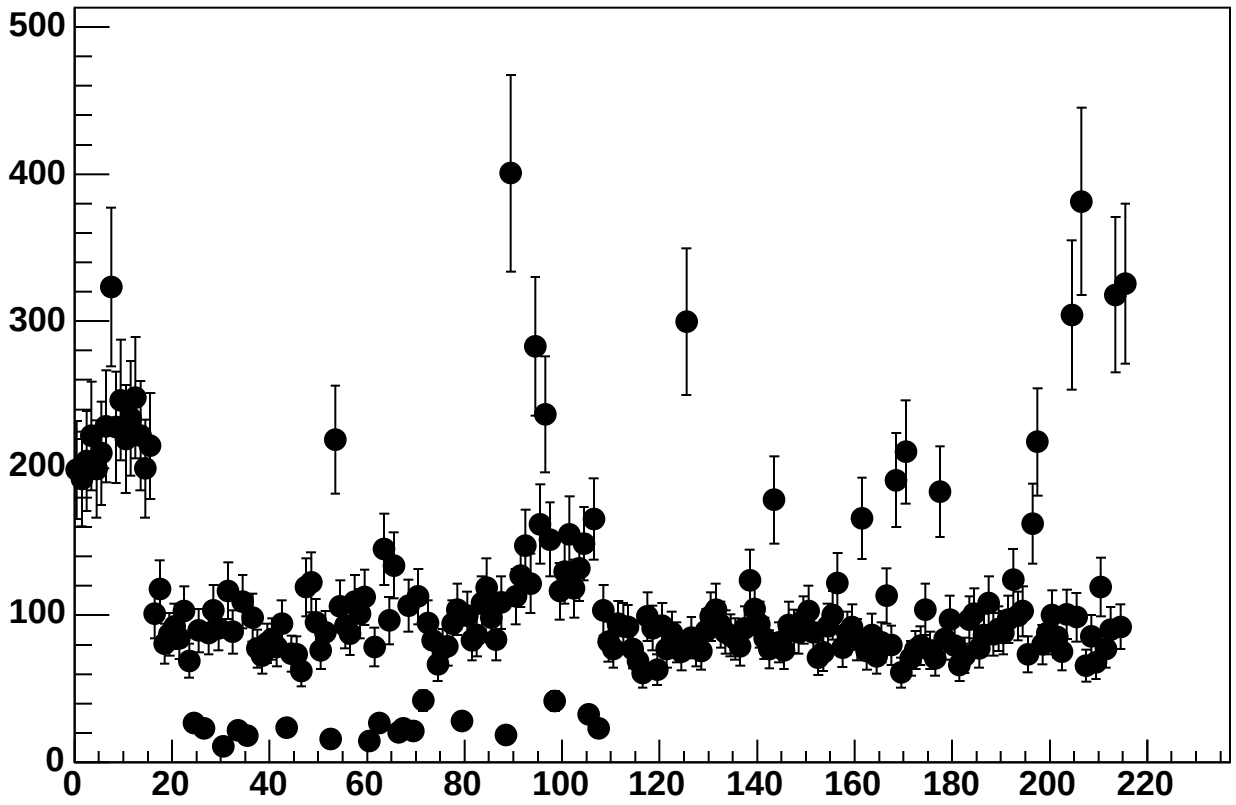
Enable 5, DAC=1600, Hold=120, ADC Noise vs 18*Chip+Chan



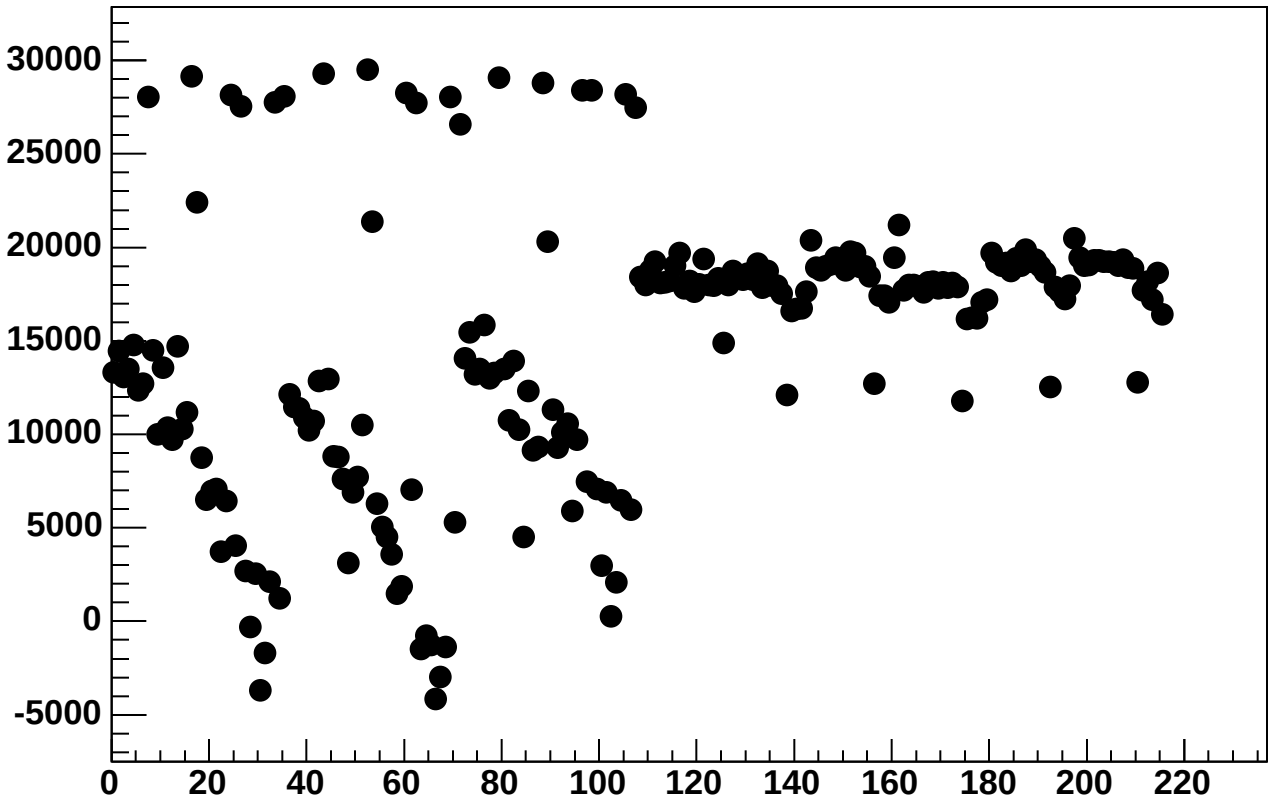
Enable 5, DAC=1600, Hold=125, ADC Mean vs 18*Chip+Chan



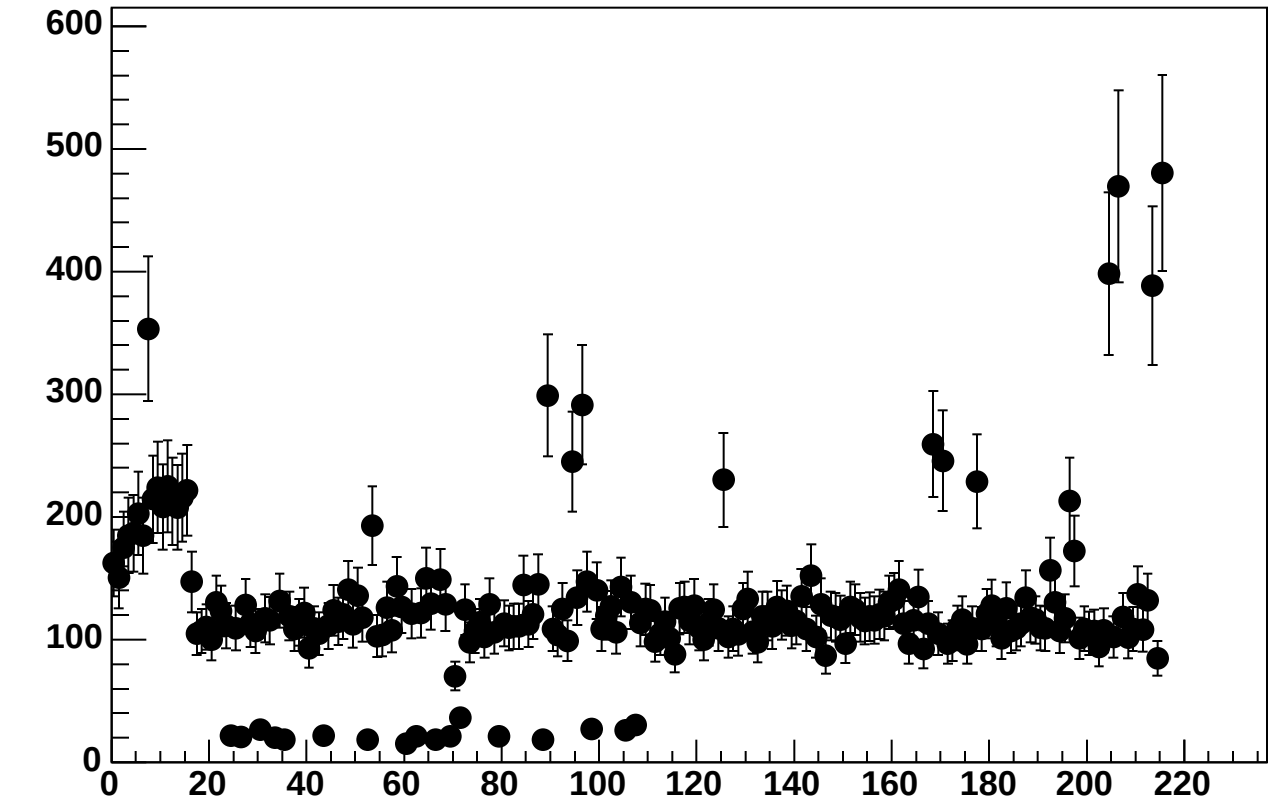
Enable 5, DAC=1600, Hold=125, ADC Noise vs 18*Chip+Chan



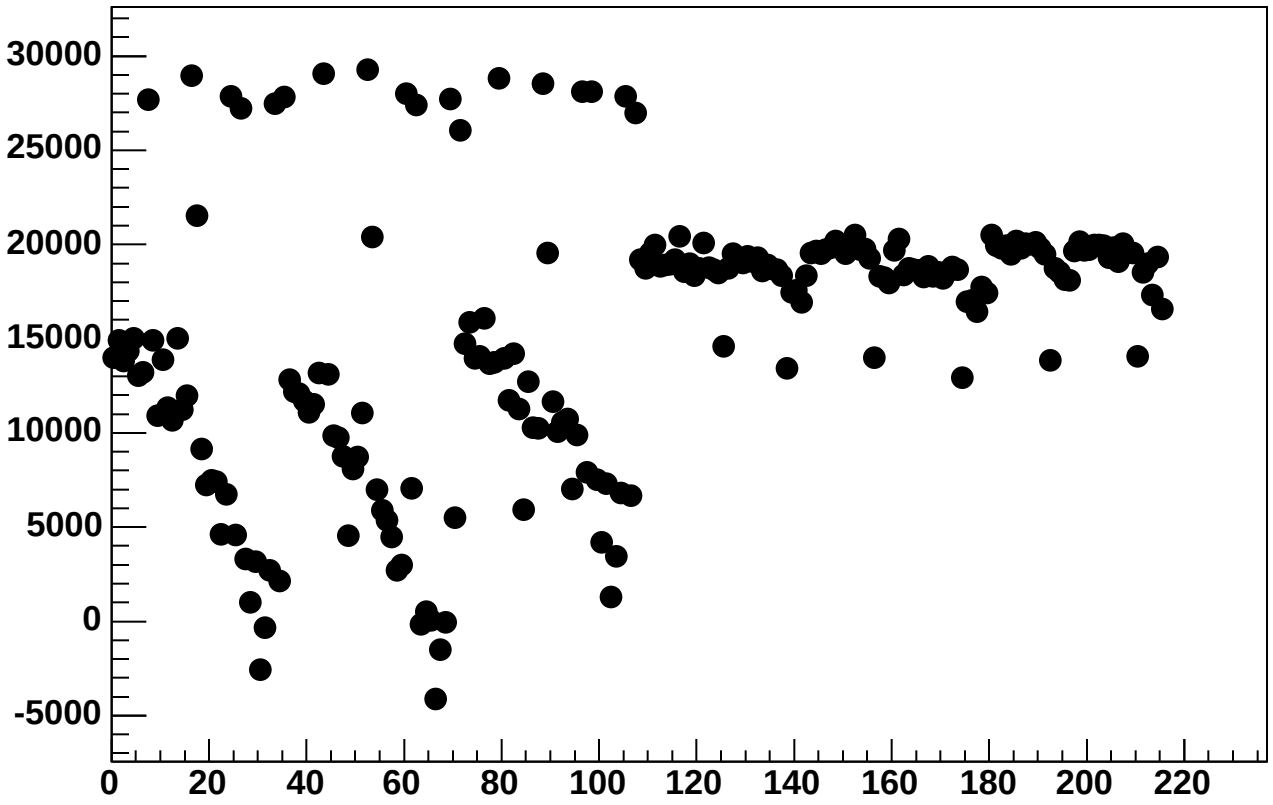
Enable 5, DAC=1600, Hold=130, ADC Mean vs 18*Chip+Chan



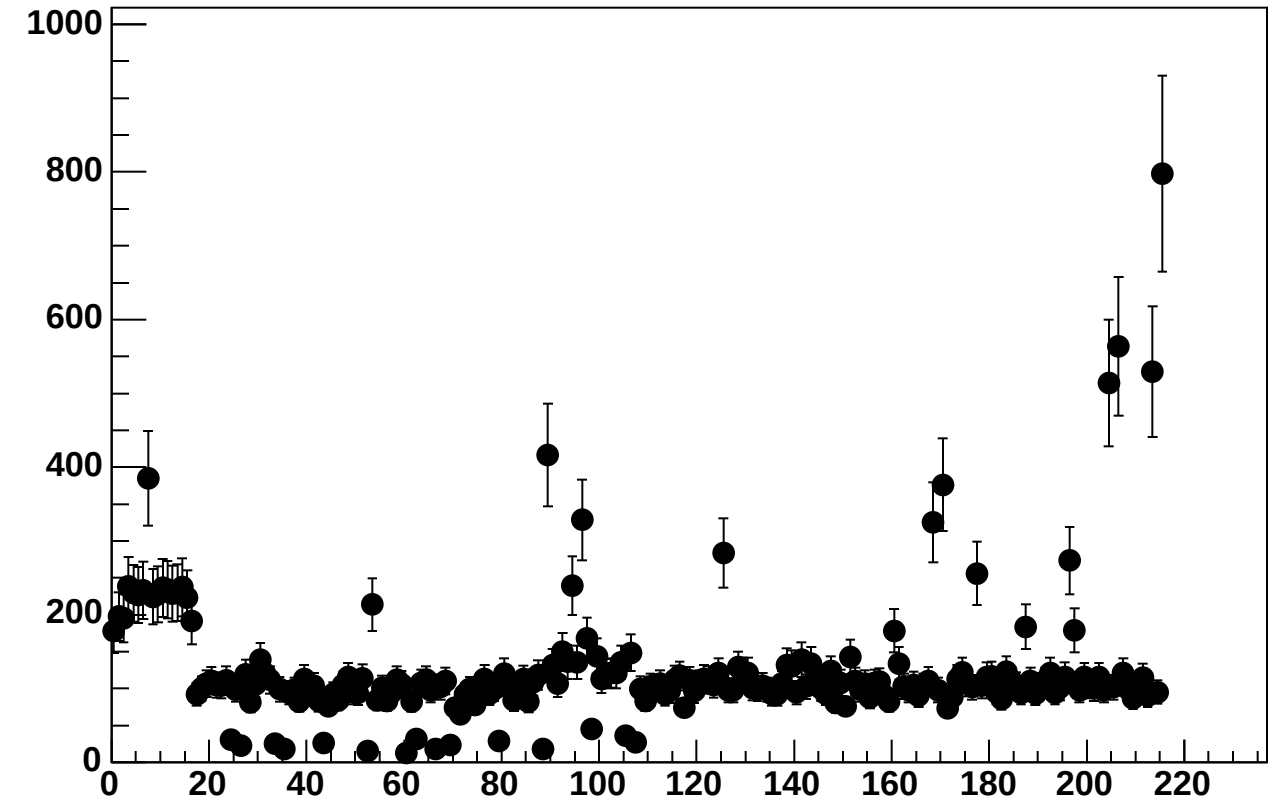
Enable 5, DAC=1600, Hold=130, ADC Noise vs 18*Chip+Chan



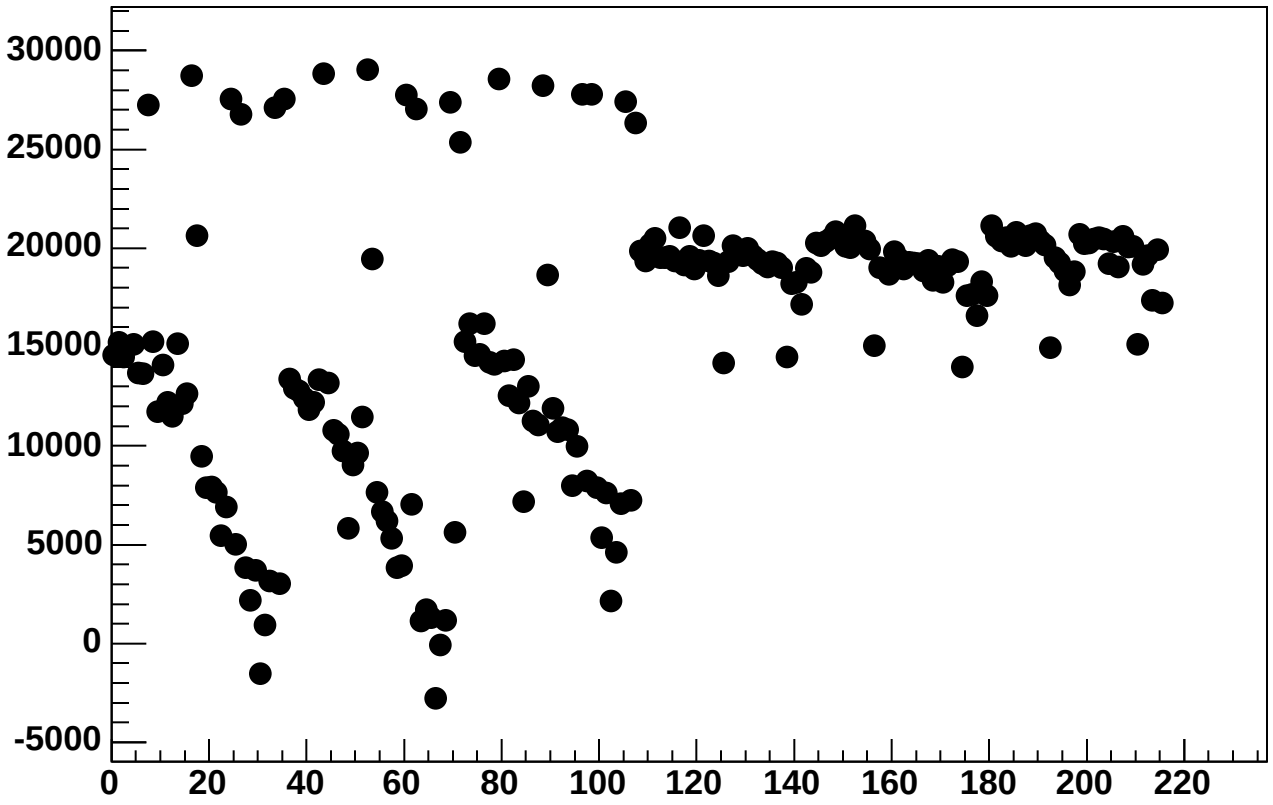
Enable 5, DAC=1600, Hold=135, ADC Mean vs 18*Chip+Chan



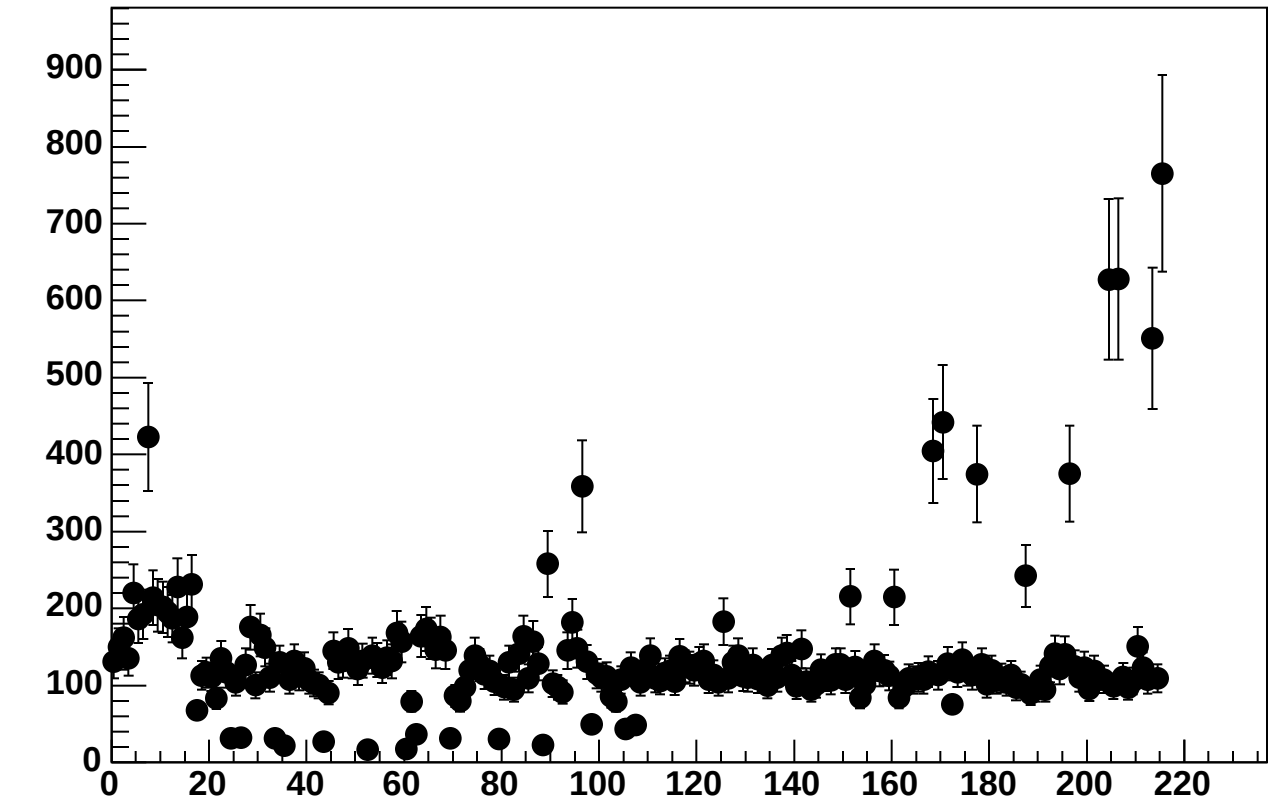
Enable 5, DAC=1600, Hold=135, ADC Noise vs 18*Chip+Chan



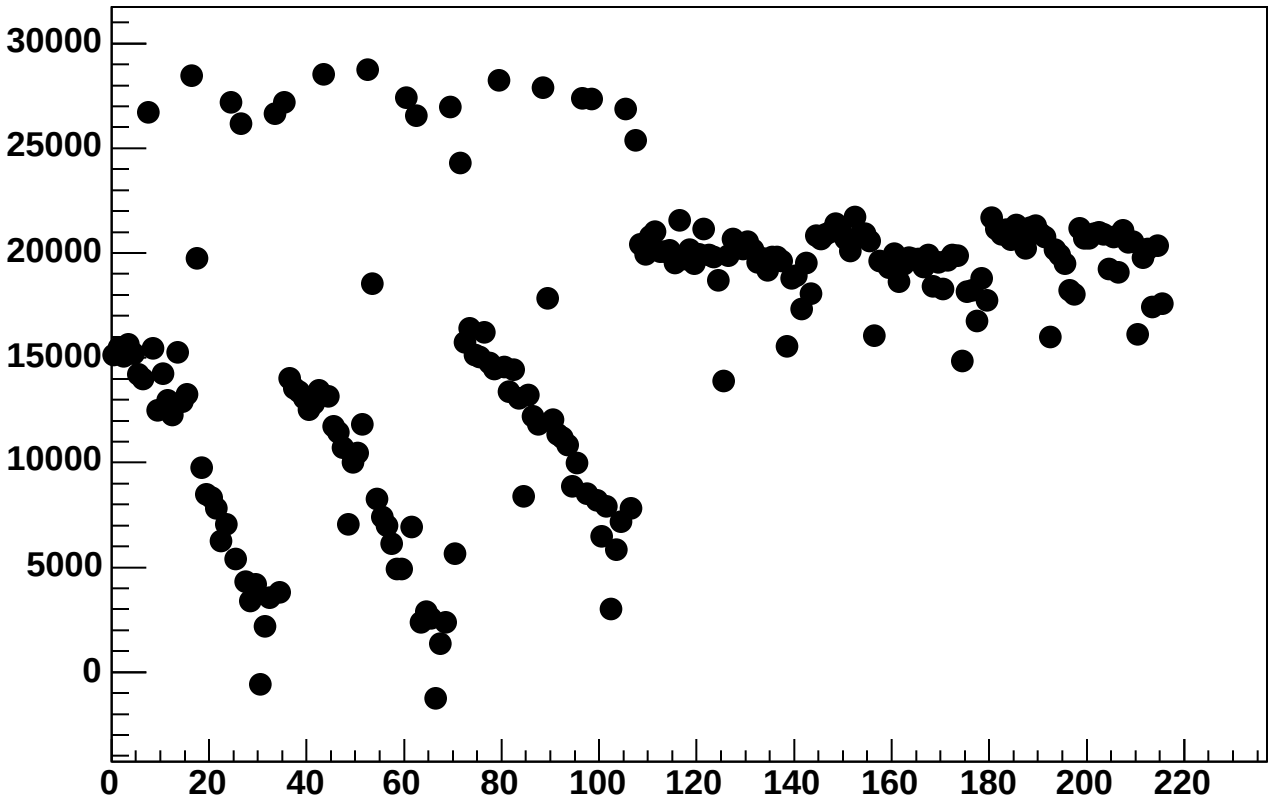
Enable 5, DAC=1600, Hold=140, ADC Mean vs 18*Chip+Chan



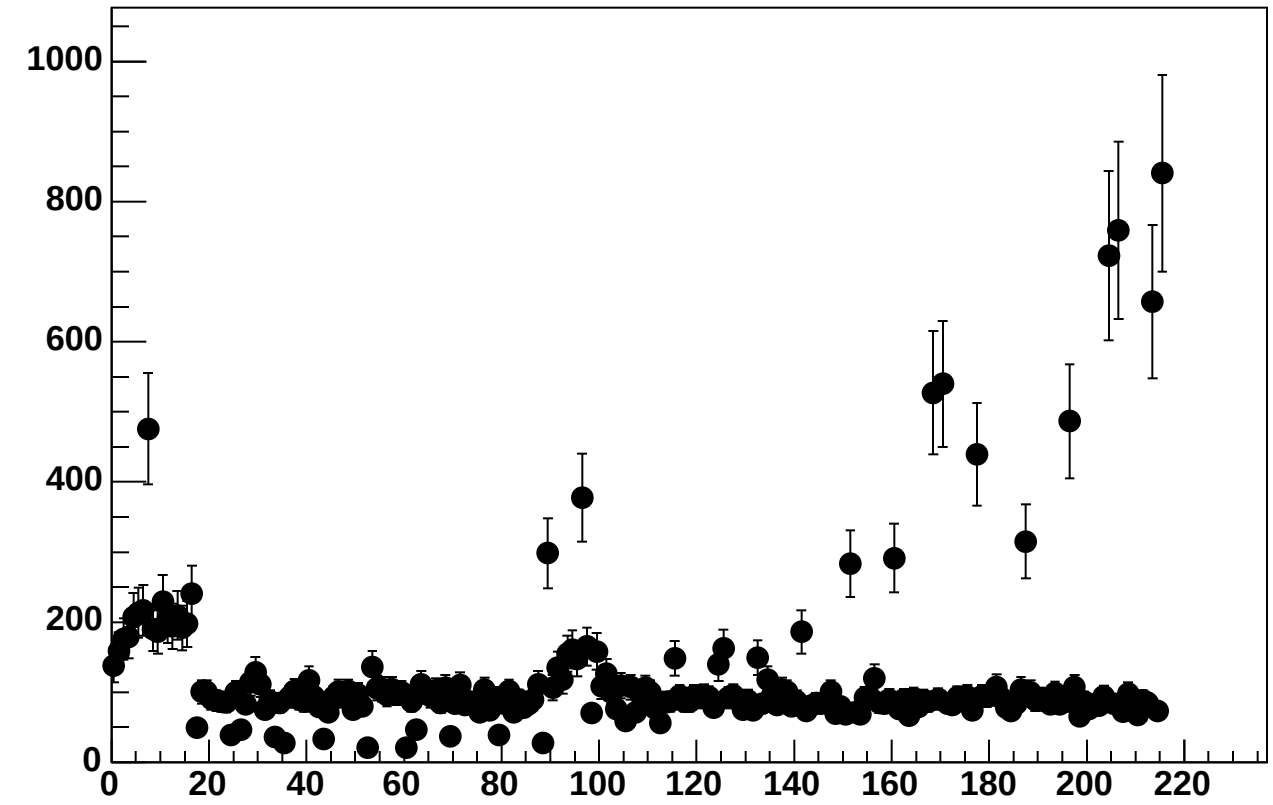
Enable 5, DAC=1600, Hold=140, ADC Noise vs 18*Chip+Chan



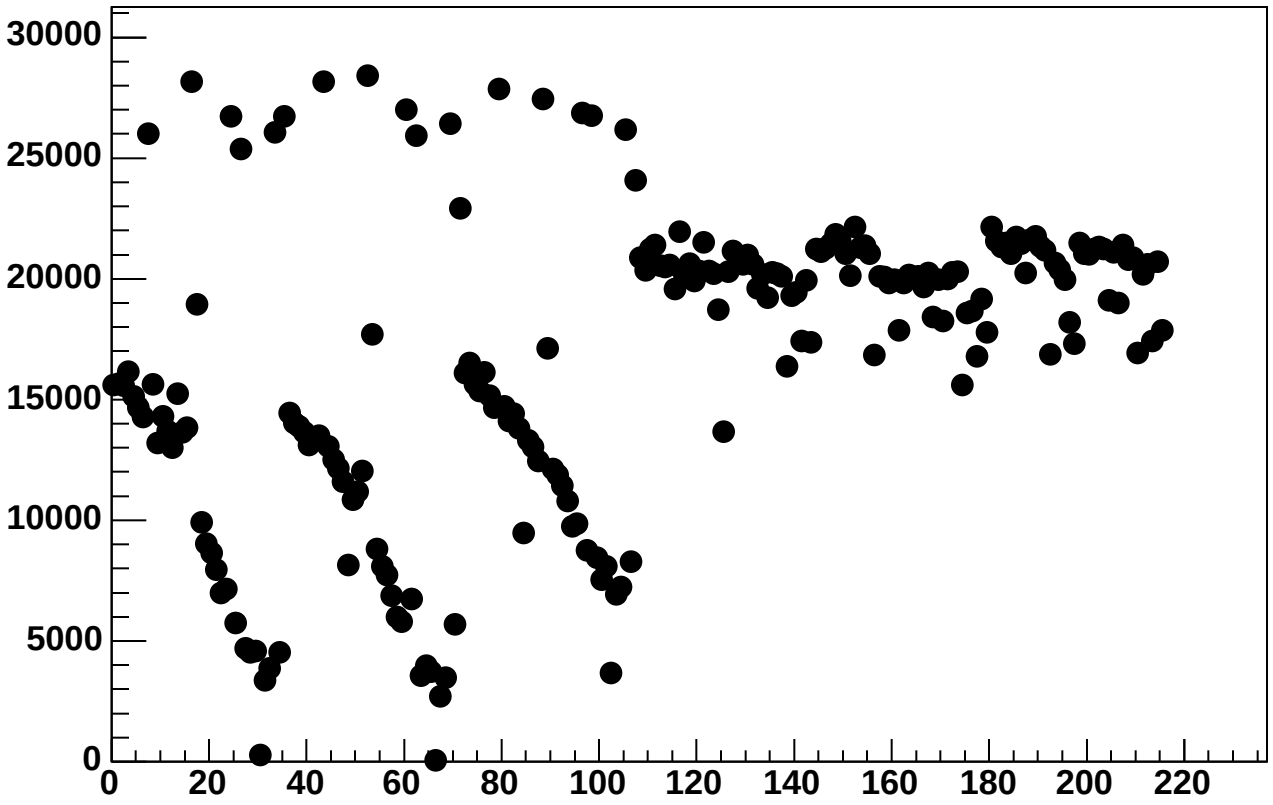
Enable 5, DAC=1600, Hold=145, ADC Mean vs 18*Chip+Chan



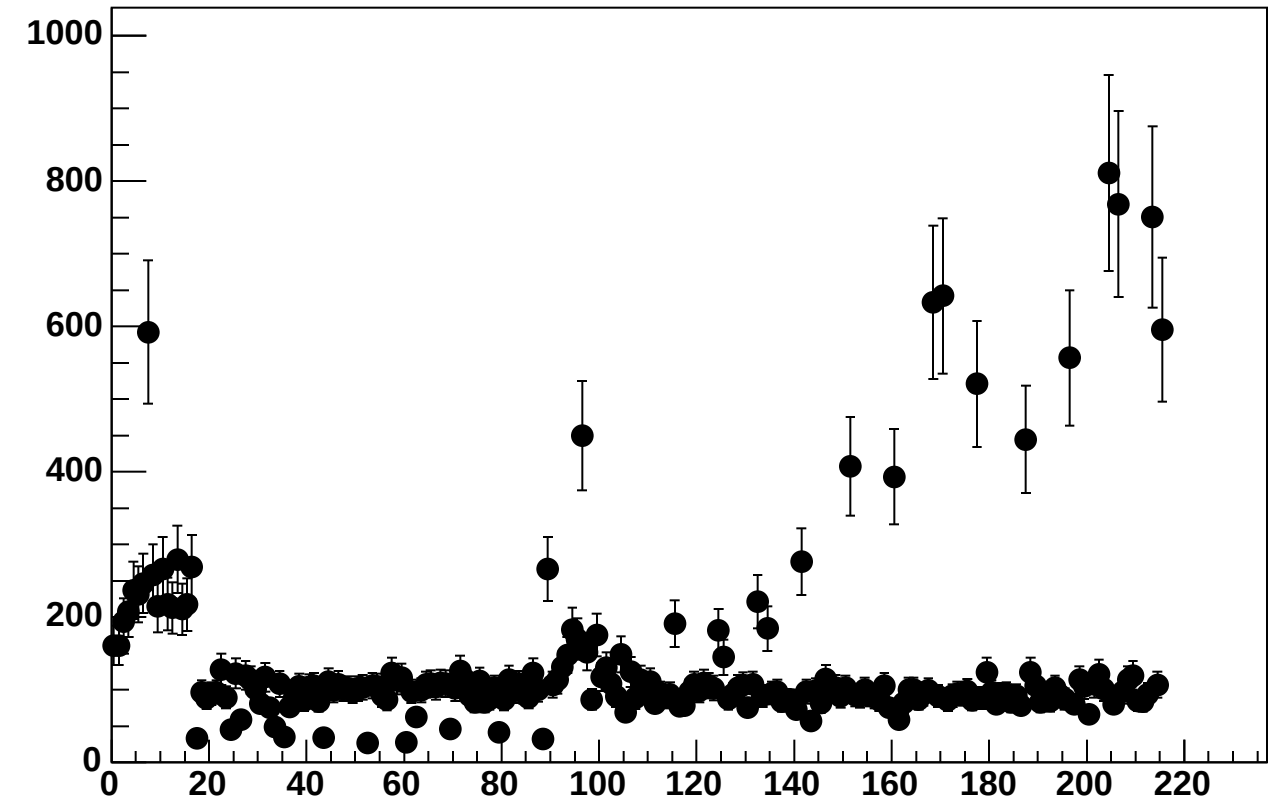
Enable 5, DAC=1600, Hold=145, ADC Noise vs 18*Chip+Chan



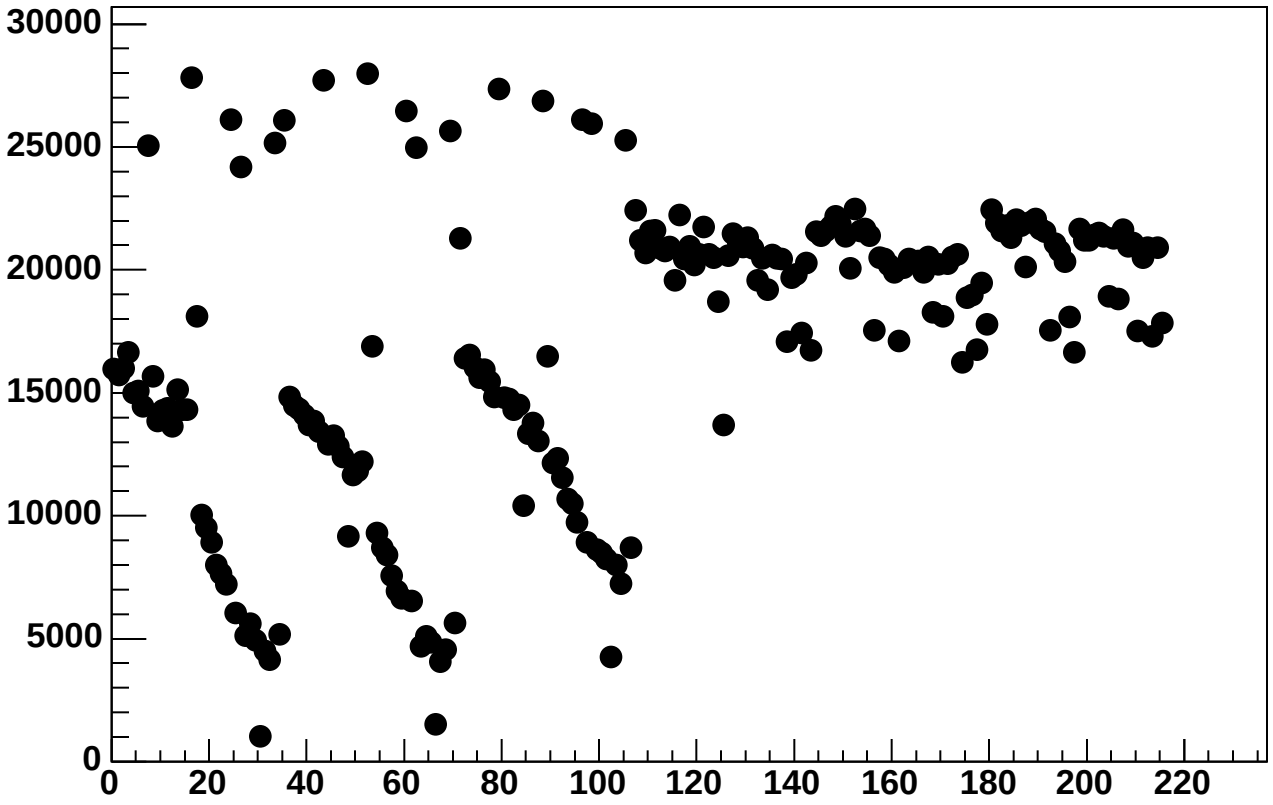
Enable 5, DAC=1600, Hold=150, ADC Mean vs 18*Chip+Chan



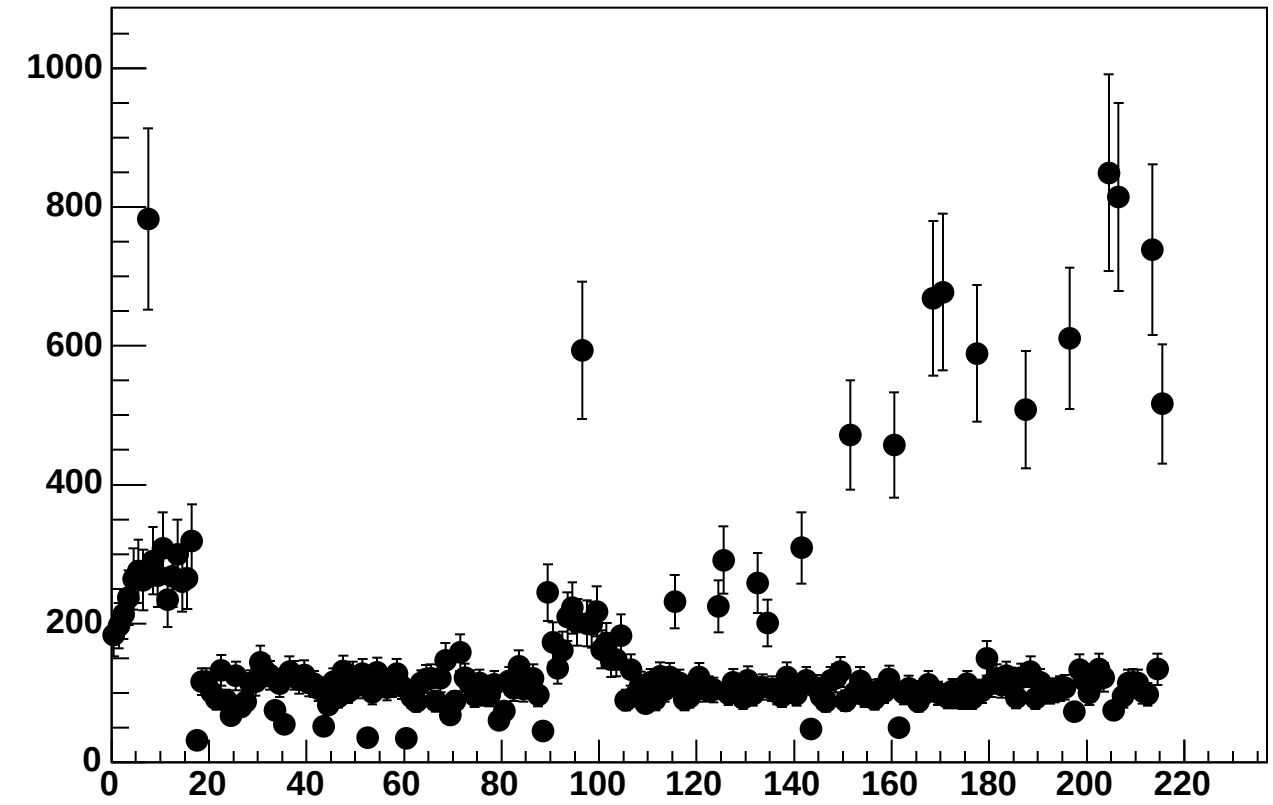
Enable 5, DAC=1600, Hold=150, ADC Noise vs 18*Chip+Chan



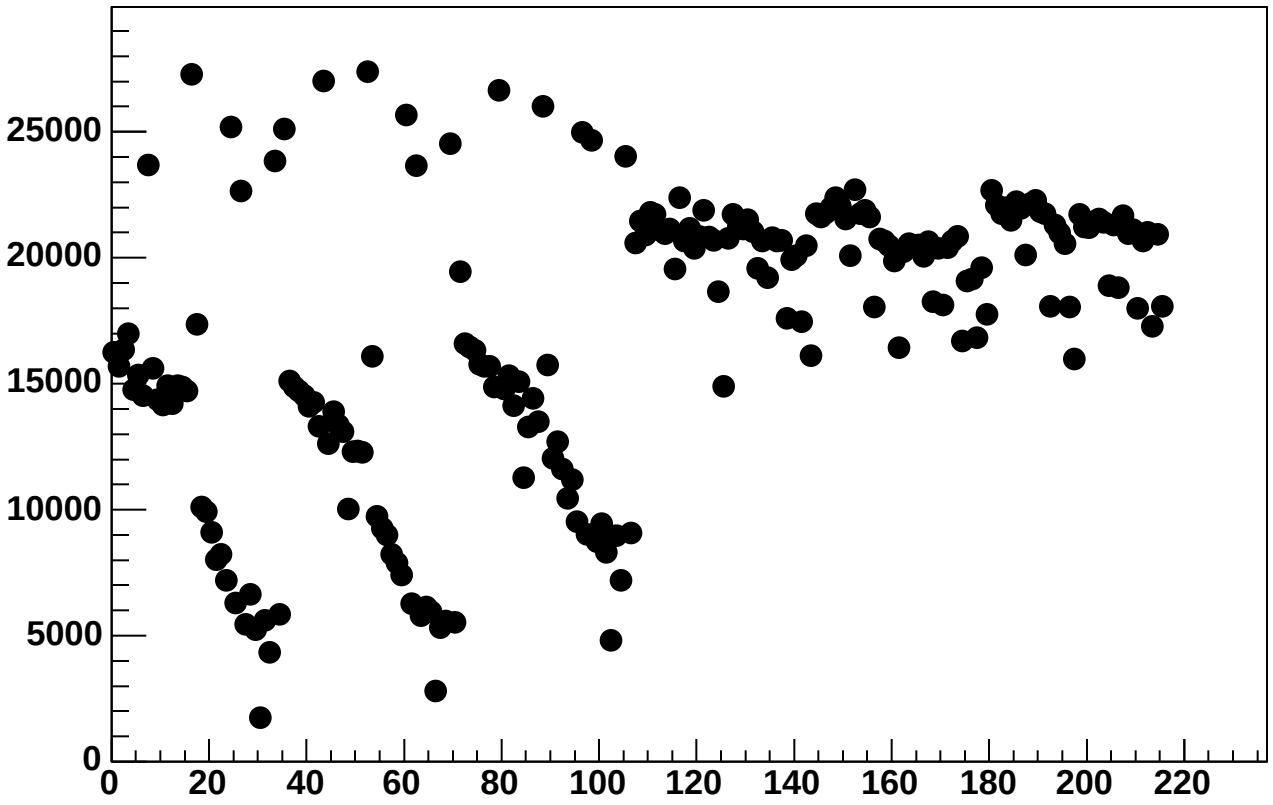
Enable 5, DAC=1600, Hold=155, ADC Mean vs 18*Chip+Chan



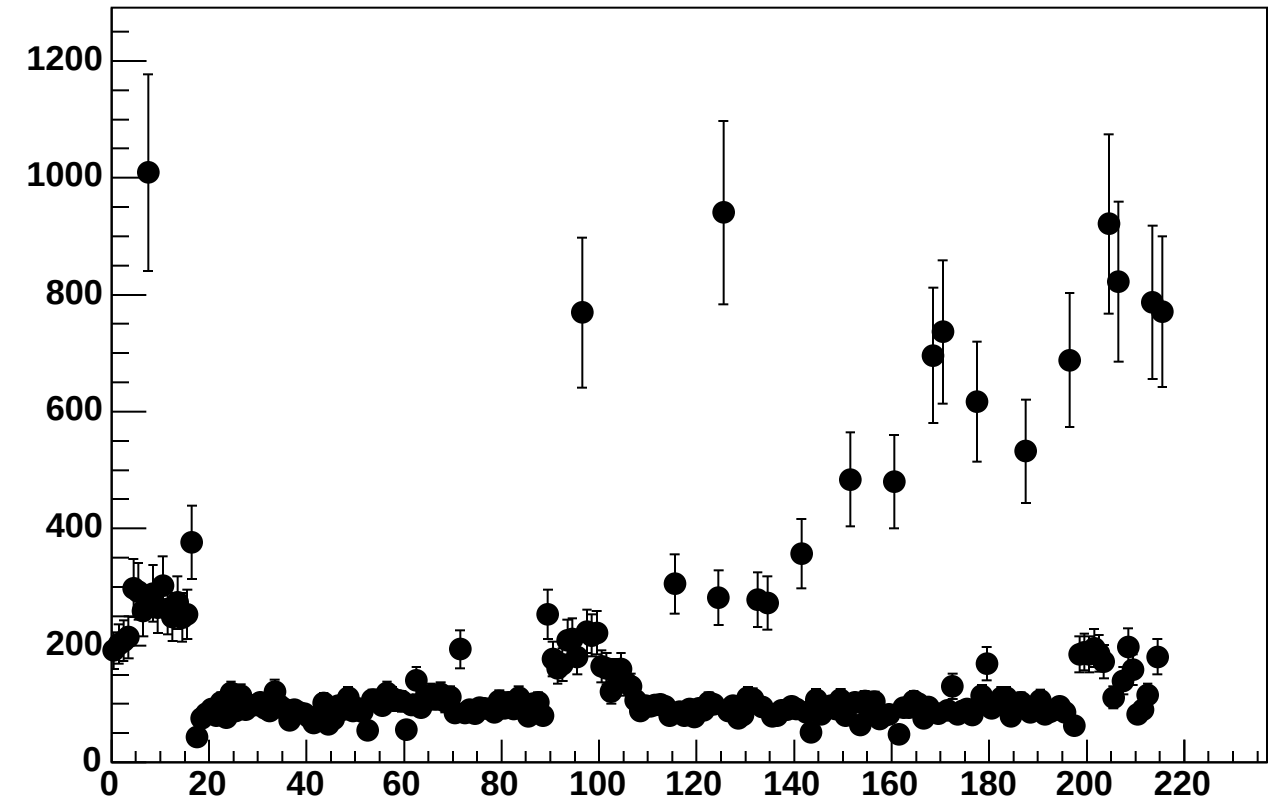
Enable 5, DAC=1600, Hold=155, ADC Noise vs 18*Chip+Chan



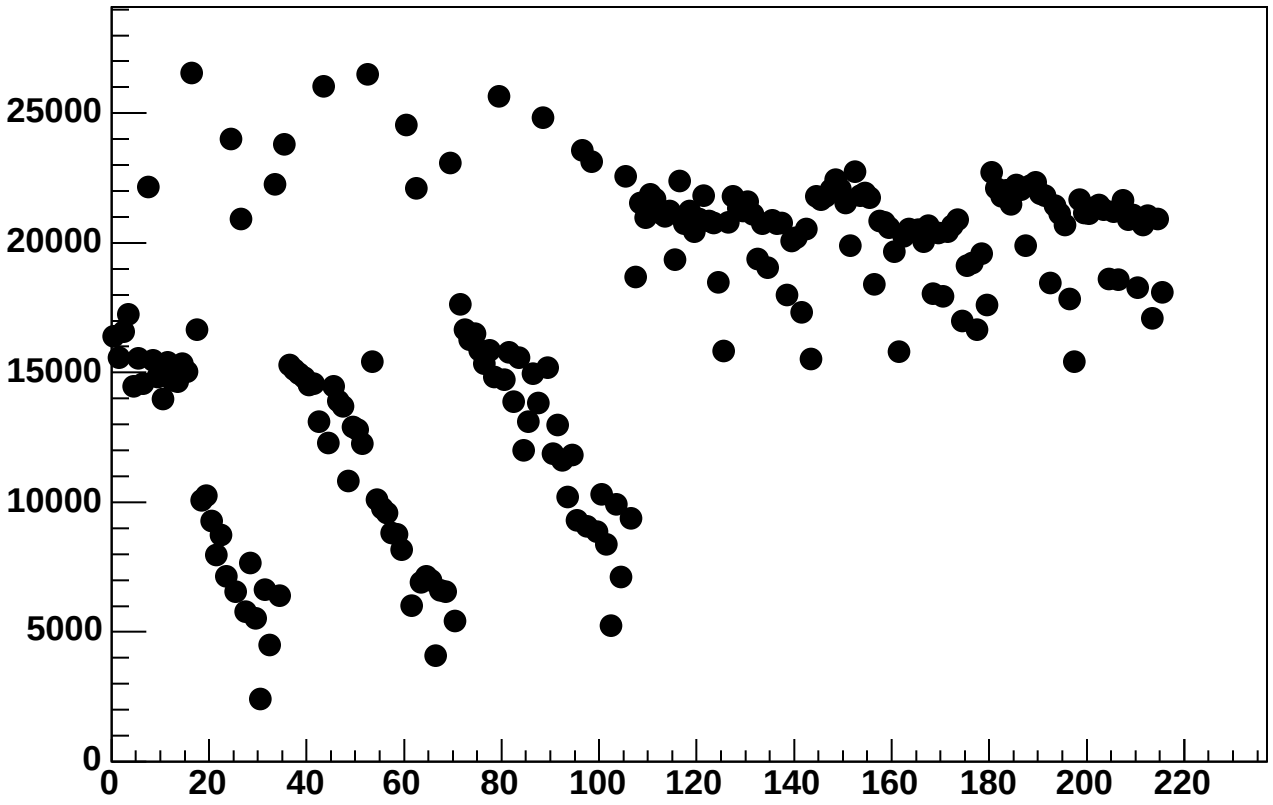
Enable 5, DAC=1600, Hold=160, ADC Mean vs 18*Chip+Chan



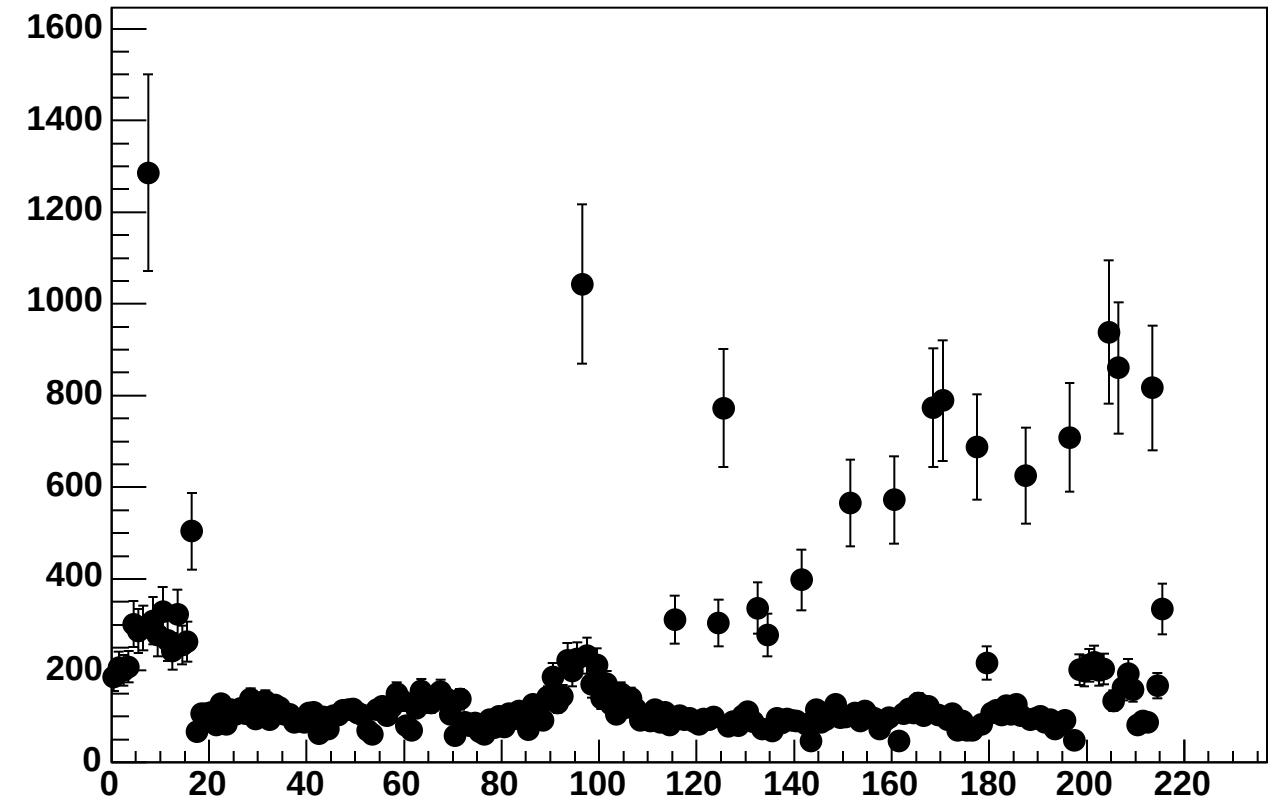
Enable 5, DAC=1600, Hold=160, ADC Noise vs 18*Chip+Chan



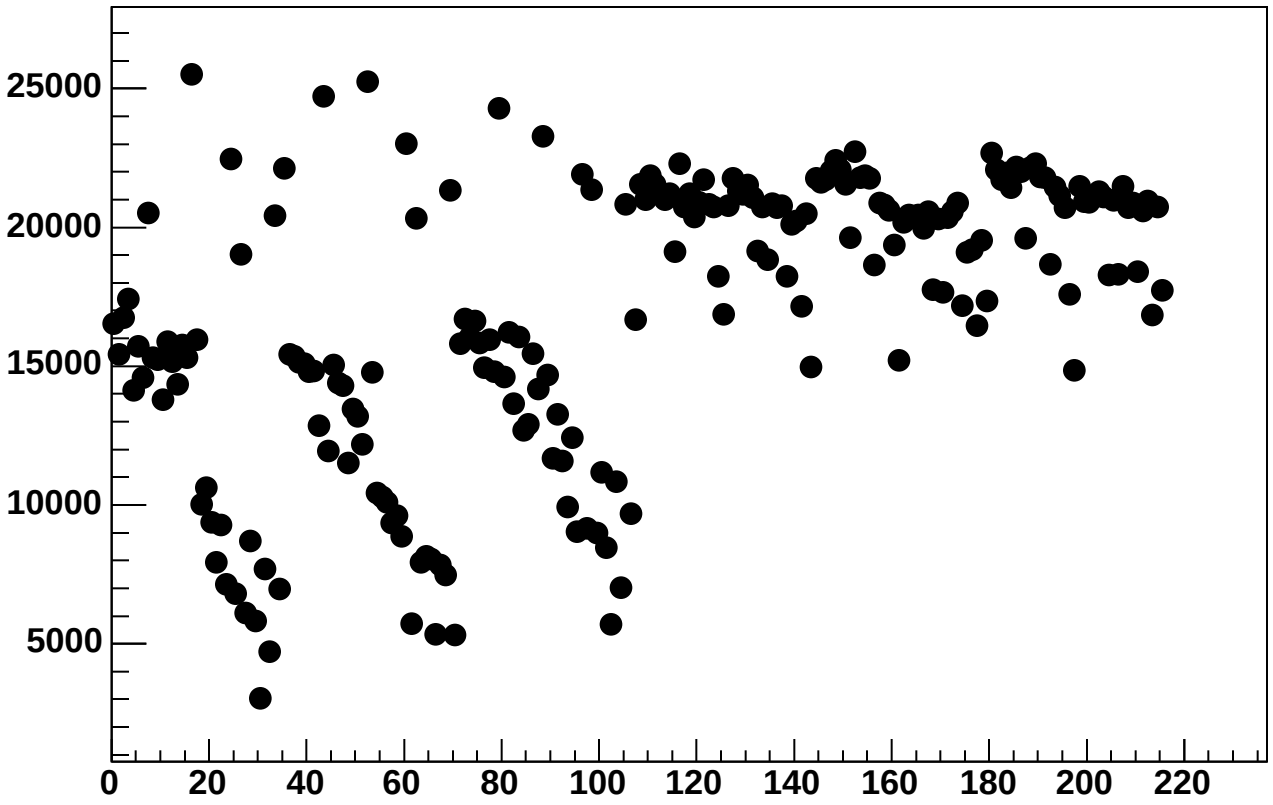
Enable 5, DAC=1600, Hold=165, ADC Mean vs 18*Chip+Chan



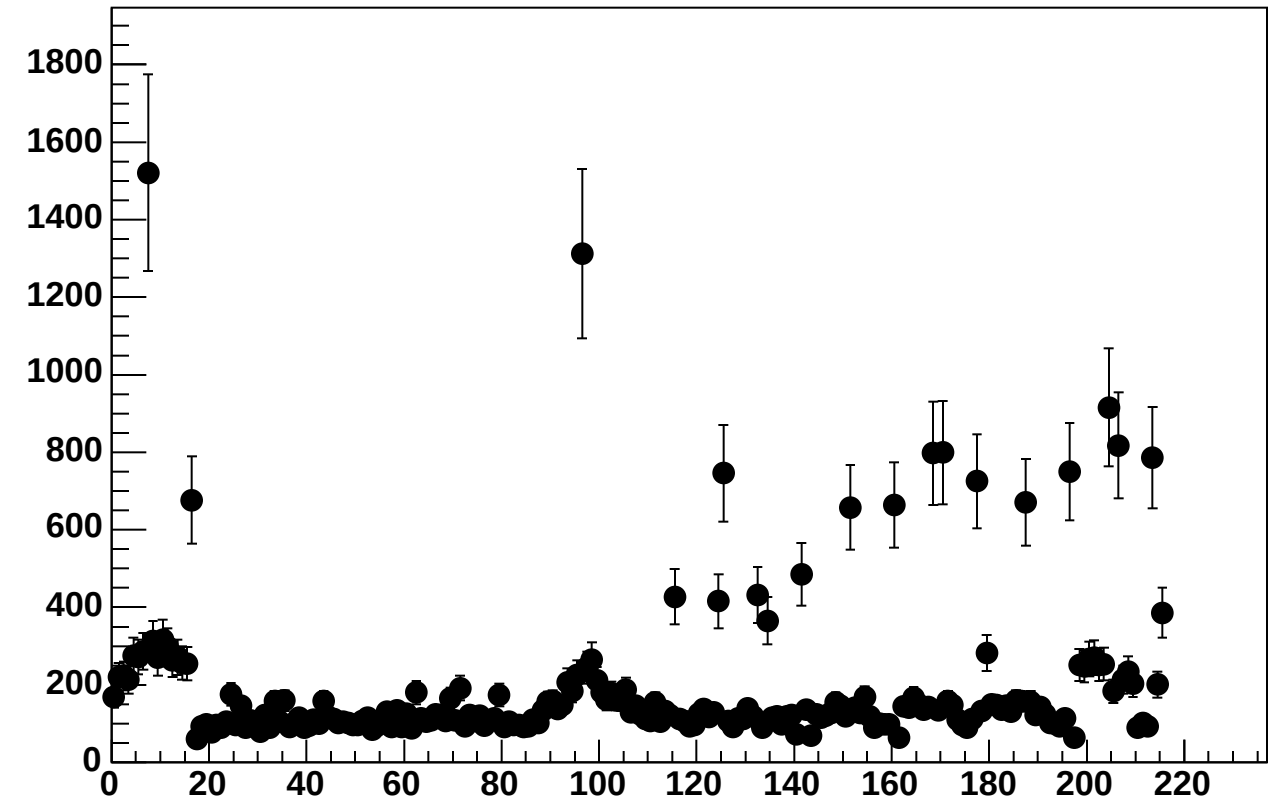
Enable 5, DAC=1600, Hold=165, ADC Noise vs 18*Chip+Chan



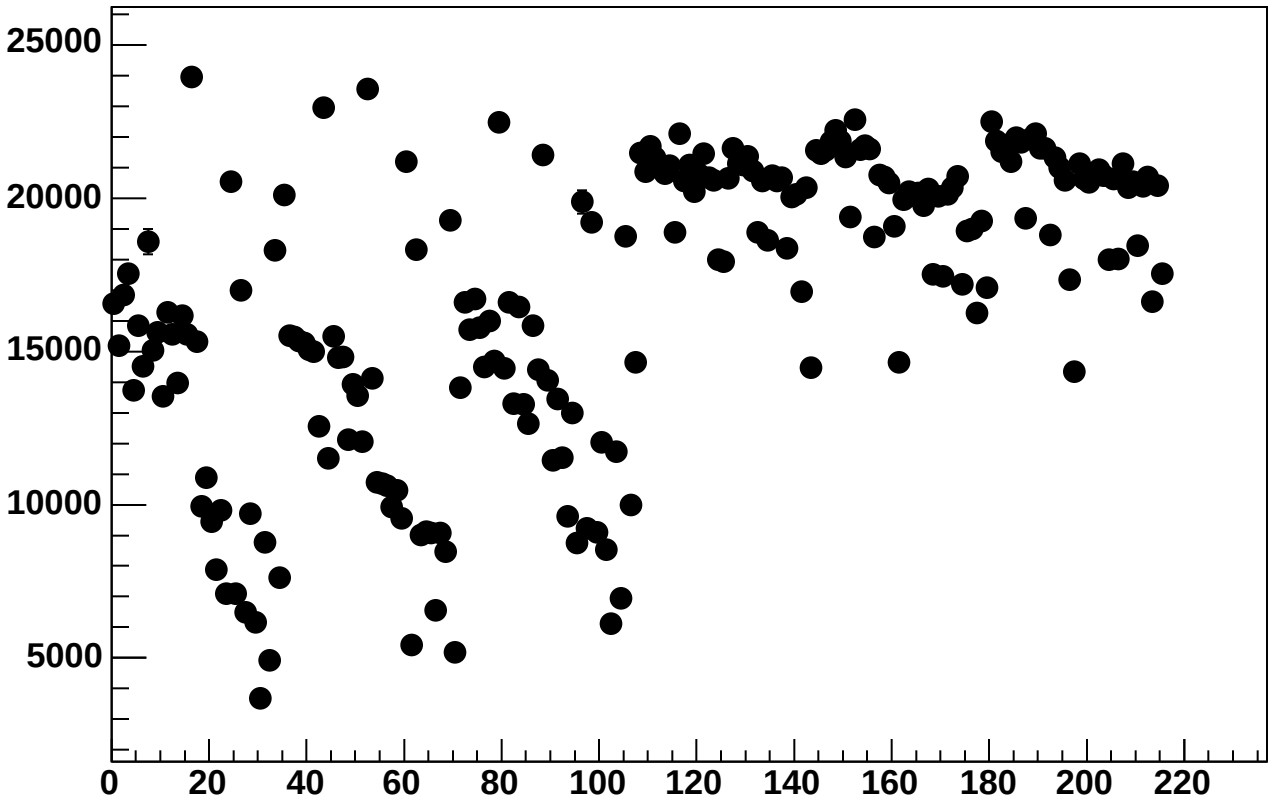
Enable 5, DAC=1600, Hold=170, ADC Mean vs 18*Chip+Chan



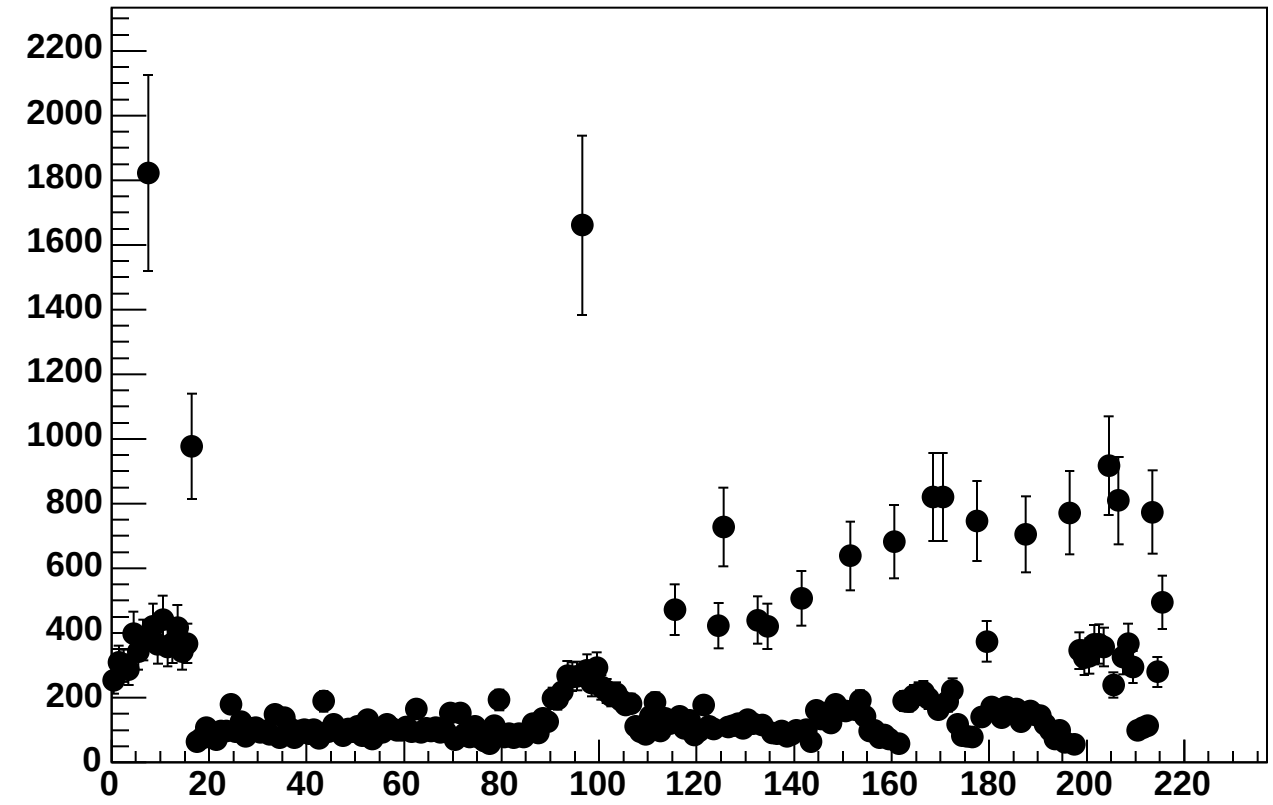
Enable 5, DAC=1600, Hold=170, ADC Noise vs 18*Chip+Chan



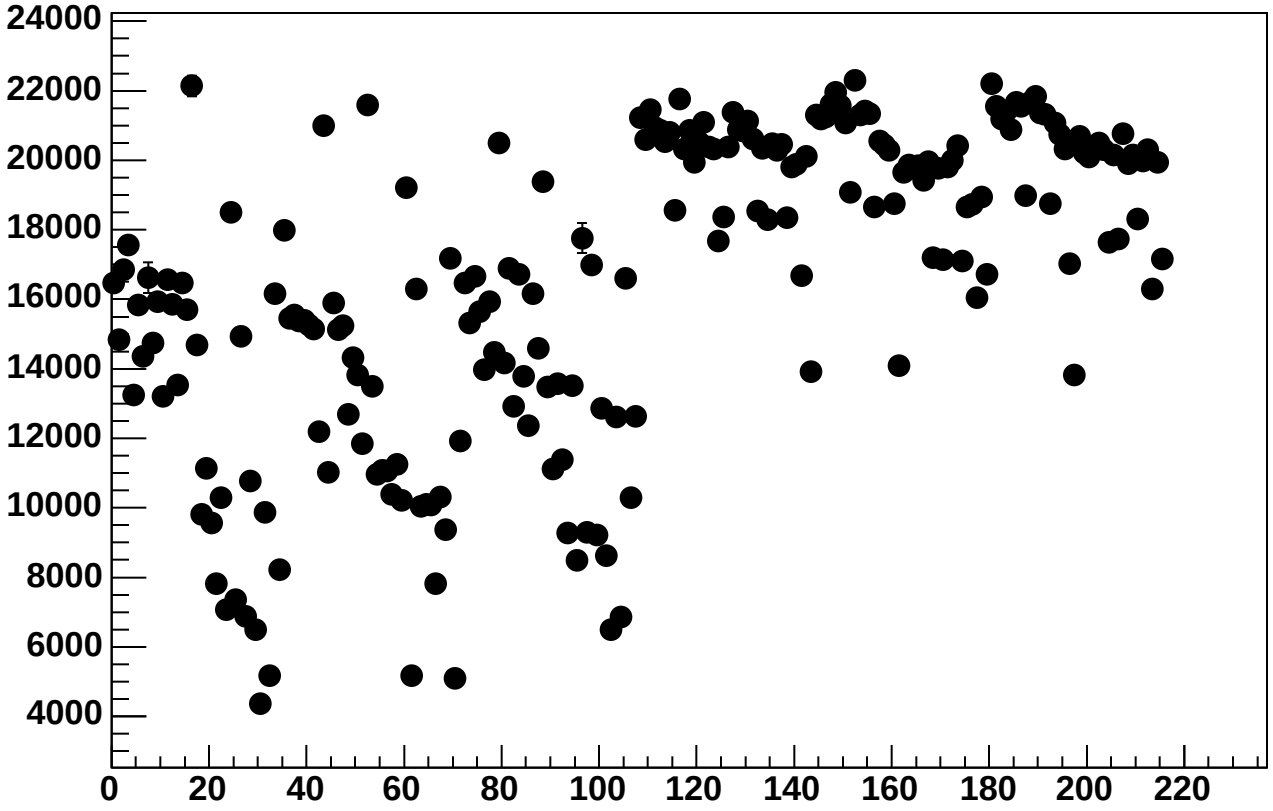
Enable 5, DAC=1600, Hold=175, ADC Mean vs 18*Chip+Chan



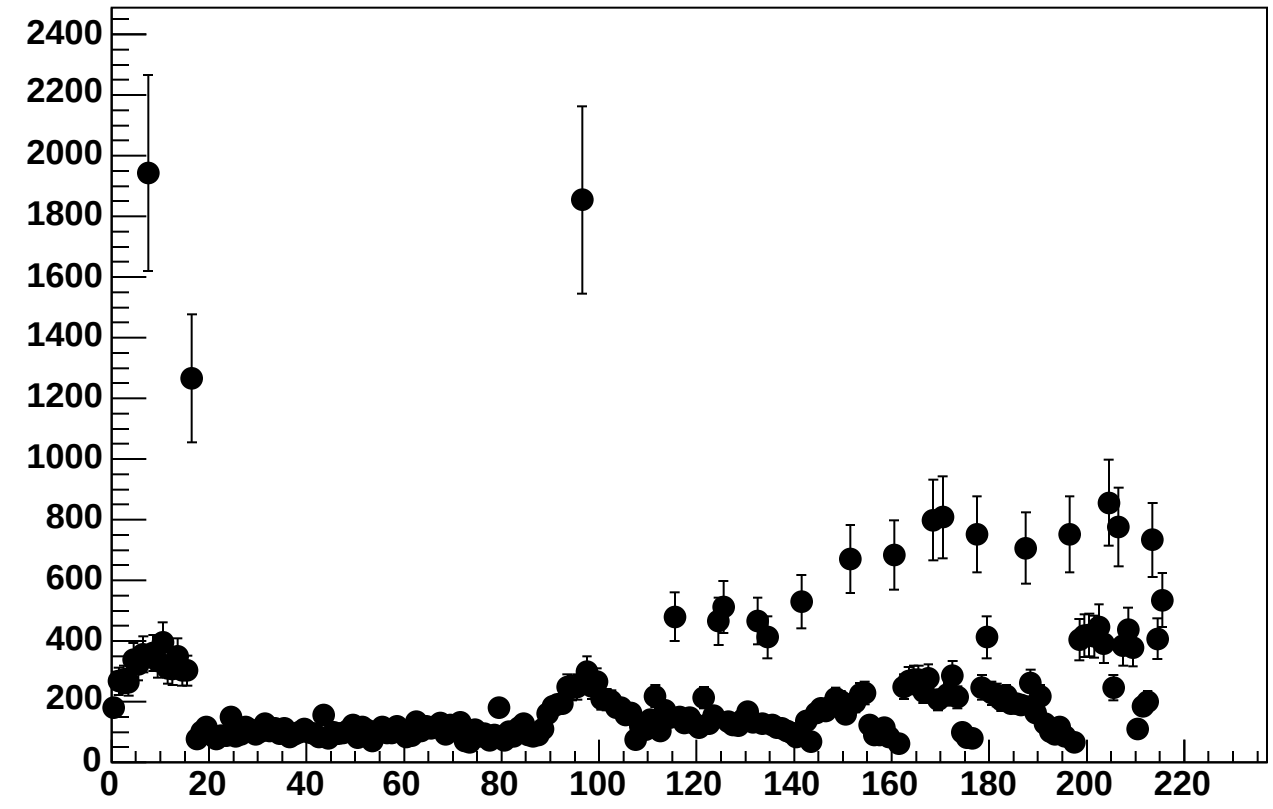
Enable 5, DAC=1600, Hold=175, ADC Noise vs 18*Chip+Chan



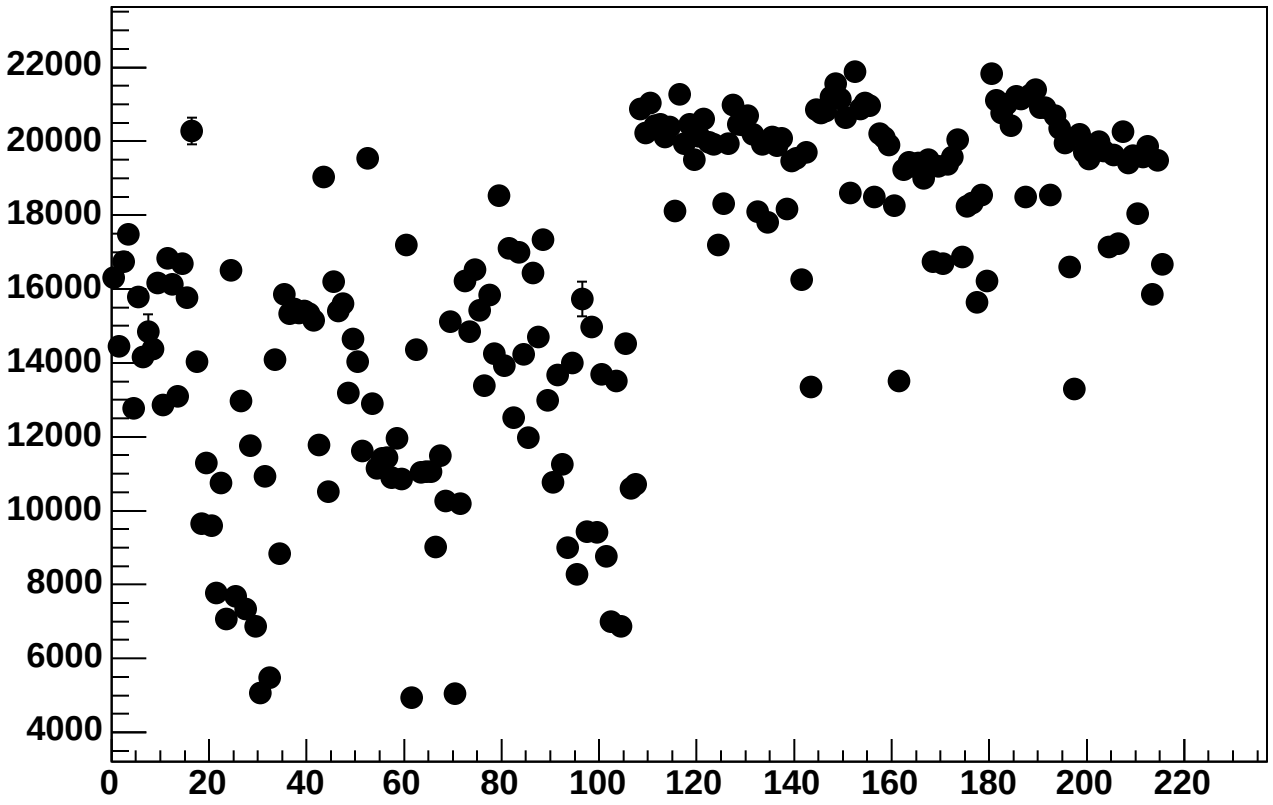
Enable 5, DAC=1600, Hold=180, ADC Mean vs 18*Chip+Chan



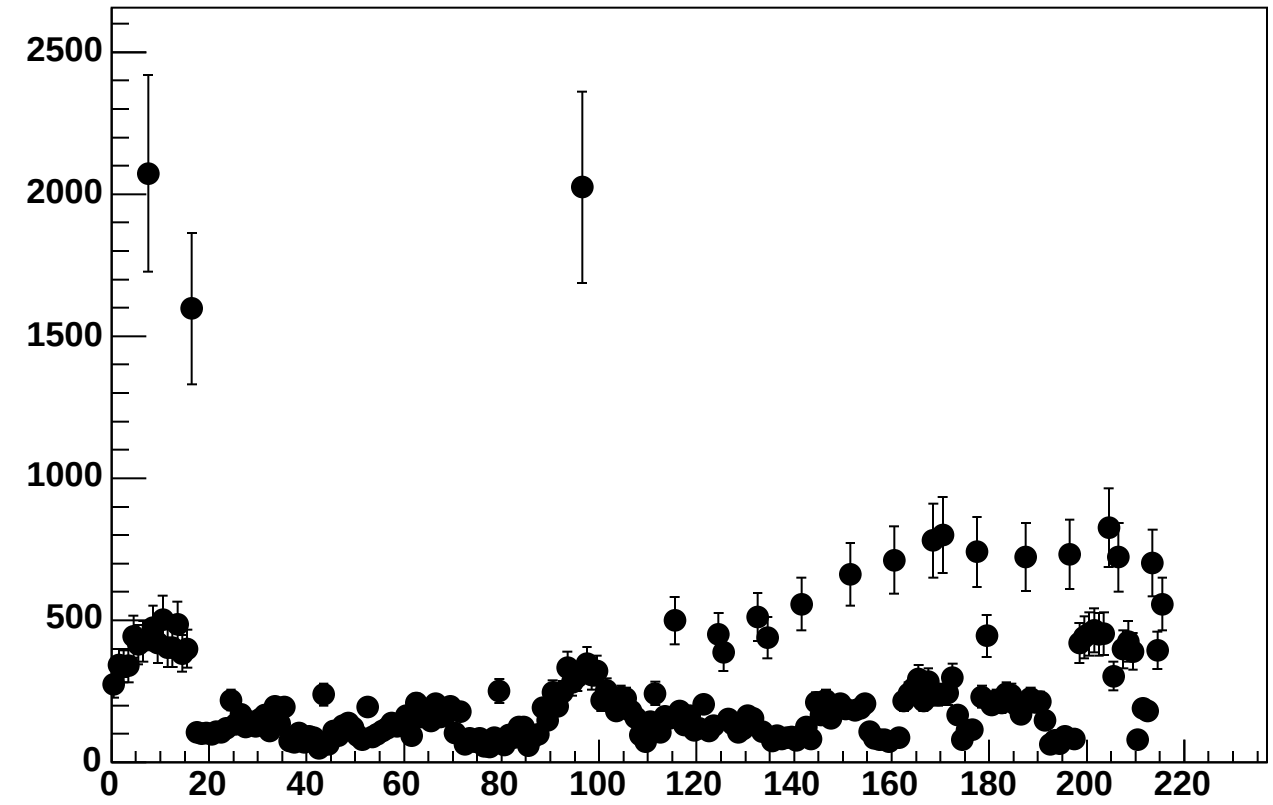
Enable 5, DAC=1600, Hold=180, ADC Noise vs 18*Chip+Chan



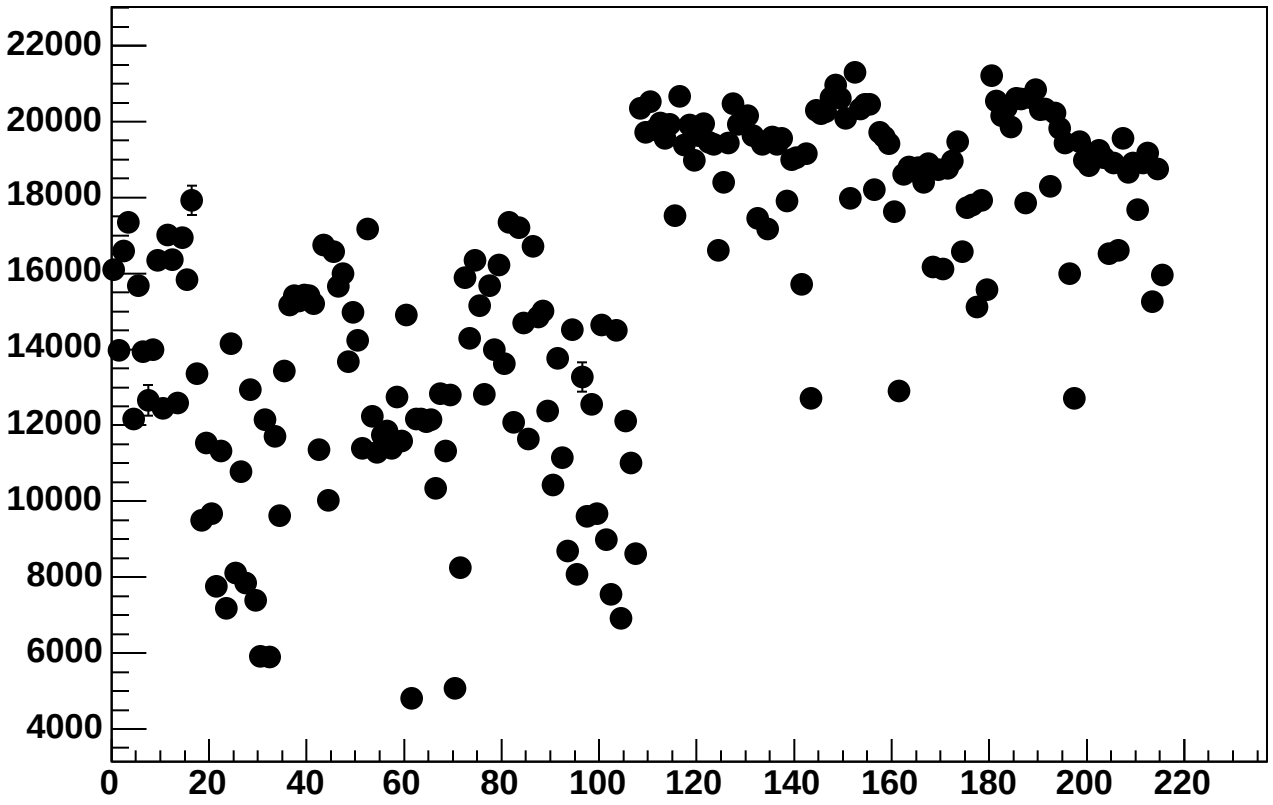
Enable 5, DAC=1600, Hold=185, ADC Mean vs 18*Chip+Chan



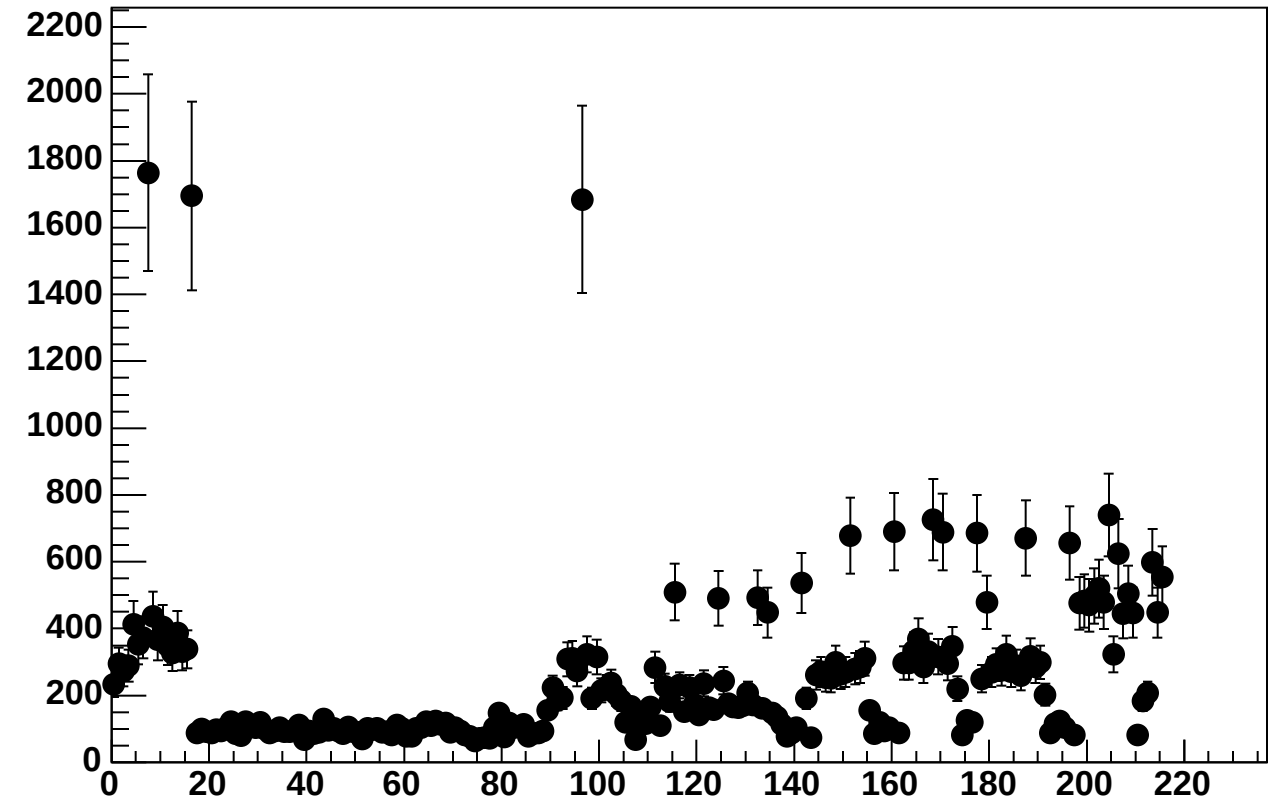
Enable 5, DAC=1600, Hold=185, ADC Noise vs 18*Chip+Chan



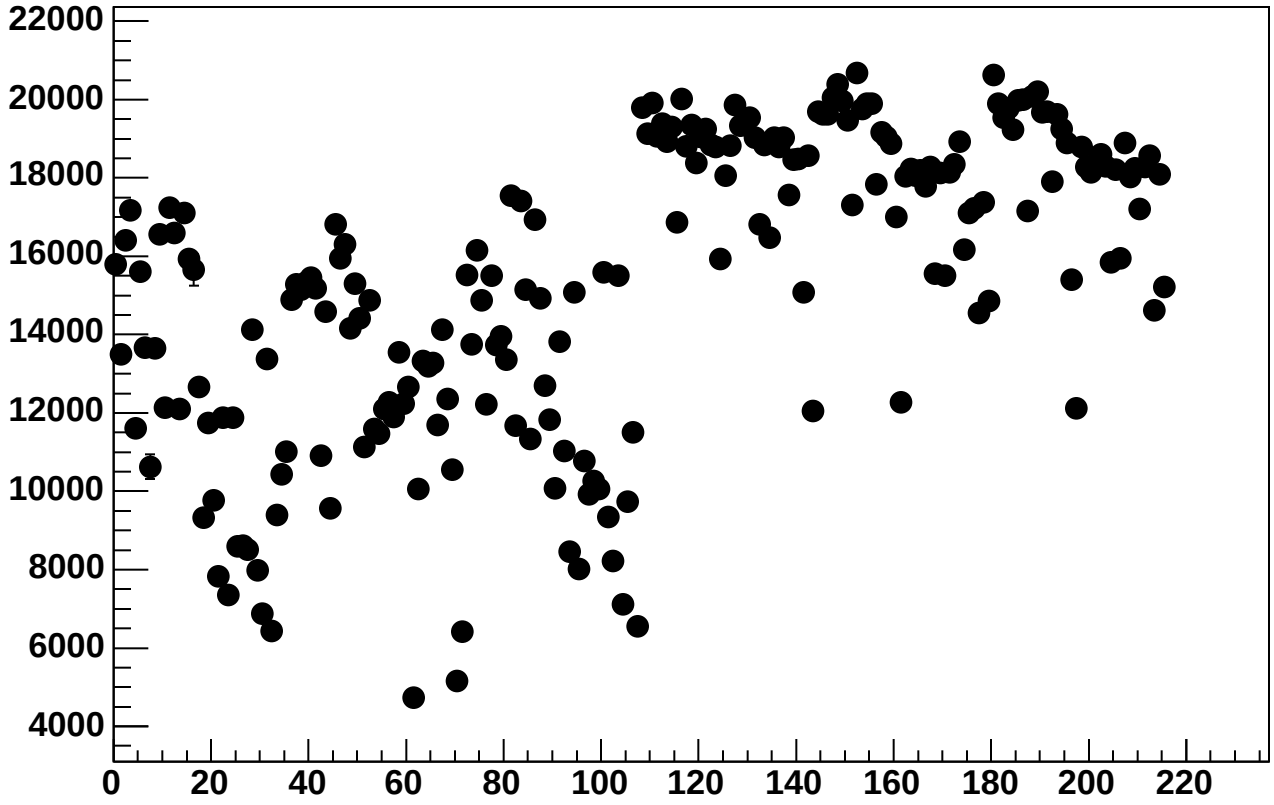
Enable 5, DAC=1600, Hold=190, ADC Mean vs 18*Chip+Chan



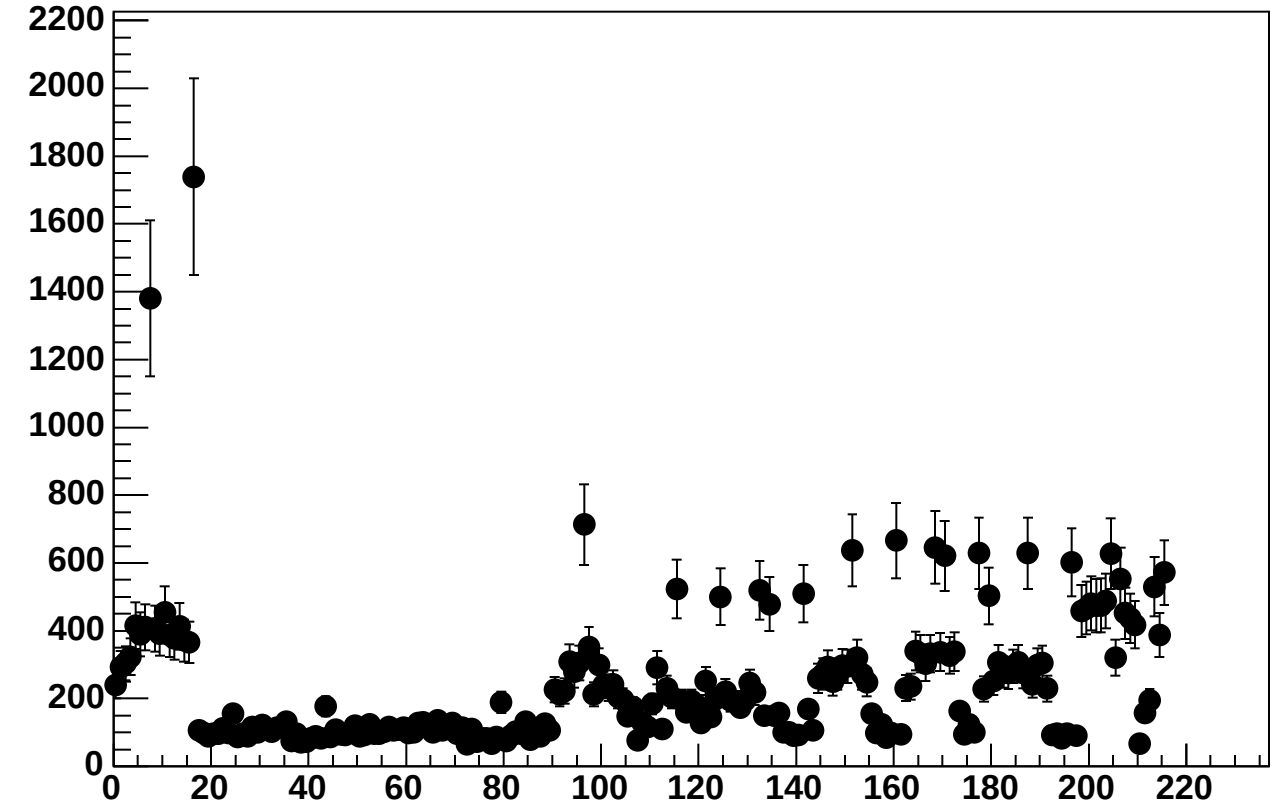
Enable 5, DAC=1600, Hold=190, ADC Noise vs 18*Chip+Chan



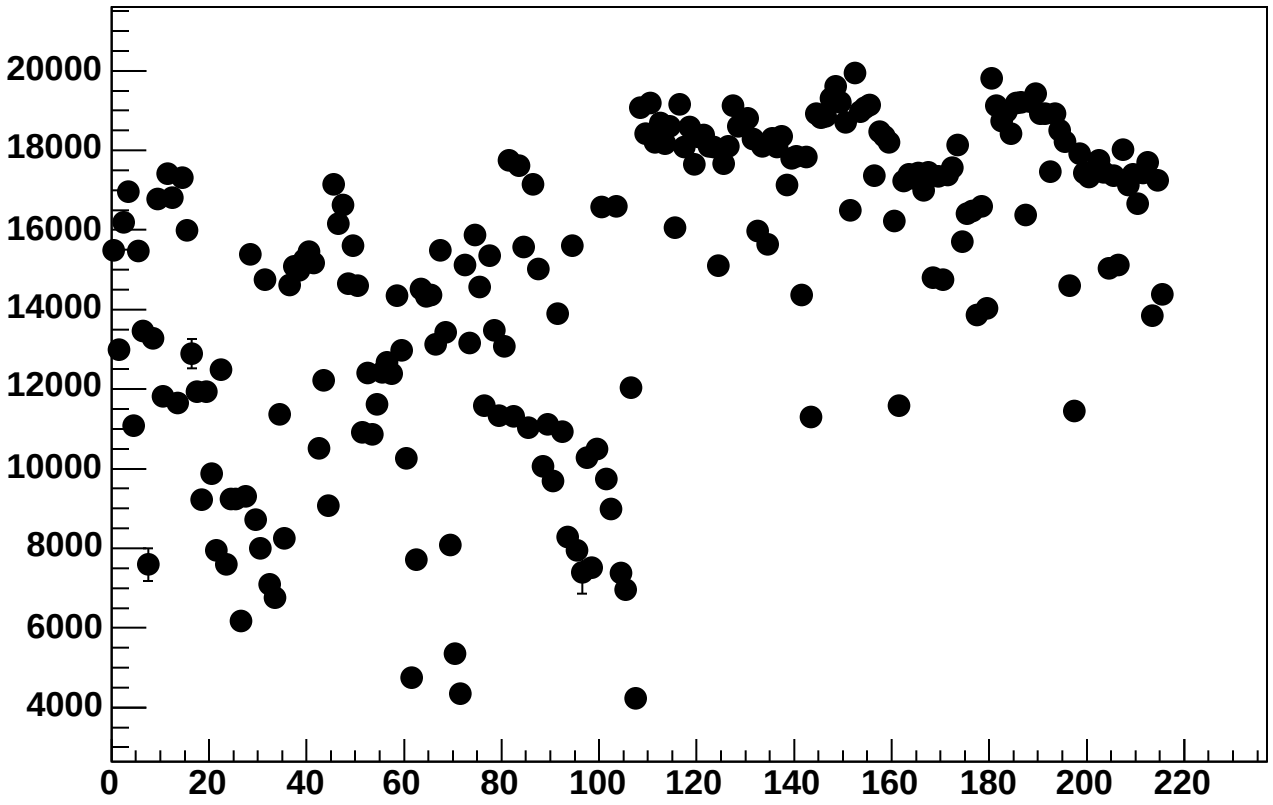
Enable 5, DAC=1600, Hold=195, ADC Mean vs 18*Chip+Chan



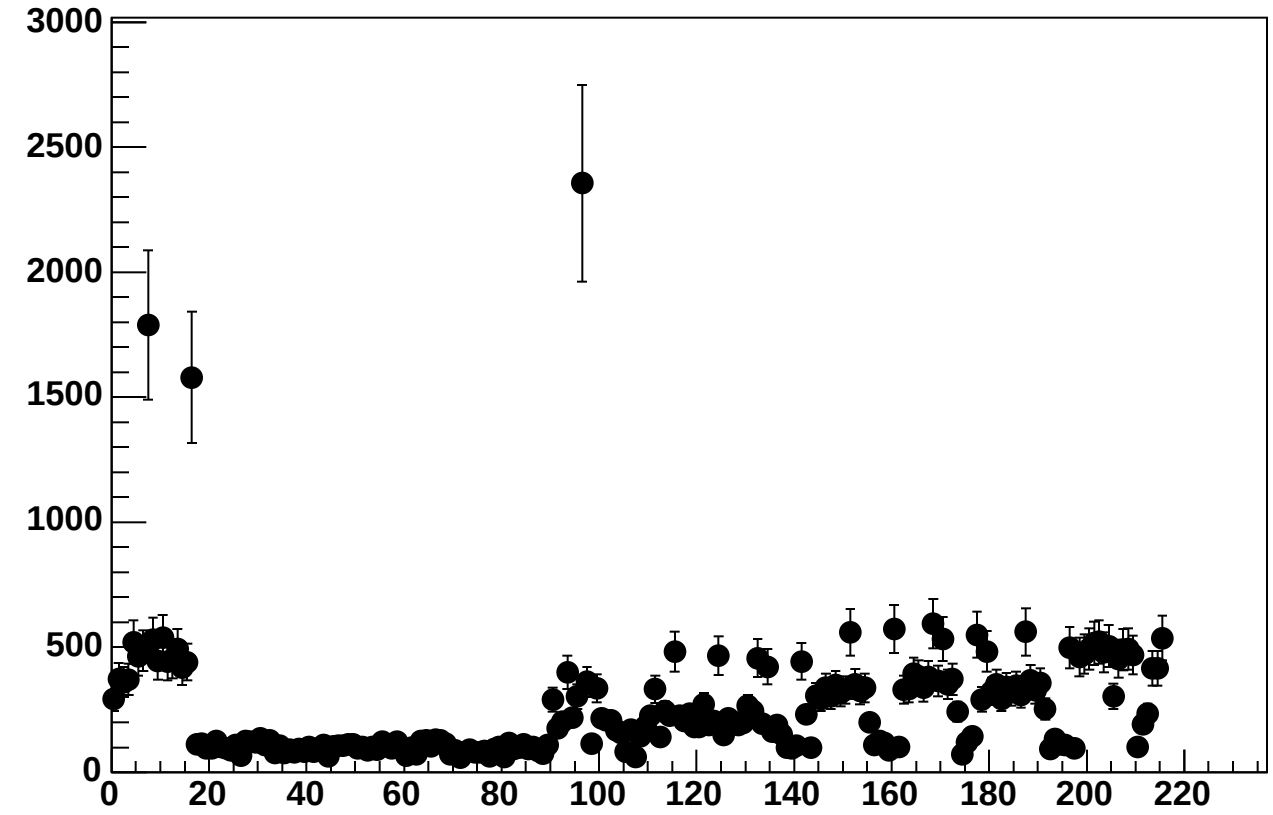
Enable 5, DAC=1600, Hold=195, ADC Noise vs 18*Chip+Chan



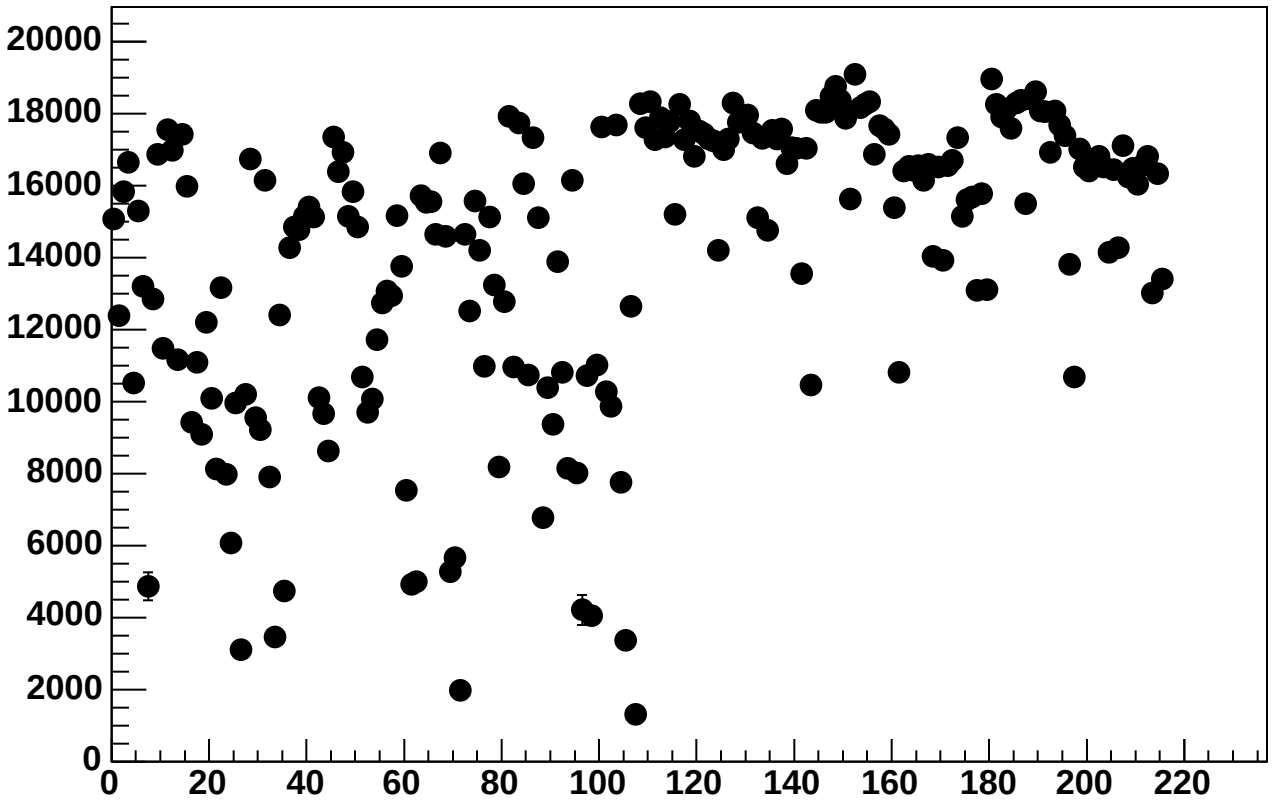
Enable 5, DAC=1600, Hold=200, ADC Mean vs 18*Chip+Chan



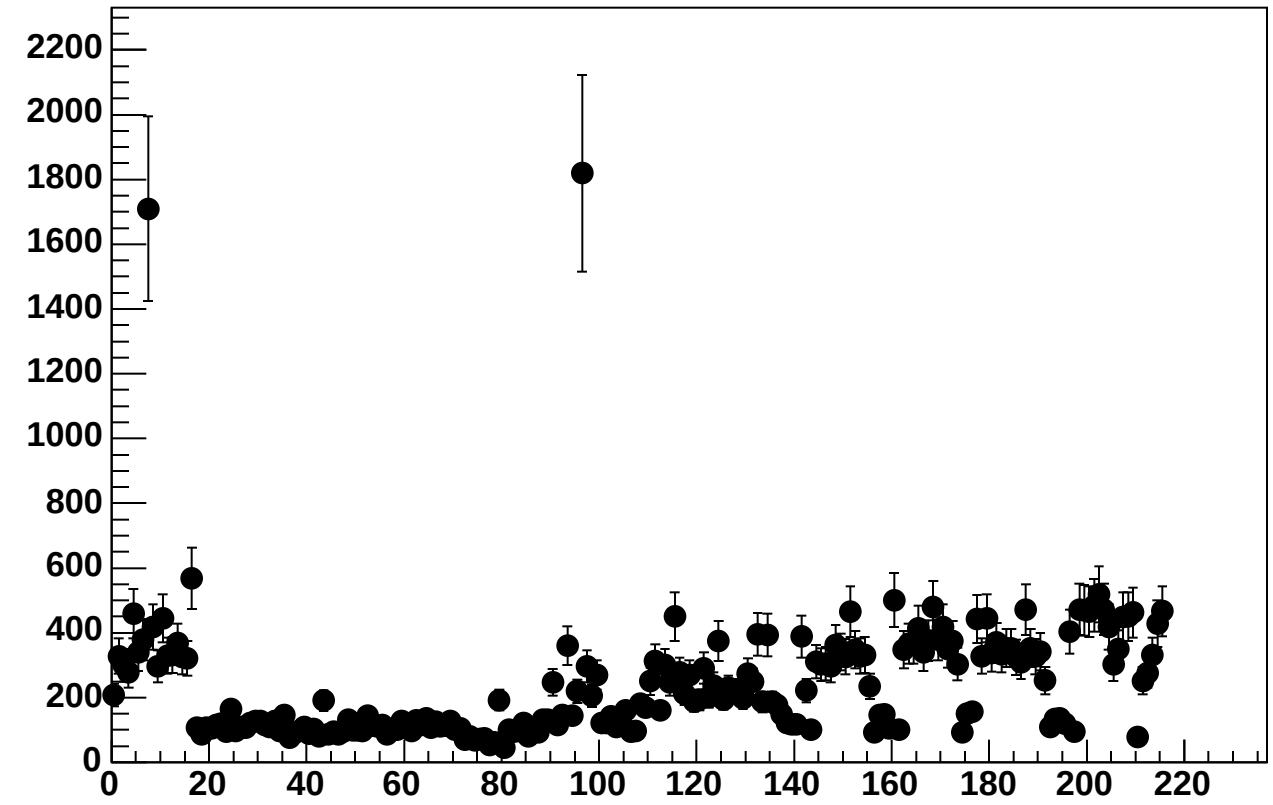
Enable 5, DAC=1600, Hold=200, ADC Noise vs 18*Chip+Chan



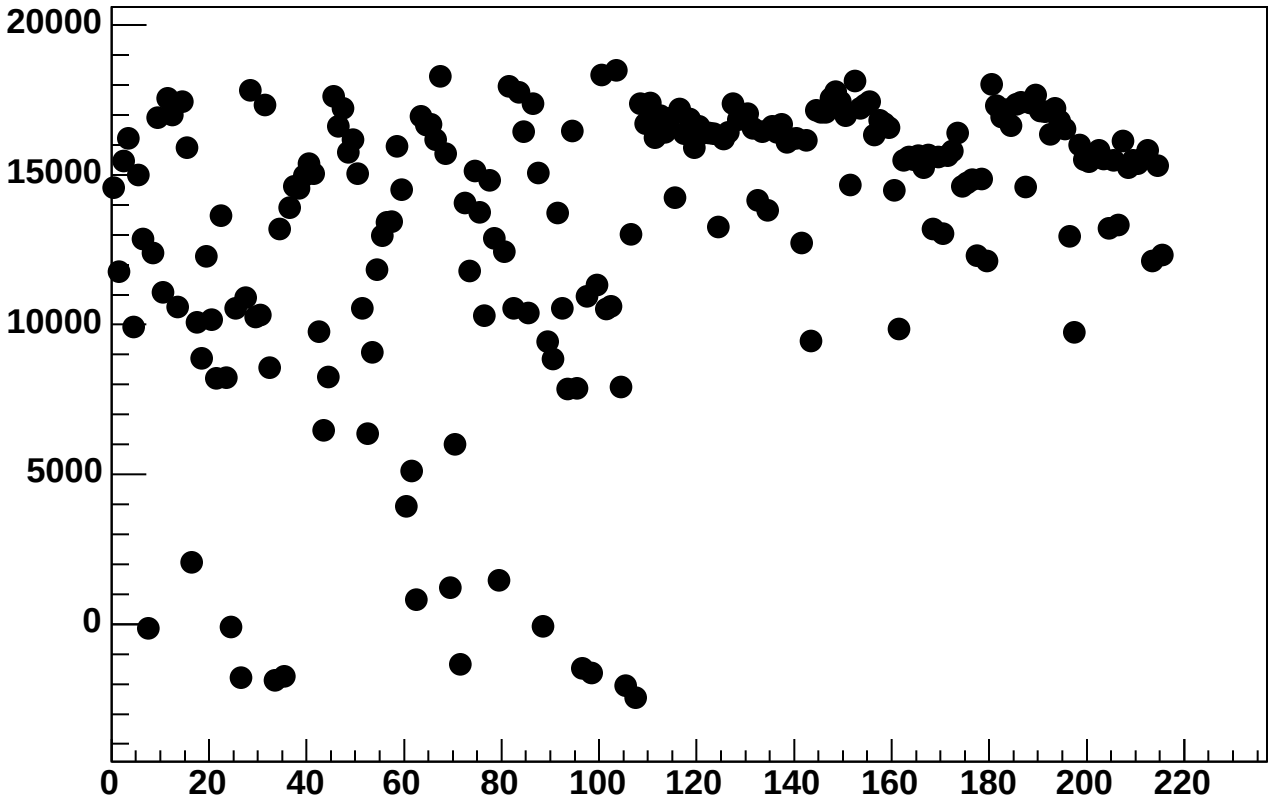
Enable 5, DAC=1600, Hold=205, ADC Mean vs 18*Chip+Chan



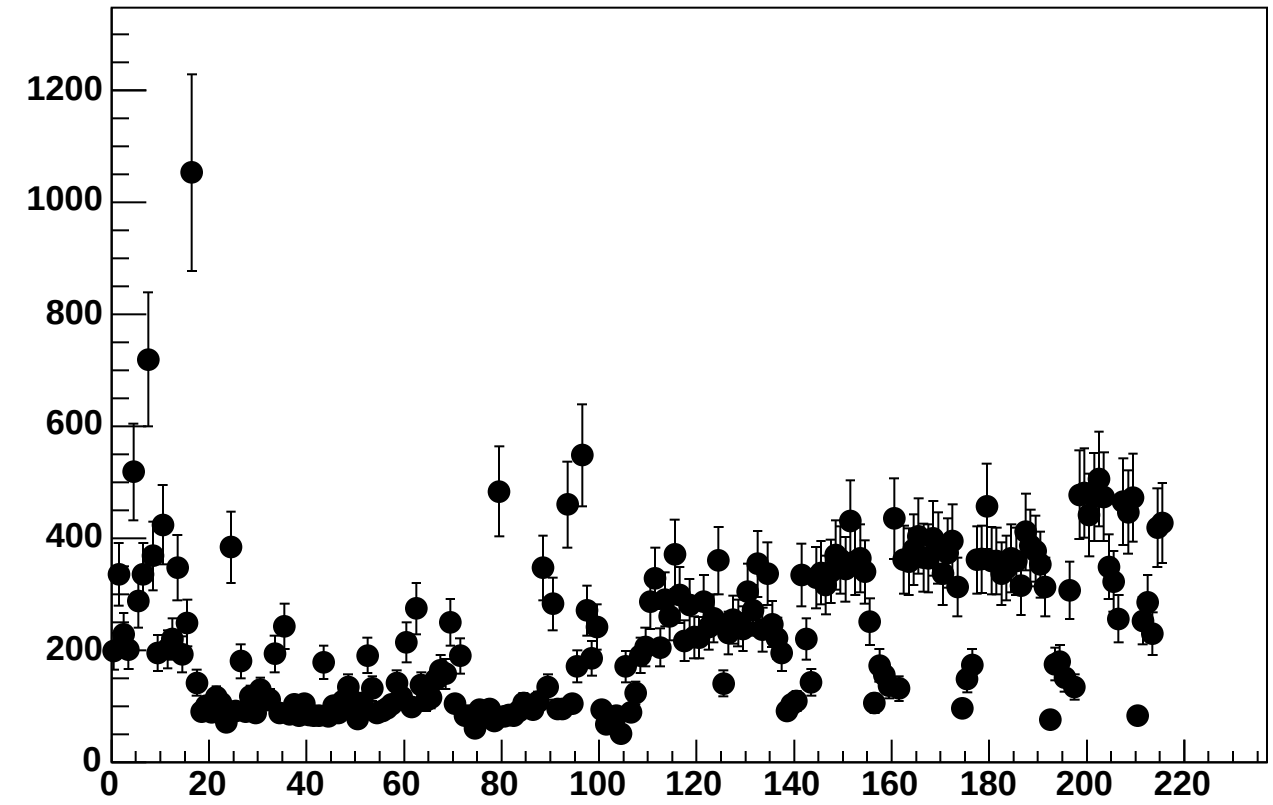
Enable 5, DAC=1600, Hold=205, ADC Noise vs 18*Chip+Chan



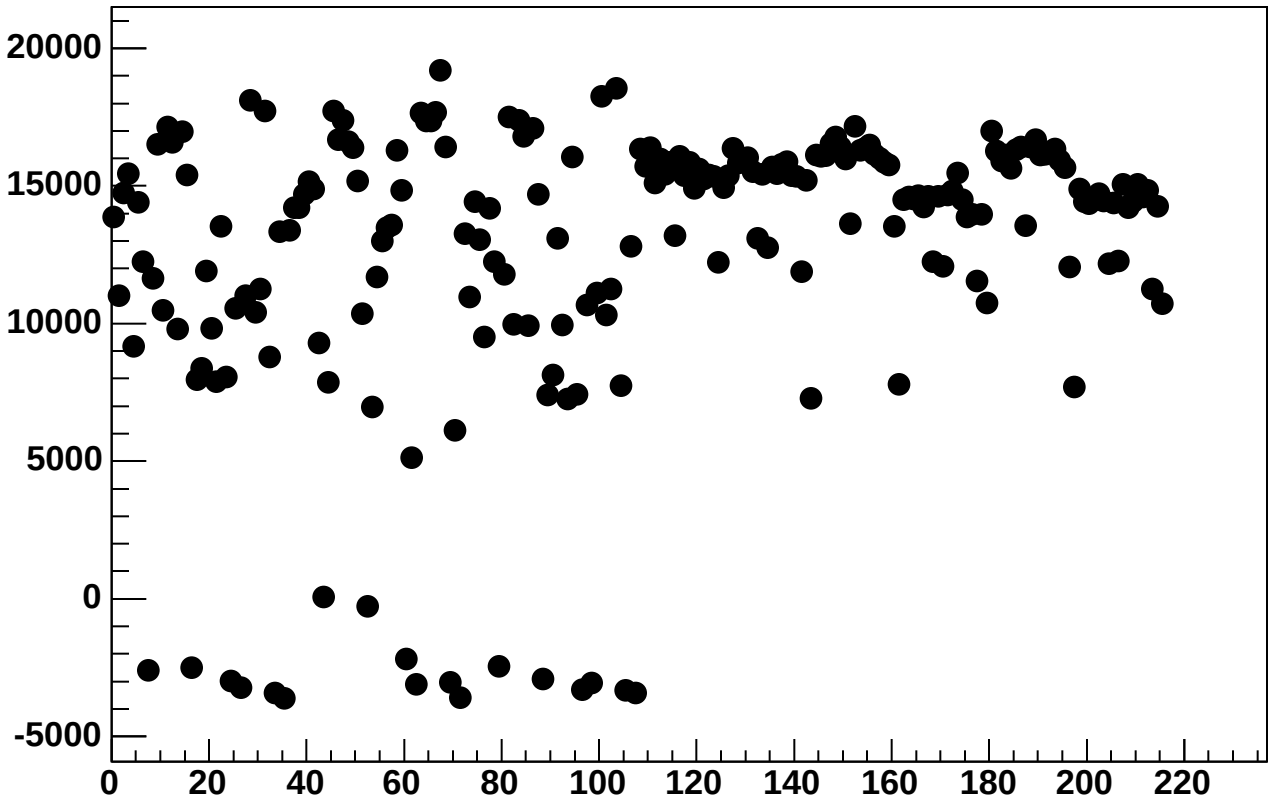
Enable 5, DAC=1600, Hold=210, ADC Mean vs 18*Chip+Chan



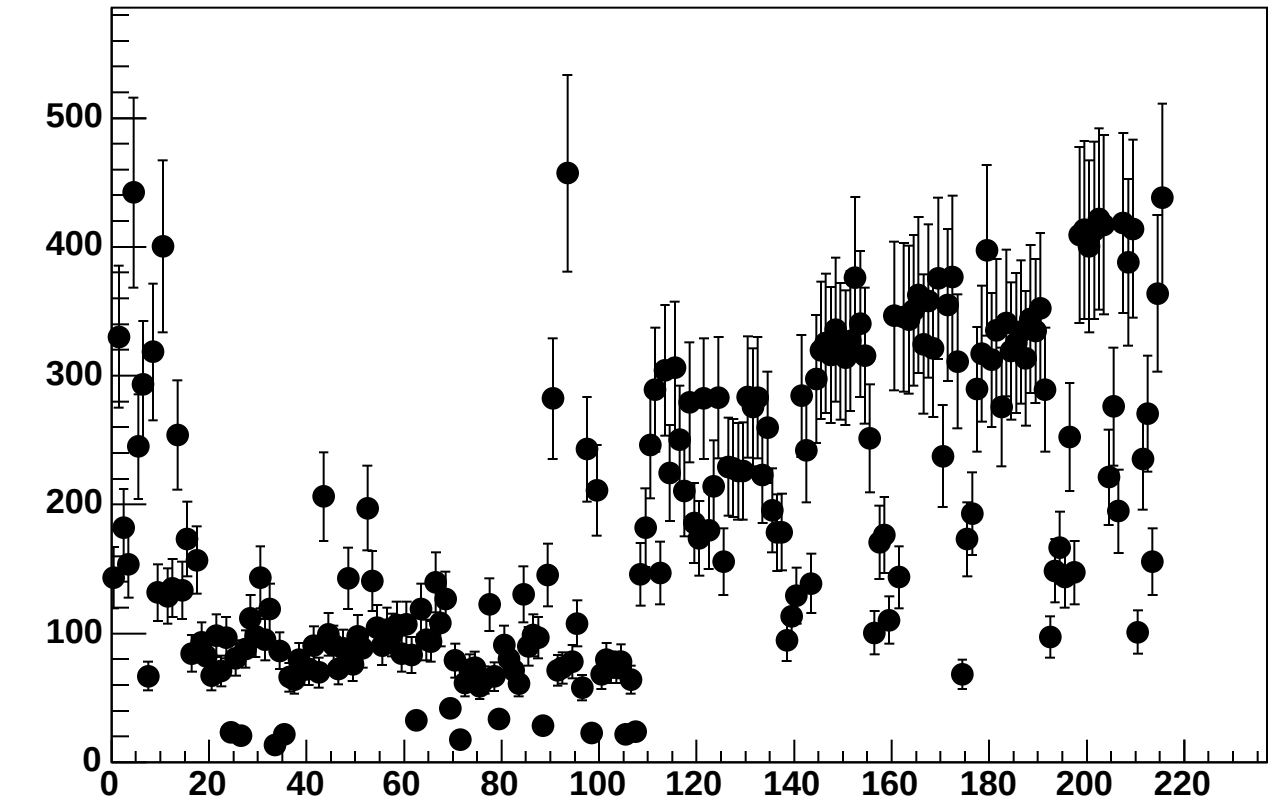
Enable 5, DAC=1600, Hold=210, ADC Noise vs 18*Chip+Chan



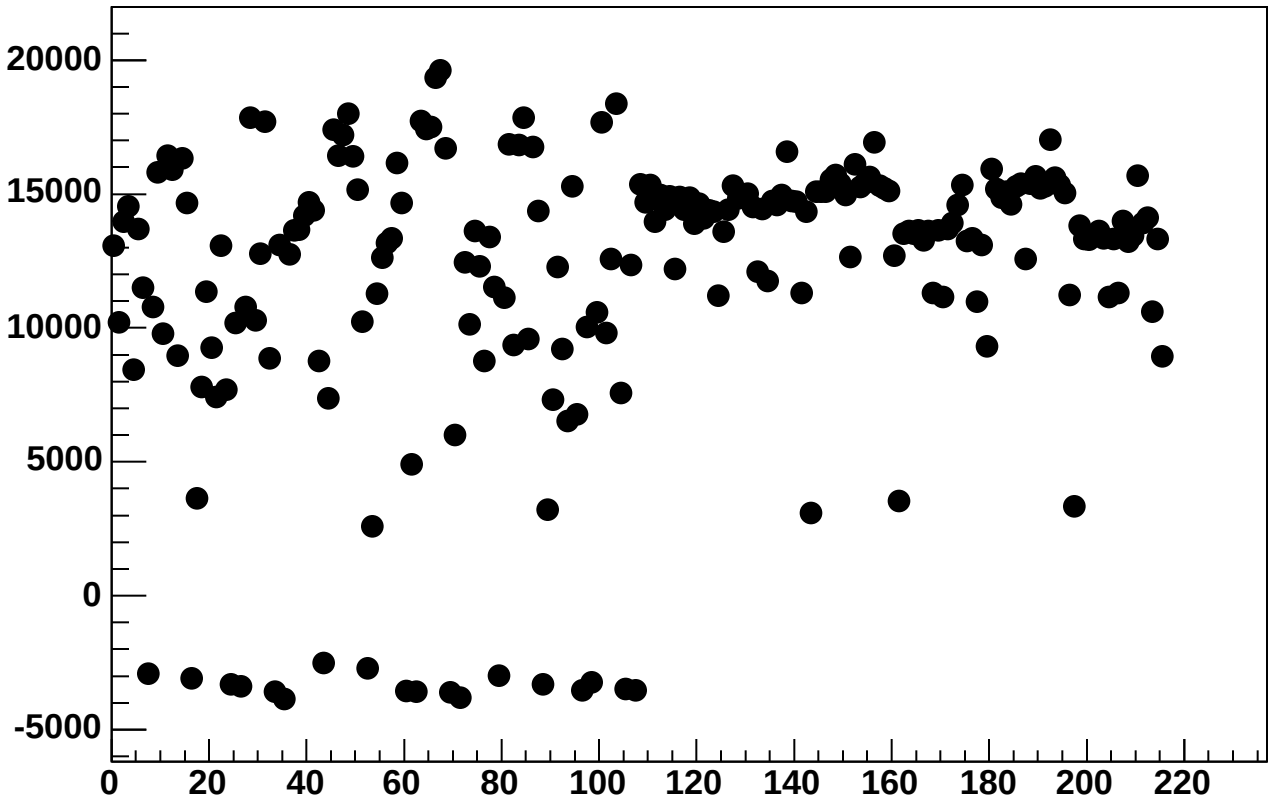
Enable 5, DAC=1600, Hold=215, ADC Mean vs 18*Chip+Chan



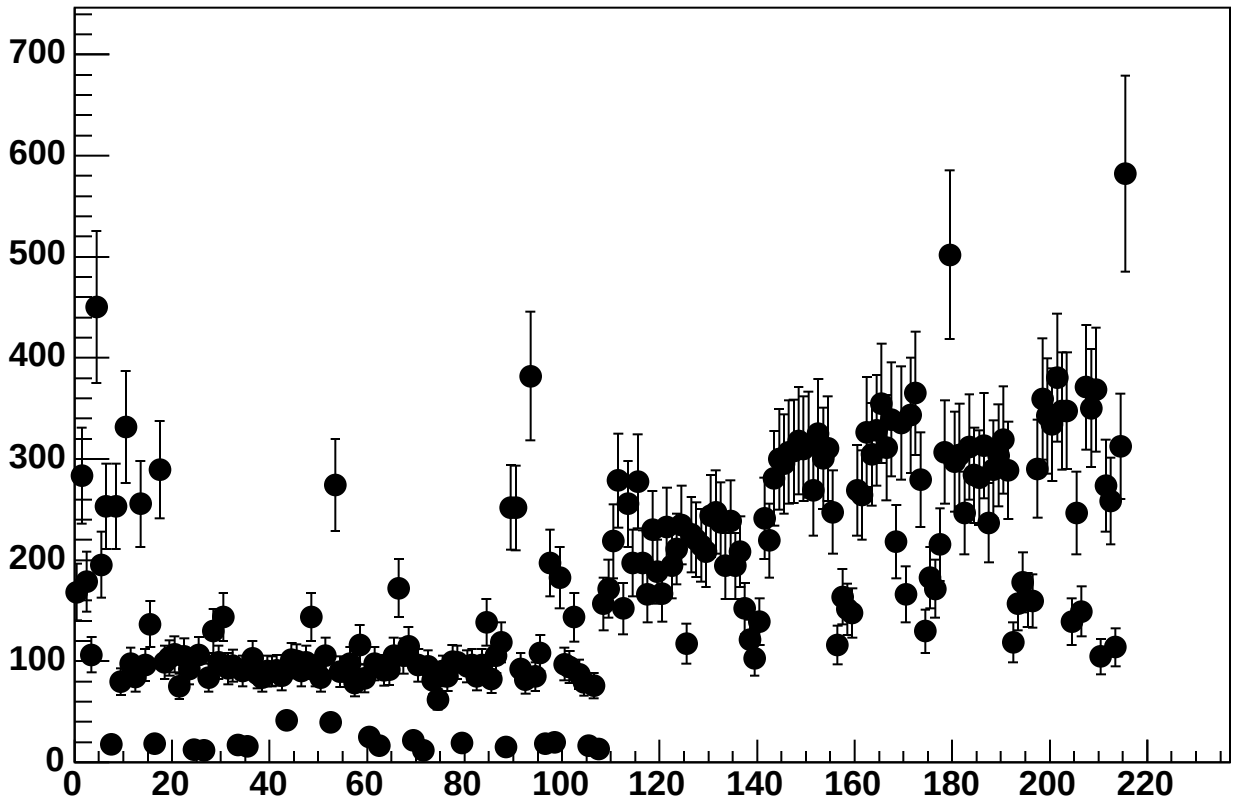
Enable 5, DAC=1600, Hold=215, ADC Noise vs 18*Chip+Chan



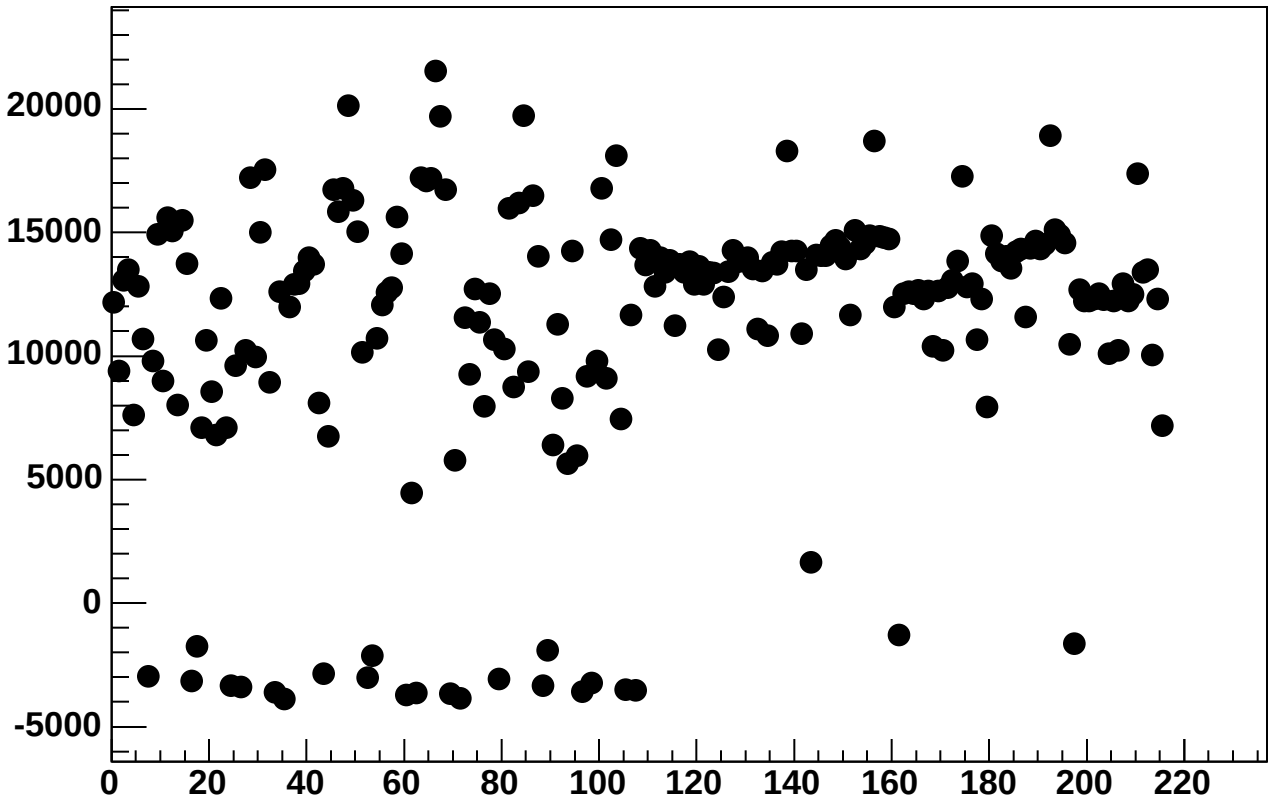
Enable 5, DAC=1600, Hold=220, ADC Mean vs 18*Chip+Chan



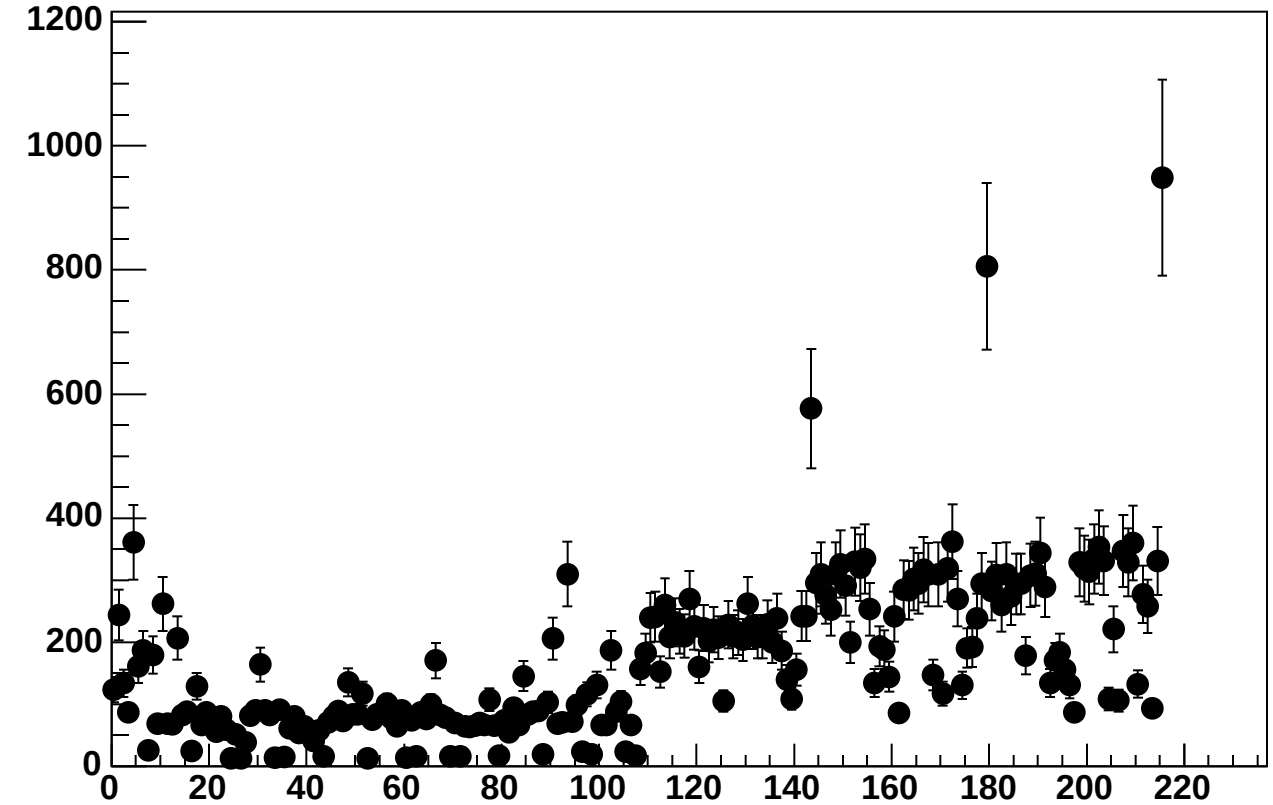
Enable 5, DAC=1600, Hold=220, ADC Noise vs 18*Chip+Chan



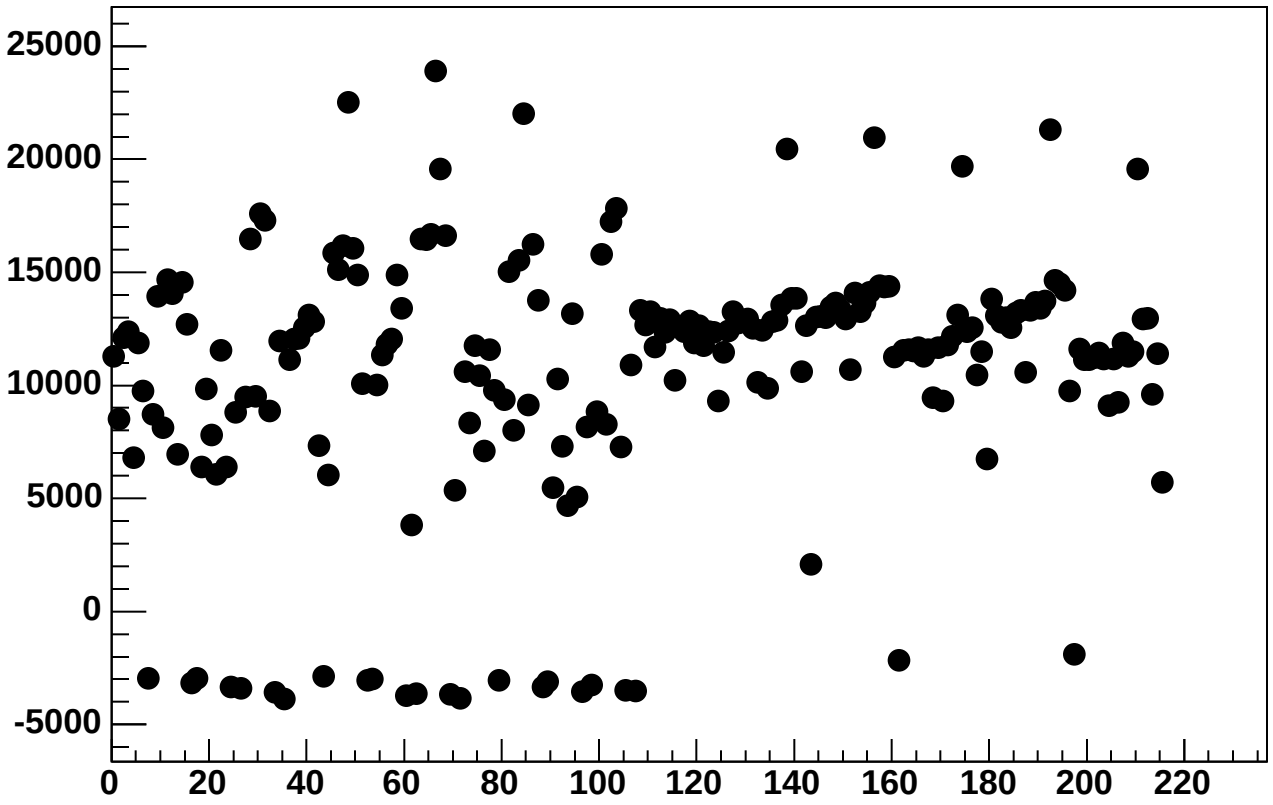
Enable 5, DAC=1600, Hold=225, ADC Mean vs 18*Chip+Chan



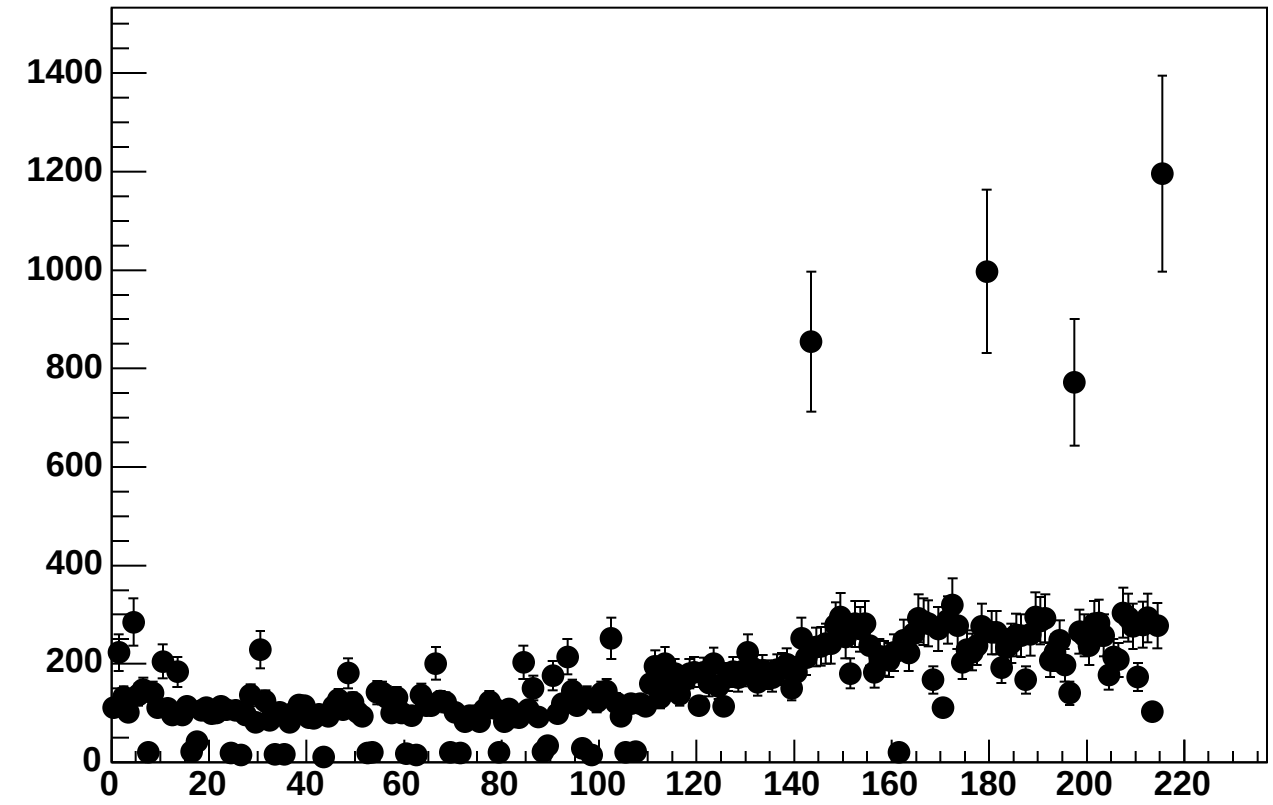
Enable 5, DAC=1600, Hold=225, ADC Noise vs 18*Chip+Chan



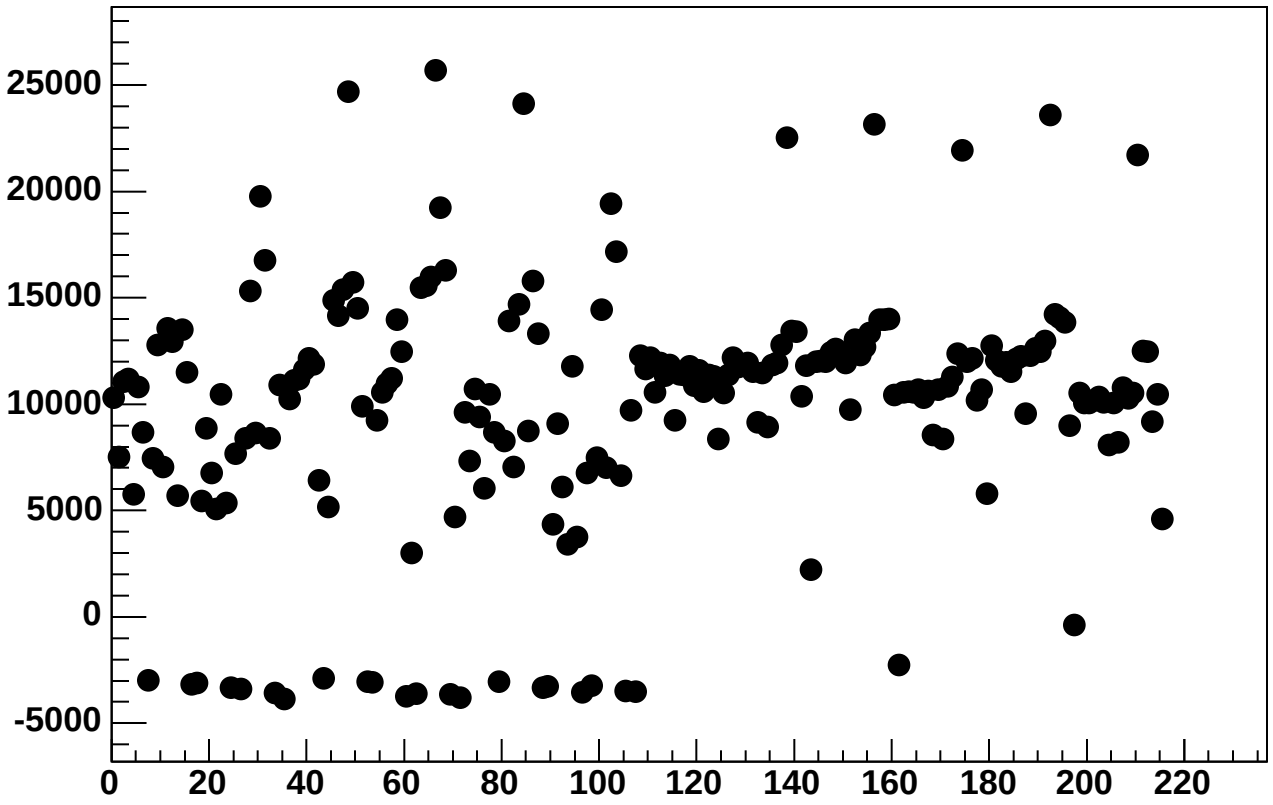
Enable 5, DAC=1600, Hold=230, ADC Mean vs 18*Chip+Chan



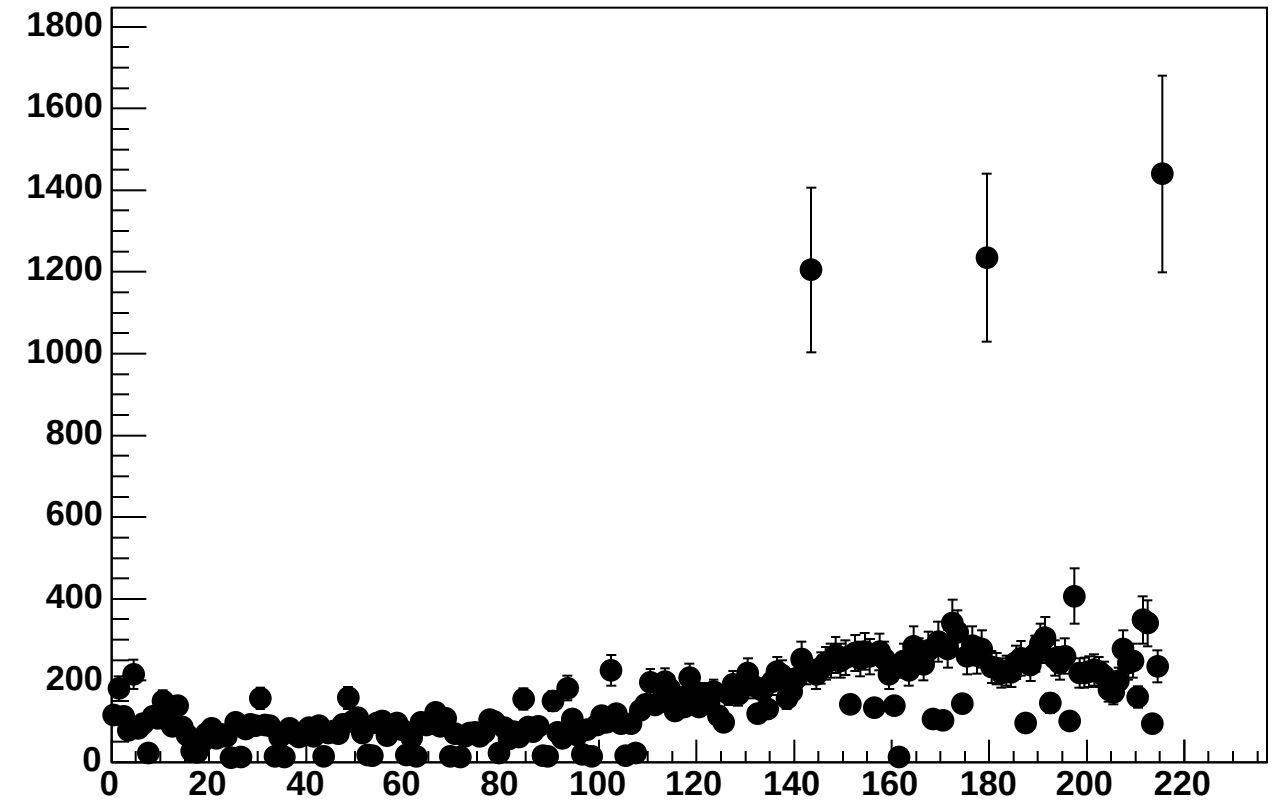
Enable 5, DAC=1600, Hold=230, ADC Noise vs 18*Chip+Chan



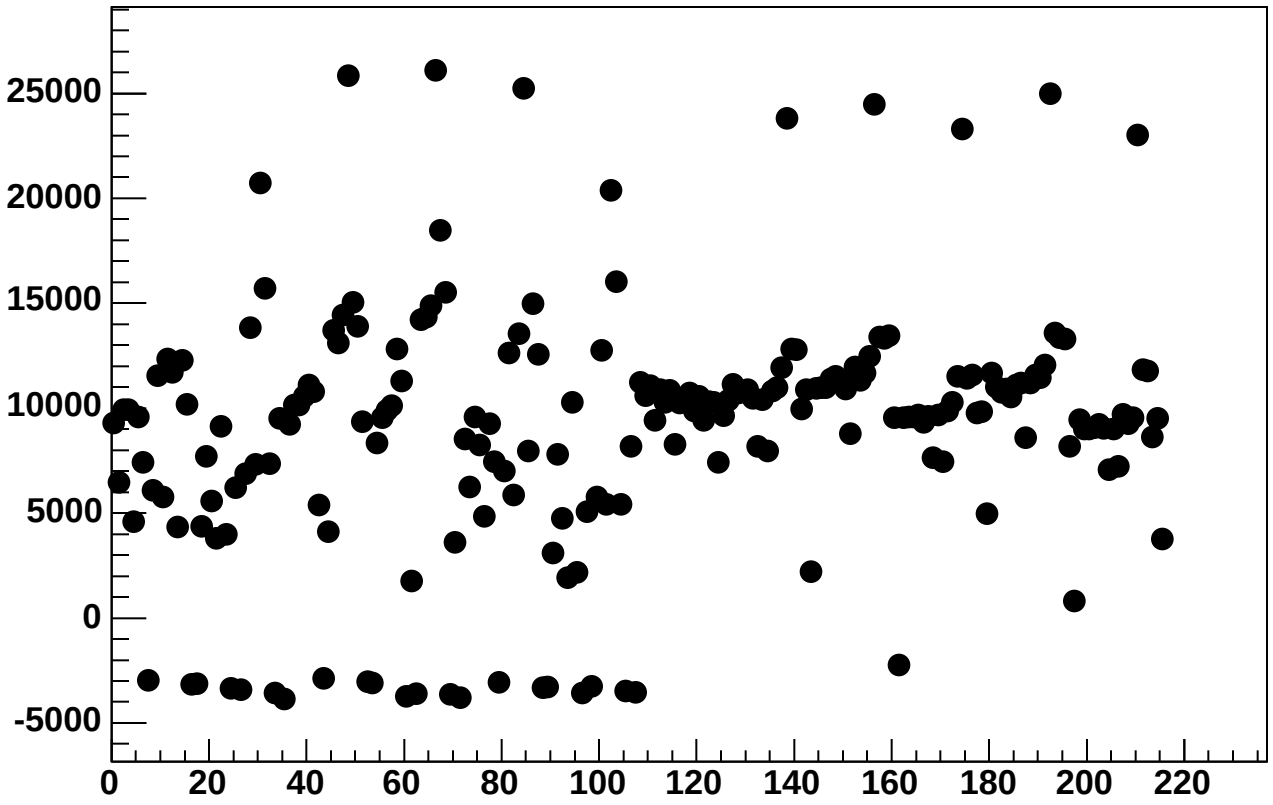
Enable 5, DAC=1600, Hold=235, ADC Mean vs 18*Chip+Chan



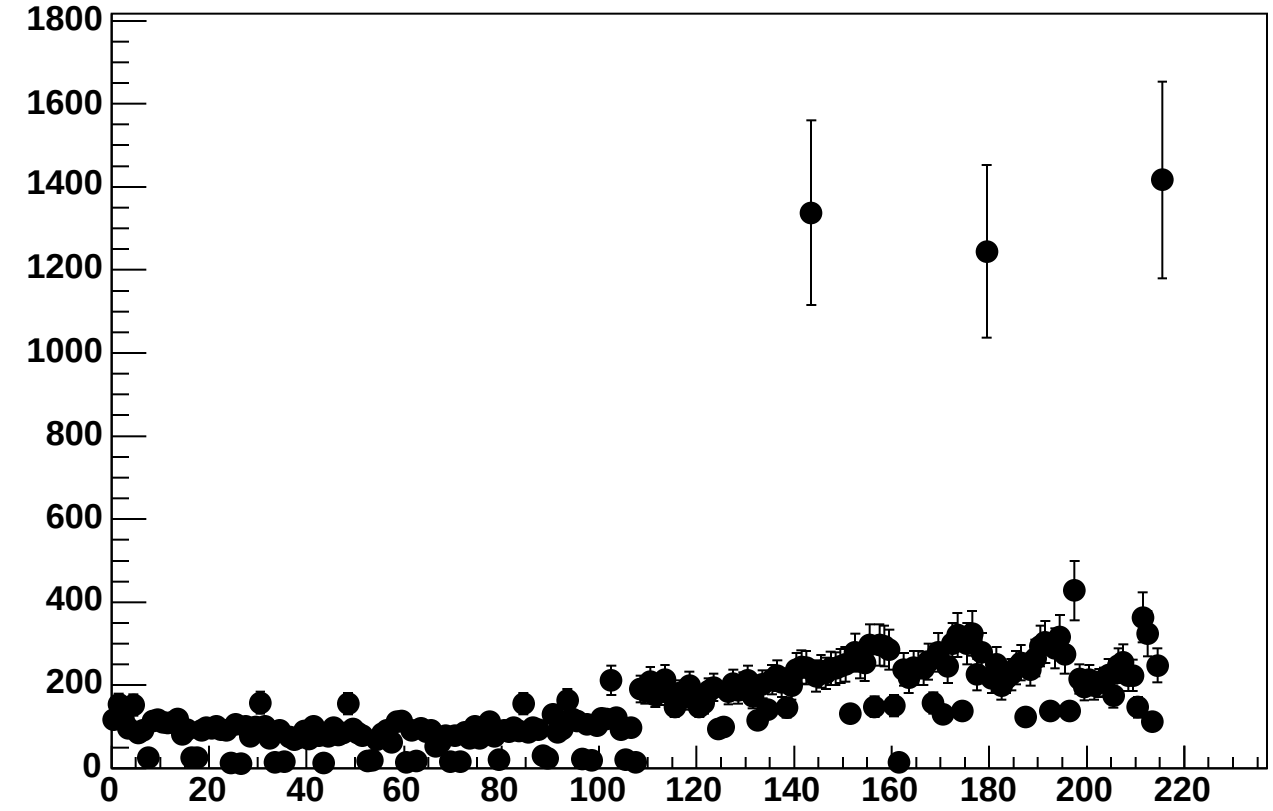
Enable 5, DAC=1600, Hold=235, ADC Noise vs 18*Chip+Chan

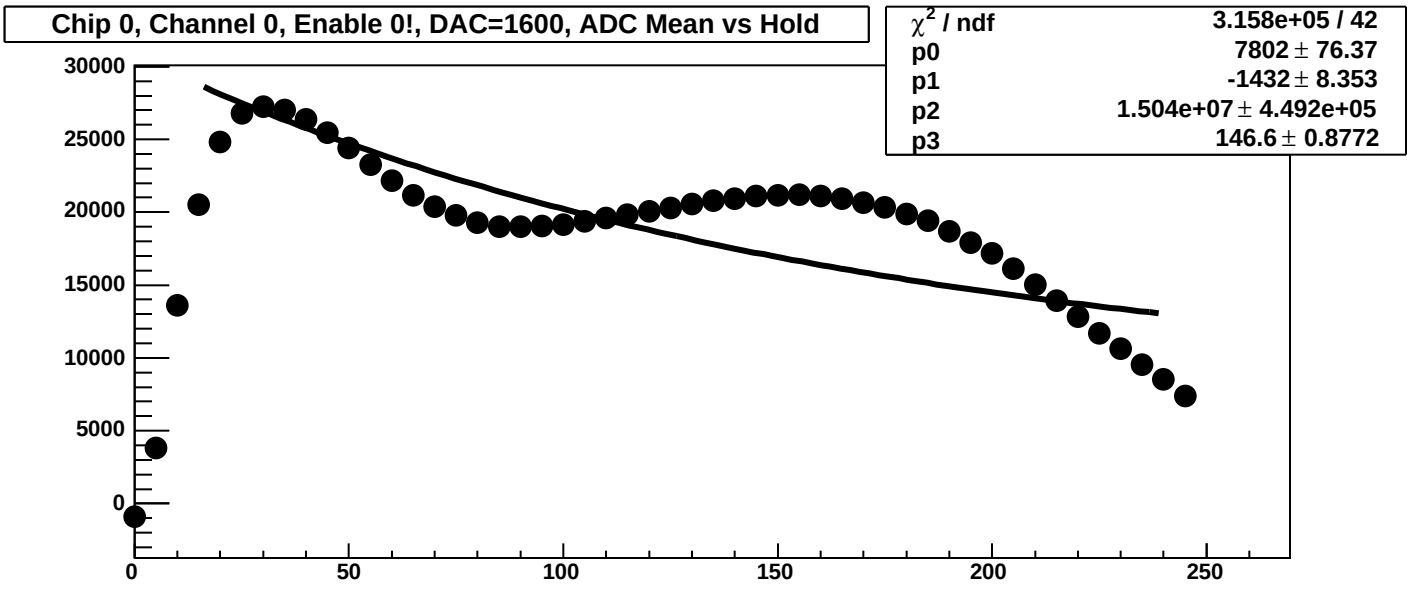


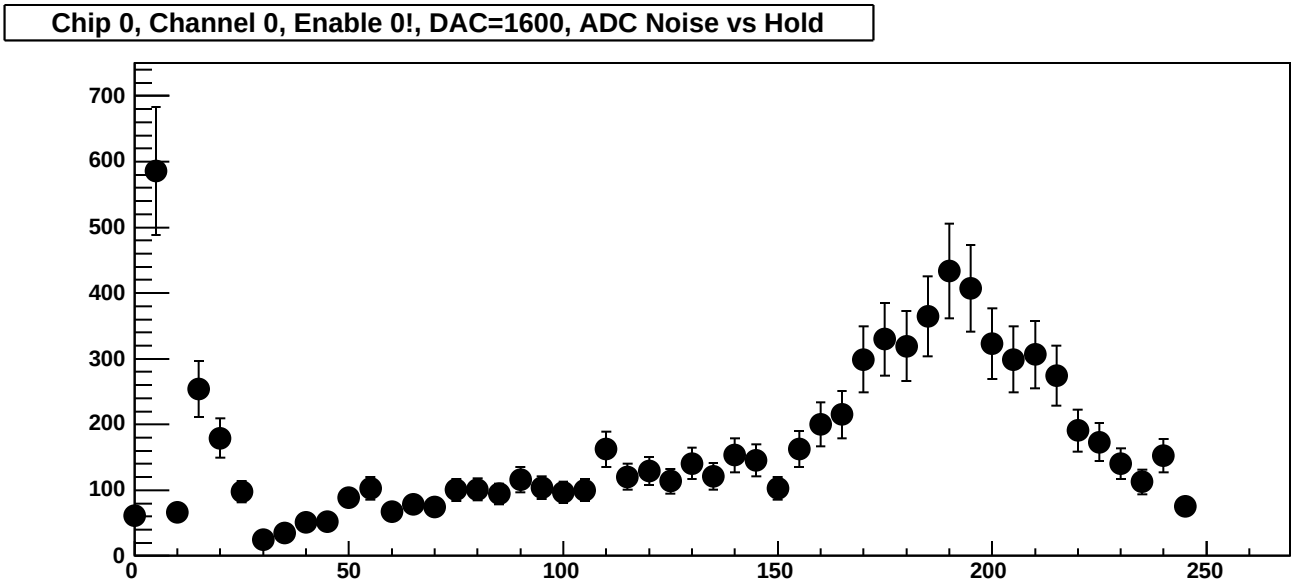
Enable 5, DAC=1600, Hold=240, ADC Mean vs 18*Chip+Chan

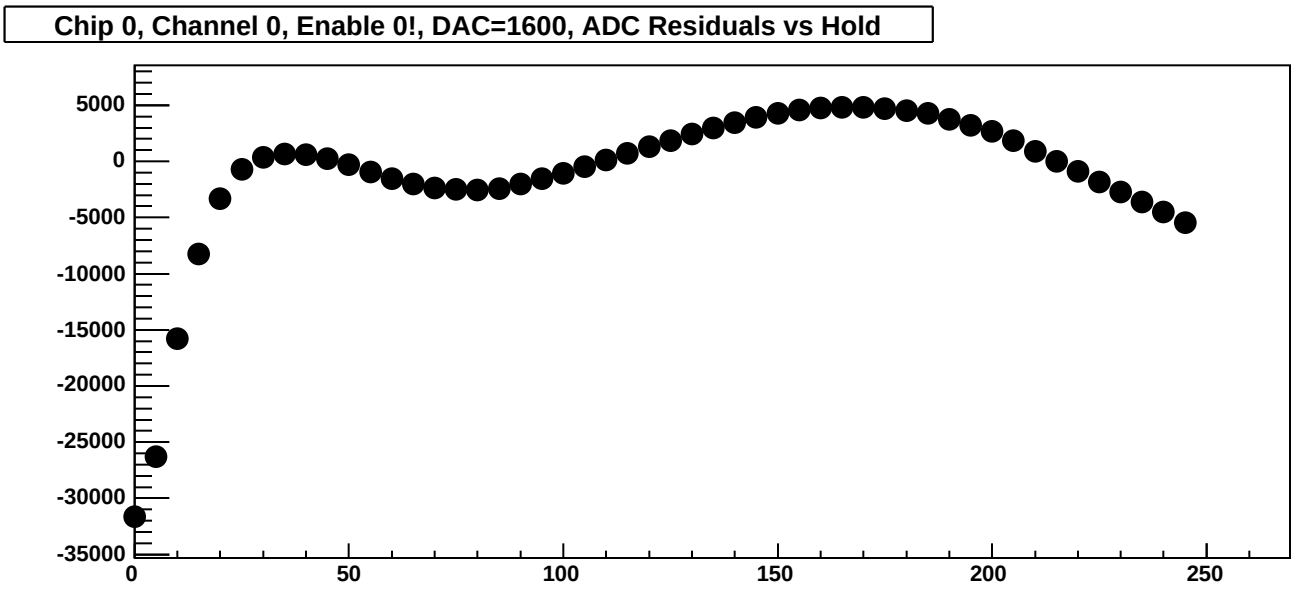


Enable 5, DAC=1600, Hold=240, ADC Noise vs 18*Chip+Chan

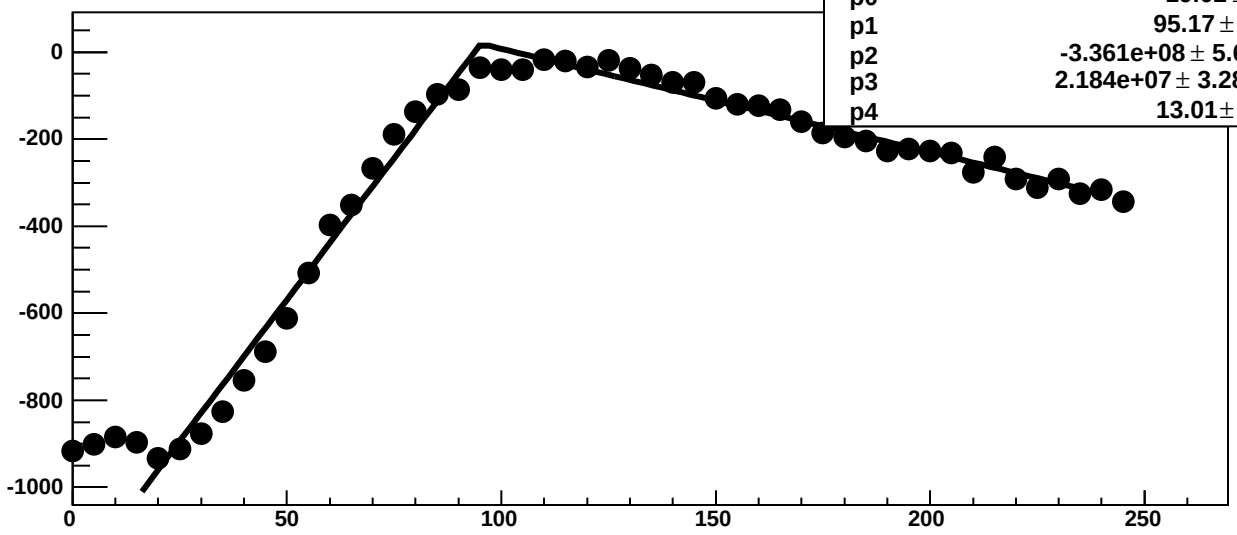



Chip 0, Channel 0, Enable 0!, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.158e+05 / 42
p0 7802 ± 76.37
p1 -1432 ± 8.353
p2 1.504e+07 ± 4.492e+05
p3 146.6 ± 0.8772
30000
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

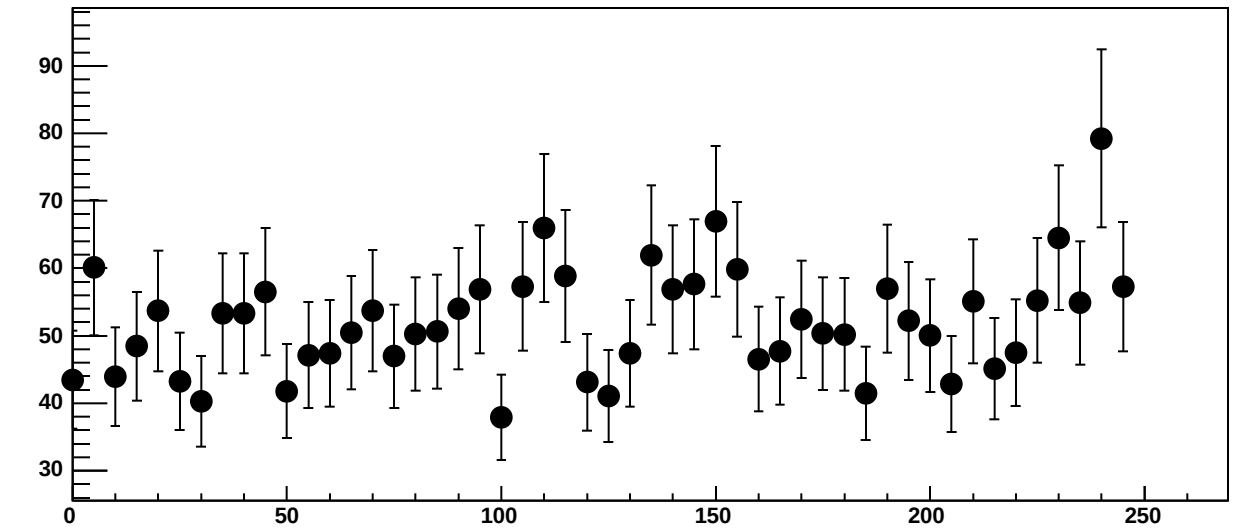

Chip 0, Channel 0, Enable 0!, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 0, Channel 0, Enable 0!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
-35000
0
50
100
150
200
250

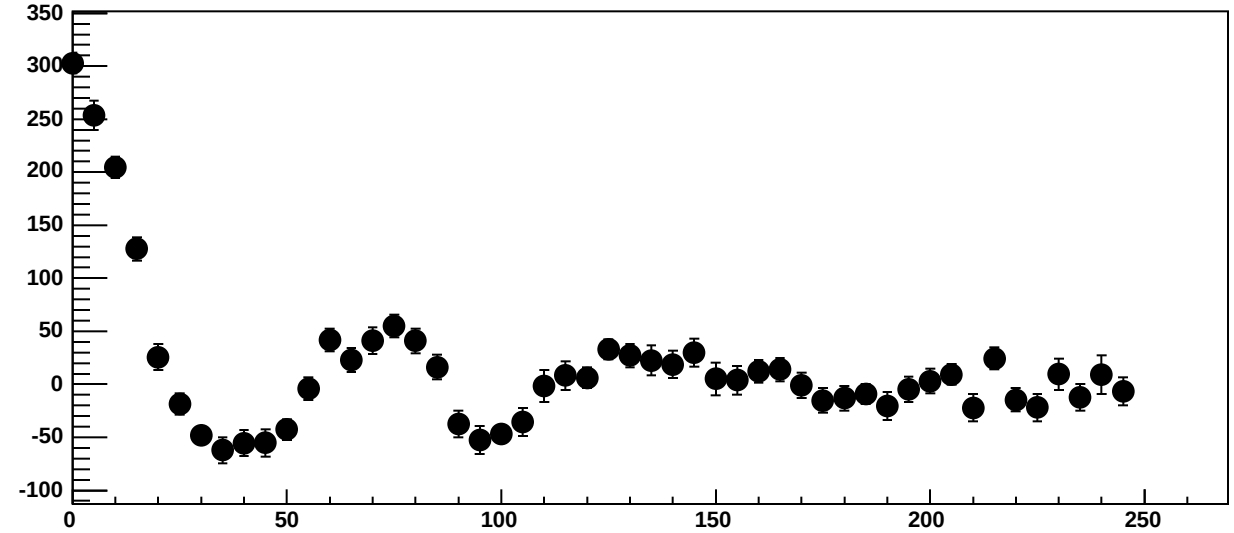
Chip 0, Channel 0, Enable 1, DAC=1600, ADC Mean vs Hold



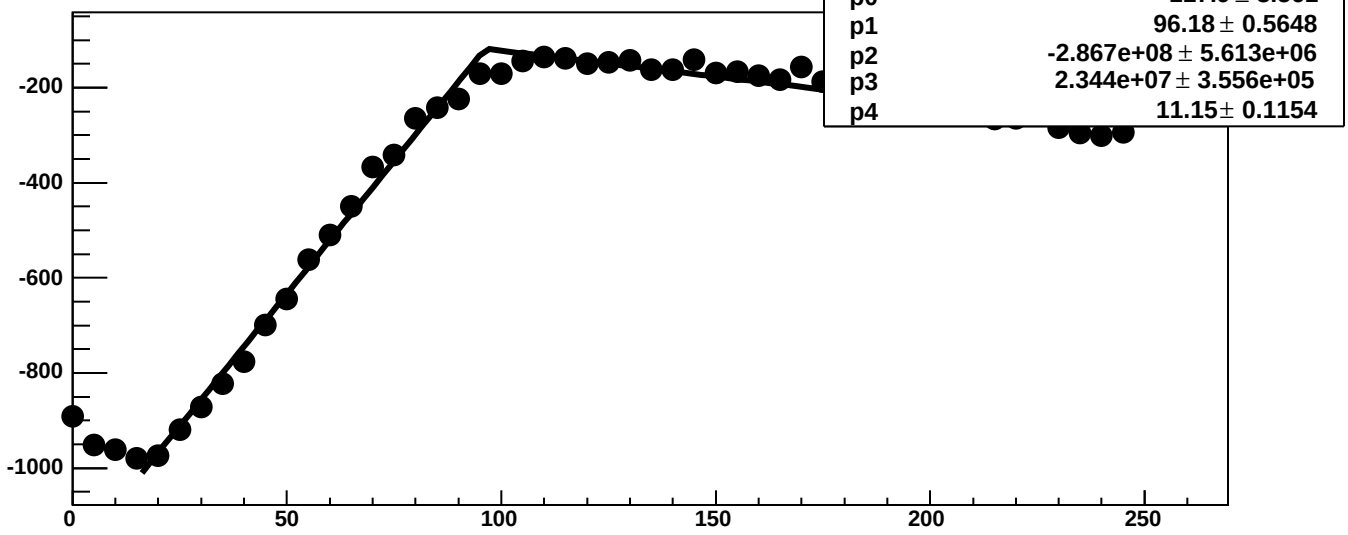
Chip 0, Channel 0, Enable 1, DAC=1600, ADC Noise vs Hold



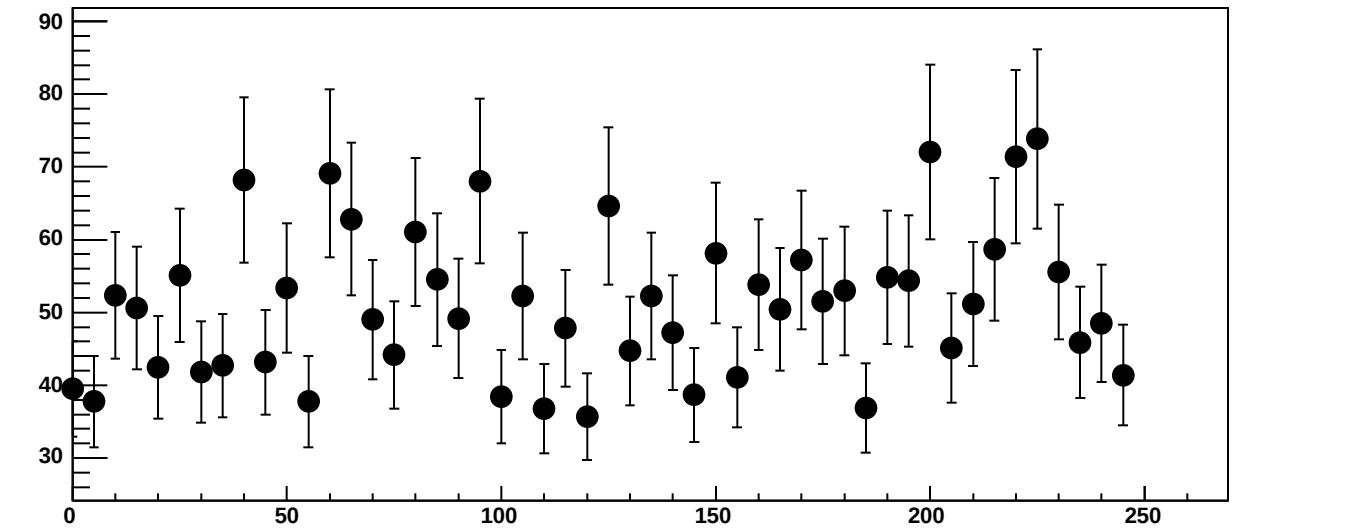
Chip 0, Channel 0, Enable 1, DAC=1600, ADC Residuals vs Hold



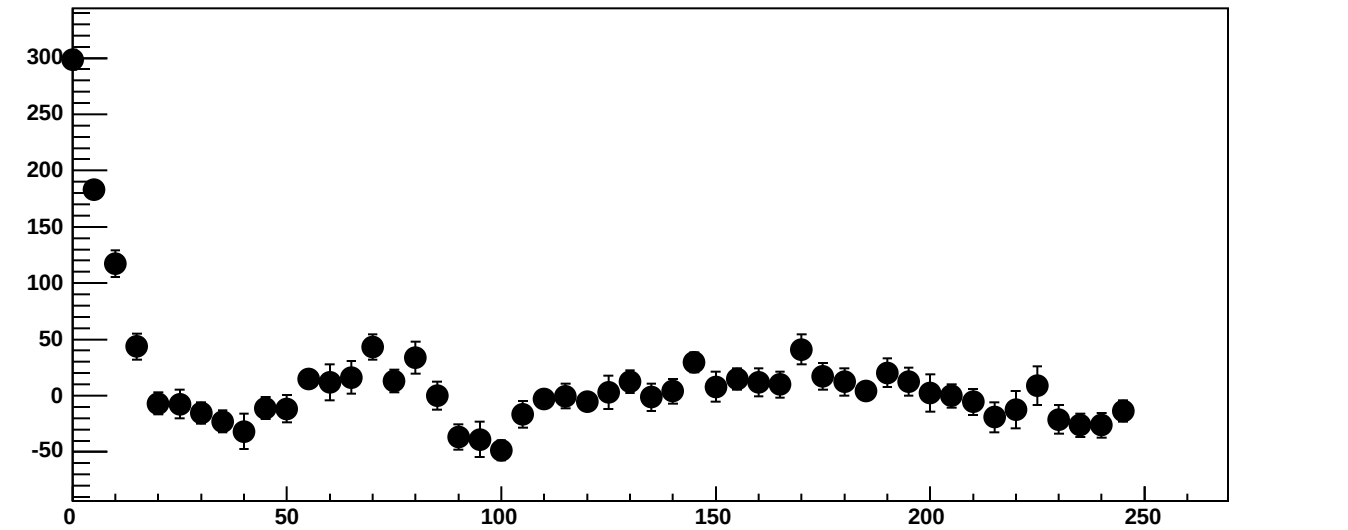
Chip 0, Channel 0, Enable 2, DAC=1600, ADC Mean vs Hold

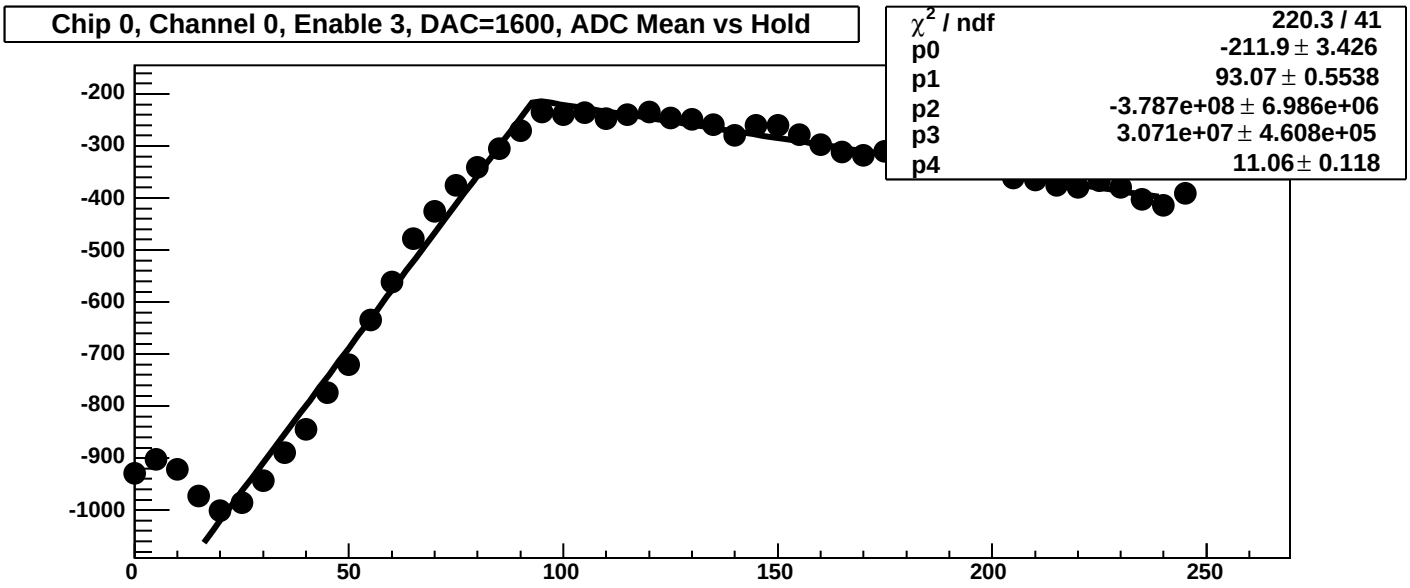


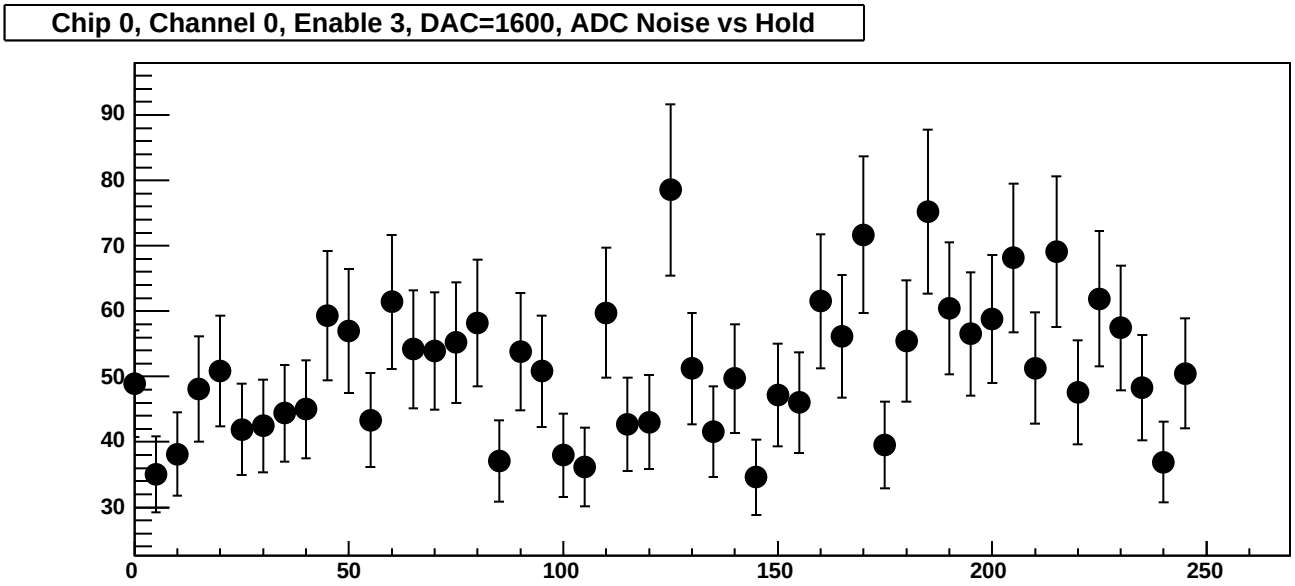
Chip 0, Channel 0, Enable 2, DAC=1600, ADC Noise vs Hold

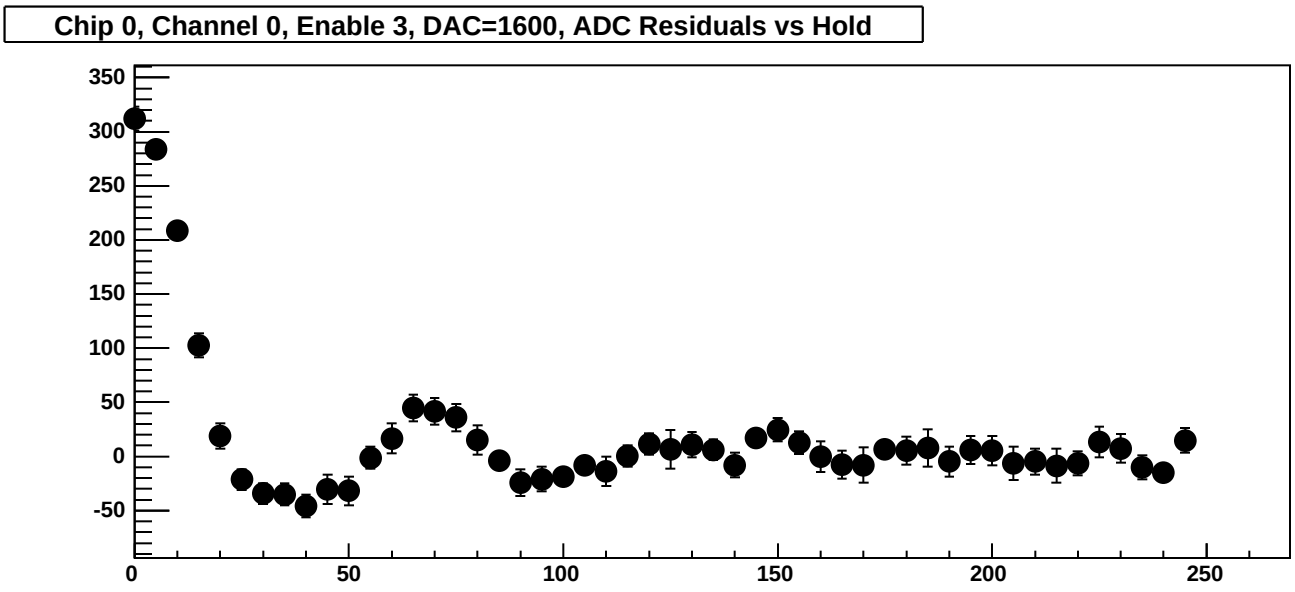


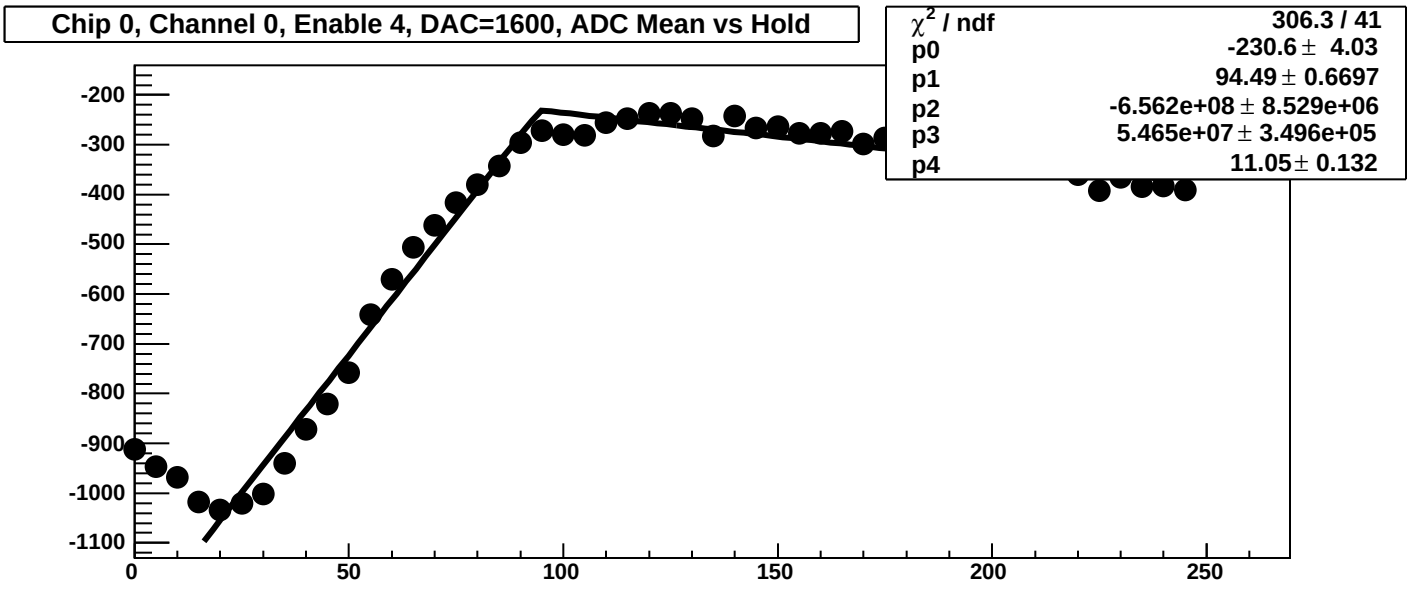
Chip 0, Channel 0, Enable 2, DAC=1600, ADC Residuals vs Hold

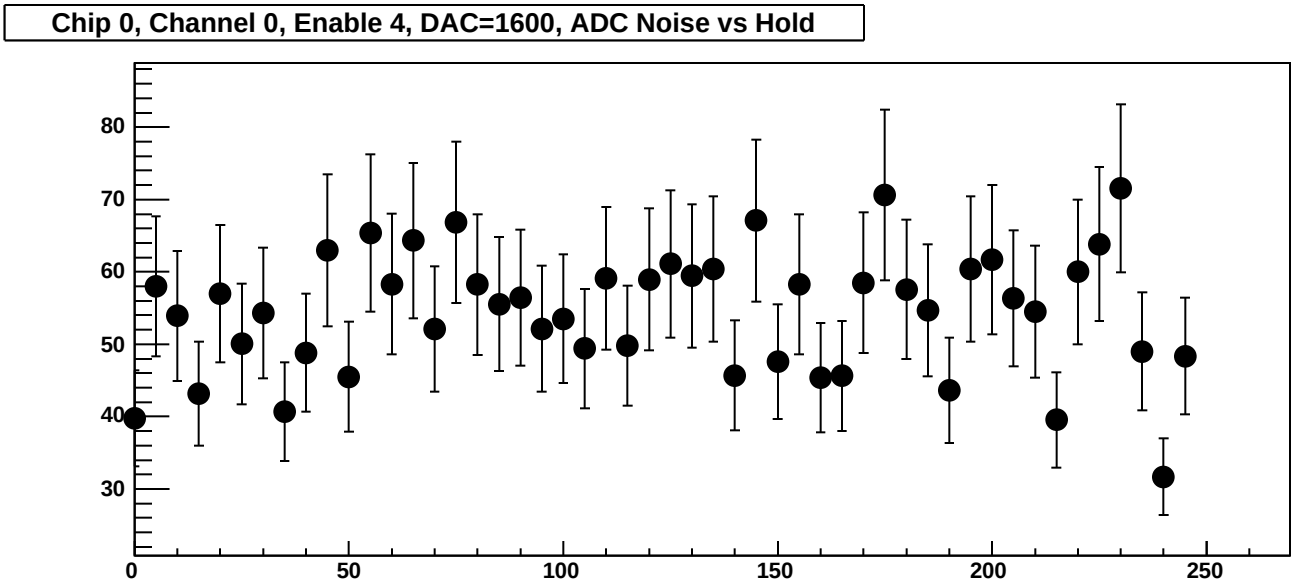


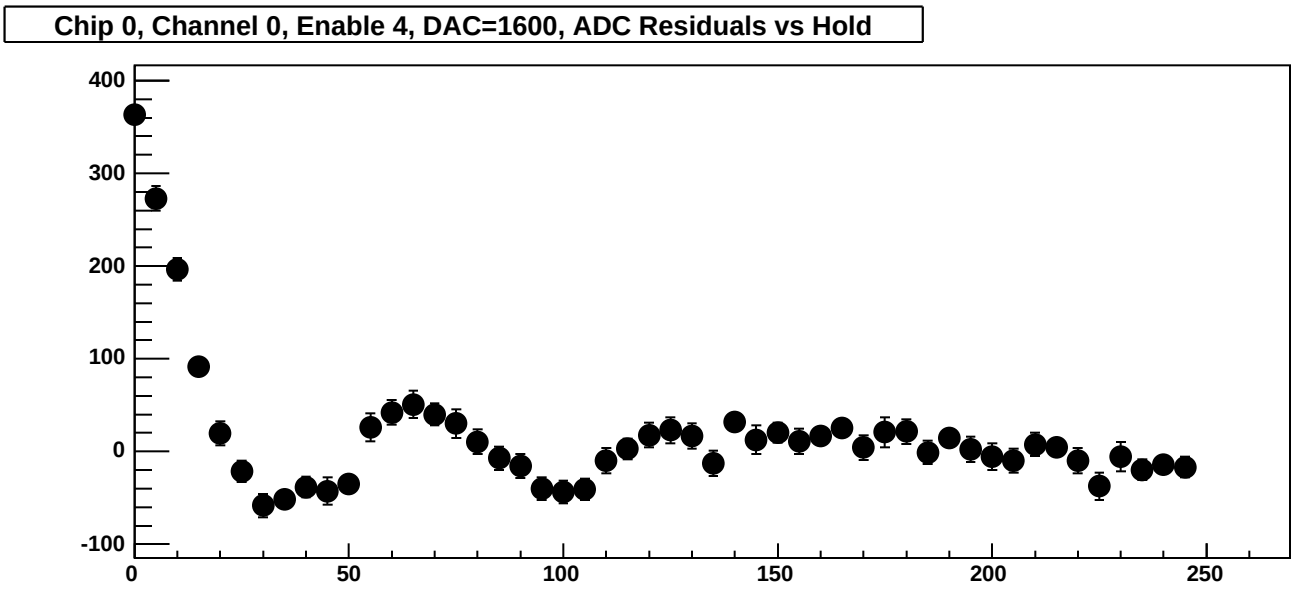

Chip 0, Channel 0, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 220.3 / 41
p0 -211.9 ± 3.426
p1 93.07 ± 0.5538
p2 -3.787e+08 ± 6.986e+06
p3 3.071e+07 ± 4.608e+05
p4 11.06 ± 0.118
-200
-300
-400
-500
-600
-700
-800
-900
-1000
0
50
100
150
200
250

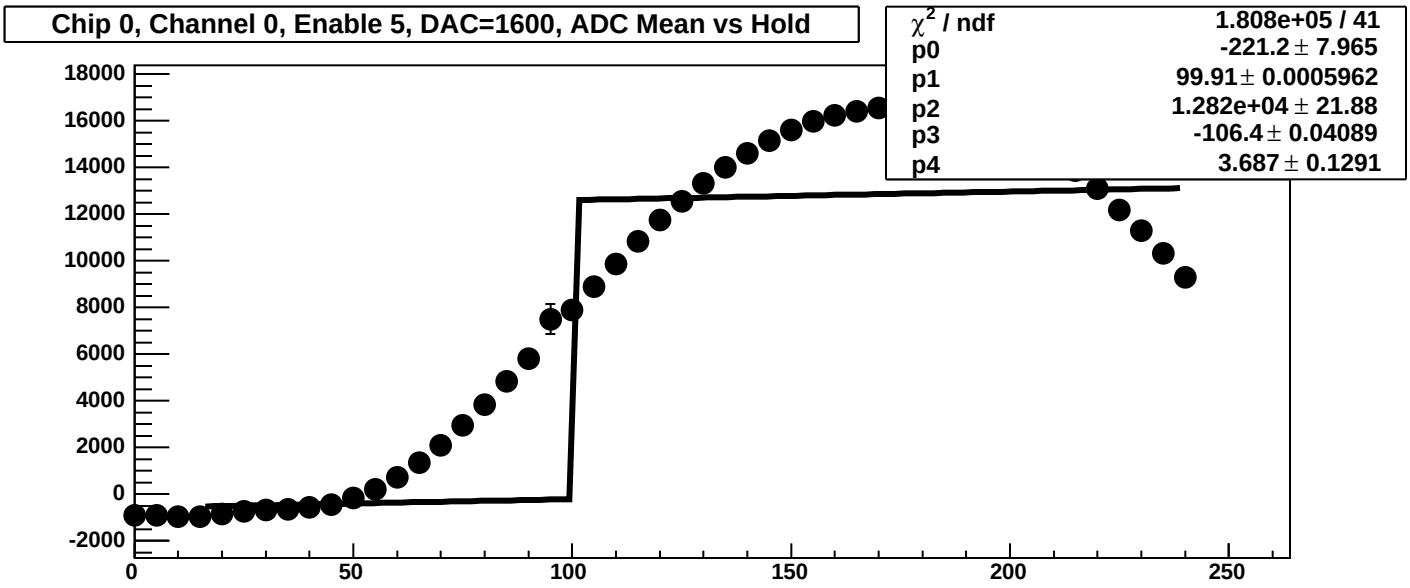

Chip 0, Channel 0, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

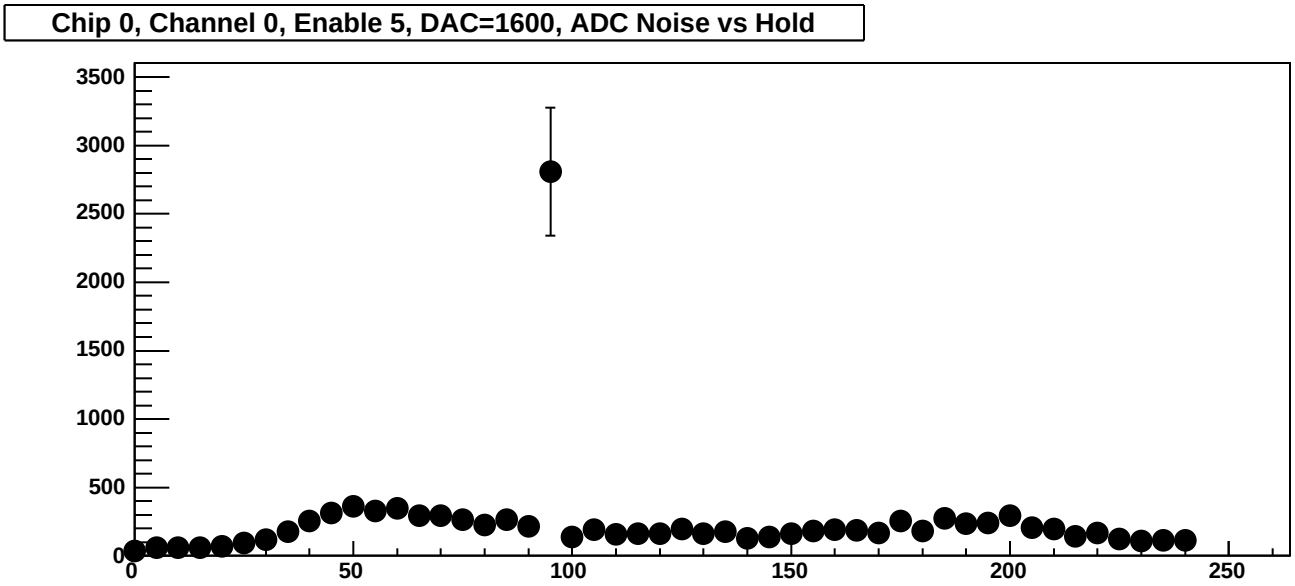

Chip 0, Channel 0, Enable 3, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
0
50
100
150
200
250

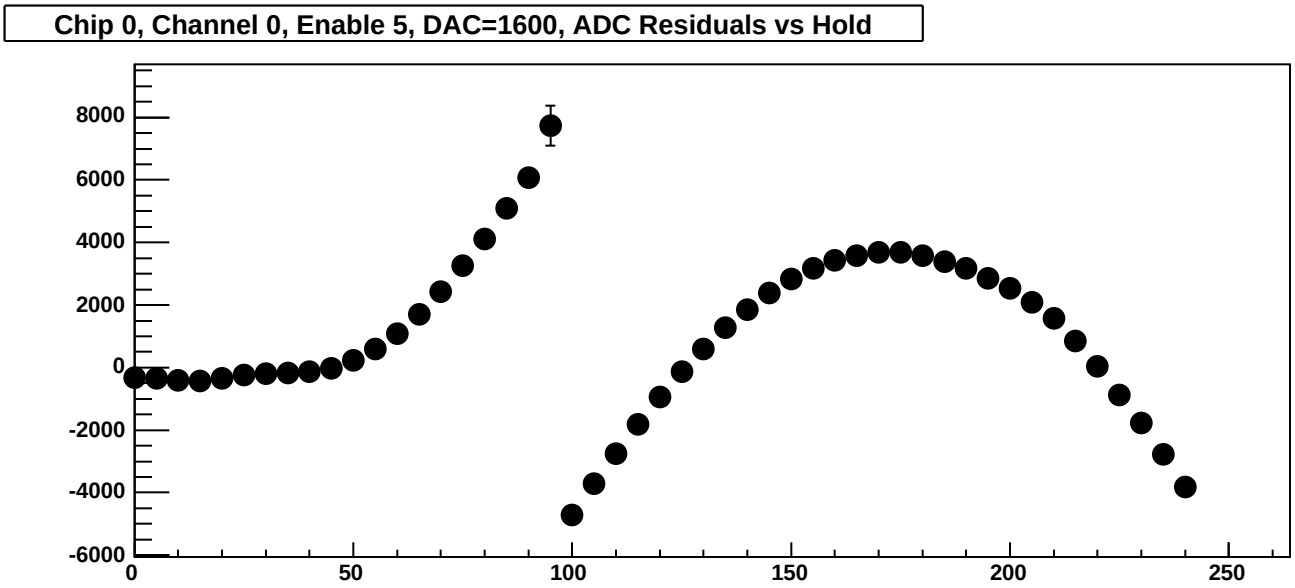

Chip 0, Channel 0, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 306.3 / 41
p0 -230.6 ± 4.03
p1 94.49 ± 0.6697
p2 -6.562e+08 ± 8.529e+06
p3 5.465e+07 ± 3.496e+05
p4 11.05 ± 0.132
-200
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
50
100
150
200
250


Chip 0, Channel 0, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

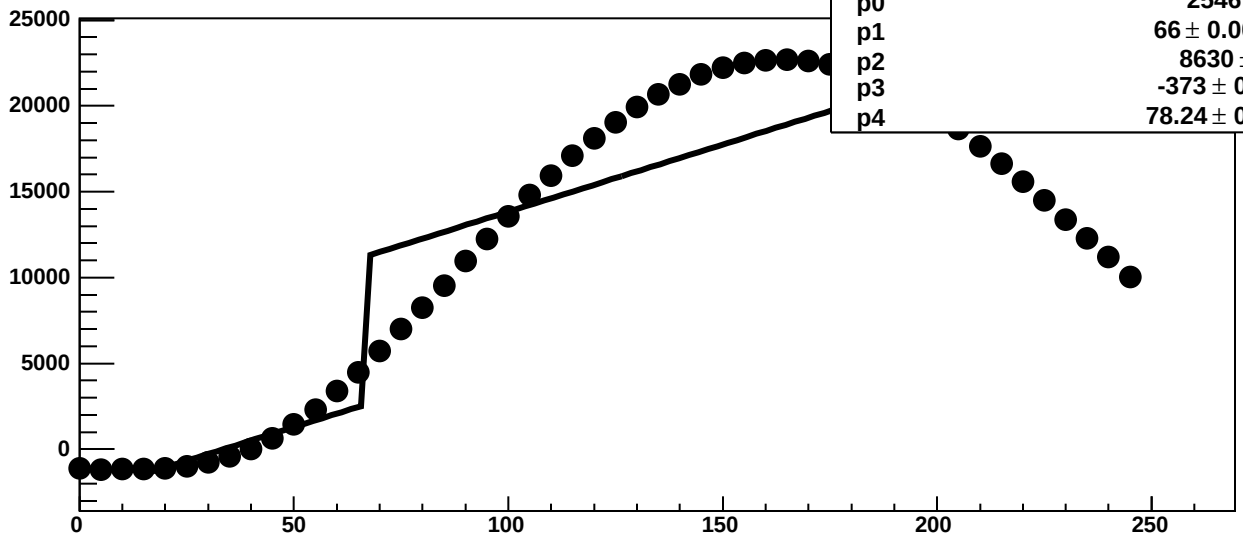

Chip 0, Channel 0, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 0, Channel 0, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.808e+05 / 41
p0 -221.2 ± 7.965
p1 99.91 ± 0.0005962
p2 1.282e+04 ± 21.88
p3 -106.4 ± 0.04089
p4 3.687 ± 0.1291
18000
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

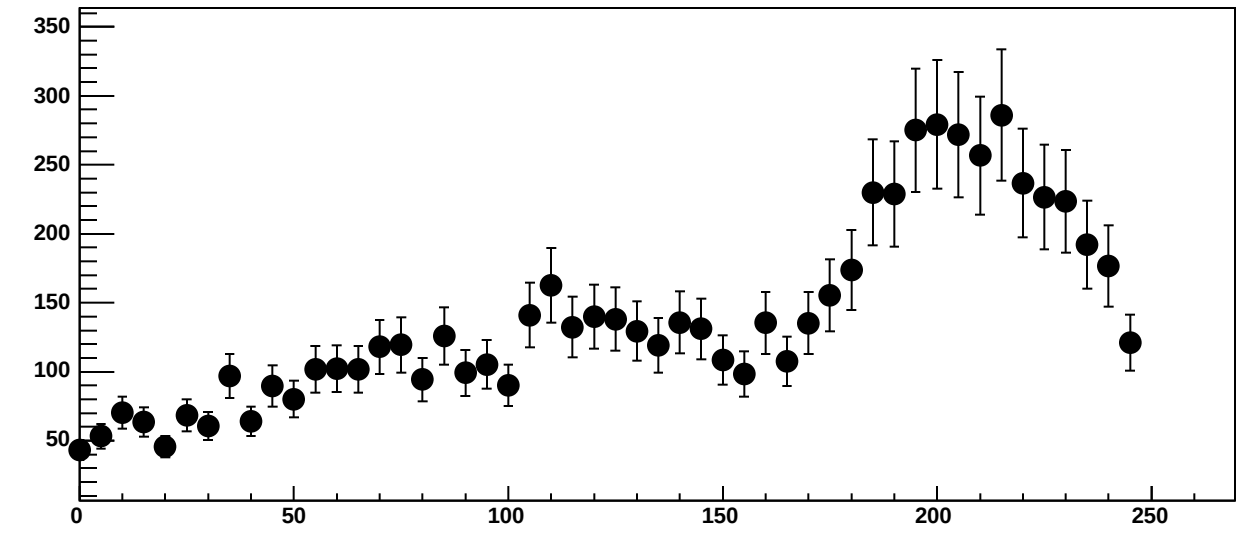

Chip 0, Channel 0, Enable 5, DAC=1600, ADC Noise vs Hold
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 0, Channel 0, Enable 5, DAC=1600, ADC Residuals vs Hold
8000
6000
4000
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

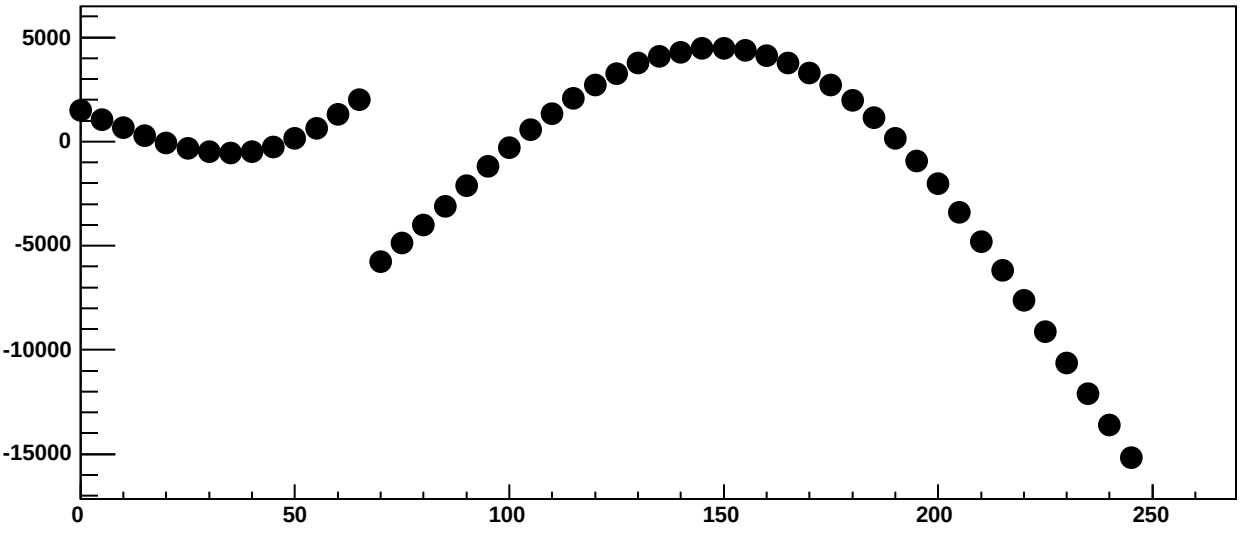
Chip 0, Channel 1, Enable 0, DAC=1600, ADC Mean vs Hold



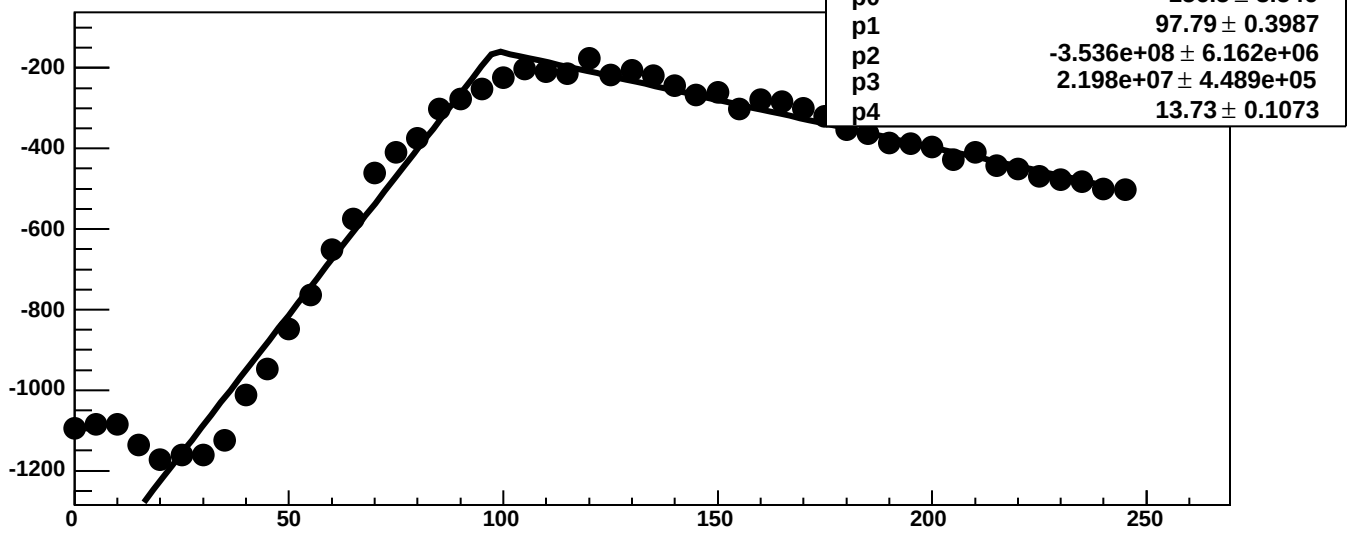
Chip 0, Channel 1, Enable 0, DAC=1600, ADC Noise vs Hold



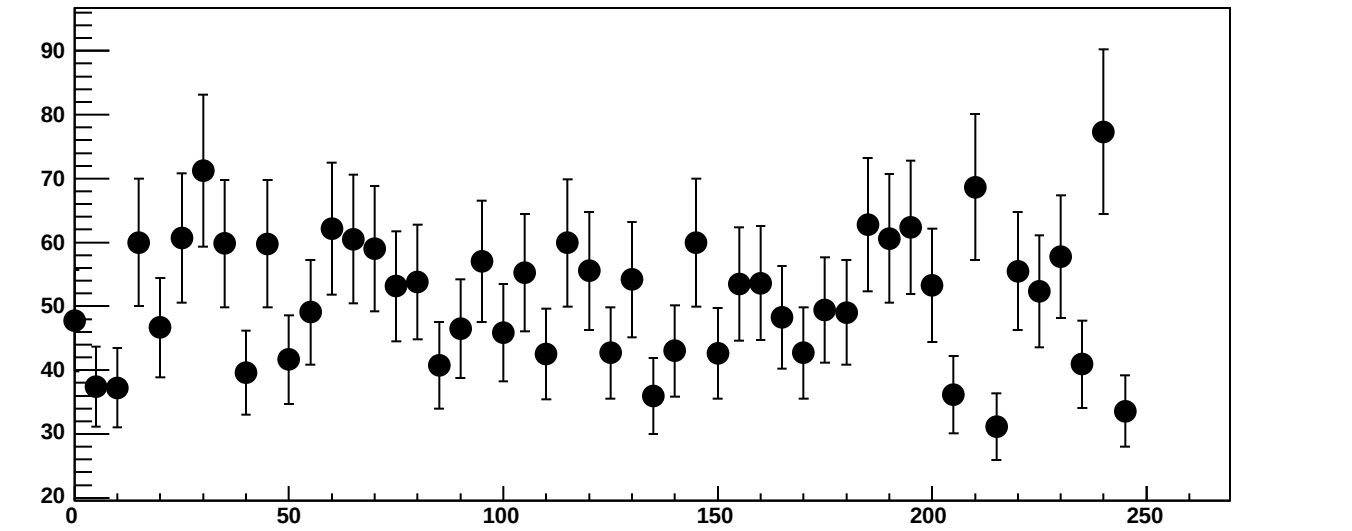
Chip 0, Channel 1, Enable 0, DAC=1600, ADC Residuals vs Hold



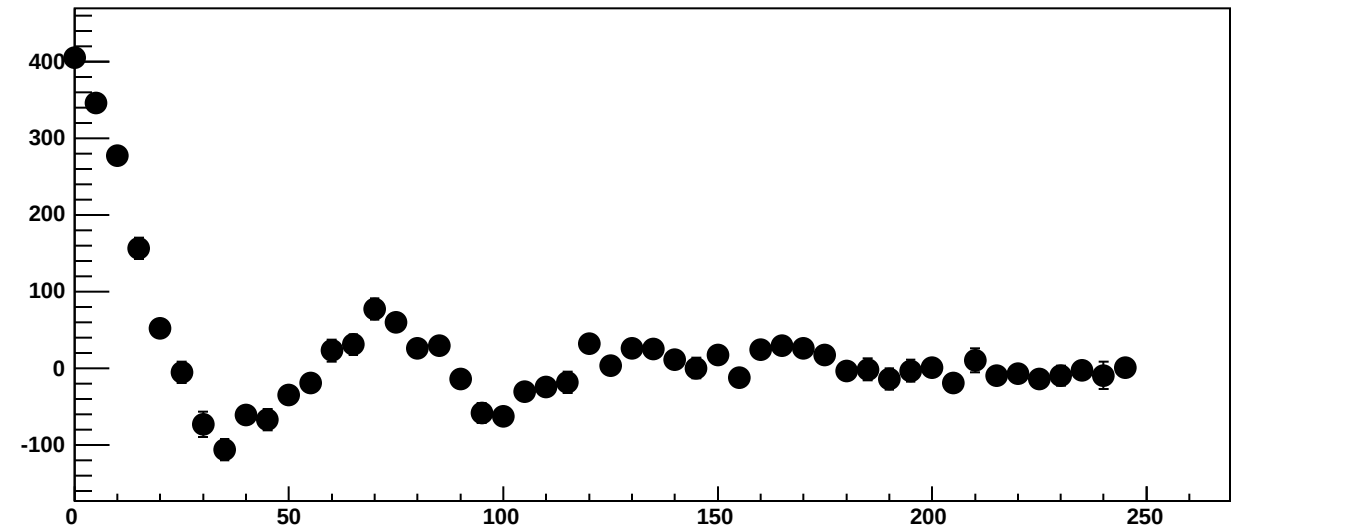
Chip 0, Channel 1, Enable 1, DAC=1600, ADC Mean vs Hold

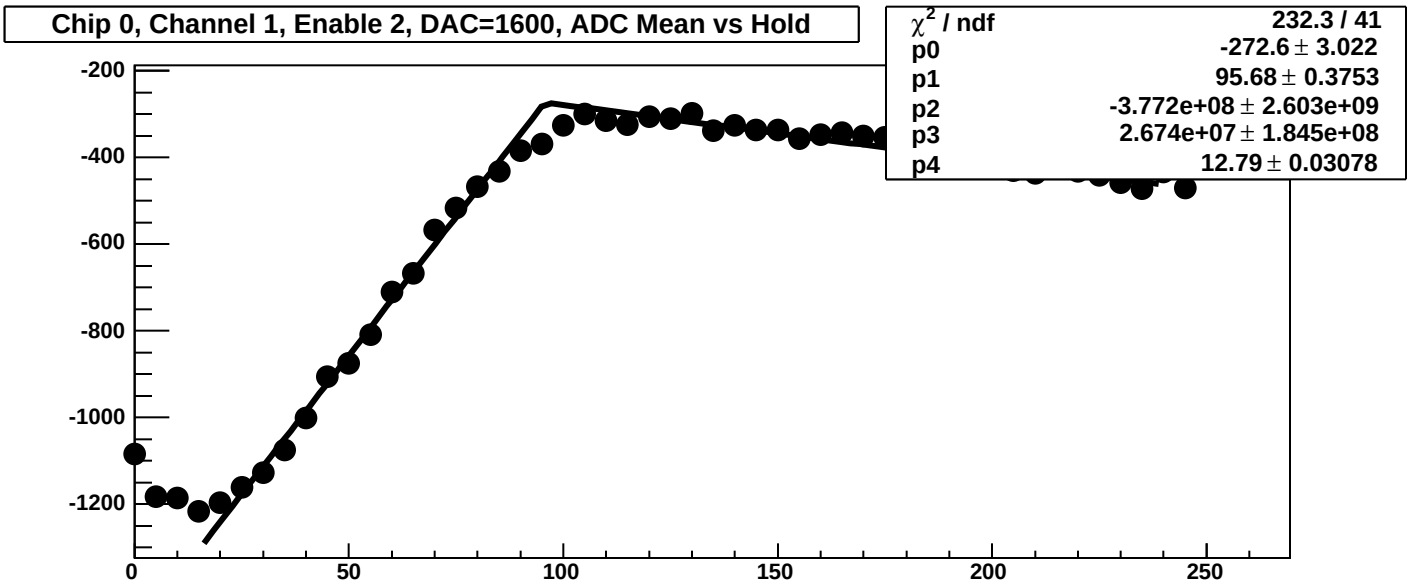


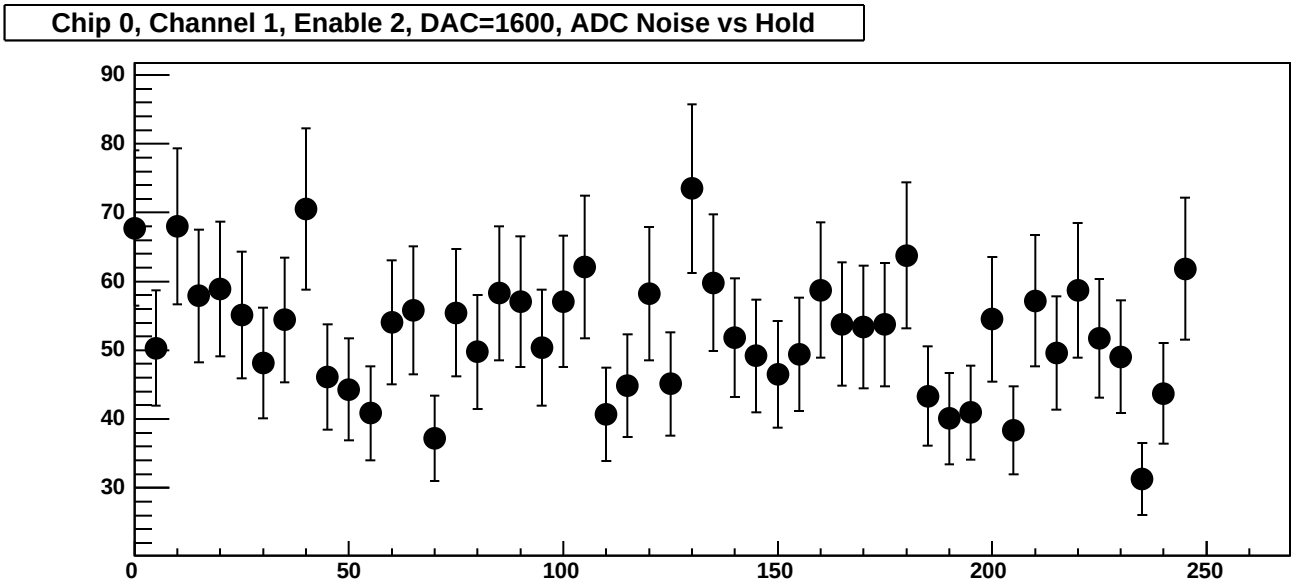
Chip 0, Channel 1, Enable 1, DAC=1600, ADC Noise vs Hold

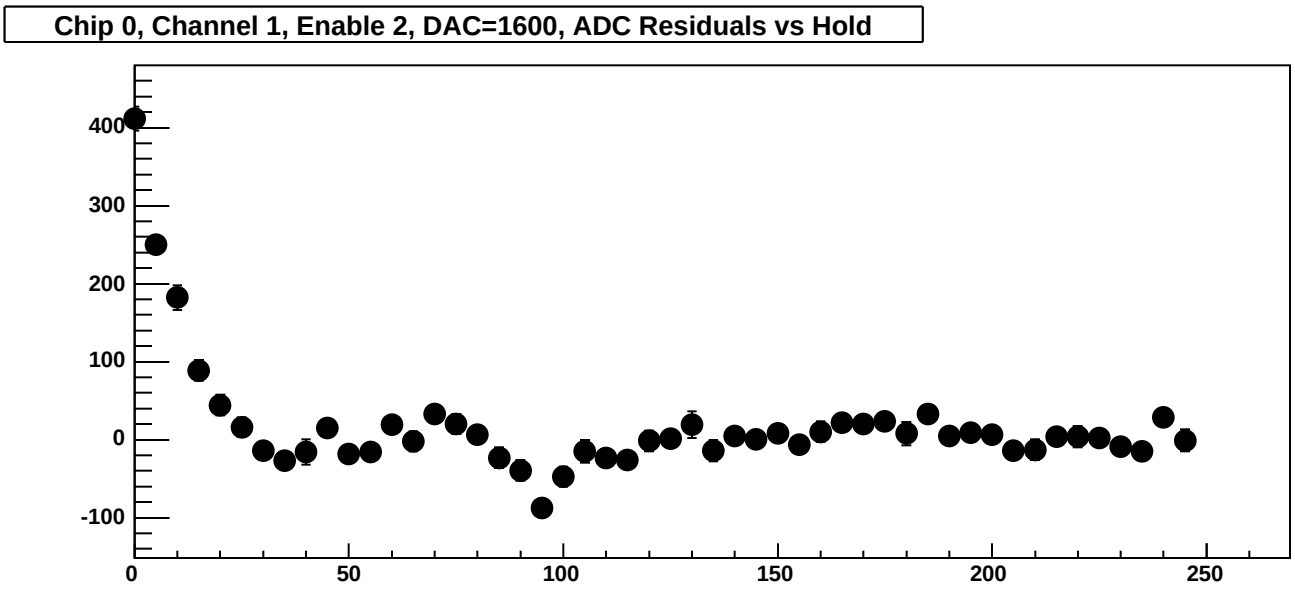


Chip 0, Channel 1, Enable 1, DAC=1600, ADC Residuals vs Hold

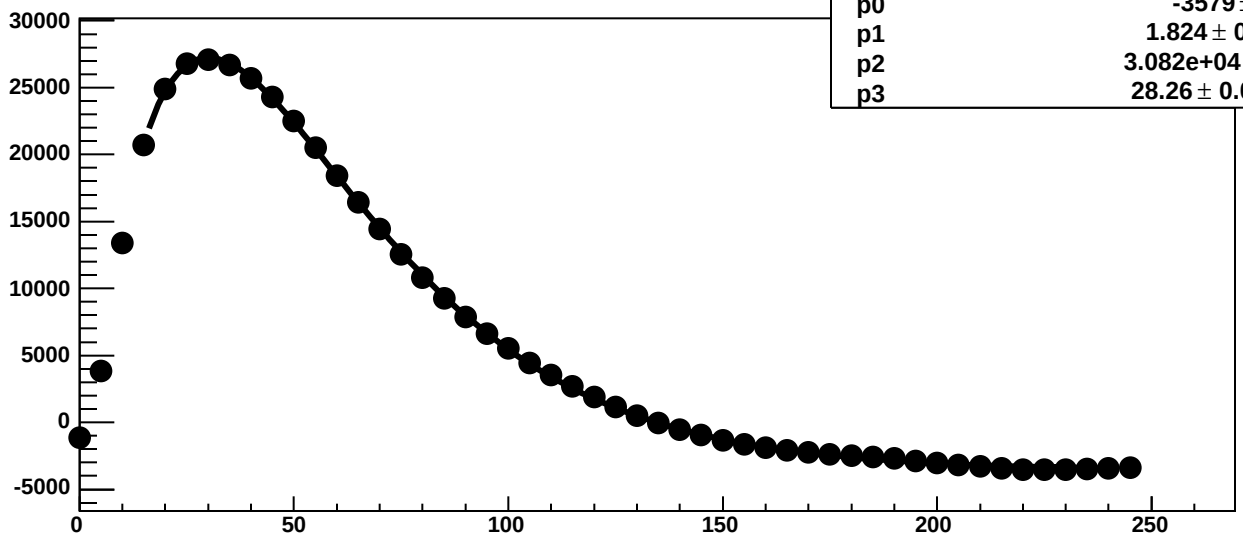



Chip 0, Channel 1, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 232.3 / 41
p0 -272.6 ± 3.022
p1 95.68 ± 0.3753
p2 -3.772e+08 ± 2.603e+09
p3 2.674e+07 ± 1.845e+08
p4 12.79 ± 0.03078
-200
-400
-600
-800
-1000
-1200
0
50
100
150
200
250

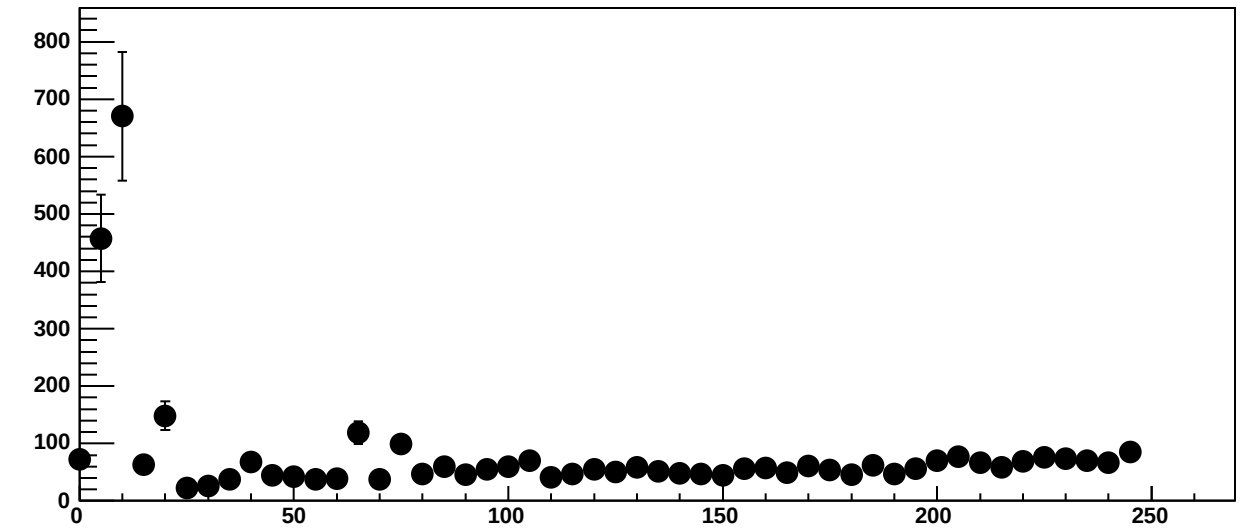

Chip 0, Channel 1, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 0, Channel 1, Enable 2, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

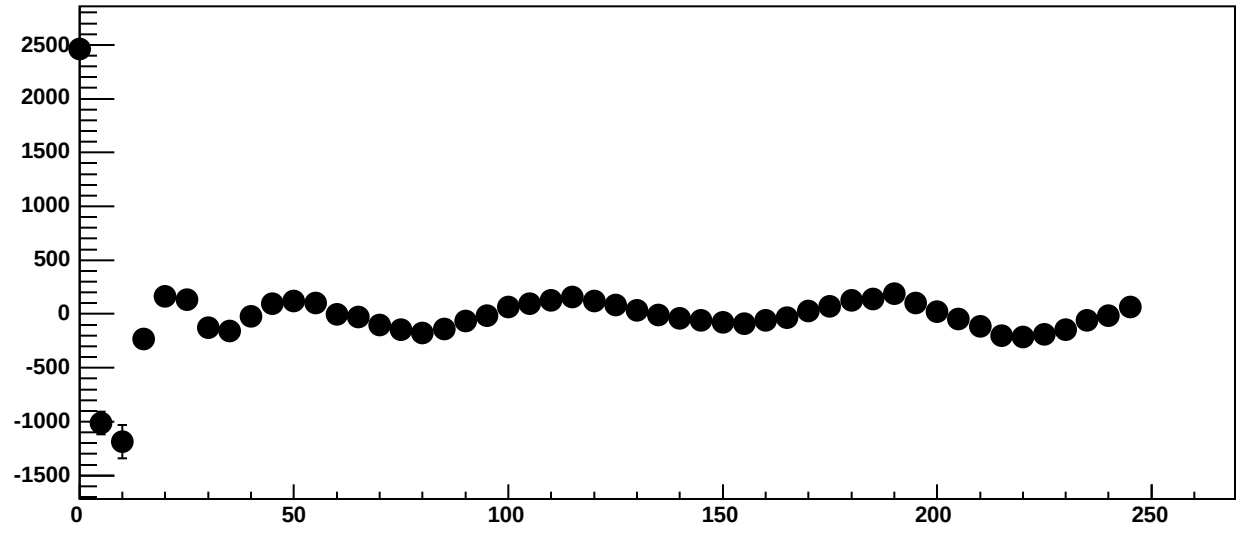
Chip 0, Channel 1, Enable 3!, DAC=1600, ADC Mean vs Hold



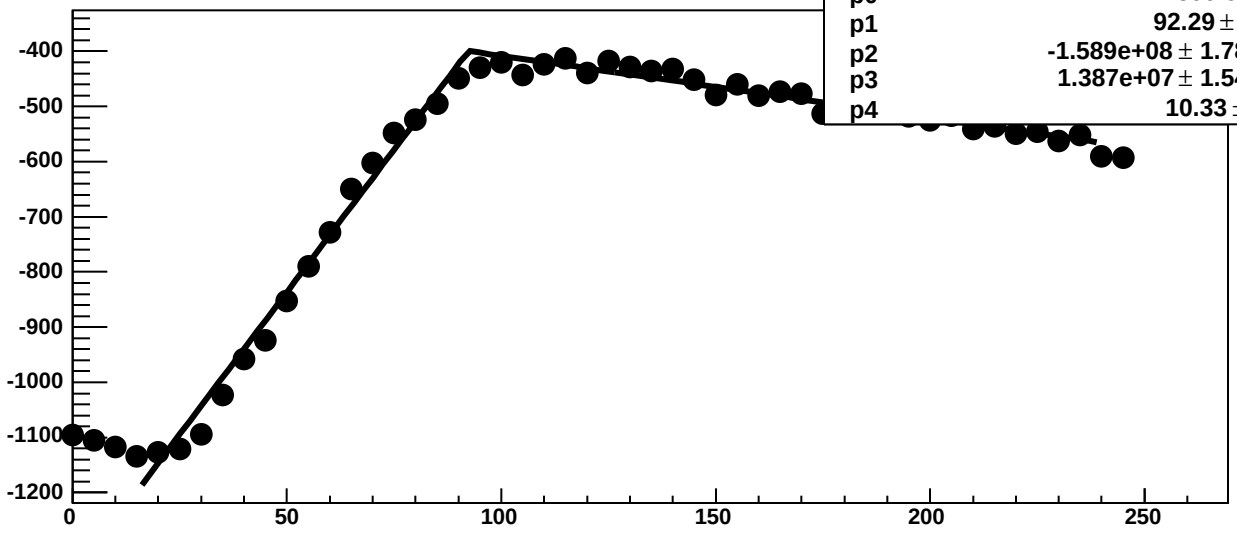
Chip 0, Channel 1, Enable 3!, DAC=1600, ADC Noise vs Hold



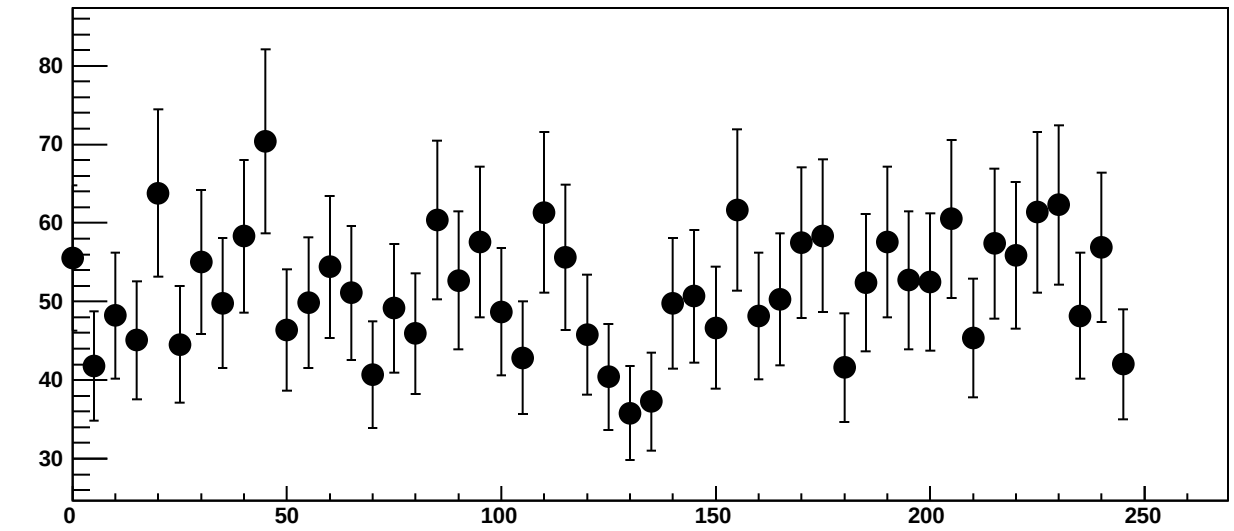
Chip 0, Channel 1, Enable 3!, DAC=1600, ADC Residuals vs Hold



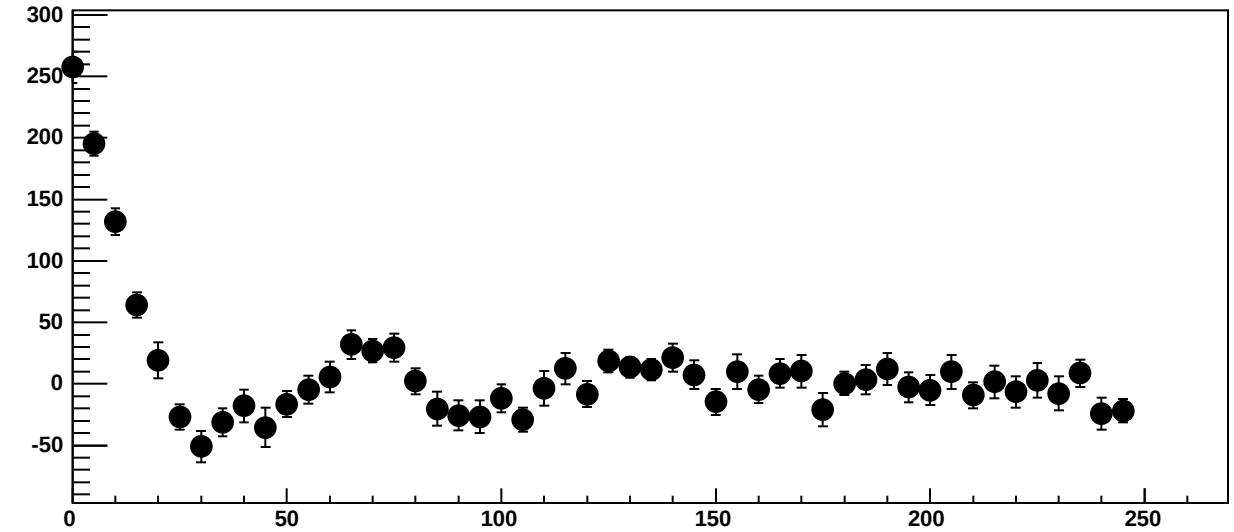
Chip 0, Channel 1, Enable 4, DAC=1600, ADC Mean vs Hold

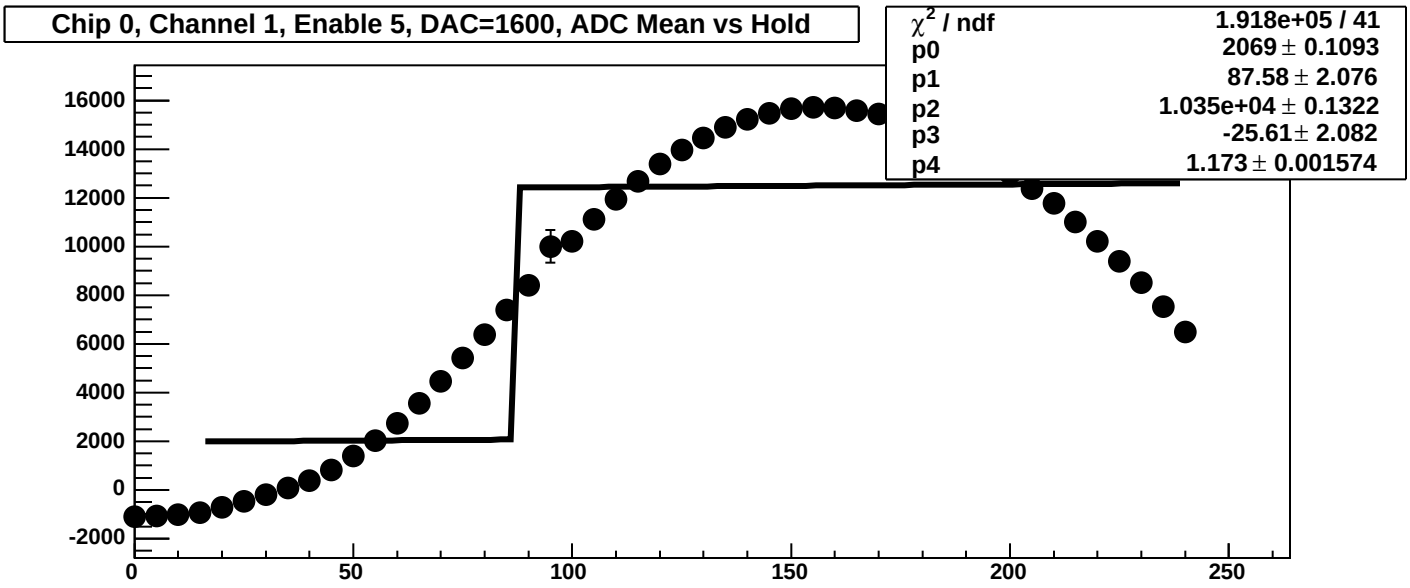


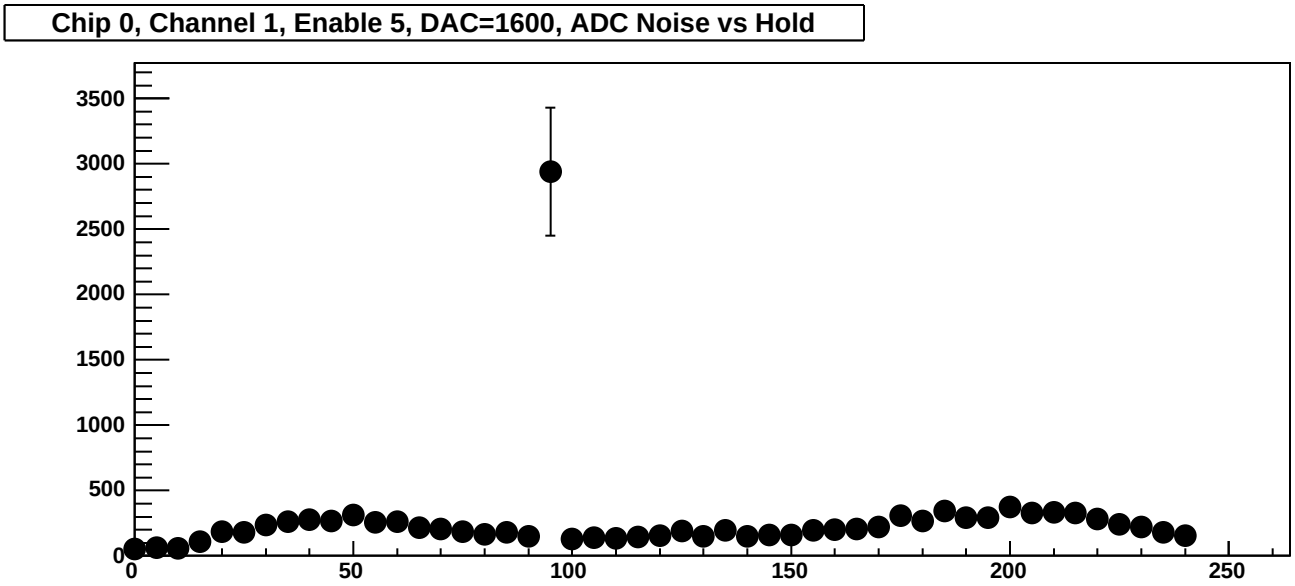
Chip 0, Channel 1, Enable 4, DAC=1600, ADC Noise vs Hold

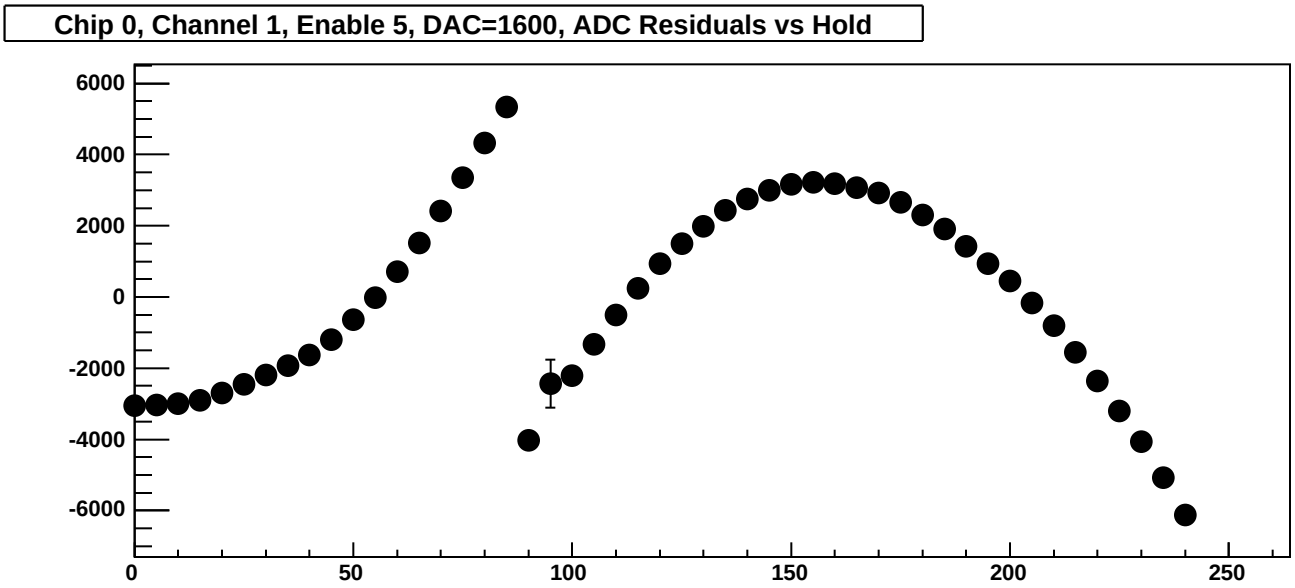


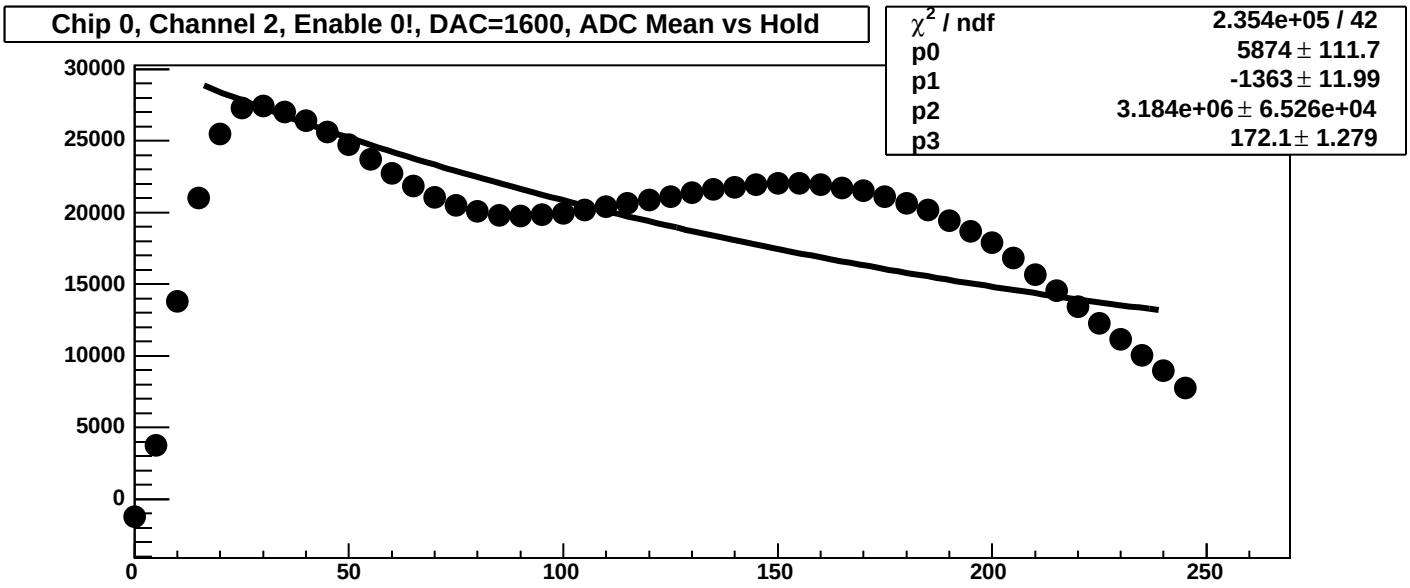
Chip 0, Channel 1, Enable 4, DAC=1600, ADC Residuals vs Hold

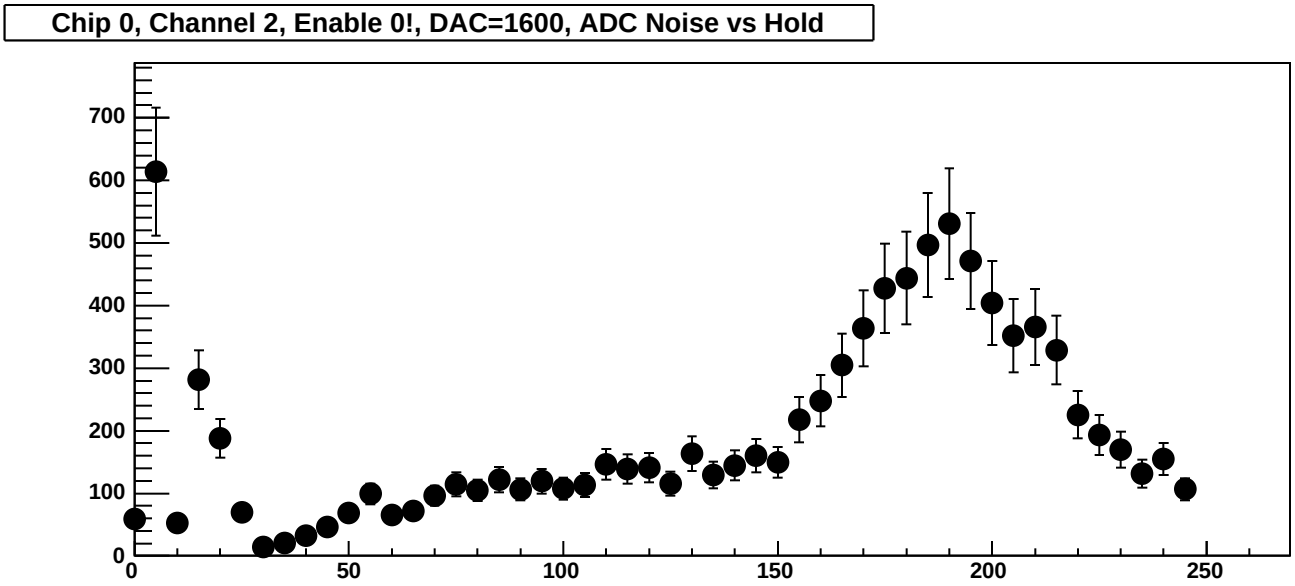


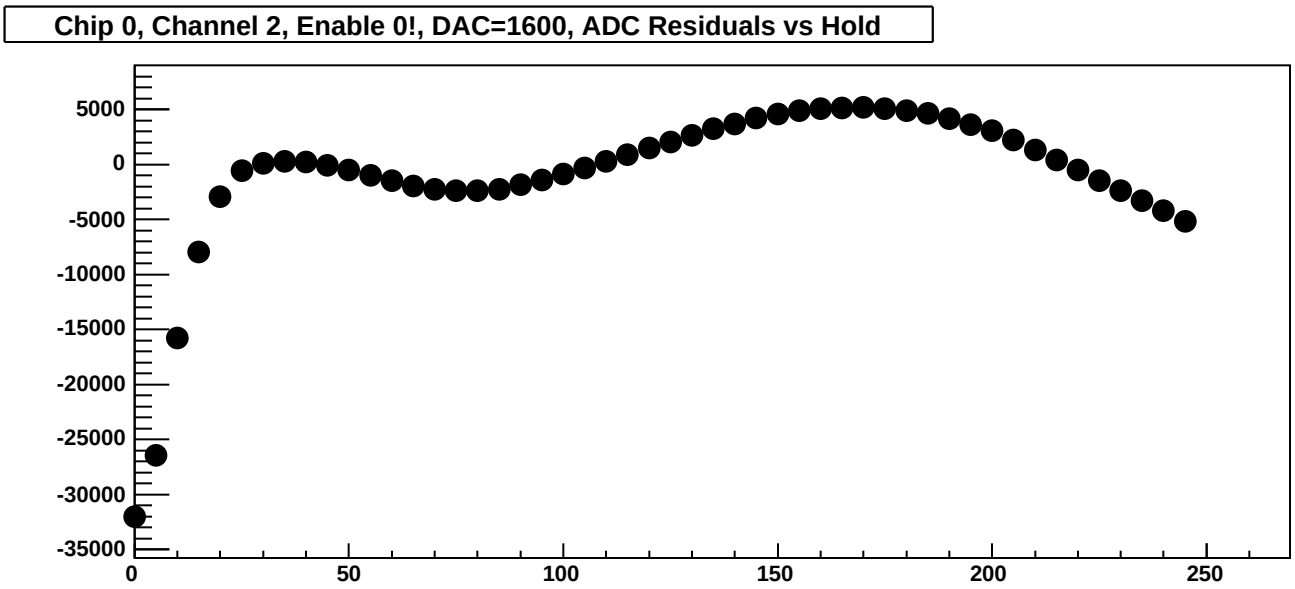

Chip 0, Channel 1, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.918e+05 / 41
p0 2069 ± 0.1093
p1 87.58 ± 2.076
p2 1.035e+04 ± 0.1322
p3 -25.61 ± 2.082
p4 1.173 ± 0.001574
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

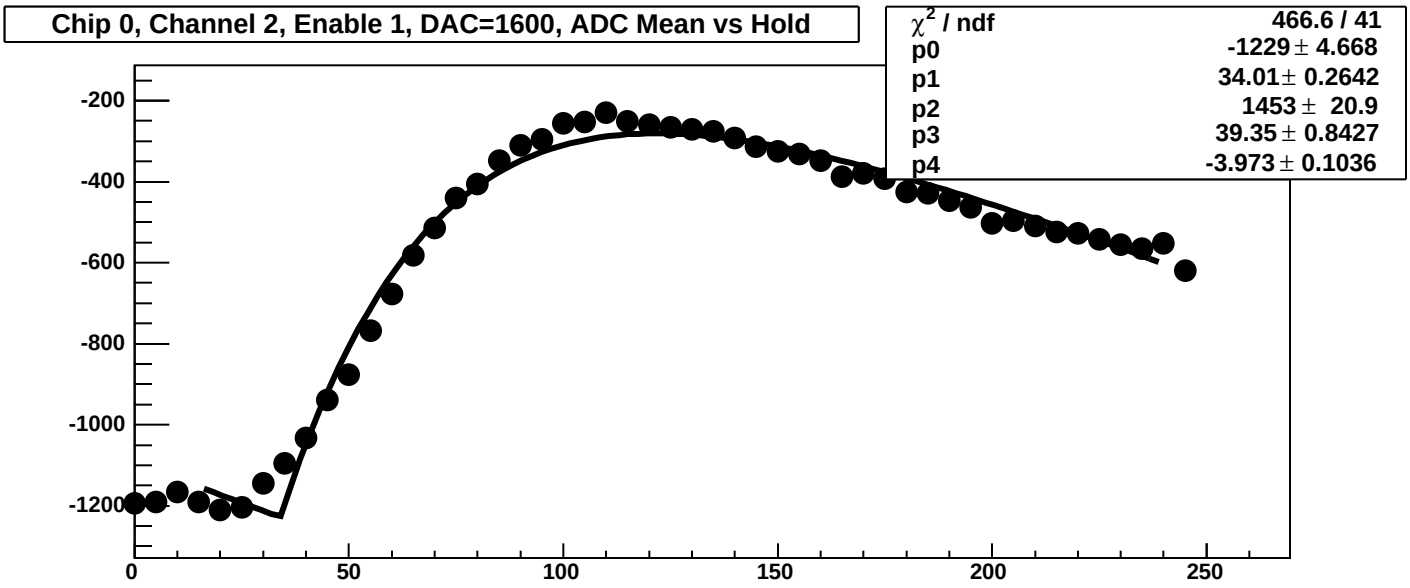

Chip 0, Channel 1, Enable 5, DAC=1600, ADC Noise vs Hold
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250

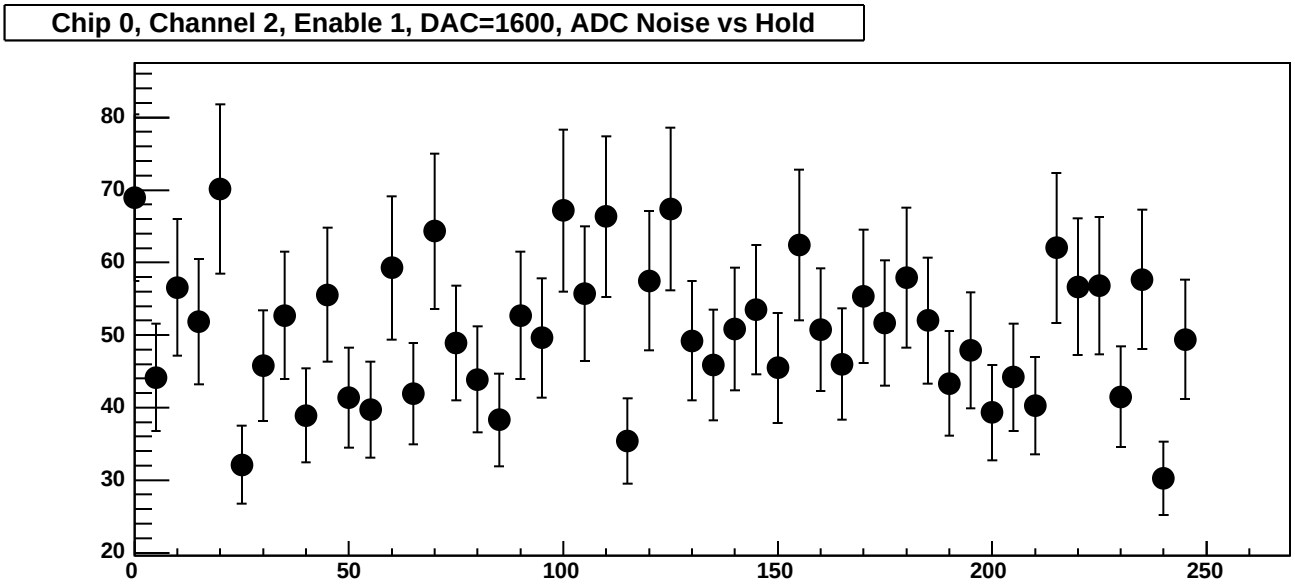

Chip 0, Channel 1, Enable 5, DAC=1600, ADC Residuals vs Hold
6000
4000
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

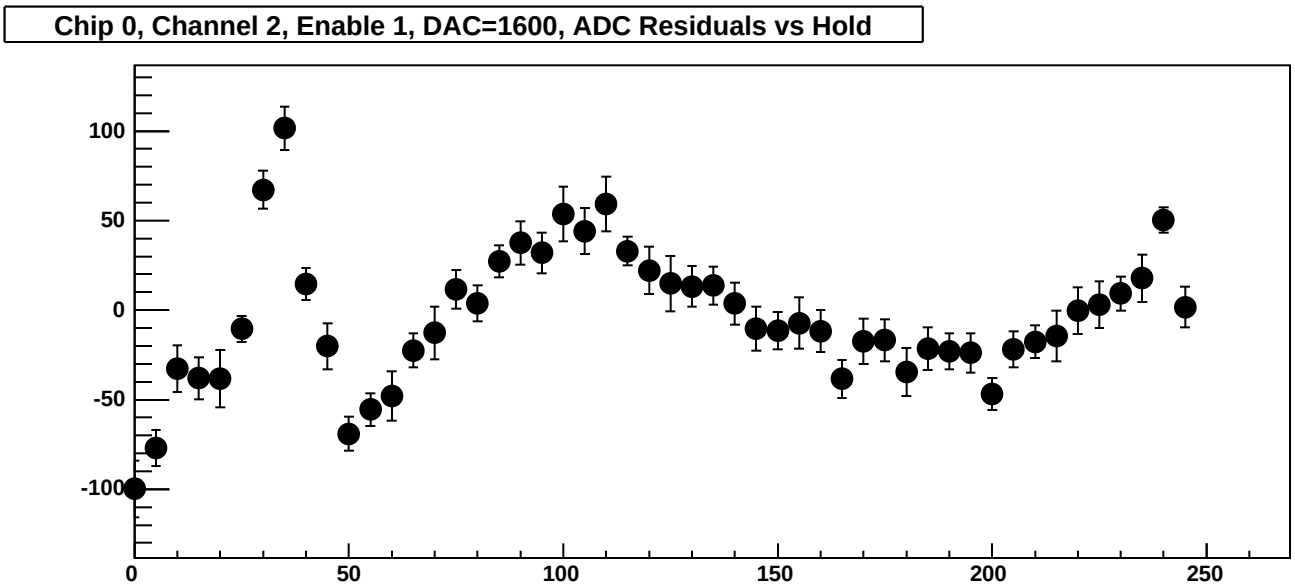

Chip 0, Channel 2, Enable 0!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.354e+05 / 42
p0 5874 ± 111.7
p1 -1363 ± 11.99
p2 3.184e+06 ± 6.526e+04
p3 172.1 ± 1.279
30000
25000
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 0, Channel 2, Enable 0!, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250

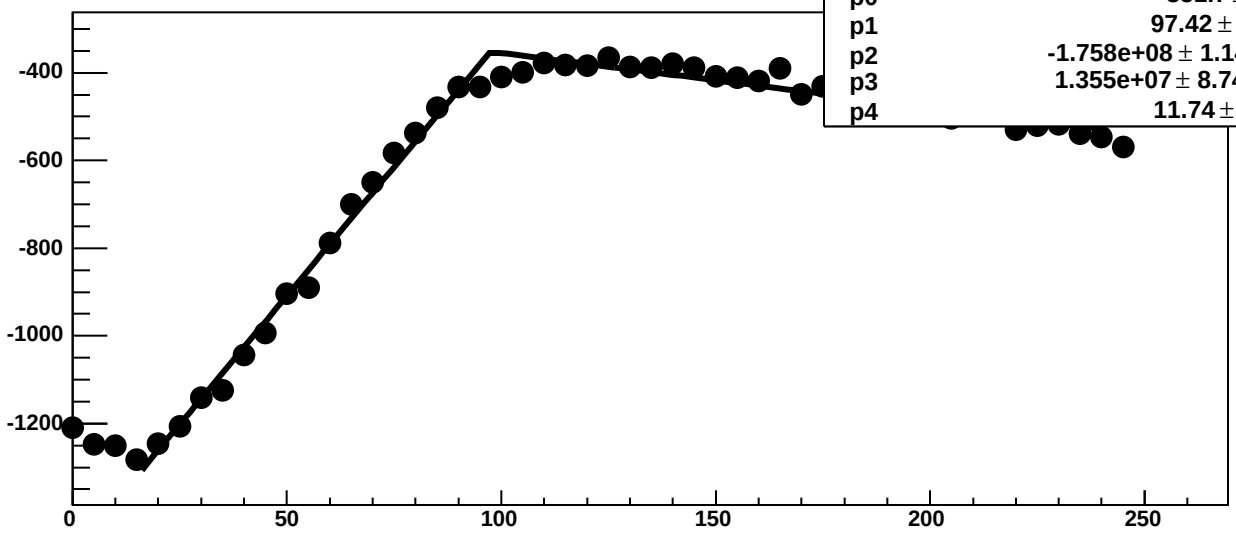

Chip 0, Channel 2, Enable 0!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
-35000
0
50
100
150
200
250


Chip 0, Channel 2, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 466.6 / 41
p0 -1229 ± 4.668
p1 34.01 ± 0.2642
p2 1453 ± 20.9
p3 39.35 ± 0.8427
p4 -3.973 ± 0.1036
-200
-400
-600
-800
-1000
-1200
0
50
100
150
200
250

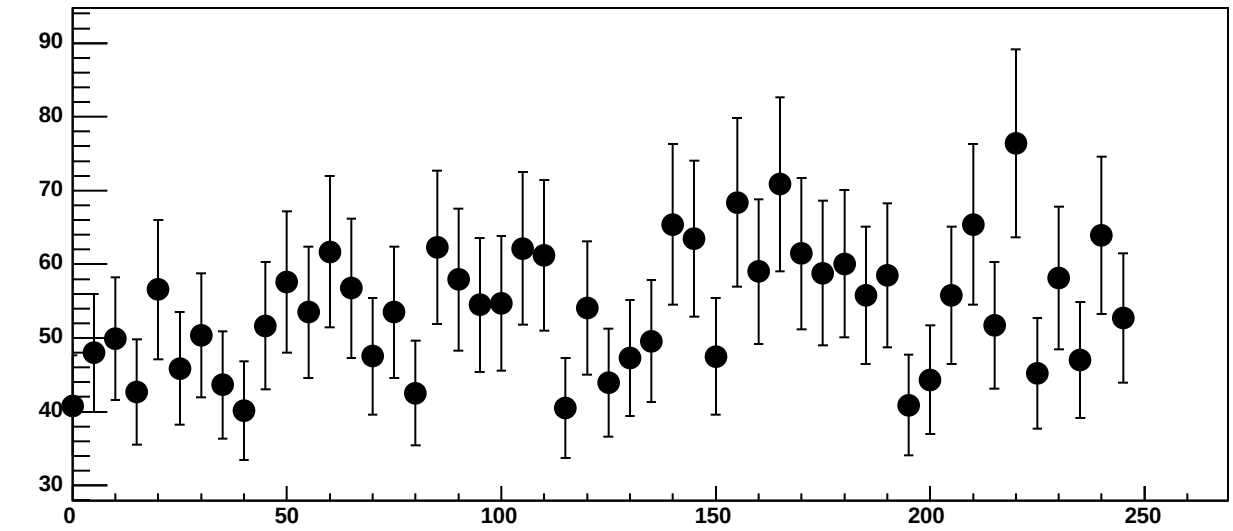

Chip 0, Channel 2, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 0, Channel 2, Enable 1, DAC=1600, ADC Residuals vs Hold
100
50
0
-50
-100
0
50
100
150
200
250

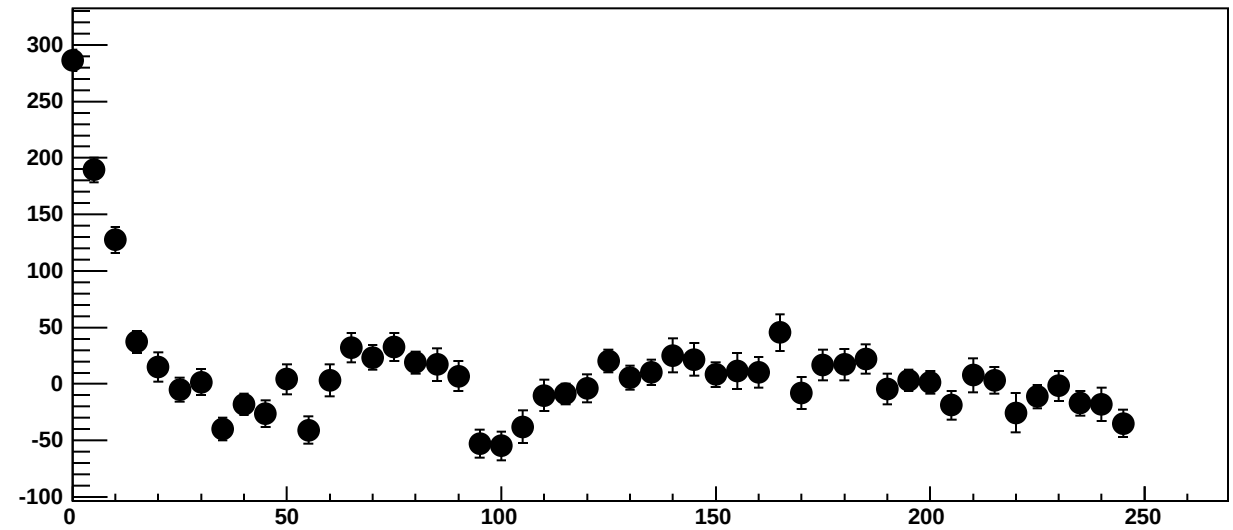
Chip 0, Channel 2, Enable 2, DAC=1600, ADC Mean vs Hold

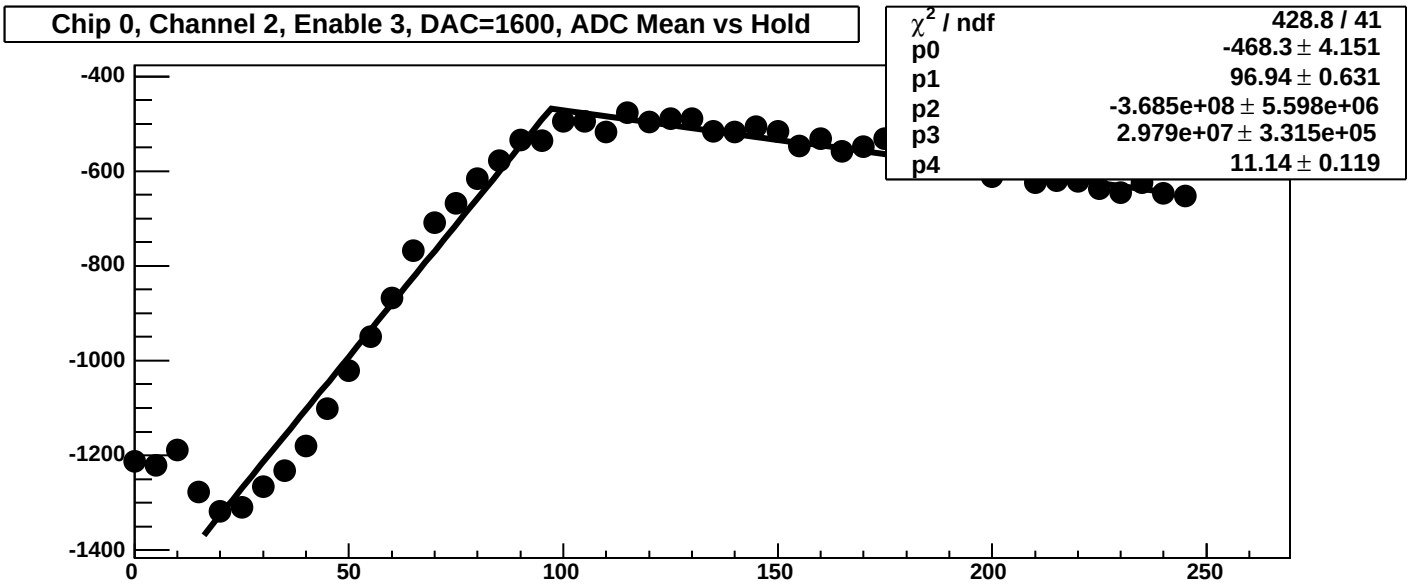


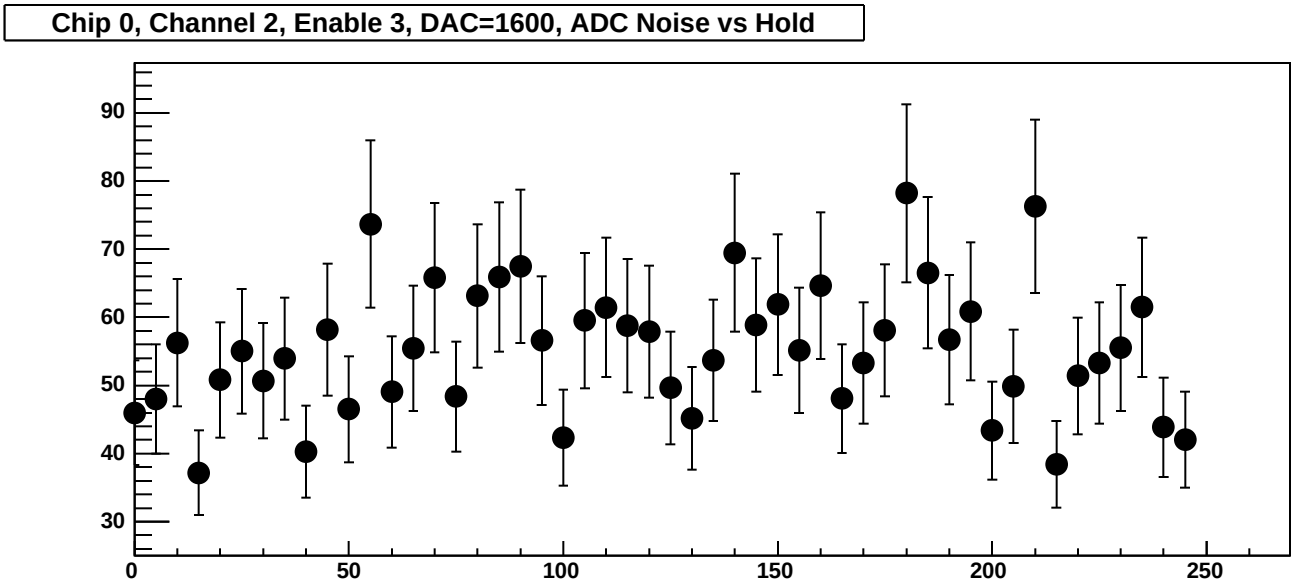
Chip 0, Channel 2, Enable 2, DAC=1600, ADC Noise vs Hold

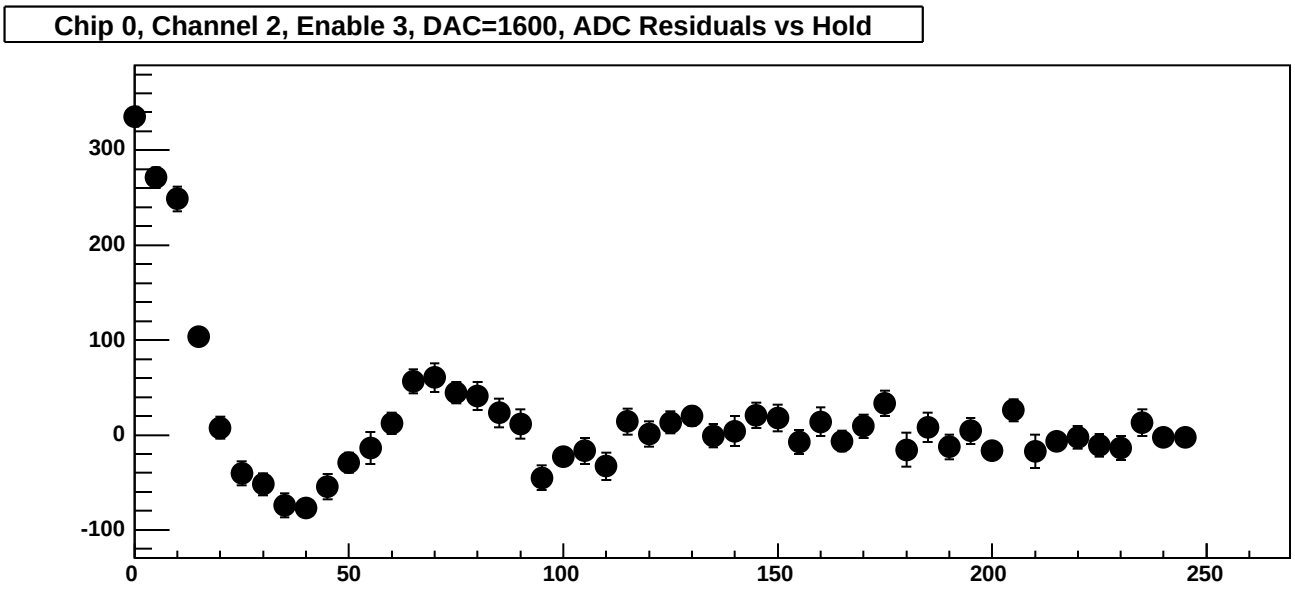


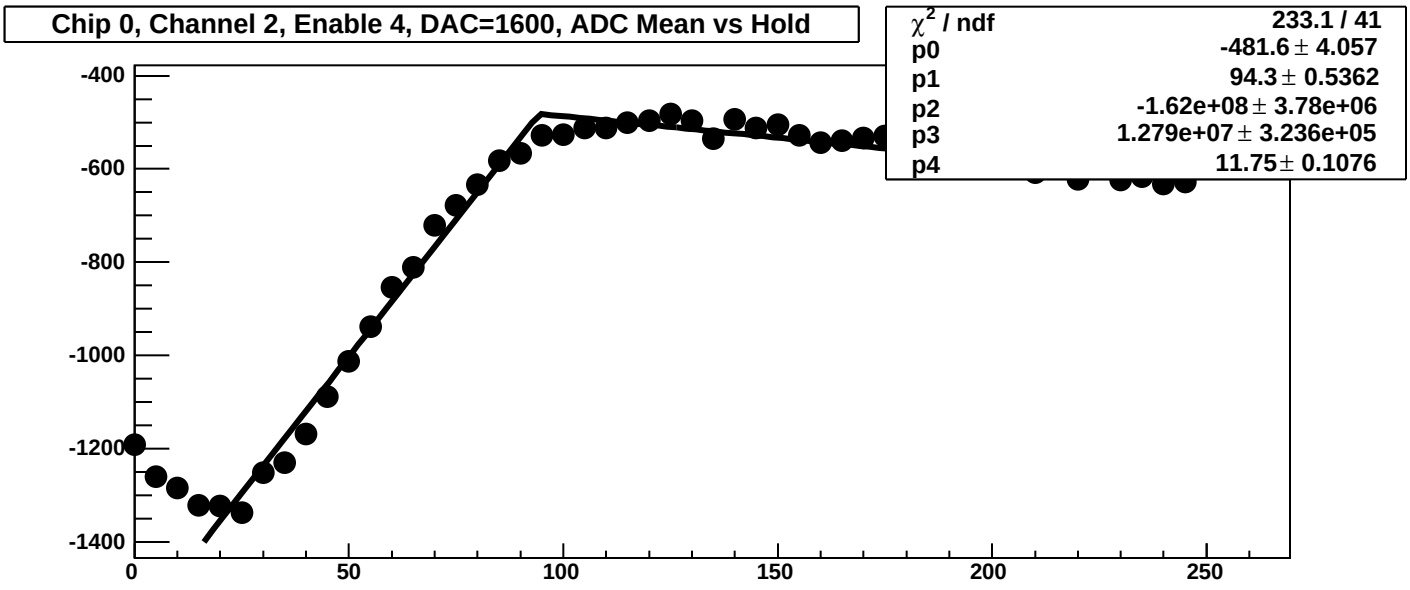
Chip 0, Channel 2, Enable 2, DAC=1600, ADC Residuals vs Hold

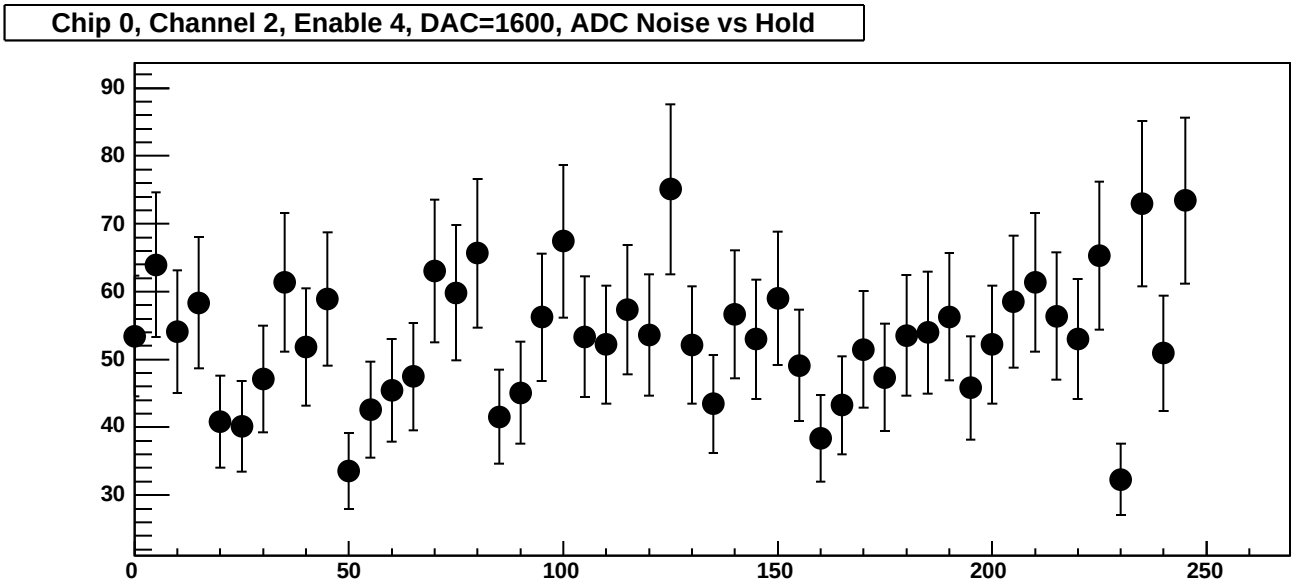


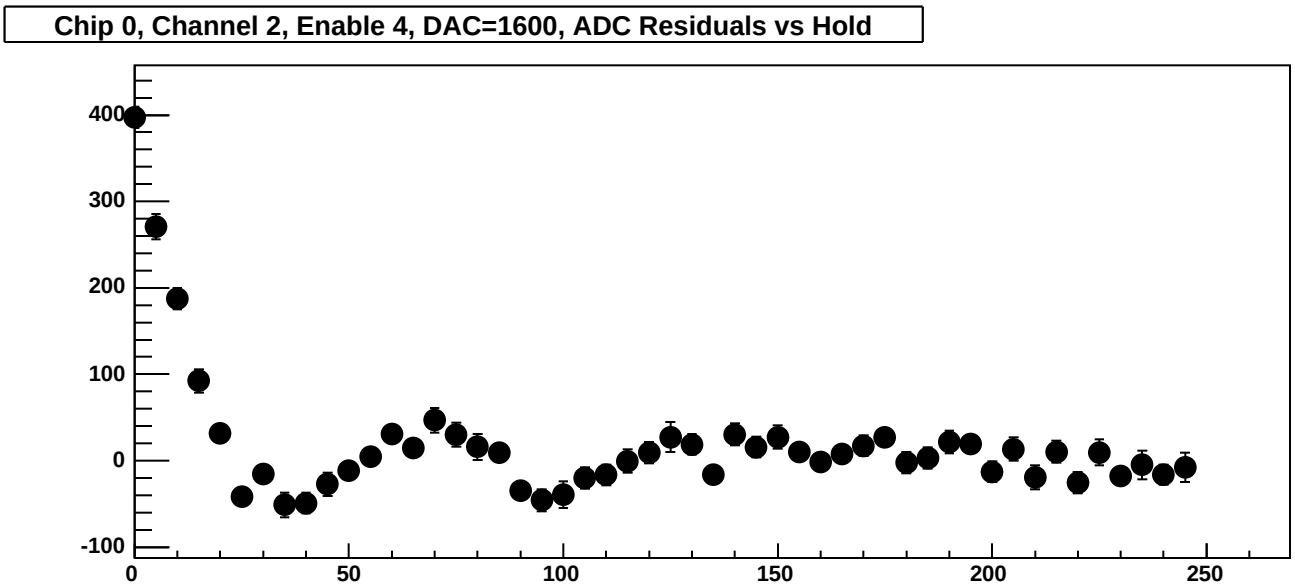

Chip 0, Channel 2, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 428.8 / 41
p0 -468.3 ± 4.151
p1 96.94 ± 0.631
p2 -3.685e+08 ± 5.598e+06
p3 2.979e+07 ± 3.315e+05
p4 11.14 ± 0.119
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

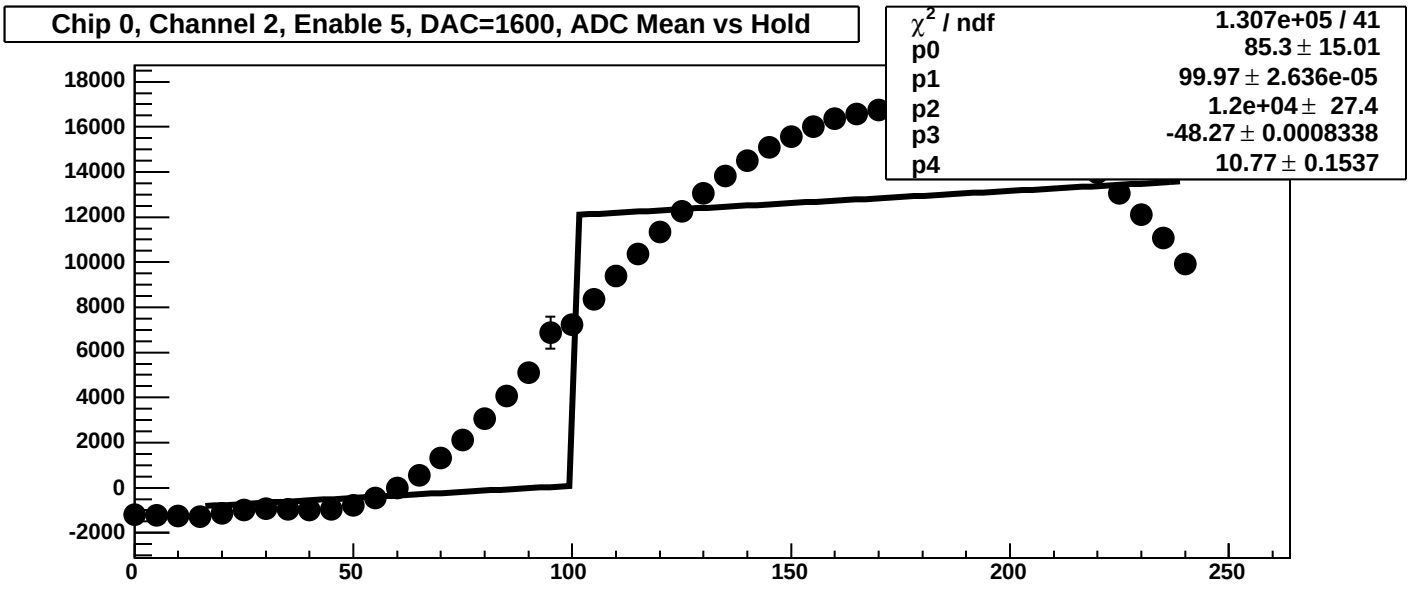

Chip 0, Channel 2, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

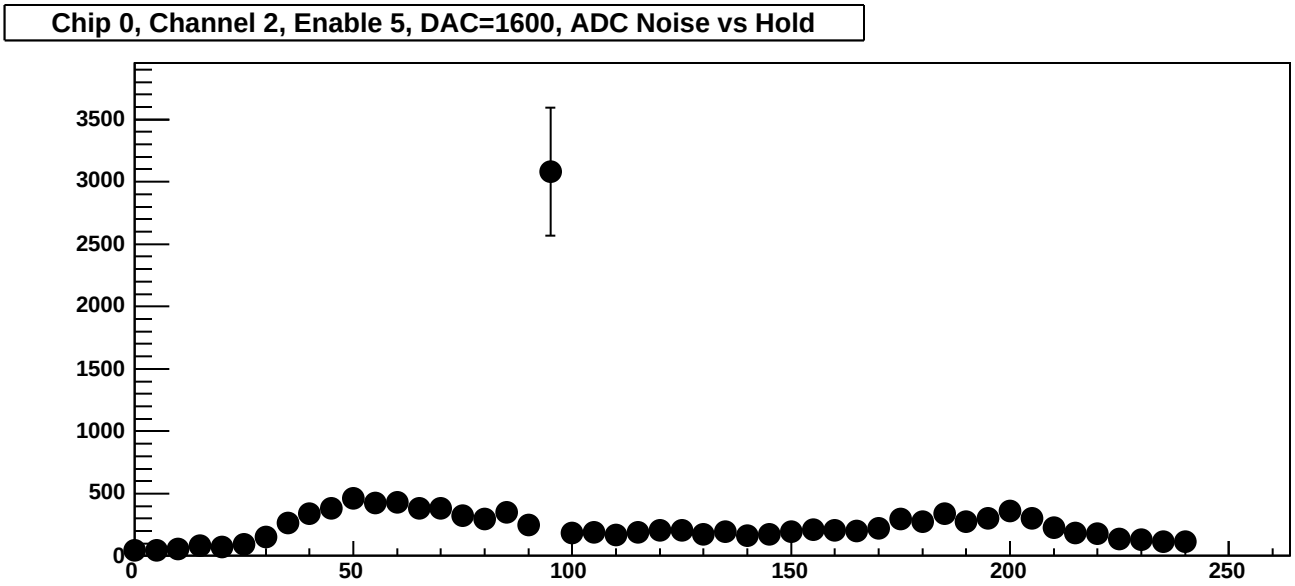

Chip 0, Channel 2, Enable 3, DAC=1600, ADC Residuals vs Hold
300
200
100
0
-100
0
50
100
150
200
250

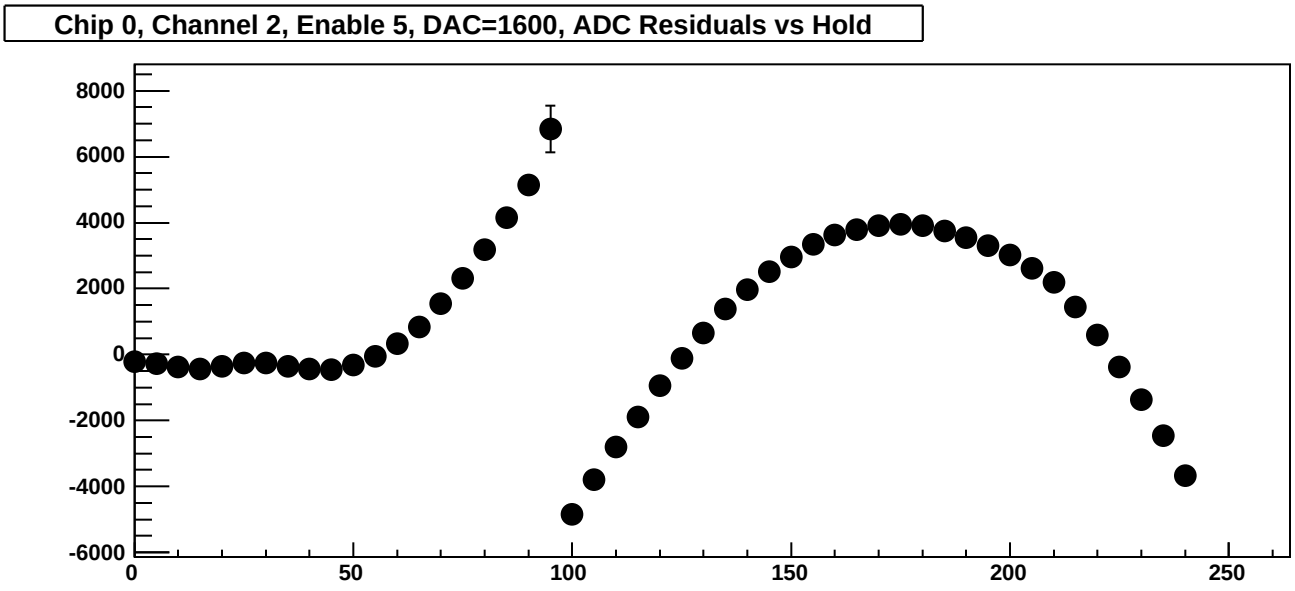

Chip 0, Channel 2, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 233.1 / 41
p0 -481.6 ± 4.057
p1 94.3 ± 0.5362
p2 -1.62e+08 ± 3.78e+06
p3 1.279e+07 ± 3.236e+05
p4 11.75 ± 0.1076
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

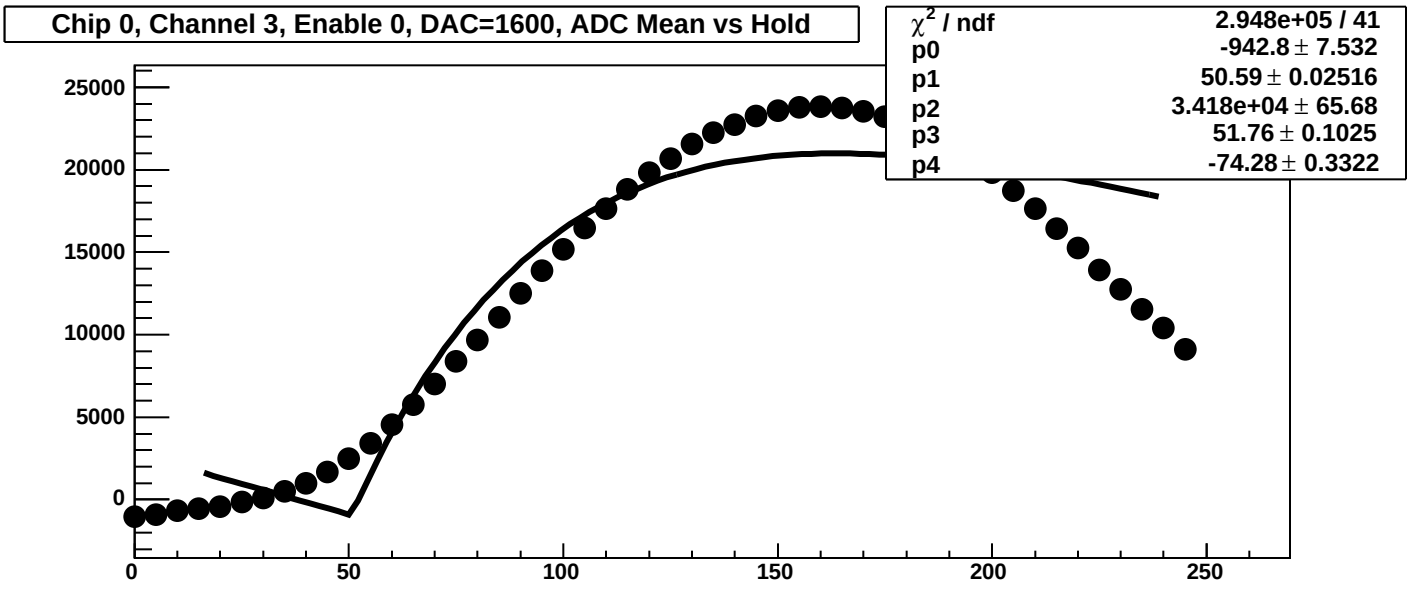

Chip 0, Channel 2, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

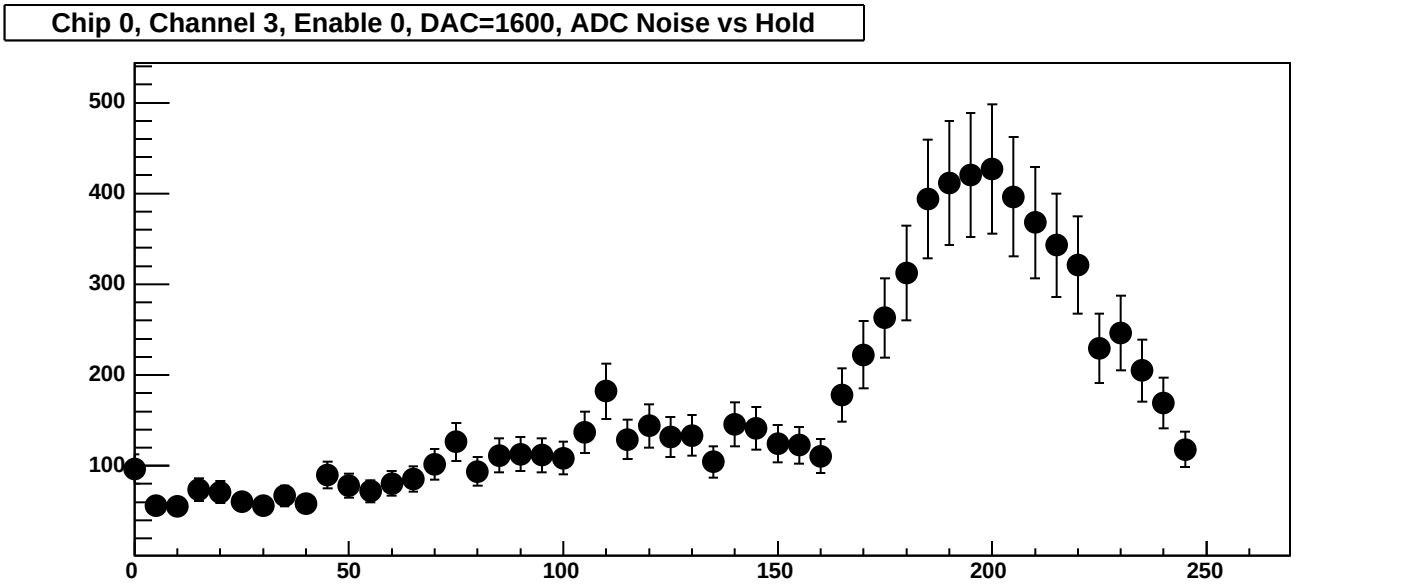

Chip 0, Channel 2, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

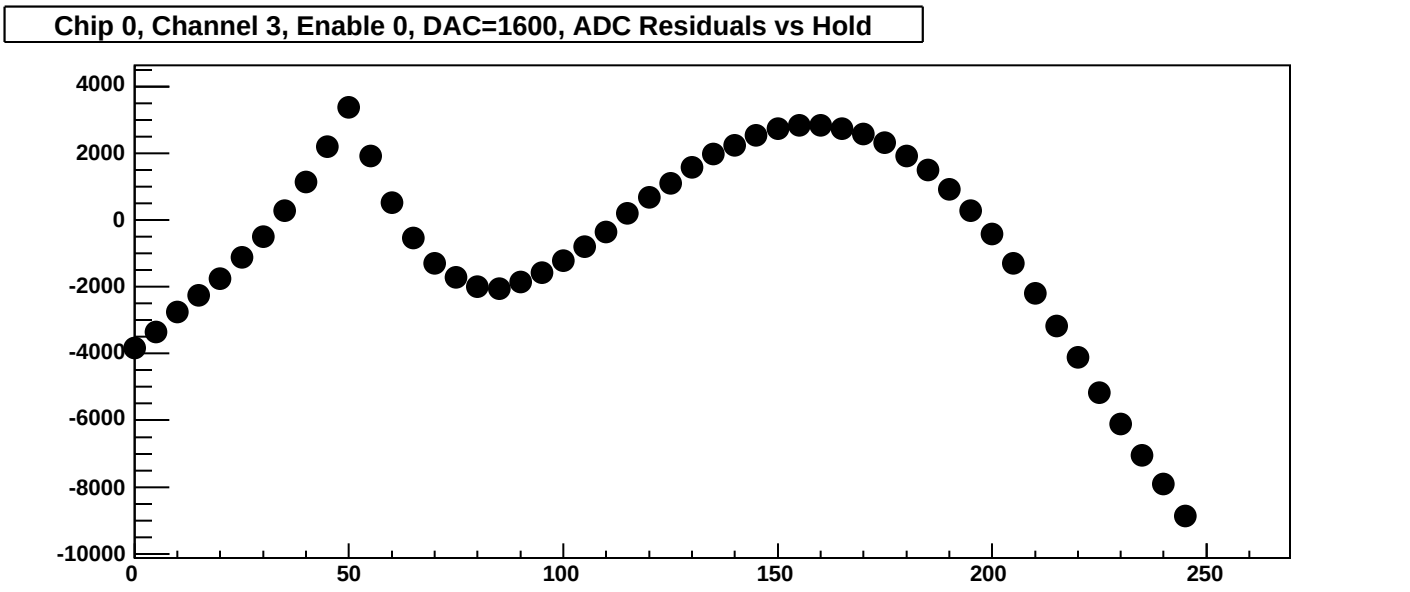

Chip 0, Channel 2, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.307e+05 / 41
p0 85.3 ± 15.01
p1 99.97 ± 2.636e-05
p2 1.2e+04 ± 27.4
p3 -48.27 ± 0.0008338
p4 10.77 ± 0.1537
18000
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

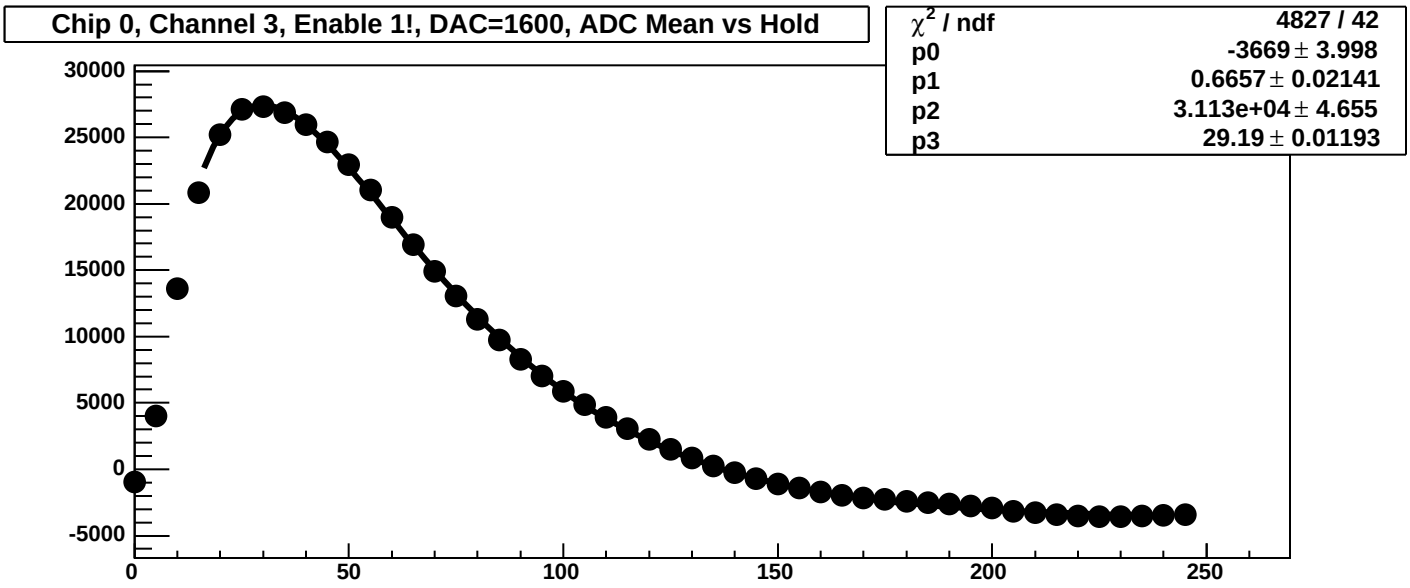

Chip 0, Channel 2, Enable 5, DAC=1600, ADC Noise vs Hold
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250

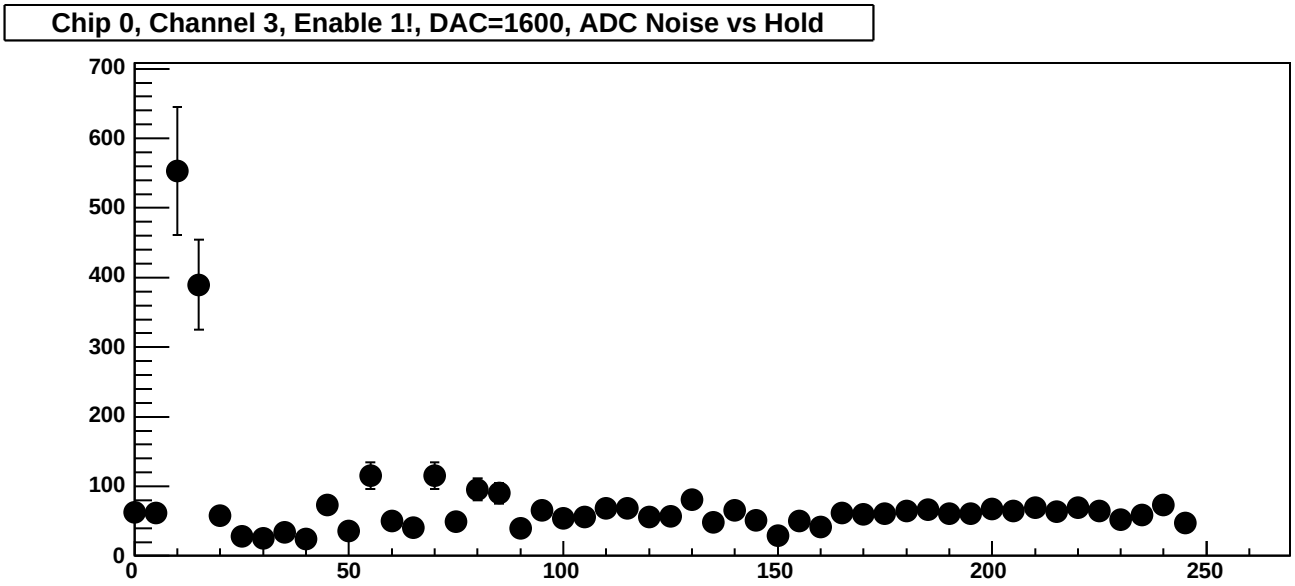

Chip 0, Channel 2, Enable 5, DAC=1600, ADC Residuals vs Hold
8000
6000
4000
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

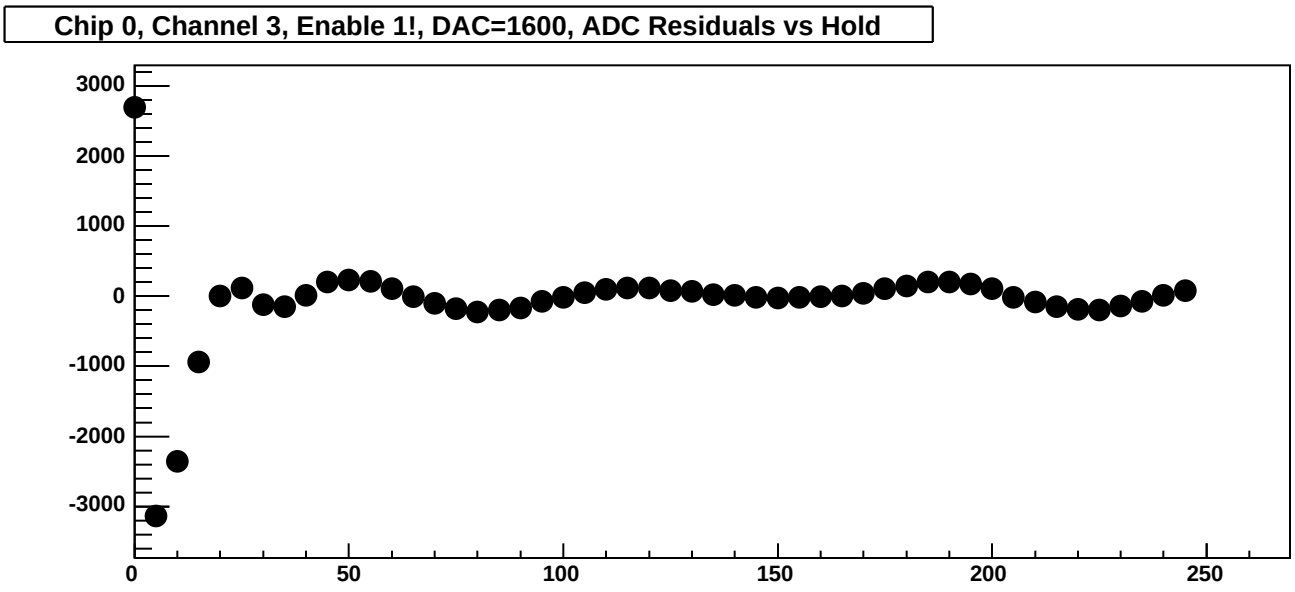

Chip 0, Channel 3, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.948e+05 / 41
p0 -942.8 ± 7.532
p1 50.59 ± 0.02516
p2 3.418e+04 ± 65.68
p3 51.76 ± 0.1025
p4 -74.28 ± 0.3322
25000
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 0, Channel 3, Enable 0, DAC=1600, ADC Noise vs Hold
500
400
300
200
100
0
50
100
150
200
250

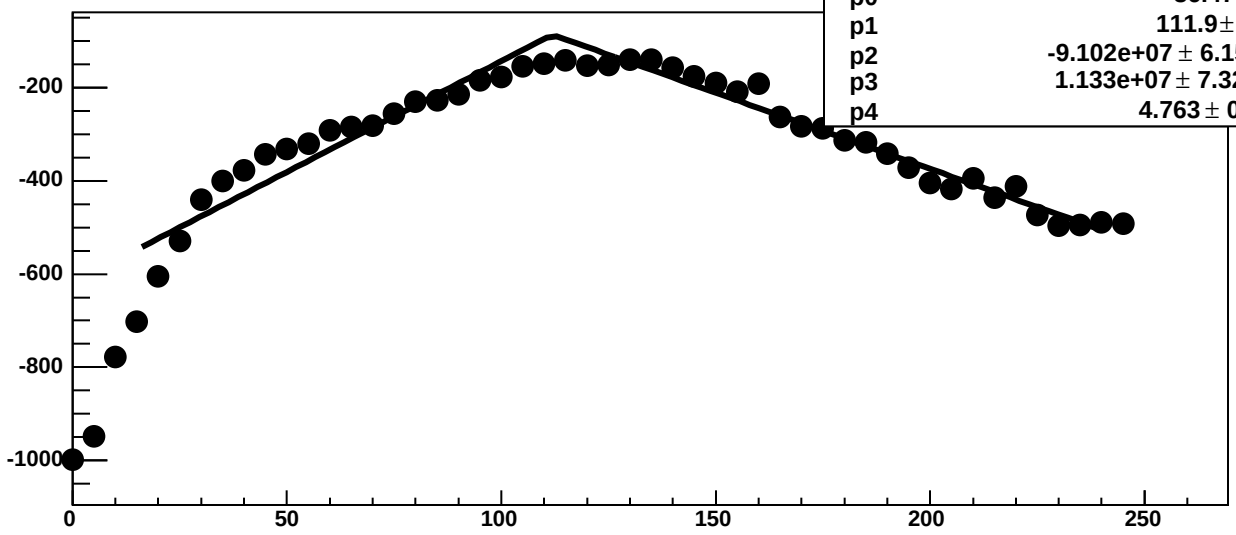

Chip 0, Channel 3, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250


Chip 0, Channel 3, Enable 1!, DAC=1600, ADC Mean vs Hold
χ² / ndf 4827 / 42
p0 -3669 ± 3.998
p1 0.6657 ± 0.02141
p2 3.113e+04 ± 4.655
p3 29.19 ± 0.01193
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

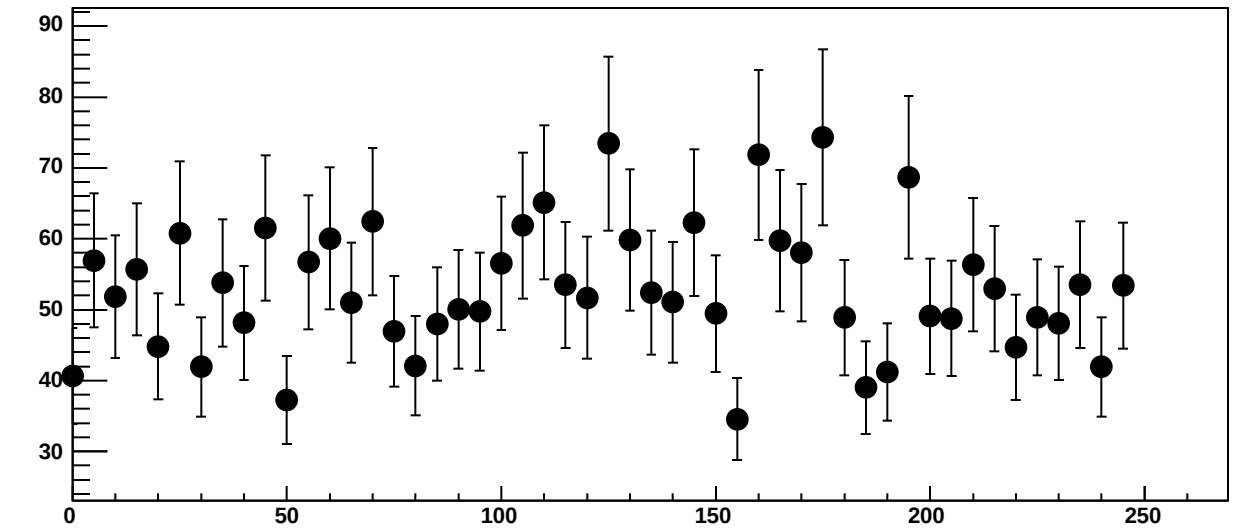

Chip 0, Channel 3, Enable 1!, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 0, Channel 3, Enable 1!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

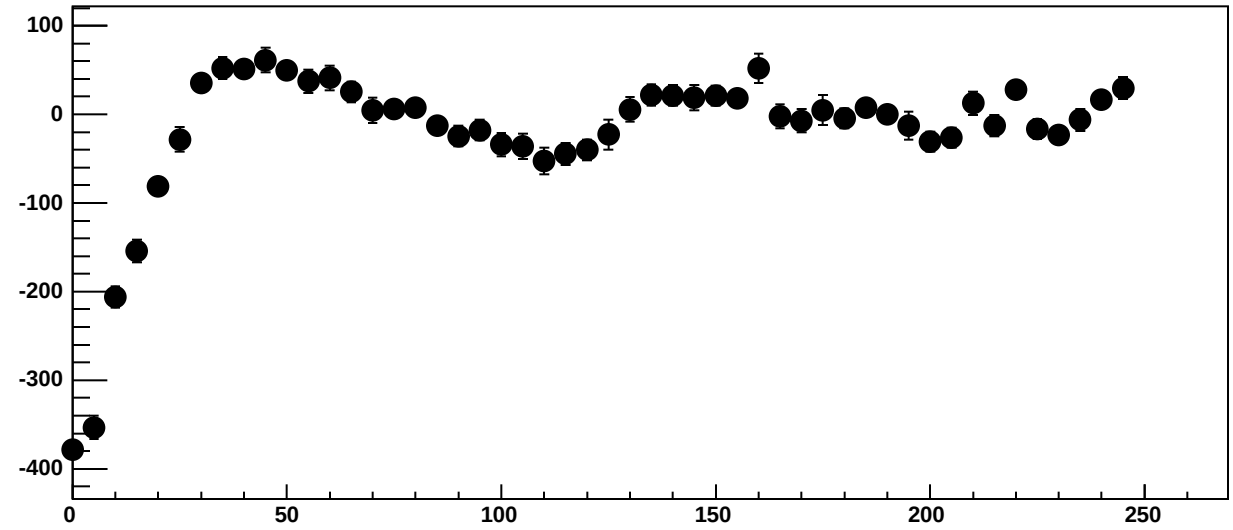
Chip 0, Channel 3, Enable 2, DAC=1600, ADC Mean vs Hold



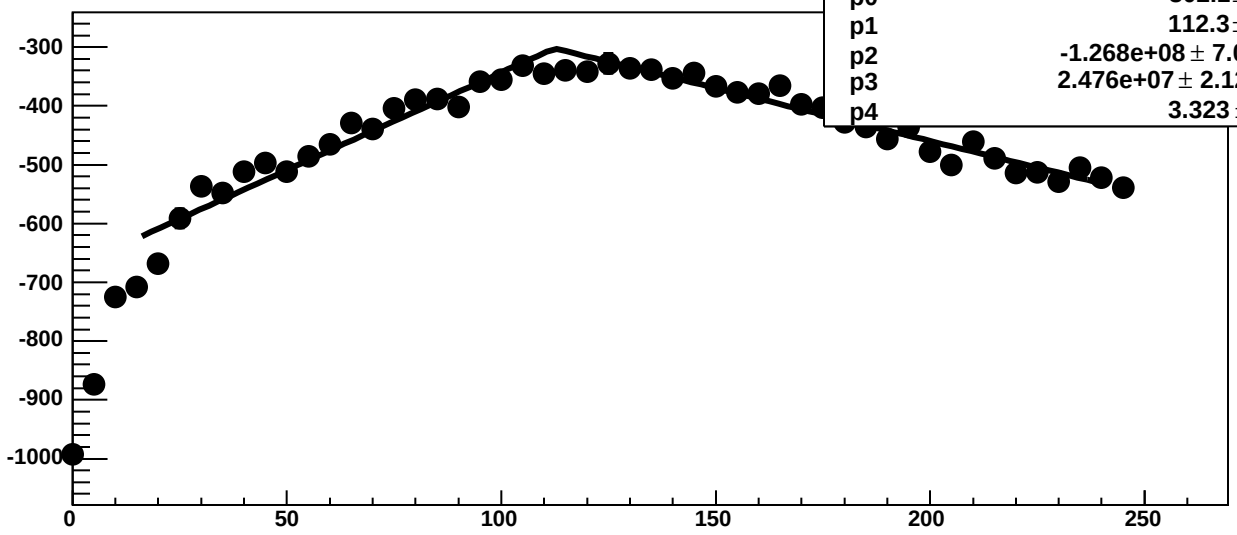
Chip 0, Channel 3, Enable 2, DAC=1600, ADC Noise vs Hold



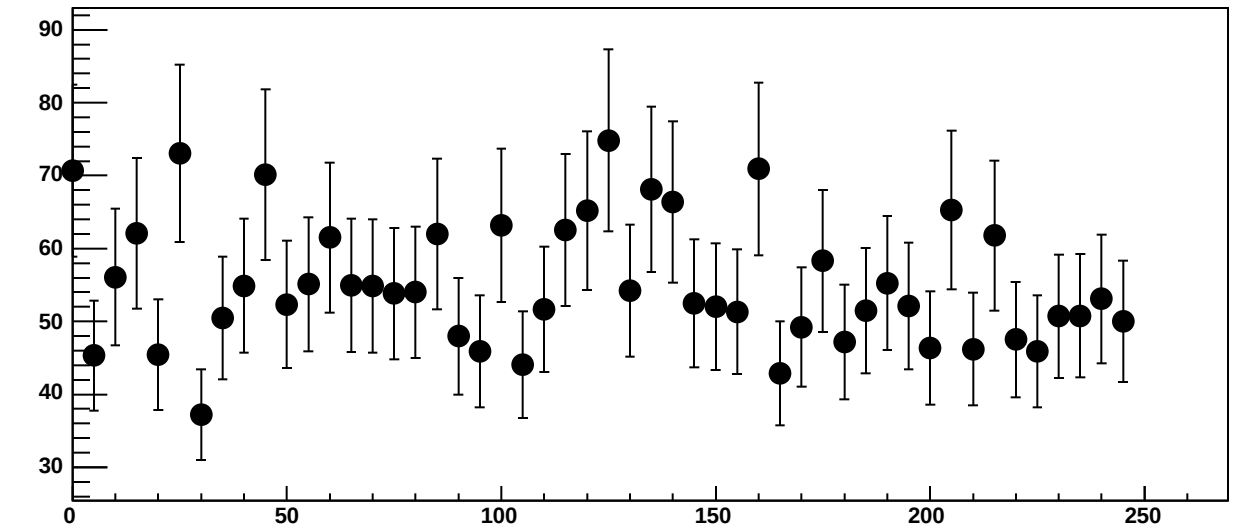
Chip 0, Channel 3, Enable 2, DAC=1600, ADC Residuals vs Hold



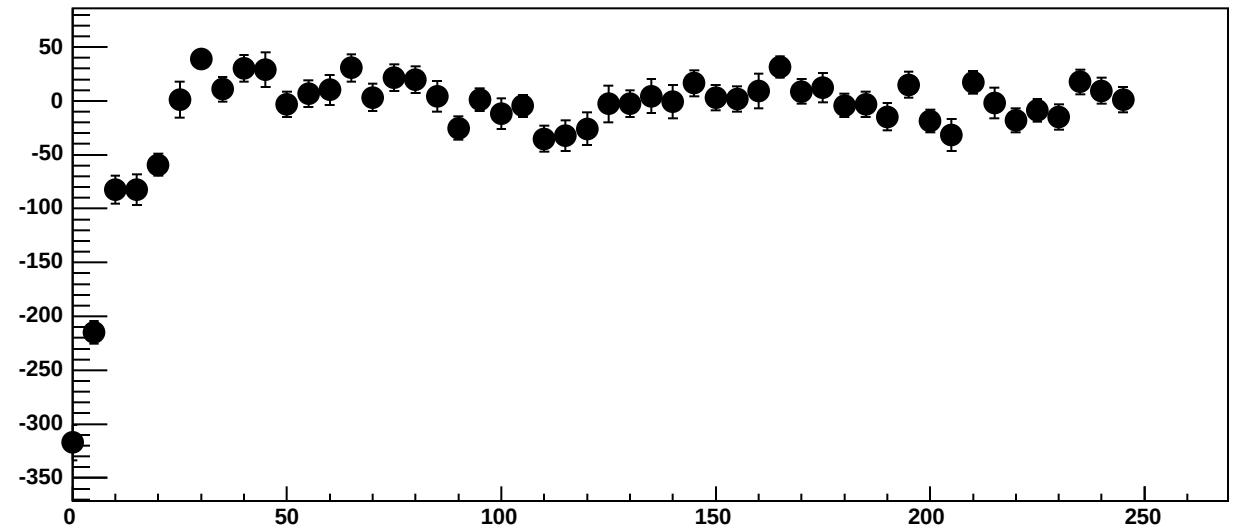
Chip 0, Channel 3, Enable 3, DAC=1600, ADC Mean vs Hold



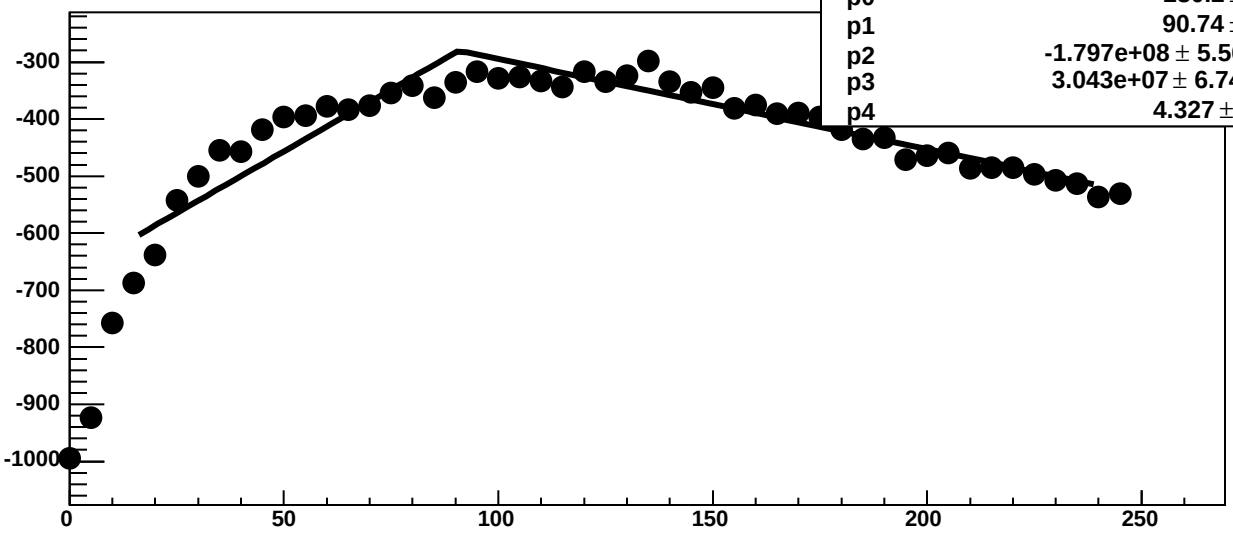
Chip 0, Channel 3, Enable 3, DAC=1600, ADC Noise vs Hold



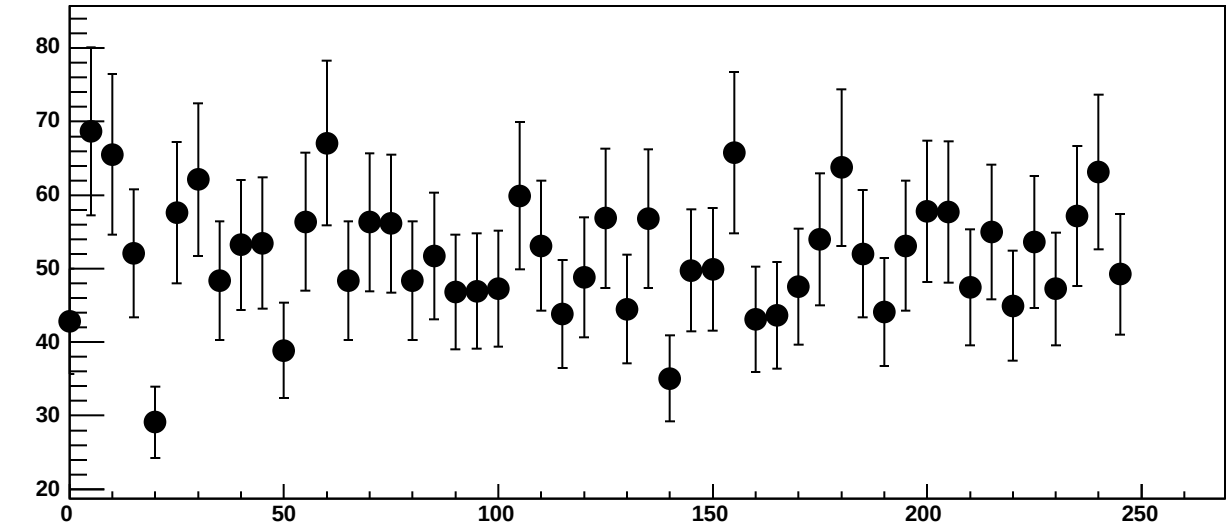
Chip 0, Channel 3, Enable 3, DAC=1600, ADC Residuals vs Hold



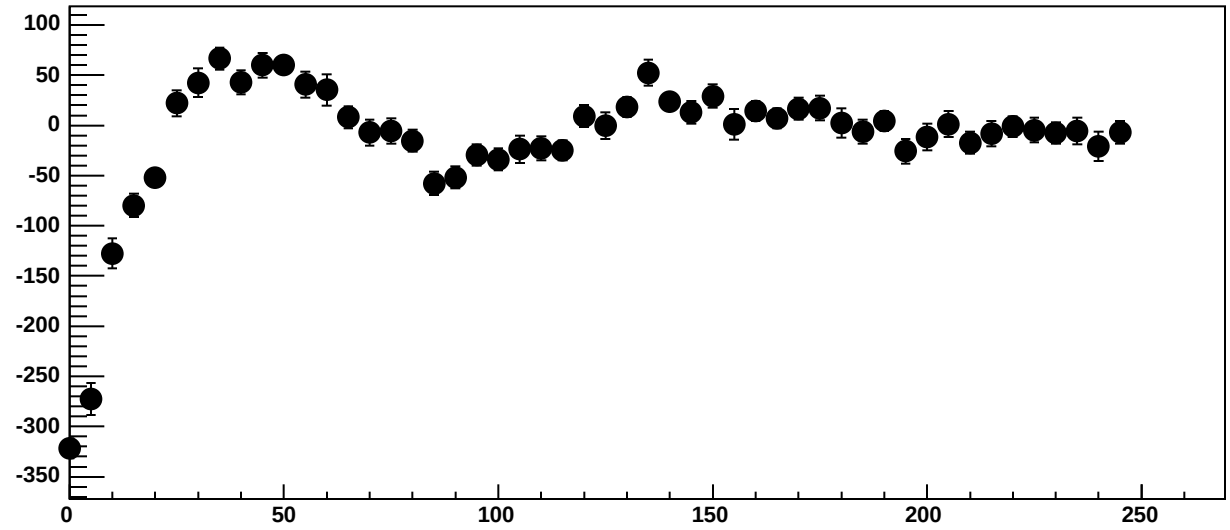
Chip 0, Channel 3, Enable 4, DAC=1600, ADC Mean vs Hold



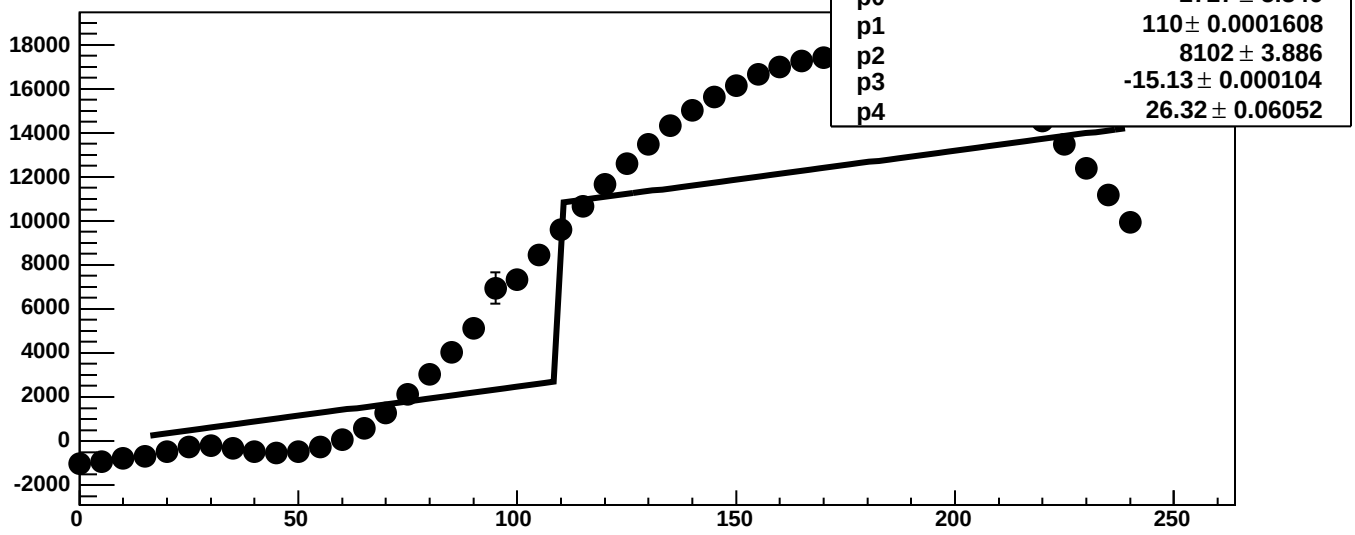
Chip 0, Channel 3, Enable 4, DAC=1600, ADC Noise vs Hold



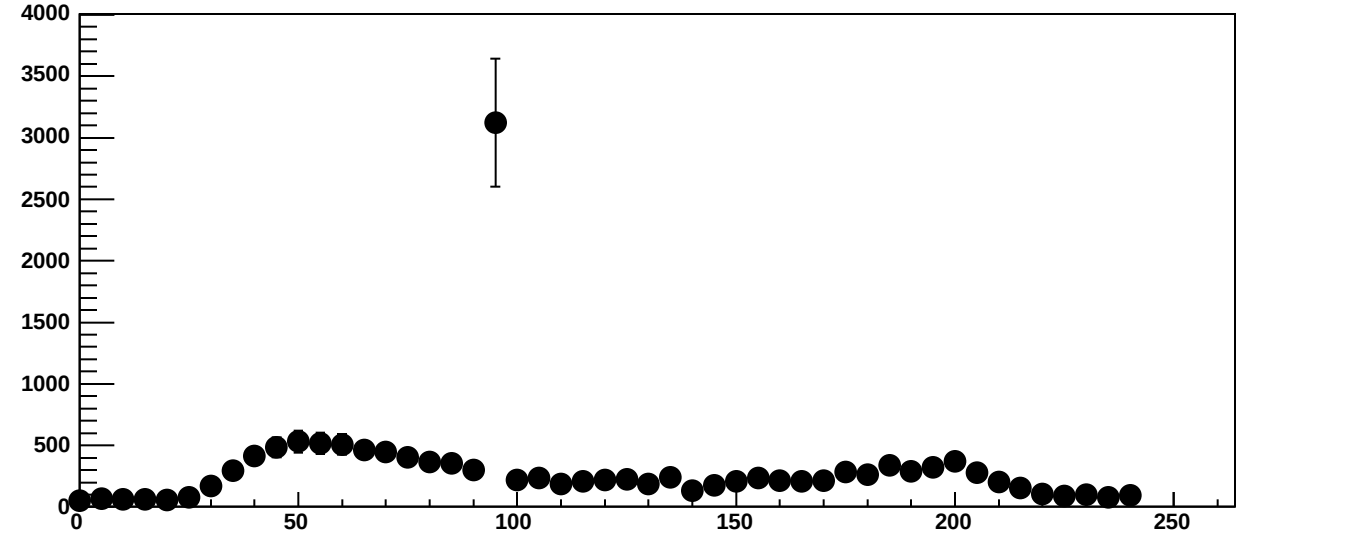
Chip 0, Channel 3, Enable 4, DAC=1600, ADC Residuals vs Hold



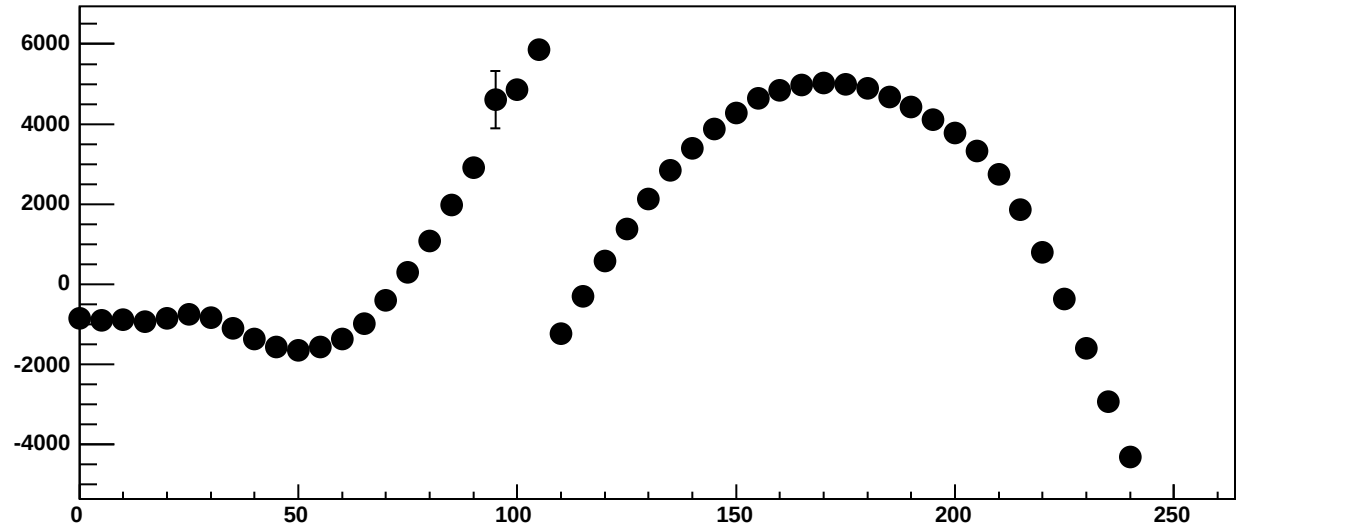
Chip 0, Channel 3, Enable 5, DAC=1600, ADC Mean vs Hold

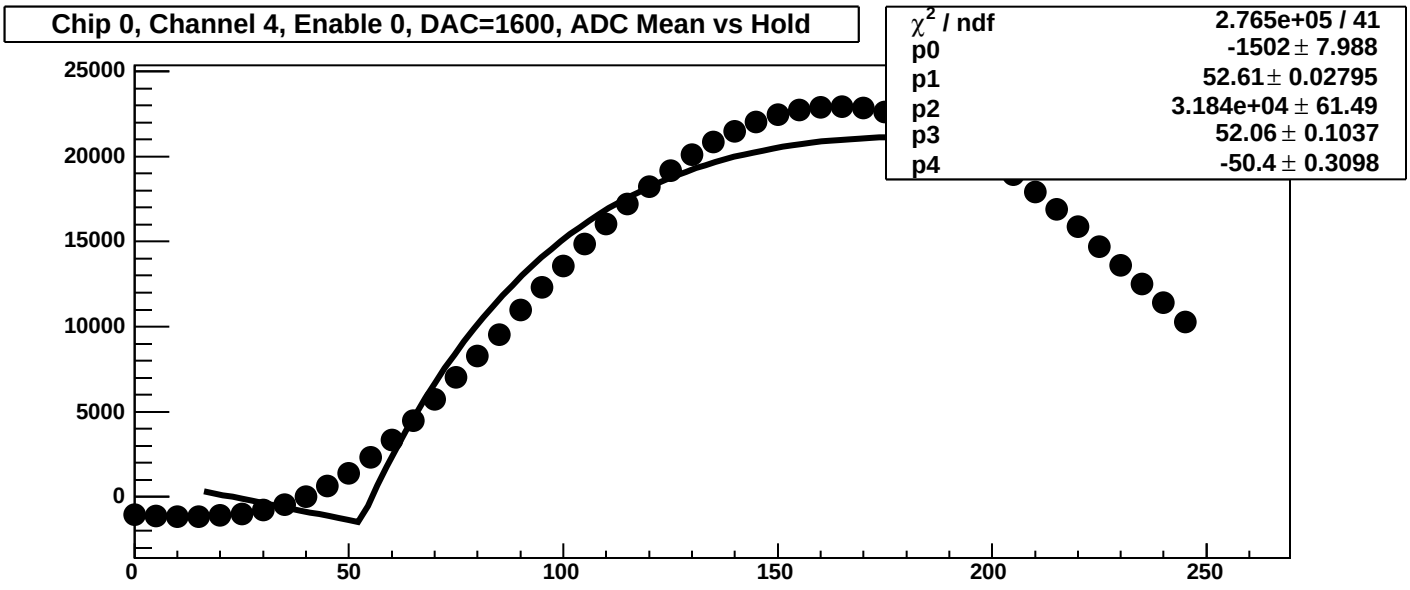


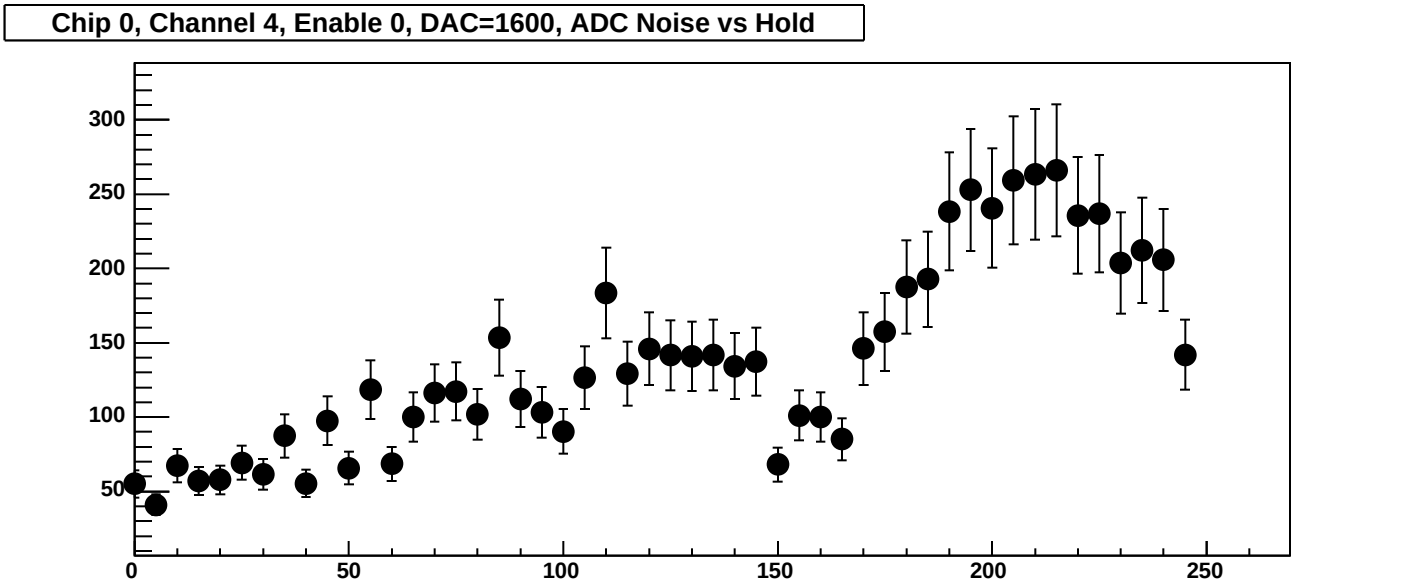
Chip 0, Channel 3, Enable 5, DAC=1600, ADC Noise vs Hold

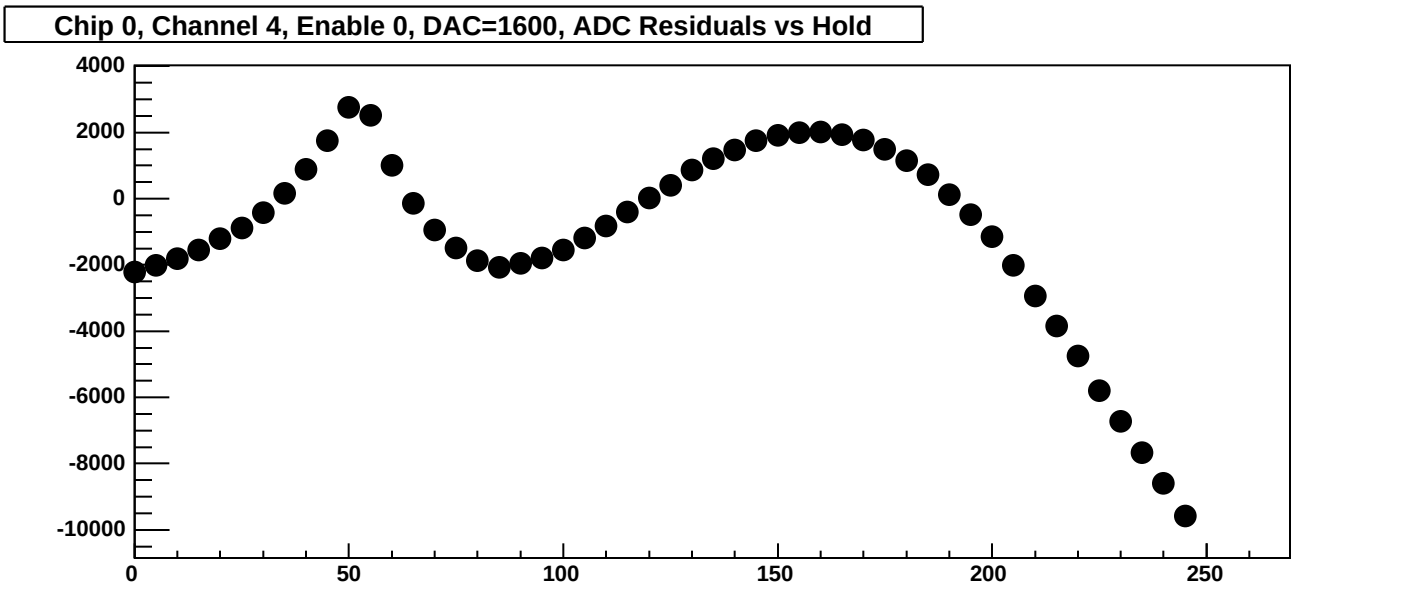


Chip 0, Channel 3, Enable 5, DAC=1600, ADC Residuals vs Hold

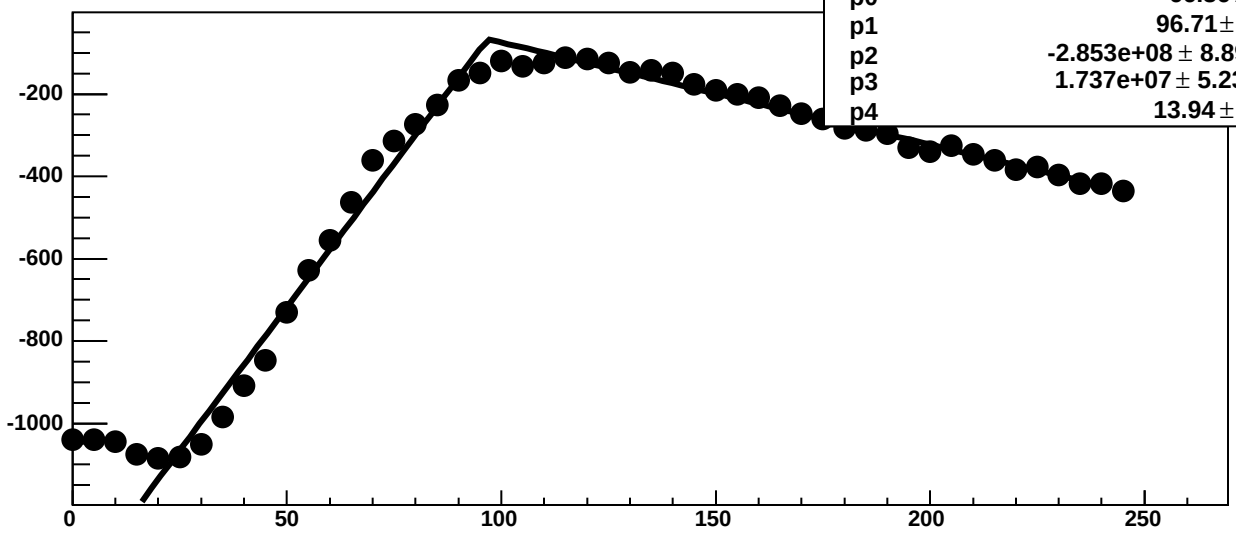



Chip 0, Channel 4, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.765e+05 / 41
p0 -1502 ± 7.988
p1 52.61 ± 0.02795
p2 3.184e+04 ± 61.49
p3 52.06 ± 0.1037
p4 -50.4 ± 0.3098
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

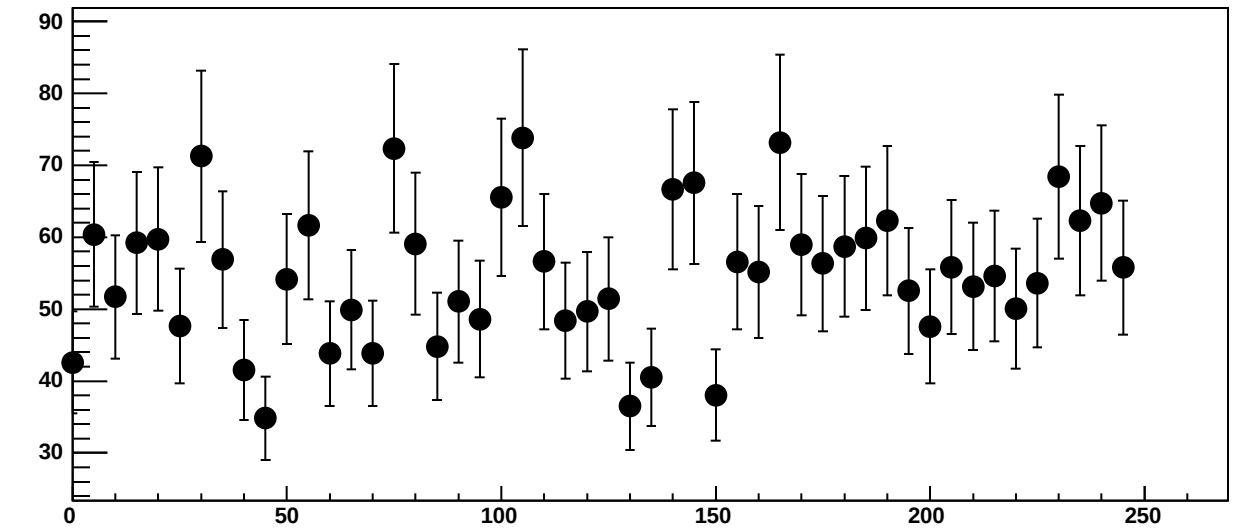

Chip 0, Channel 4, Enable 0, DAC=1600, ADC Noise vs Hold
300
250
200
150
100
50
0
50
100
150
200
250


Chip 0, Channel 4, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

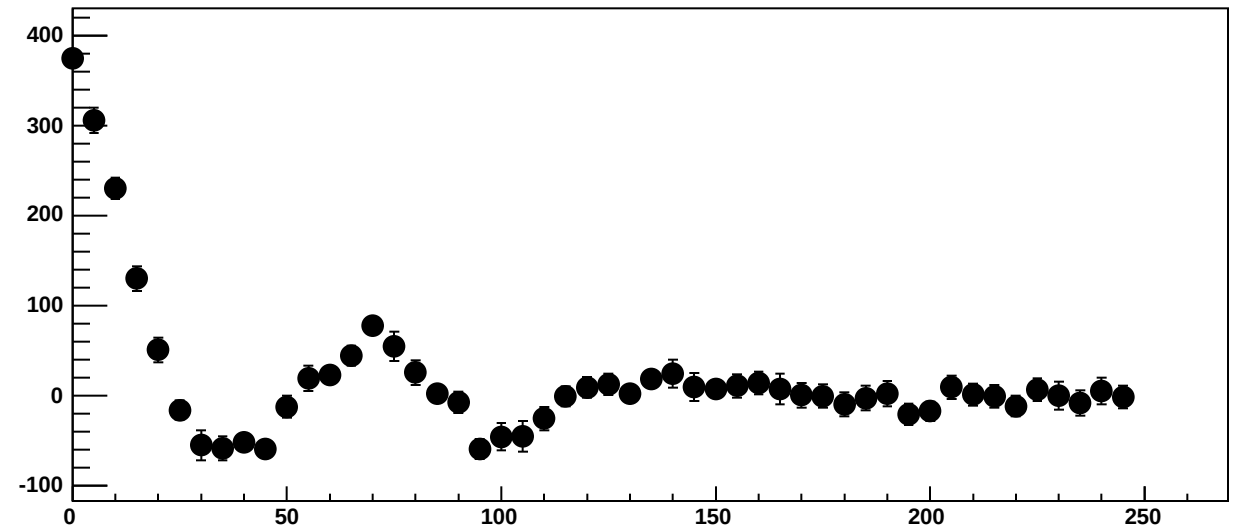
Chip 0, Channel 4, Enable 1, DAC=1600, ADC Mean vs Hold



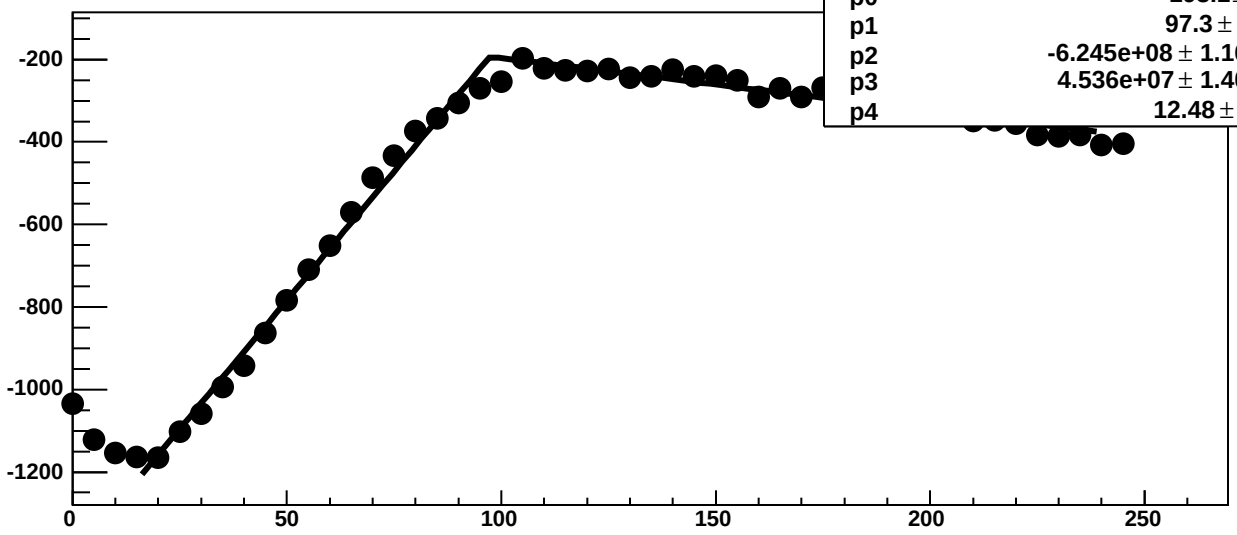
Chip 0, Channel 4, Enable 1, DAC=1600, ADC Noise vs Hold



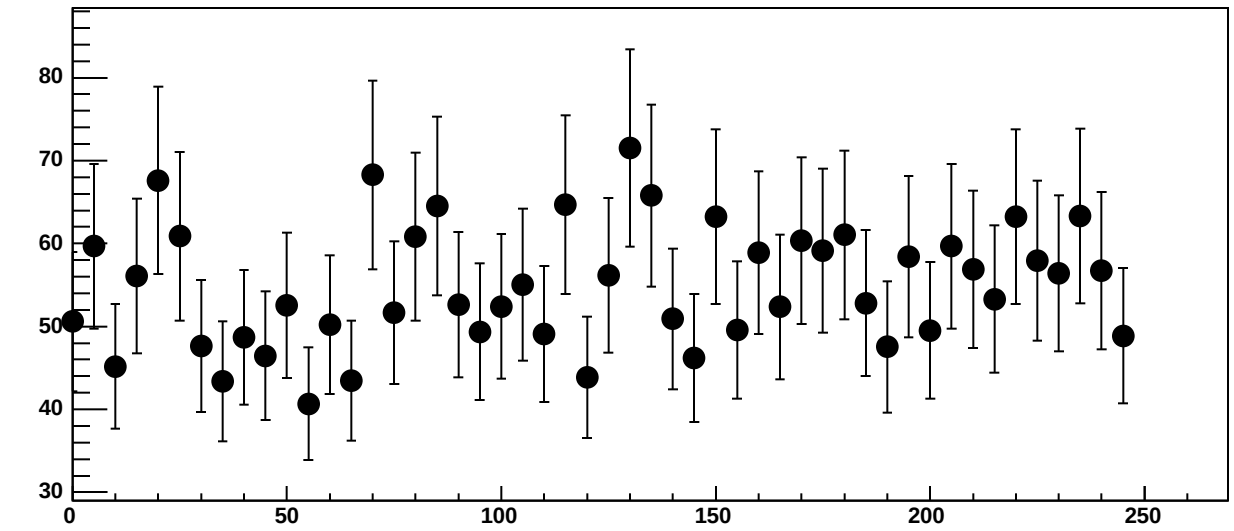
Chip 0, Channel 4, Enable 1, DAC=1600, ADC Residuals vs Hold



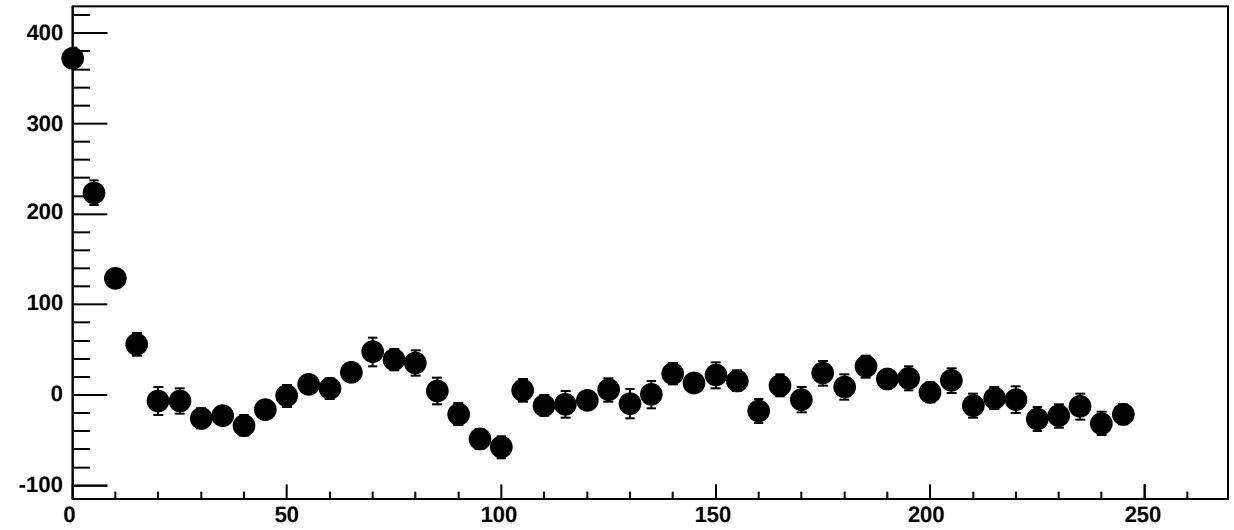
Chip 0, Channel 4, Enable 2, DAC=1600, ADC Mean vs Hold



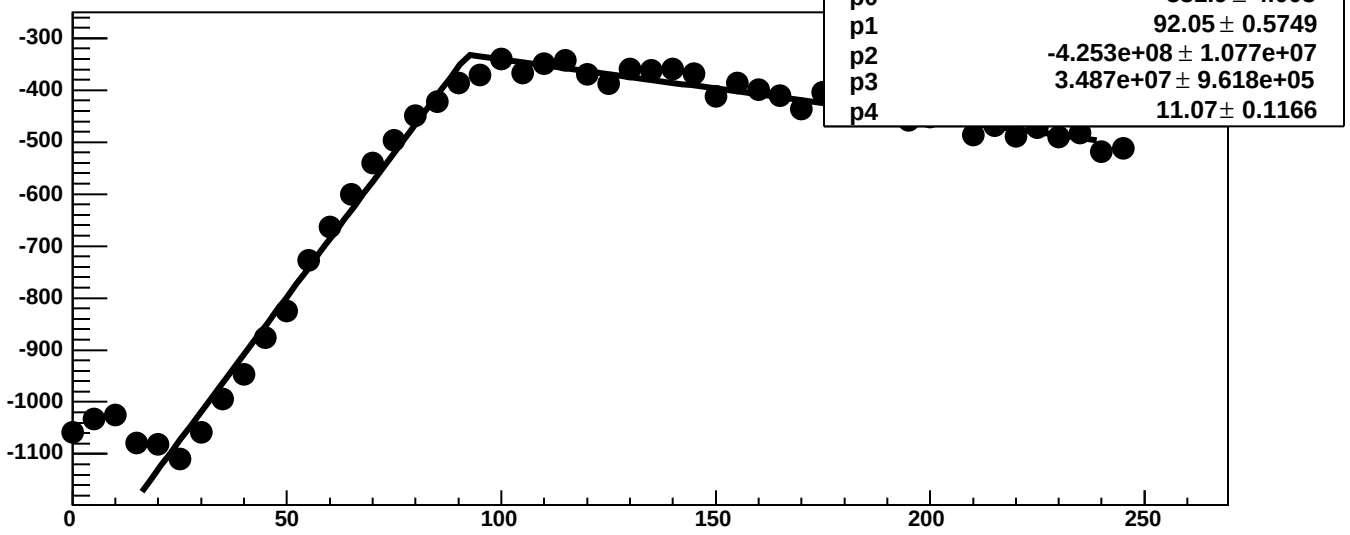
Chip 0, Channel 4, Enable 2, DAC=1600, ADC Noise vs Hold



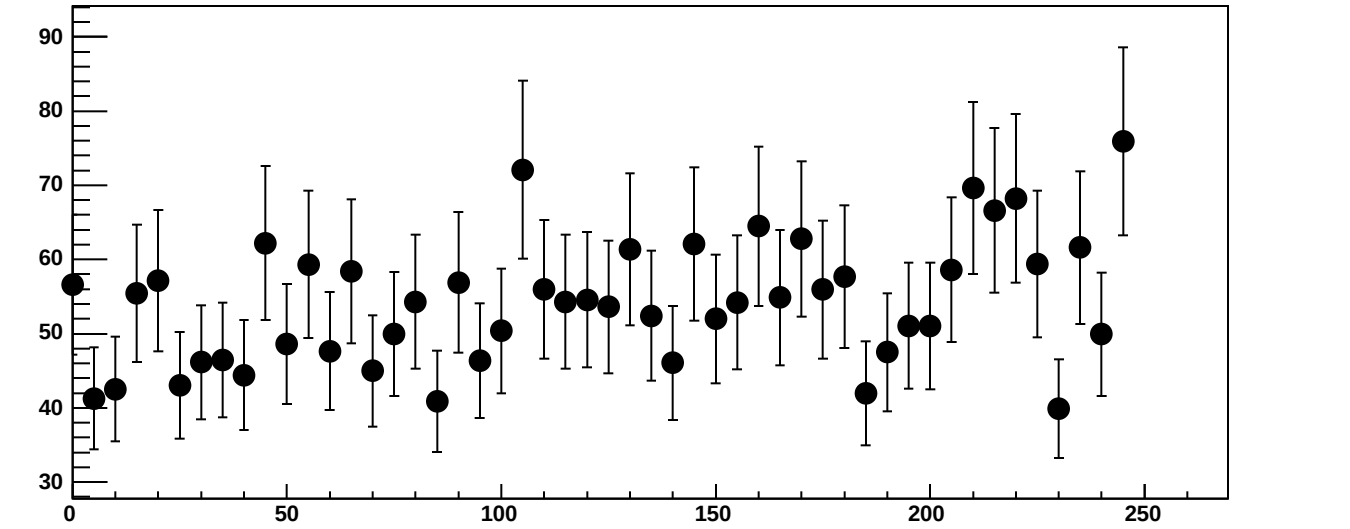
Chip 0, Channel 4, Enable 2, DAC=1600, ADC Residuals vs Hold



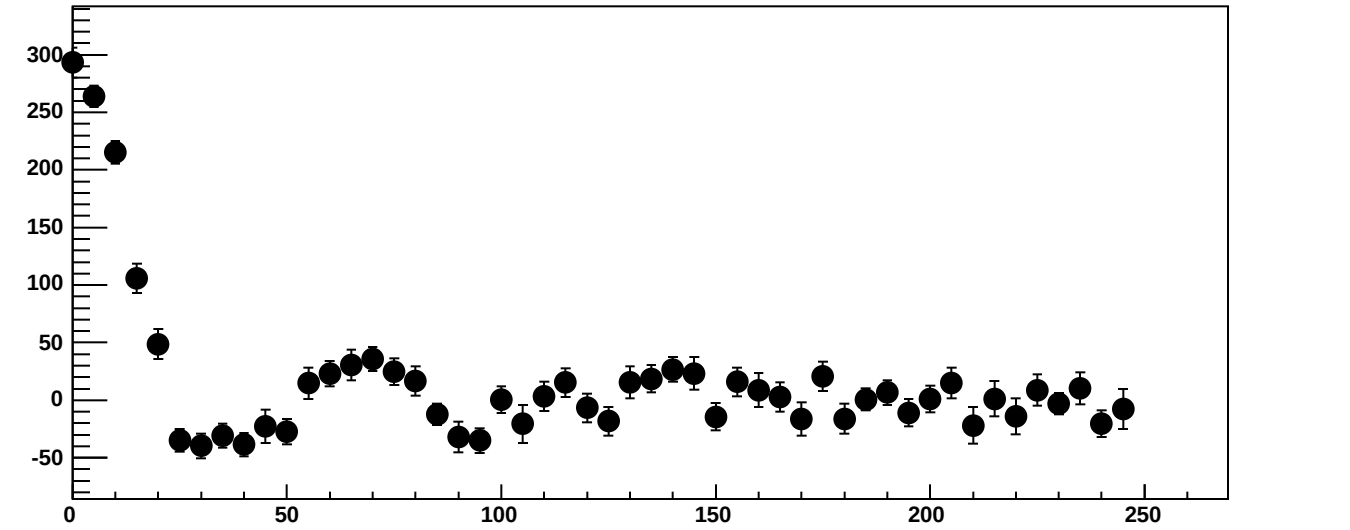
Chip 0, Channel 4, Enable 3, DAC=1600, ADC Mean vs Hold



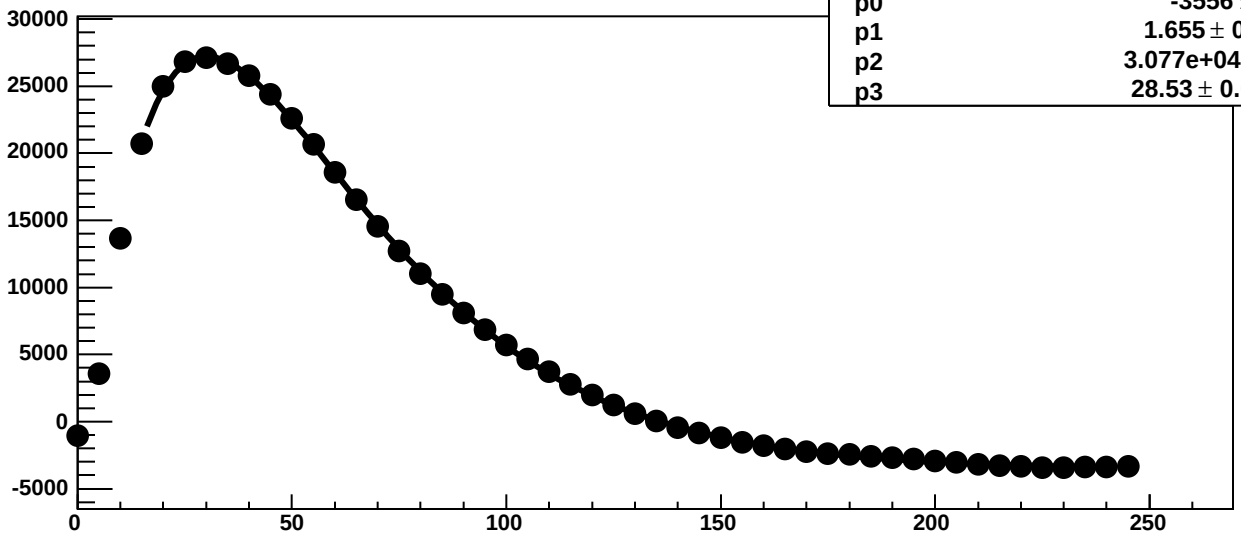
Chip 0, Channel 4, Enable 3, DAC=1600, ADC Noise vs Hold



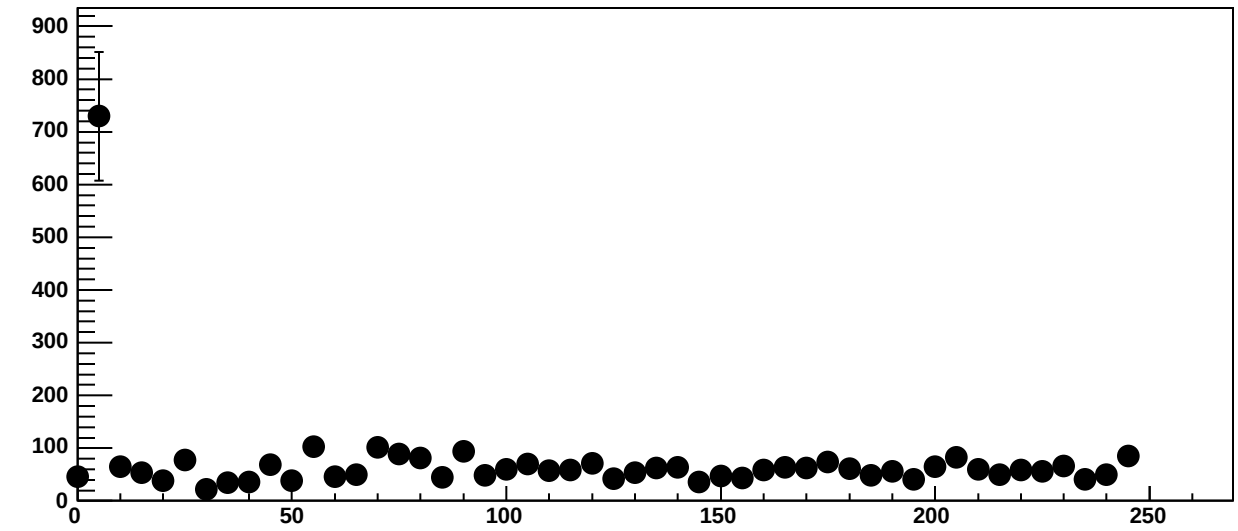
Chip 0, Channel 4, Enable 3, DAC=1600, ADC Residuals vs Hold



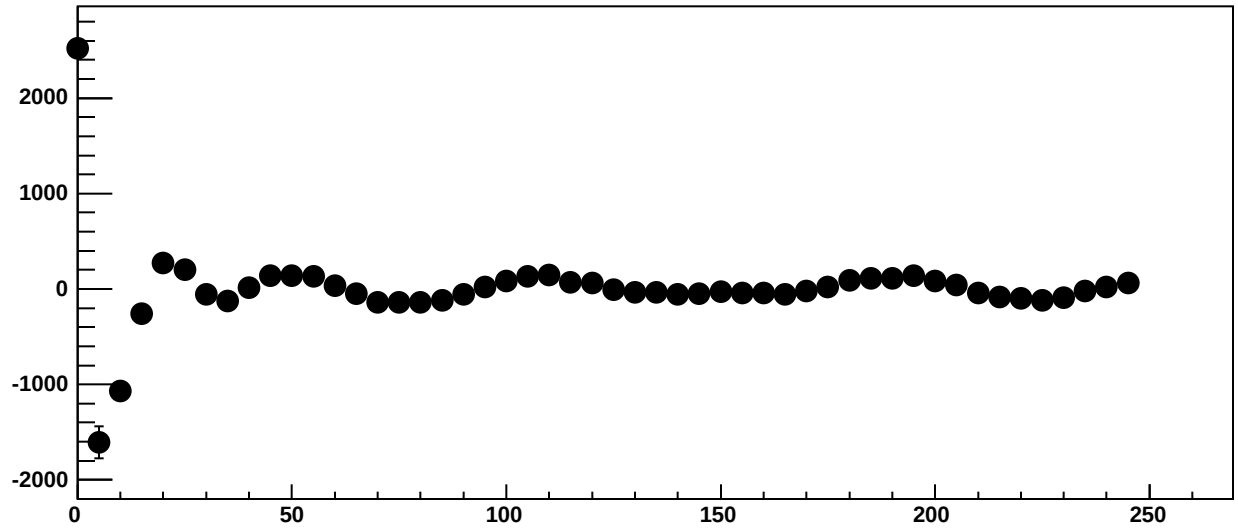
Chip 0, Channel 4, Enable 4!, DAC=1600, ADC Mean vs Hold



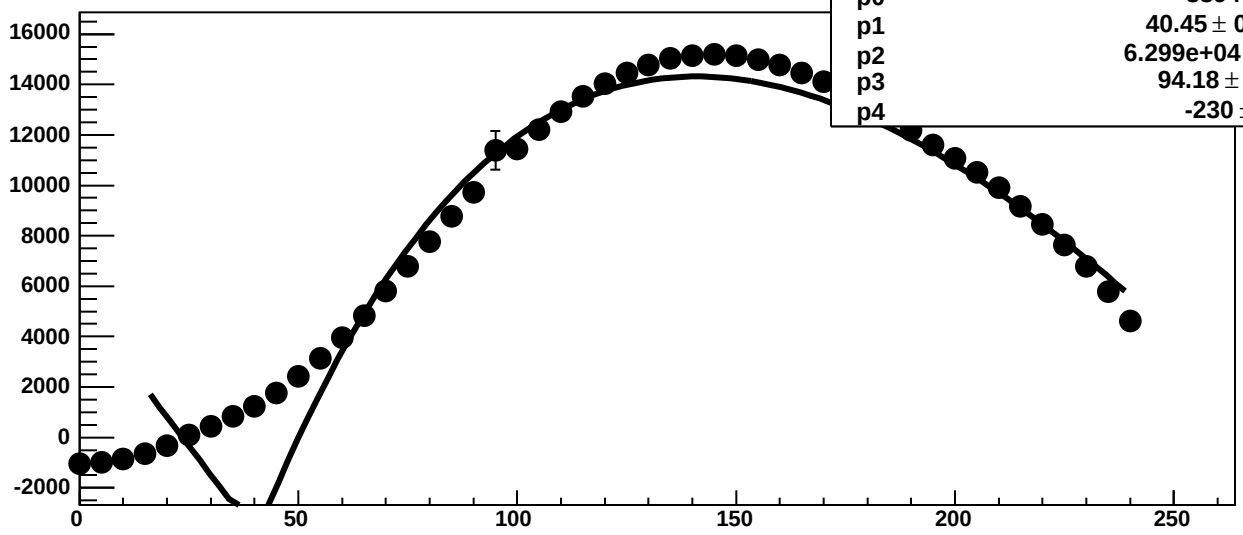
Chip 0, Channel 4, Enable 4!, DAC=1600, ADC Noise vs Hold



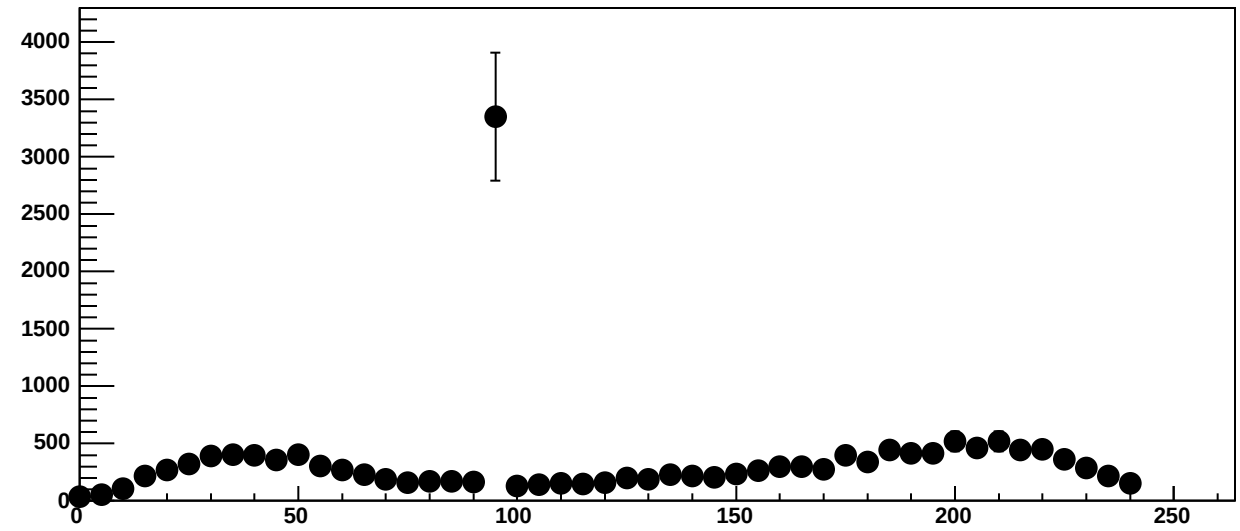
Chip 0, Channel 4, Enable 4!, DAC=1600, ADC Residuals vs Hold



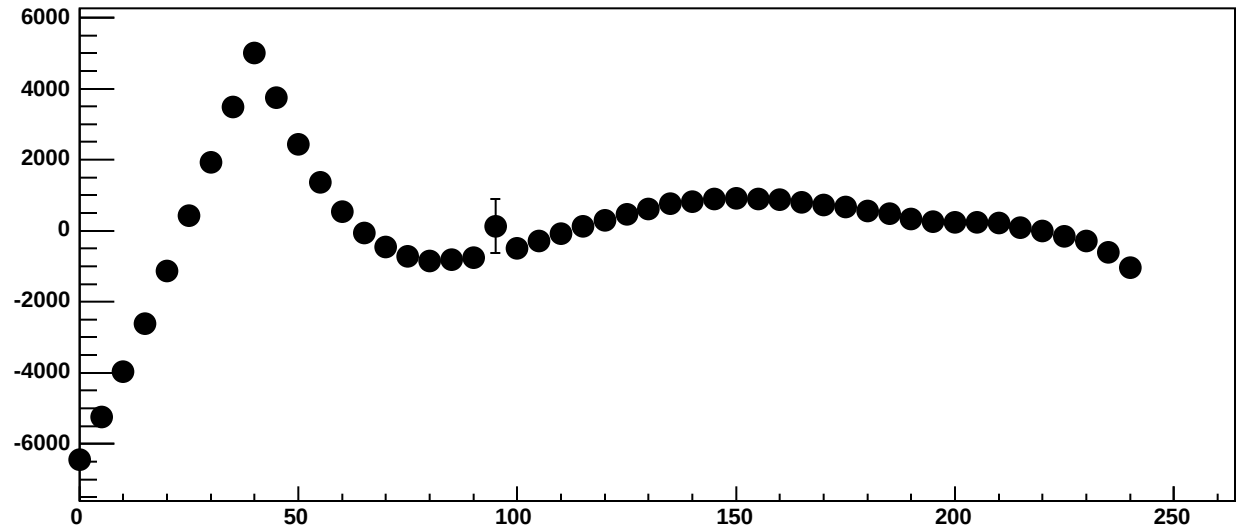
Chip 0, Channel 4, Enable 5, DAC=1600, ADC Mean vs Hold

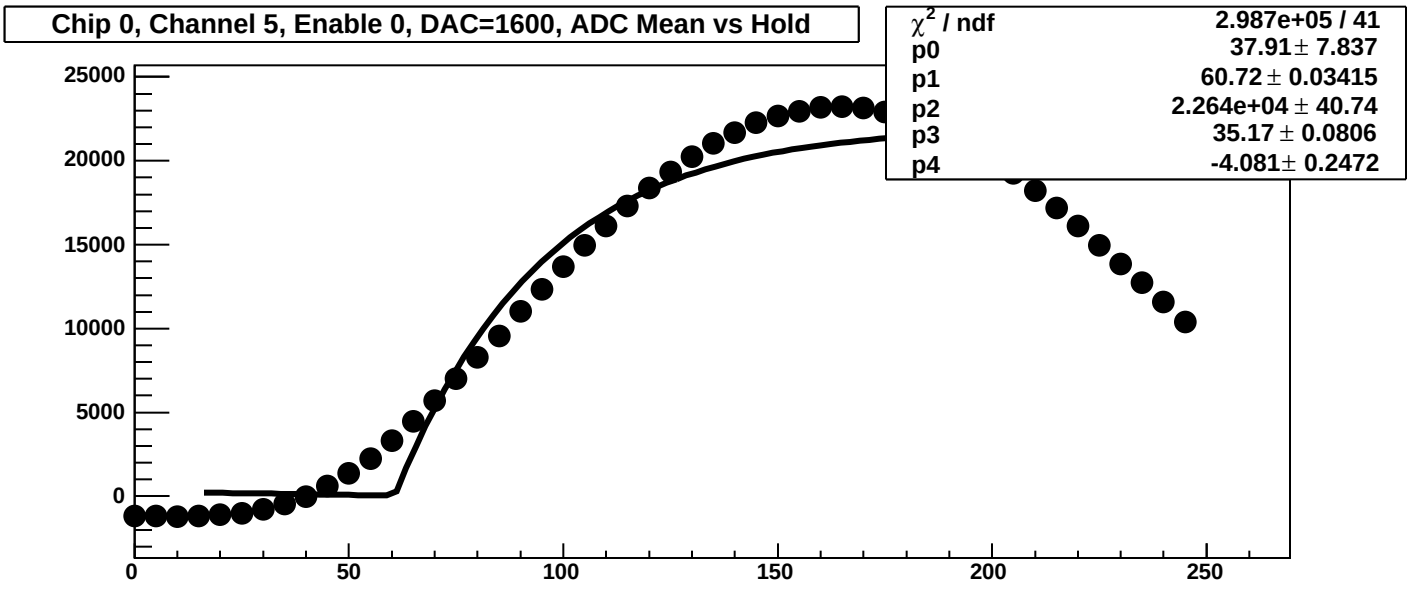


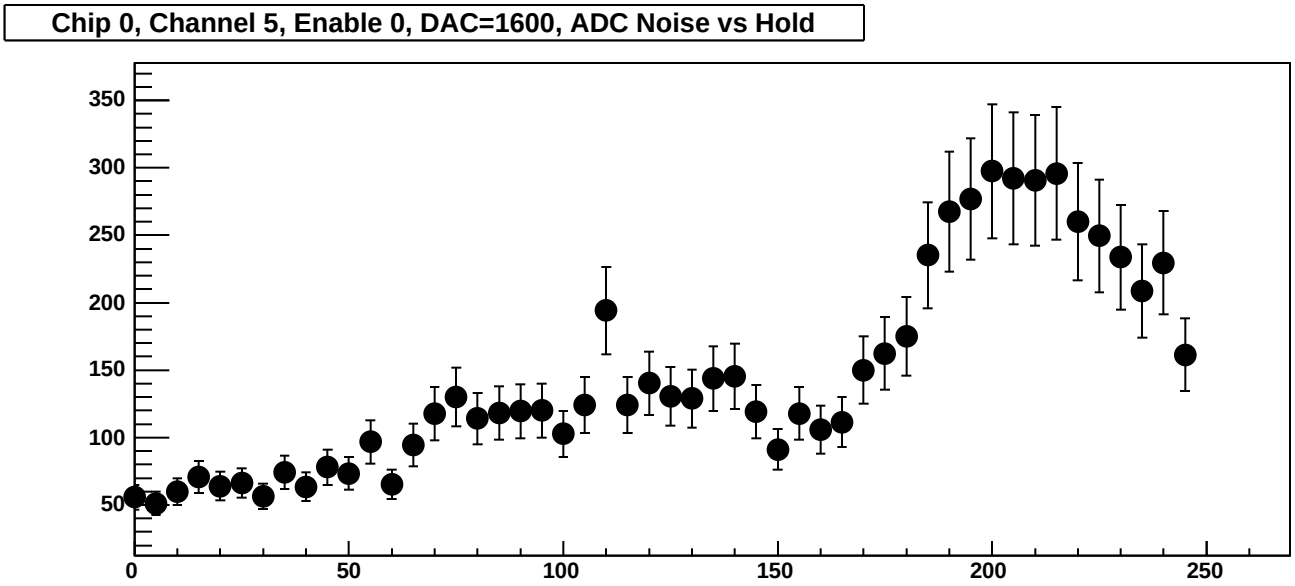
Chip 0, Channel 4, Enable 5, DAC=1600, ADC Noise vs Hold

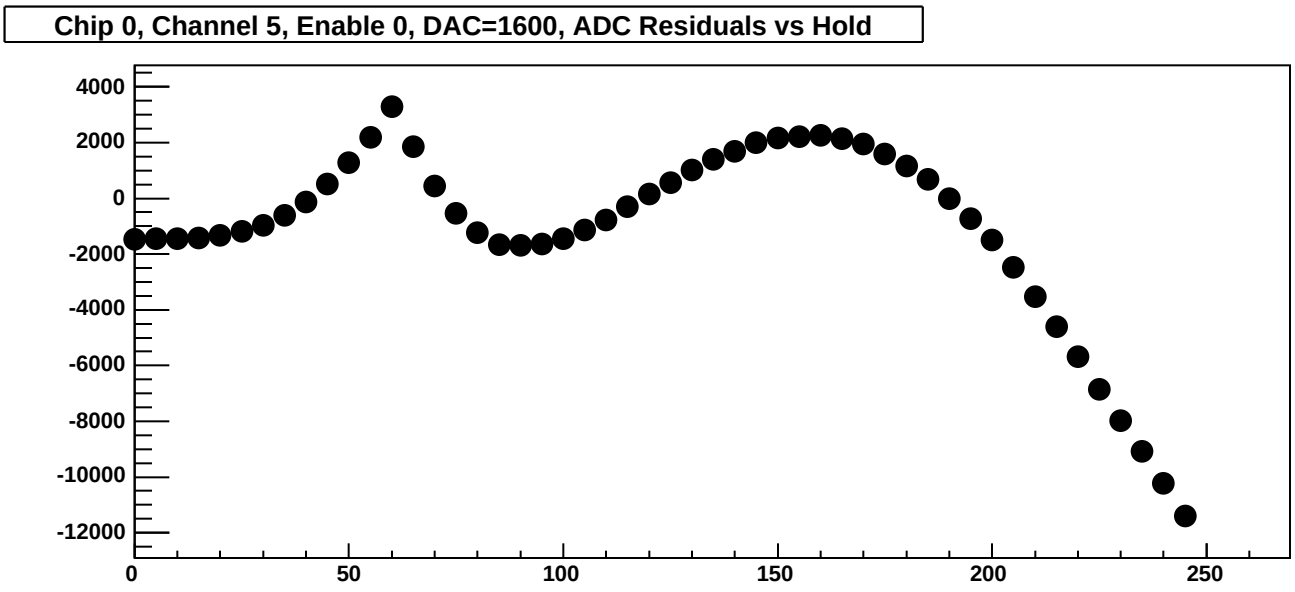


Chip 0, Channel 4, Enable 5, DAC=1600, ADC Residuals vs Hold

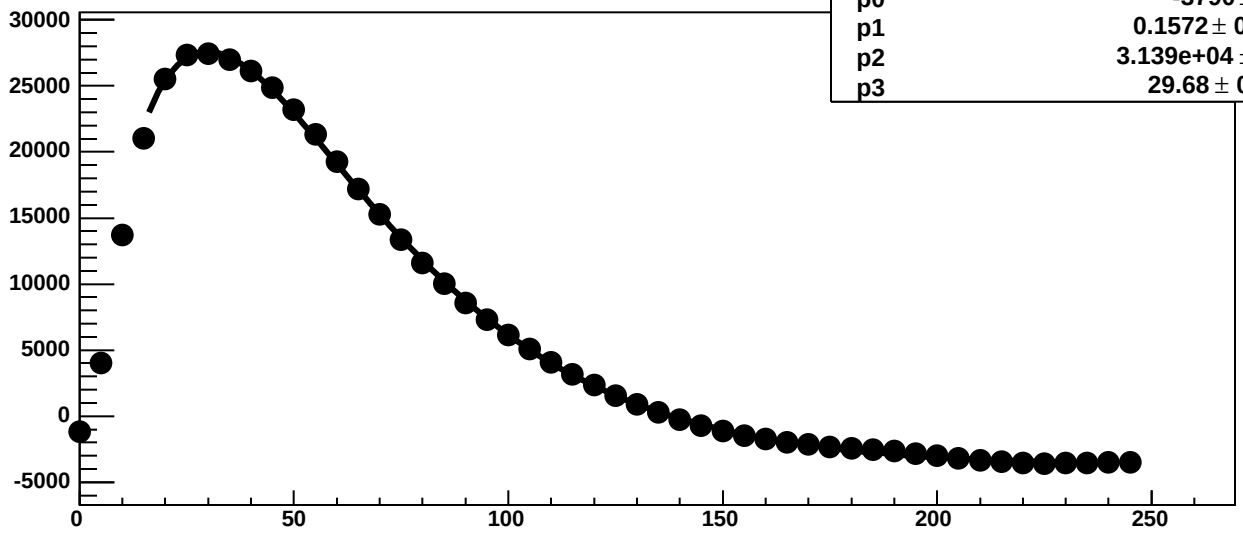



Chip 0, Channel 5, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.987e+05 / 41
p0 37.91 ± 7.837
p1 60.72 ± 0.03415
p2 2.264e+04 ± 40.74
p3 35.17 ± 0.0806
p4 -4.081 ± 0.2472
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

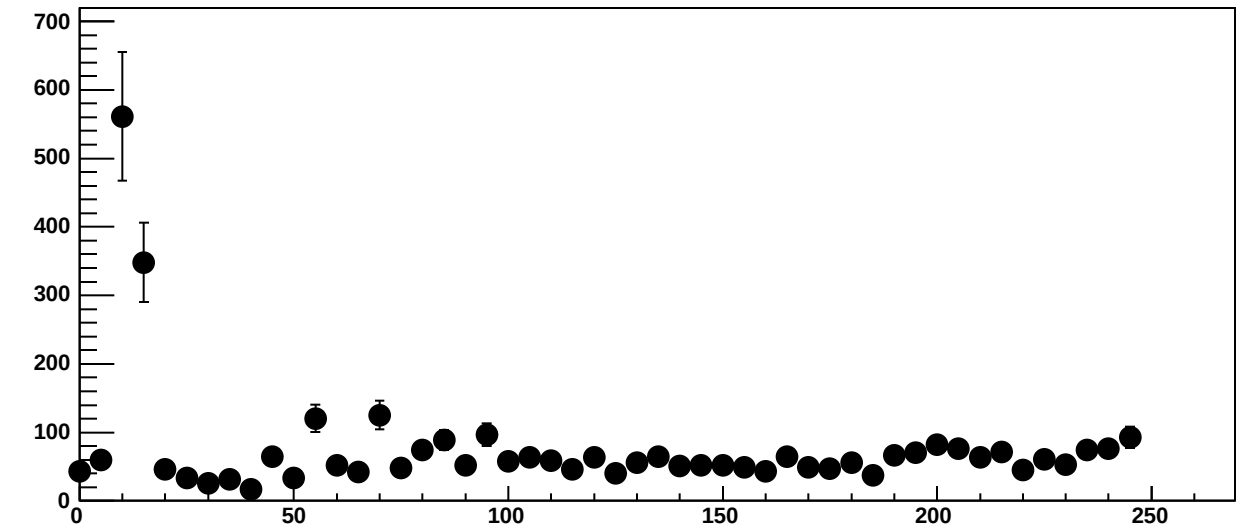

Chip 0, Channel 5, Enable 0, DAC=1600, ADC Noise vs Hold
350
300
250
200
150
100
50
0
50
100
150
200
250


Chip 0, Channel 5, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
0
50
100
150
200
250

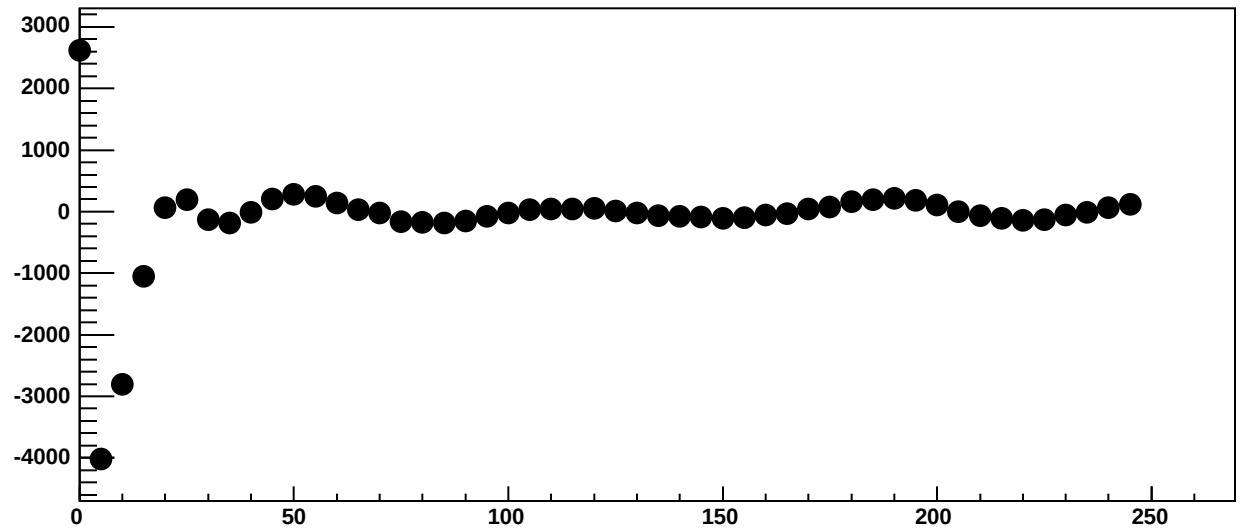
Chip 0, Channel 5, Enable 1!, DAC=1600, ADC Mean vs Hold



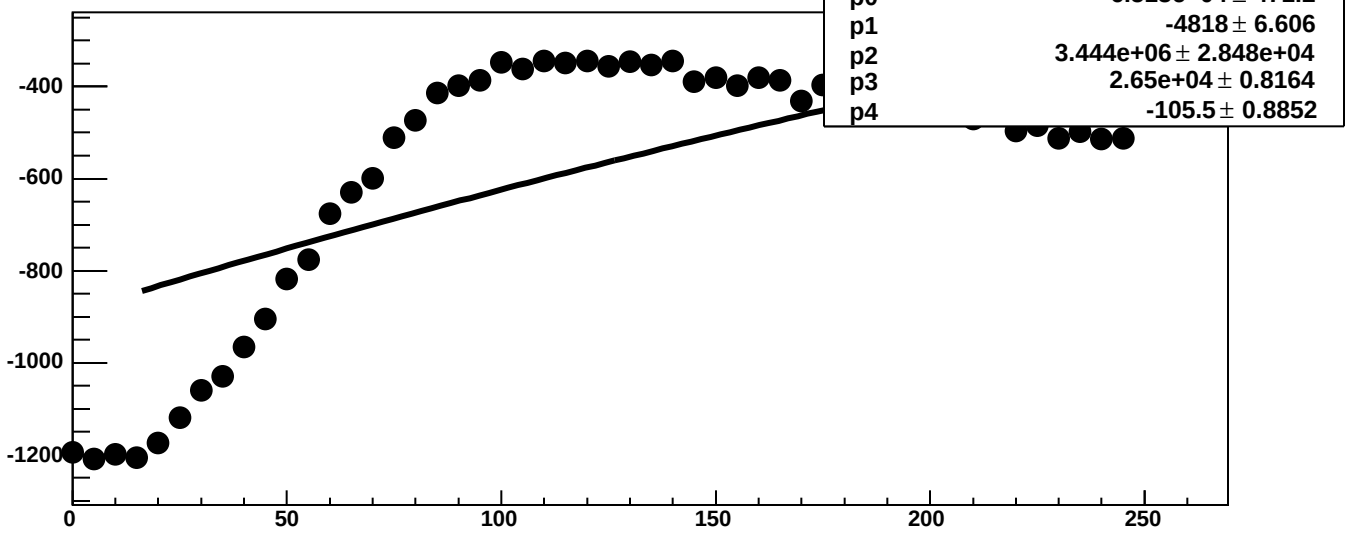
Chip 0, Channel 5, Enable 1!, DAC=1600, ADC Noise vs Hold



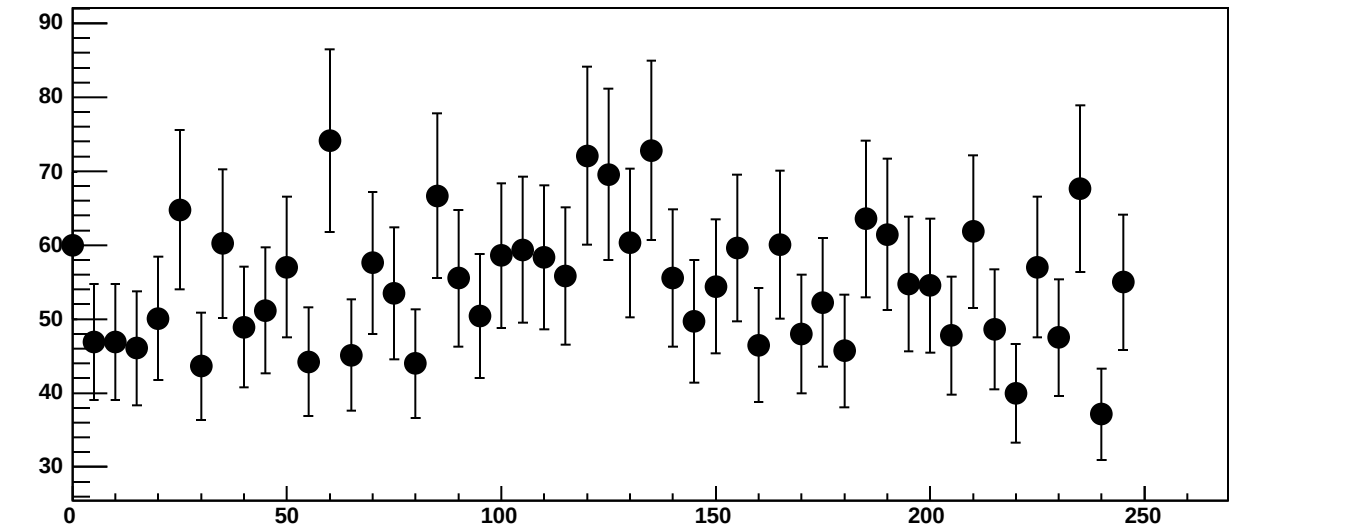
Chip 0, Channel 5, Enable 1!, DAC=1600, ADC Residuals vs Hold



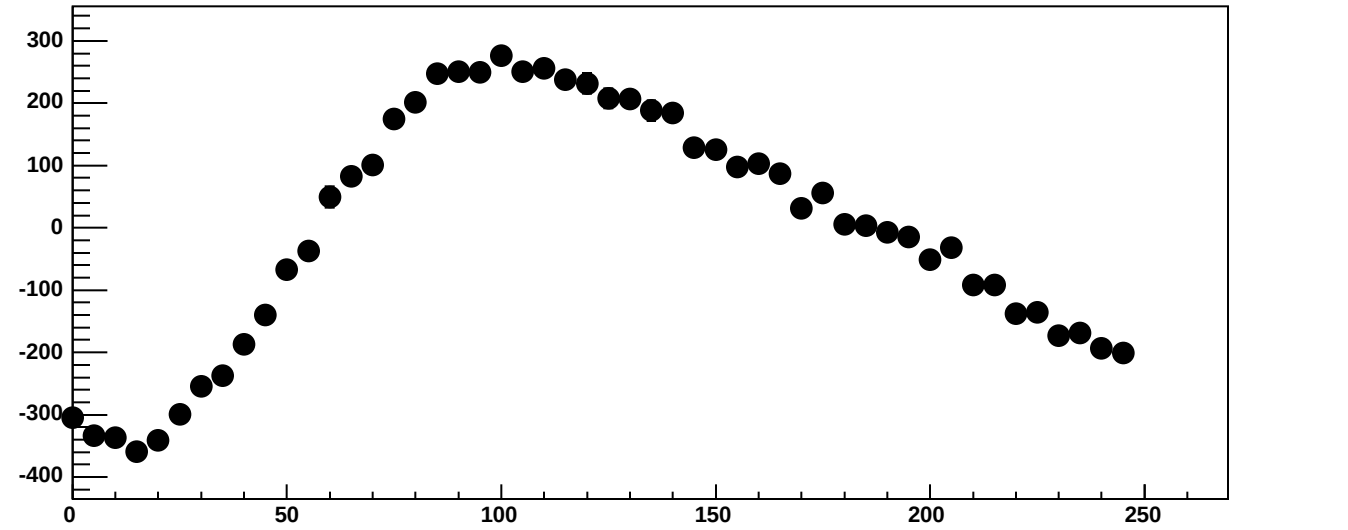
Chip 0, Channel 5, Enable 2, DAC=1600, ADC Mean vs Hold

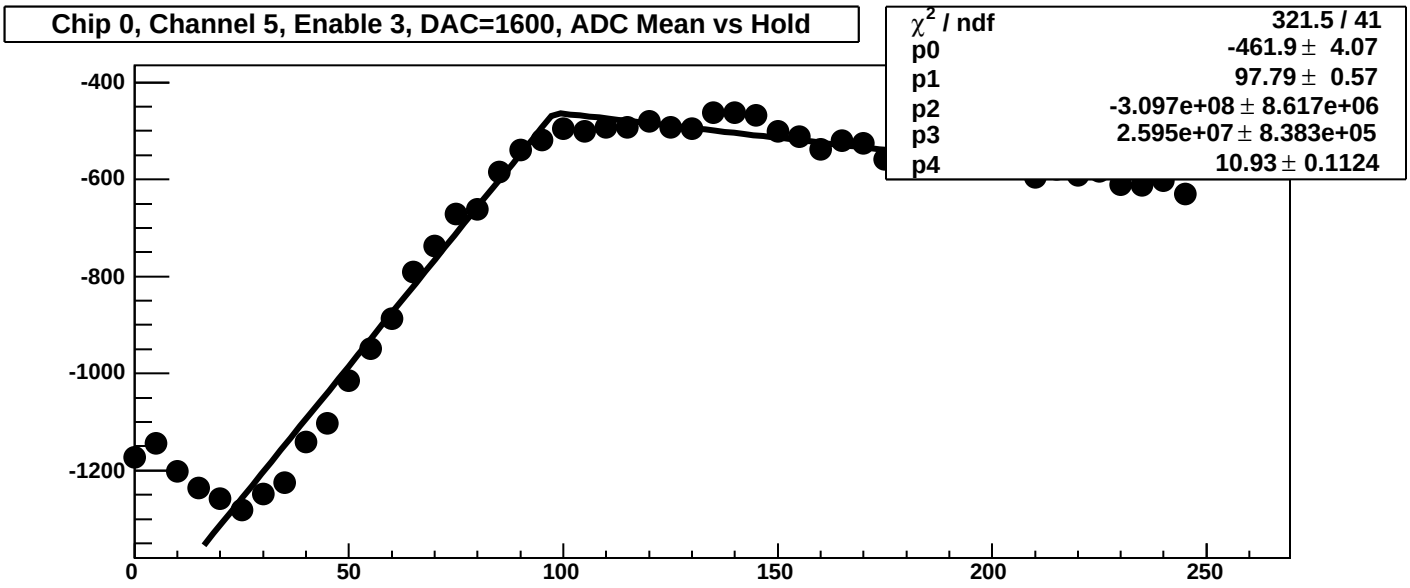


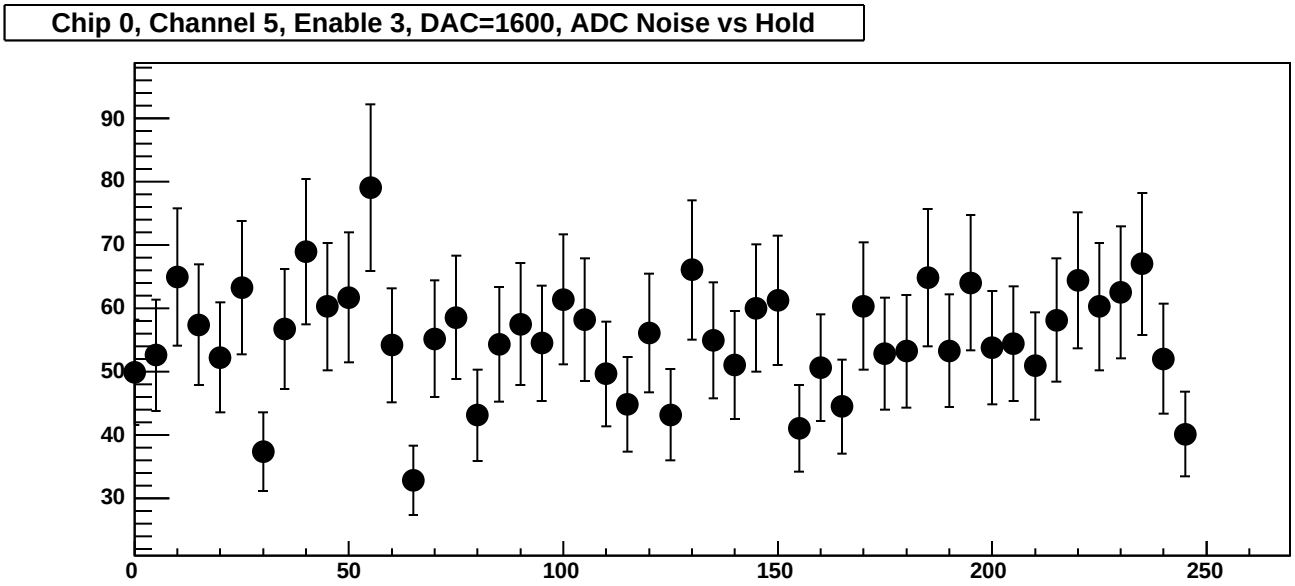
Chip 0, Channel 5, Enable 2, DAC=1600, ADC Noise vs Hold

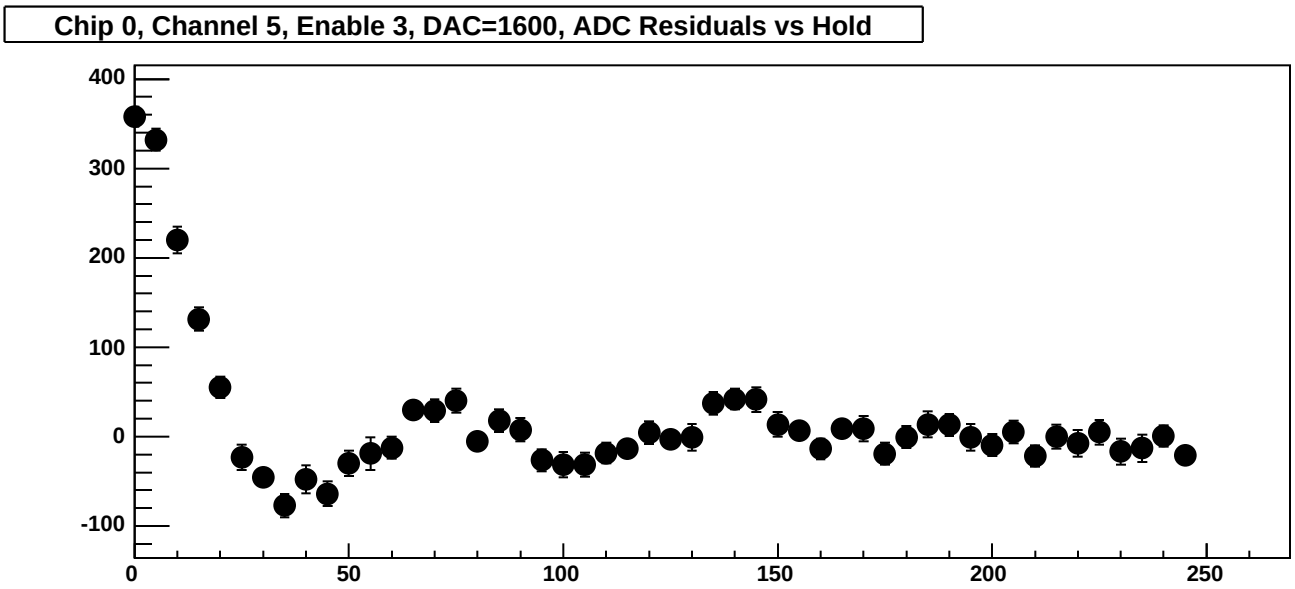


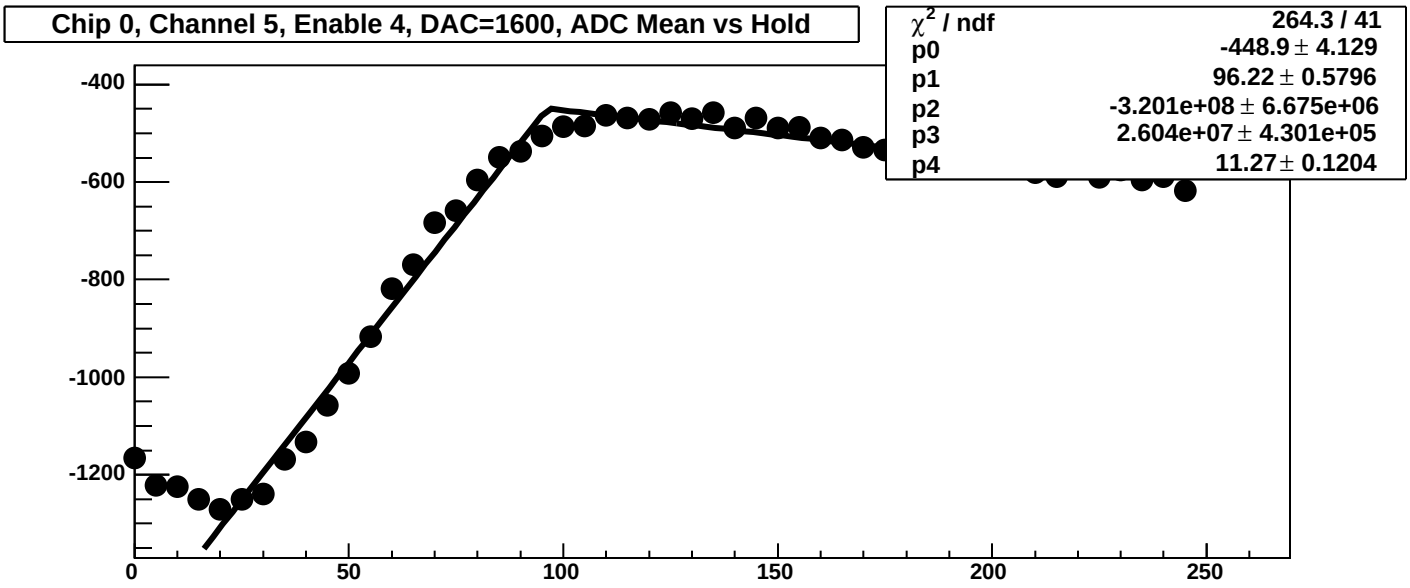
Chip 0, Channel 5, Enable 2, DAC=1600, ADC Residuals vs Hold

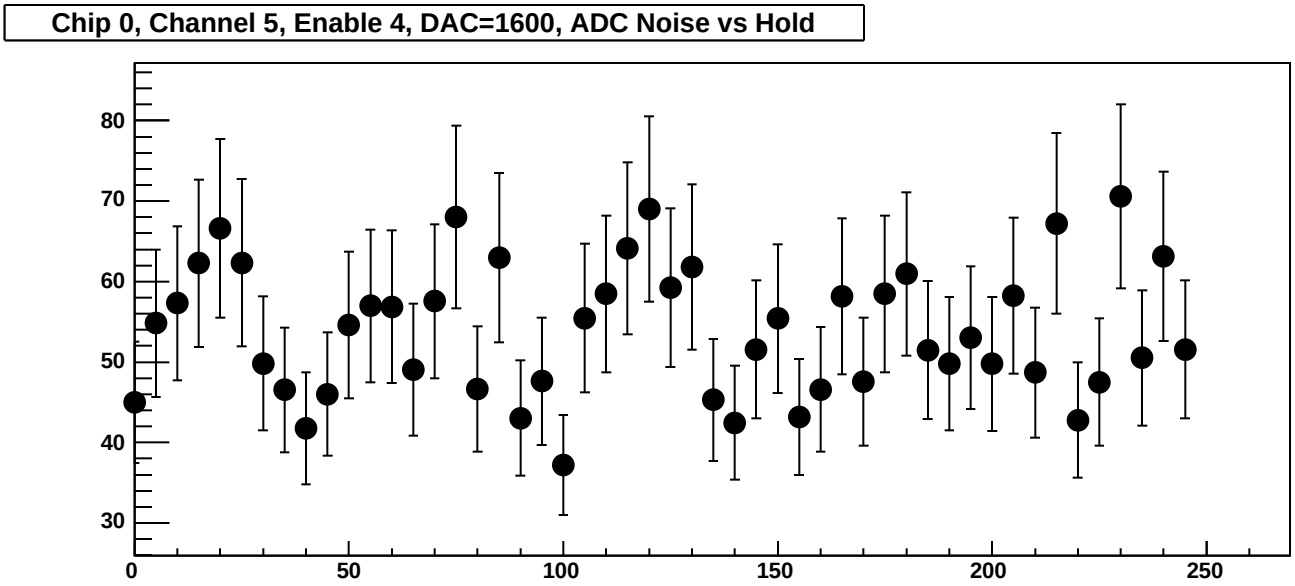


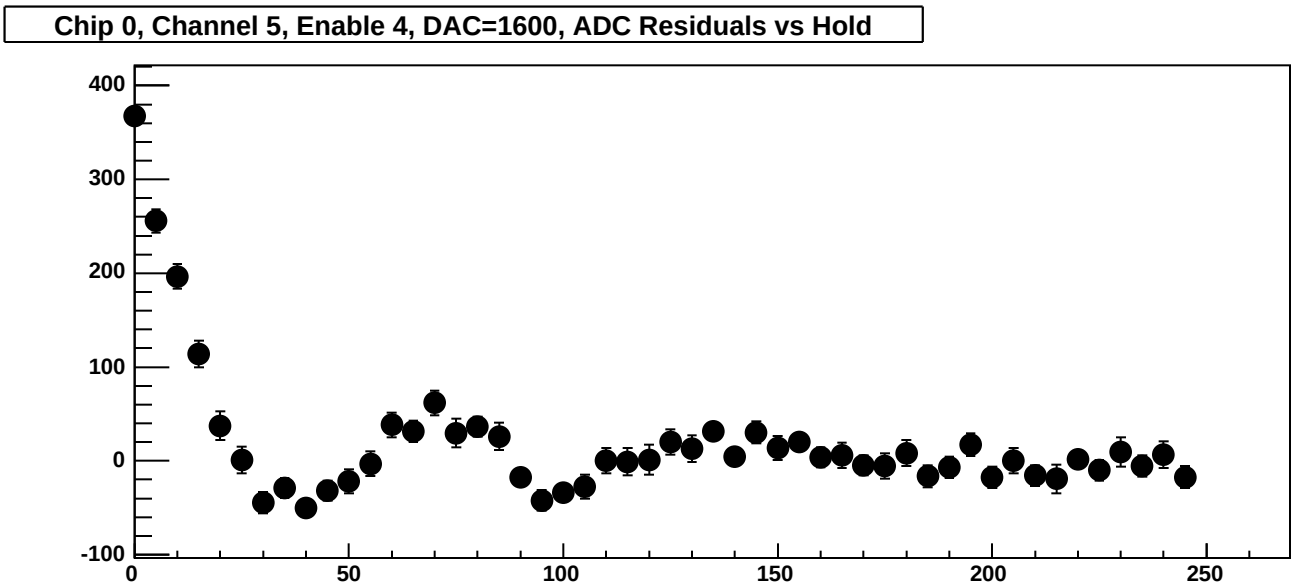

Chip 0, Channel 5, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 321.5 / 41
p0 -461.9 ± 4.07
p1 97.79 ± 0.57
p2 -3.097e+08 ± 8.617e+06
p3 2.595e+07 ± 8.383e+05
p4 10.93 ± 0.1124
-400
-600
-800
-1000
-1200
0
50
100
150
200
250


Chip 0, Channel 5, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

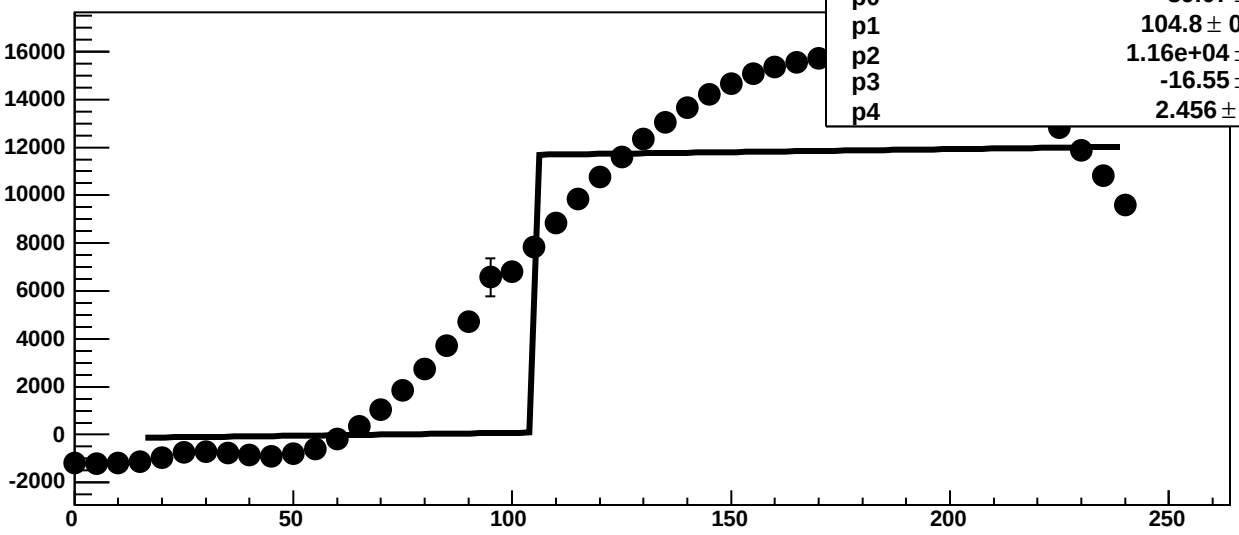

Chip 0, Channel 5, Enable 3, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 0, Channel 5, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 264.3 / 41
p0 -448.9 ± 4.129
p1 96.22 ± 0.5796
p2 -3.201e+08 ± 6.675e+06
p3 2.604e+07 ± 4.301e+05
p4 11.27 ± 0.1204
-400
-600
-800
-1000
-1200
0
50
100
150
200
250

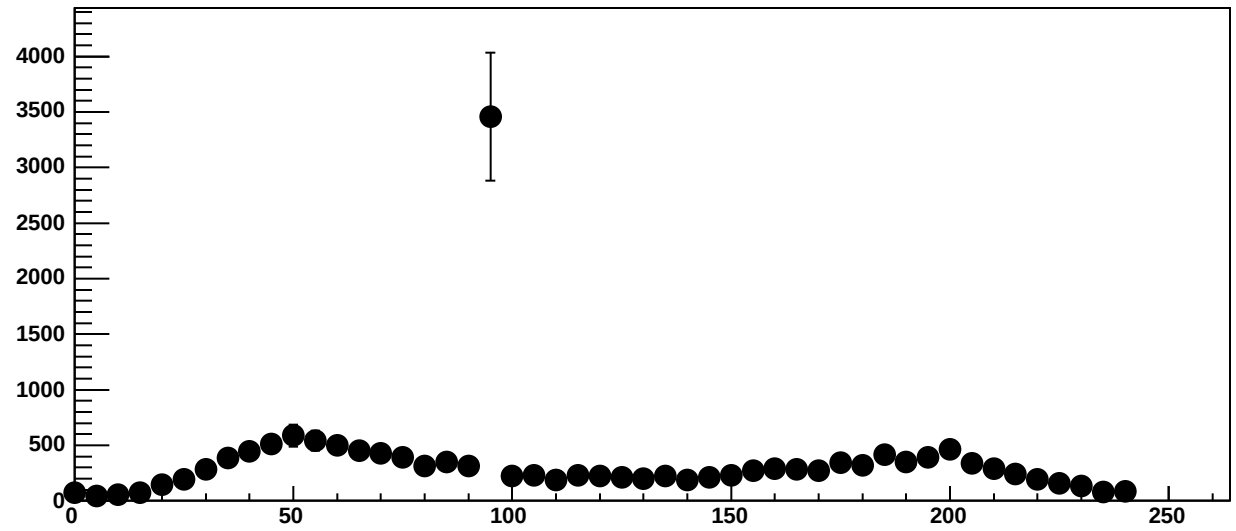

Chip 0, Channel 5, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 0, Channel 5, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

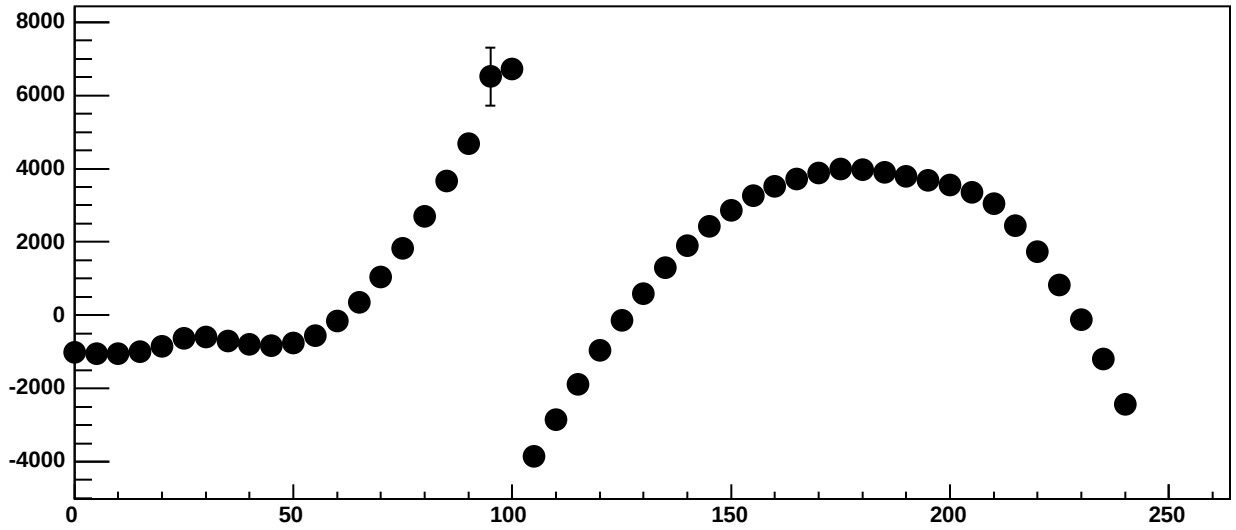
Chip 0, Channel 5, Enable 5, DAC=1600, ADC Mean vs Hold

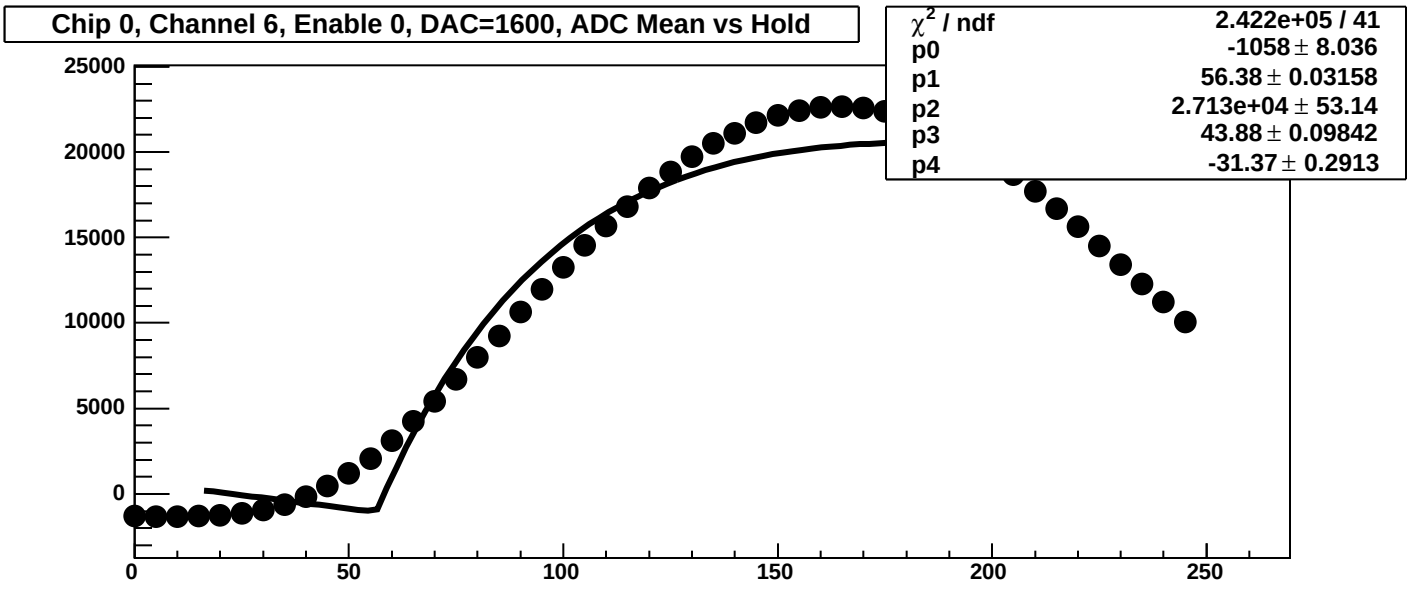


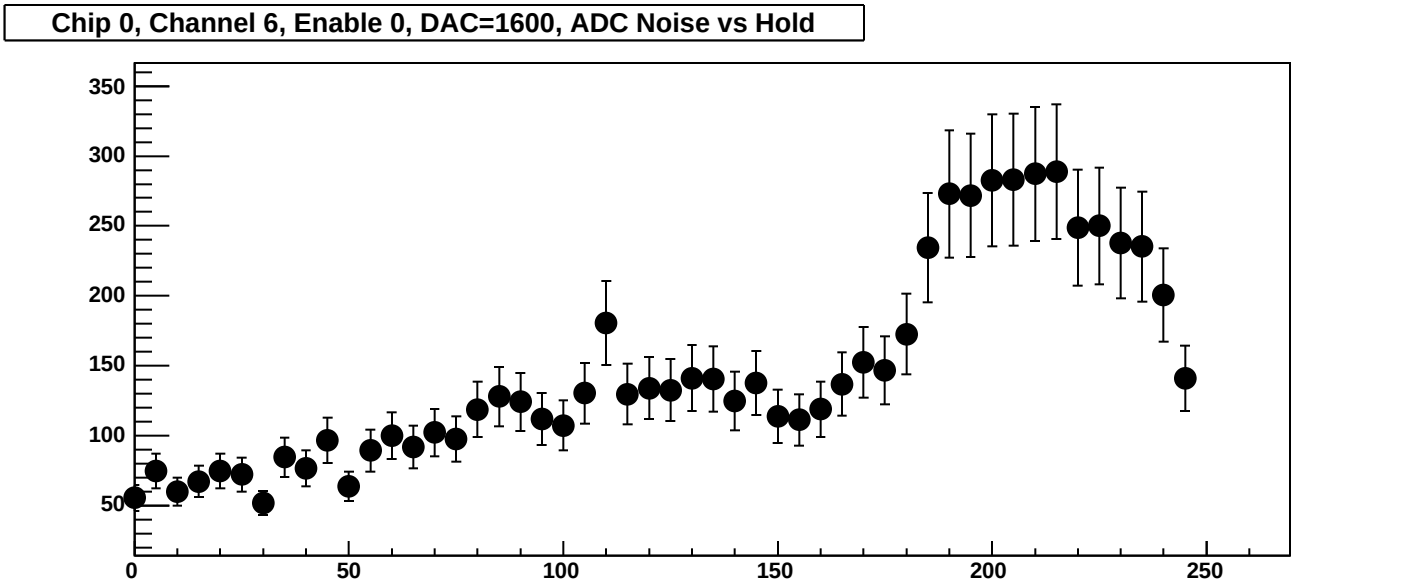
Chip 0, Channel 5, Enable 5, DAC=1600, ADC Noise vs Hold

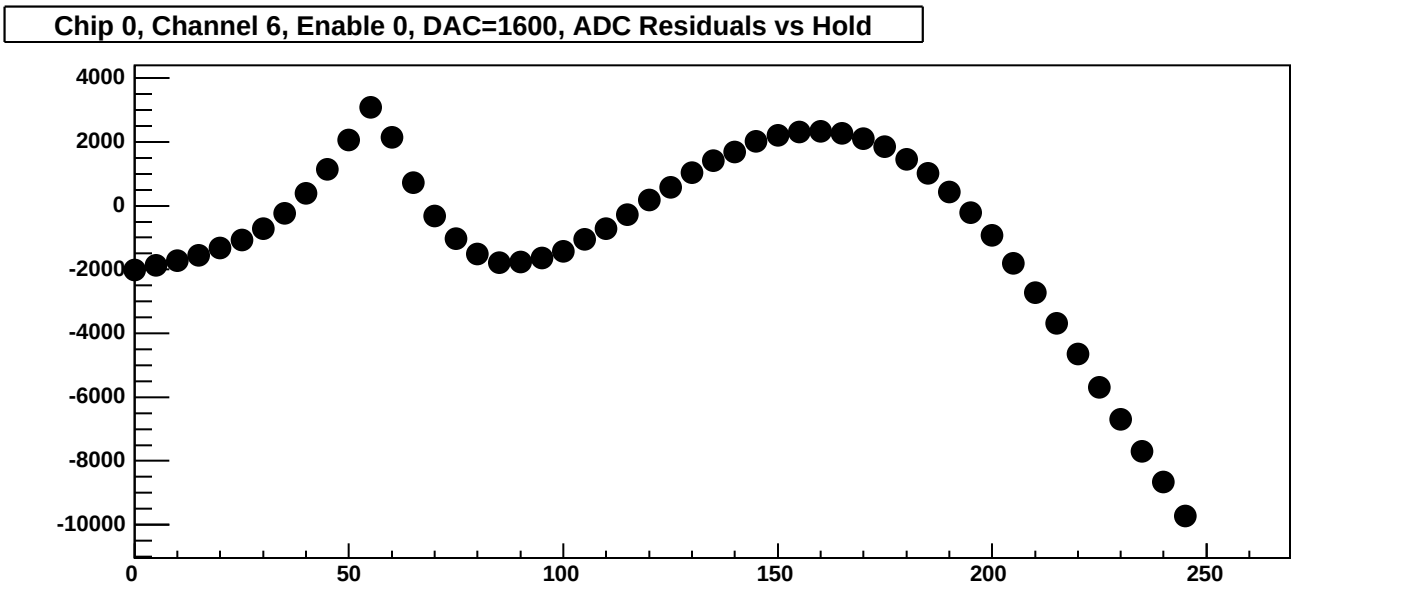


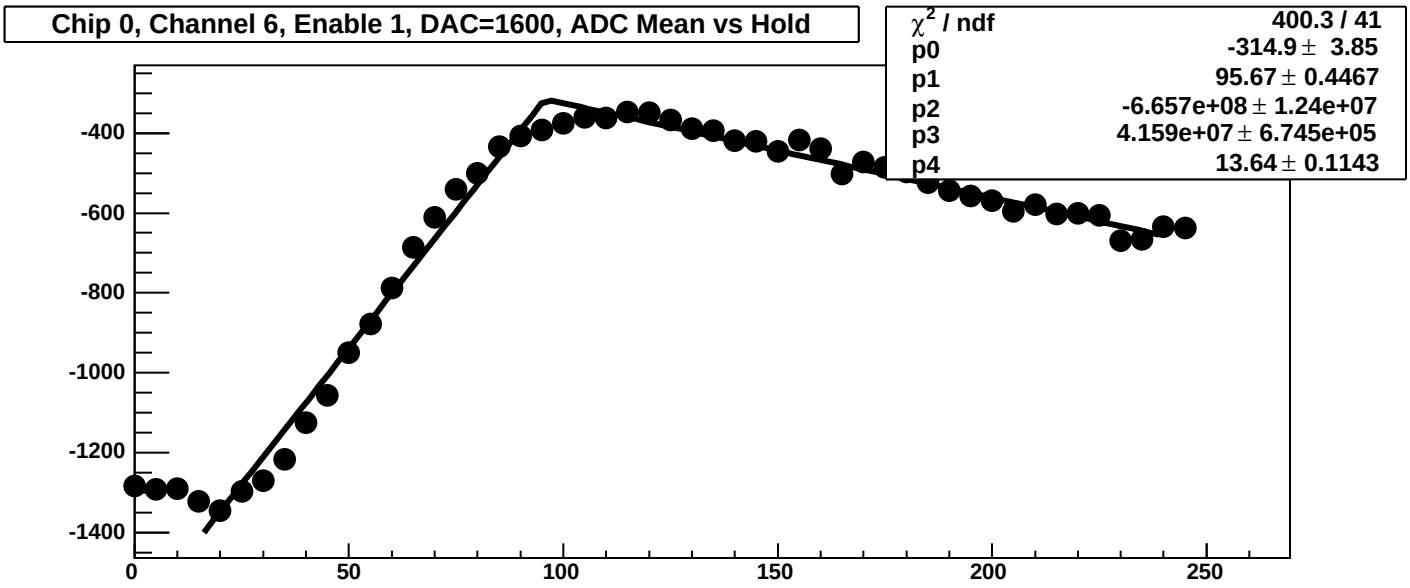
Chip 0, Channel 5, Enable 5, DAC=1600, ADC Residuals vs Hold

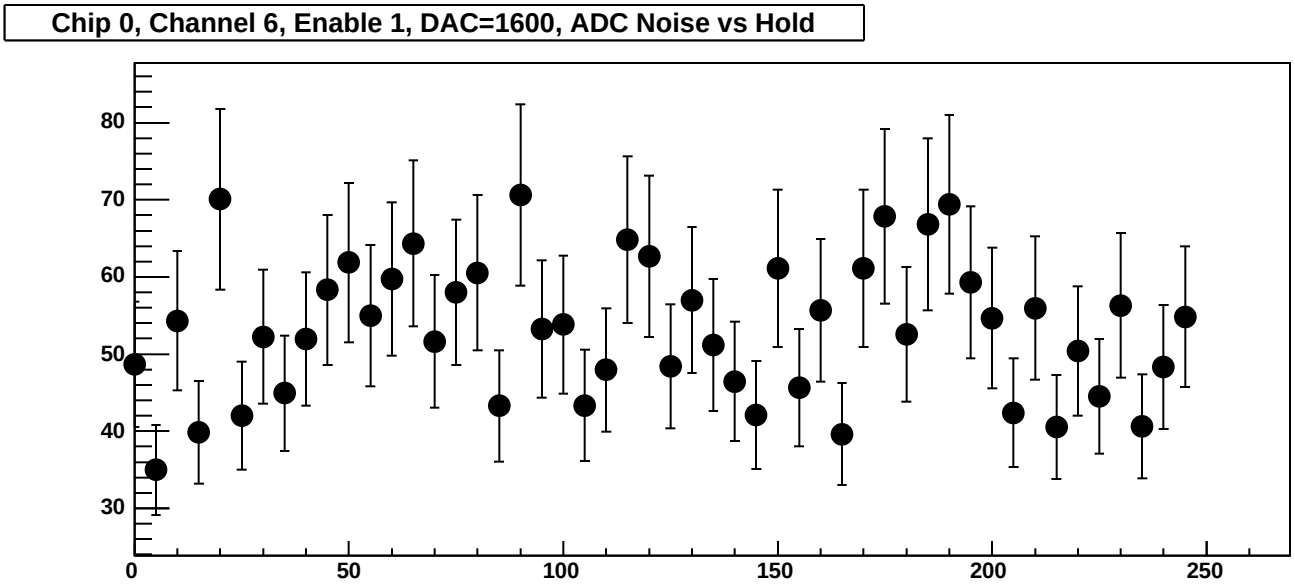


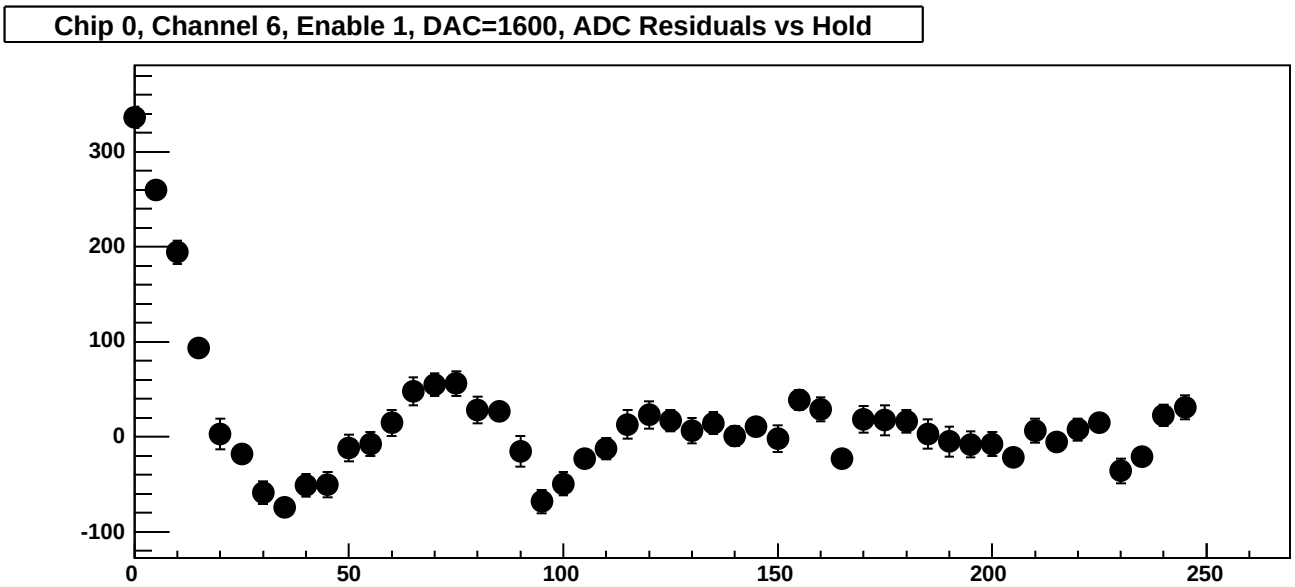

Chip 0, Channel 6, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.422e+05 / 41
p0 -1058 ± 8.036
p1 56.38 ± 0.03158
p2 2.713e+04 ± 53.14
p3 43.88 ± 0.09842
p4 -31.37 ± 0.2913
25000
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 0, Channel 6, Enable 0, DAC=1600, ADC Noise vs Hold
350
300
250
200
150
100
50
0
50
100
150
200
250

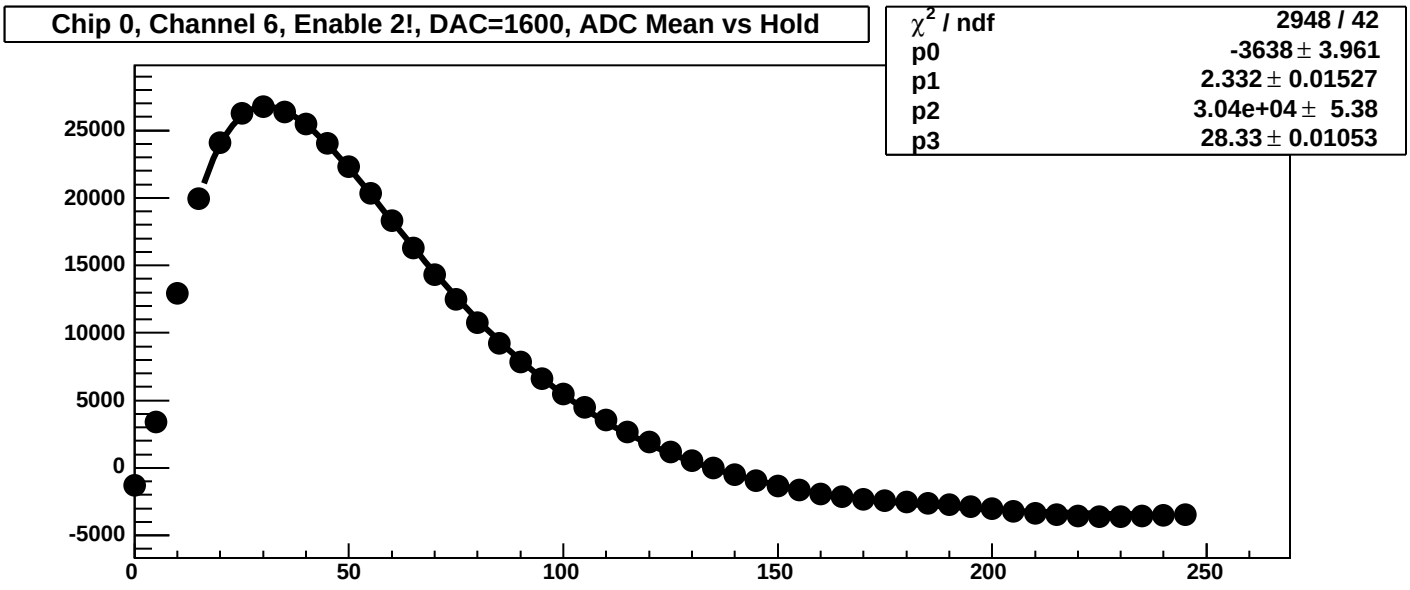

Chip 0, Channel 6, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250


Chip 0, Channel 6, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 400.3 / 41
p0 -314.9 ± 3.85
p1 95.67 ± 0.4467
p2 -6.657e+08 ± 1.24e+07
p3 4.159e+07 ± 6.745e+05
p4 13.64 ± 0.1143
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

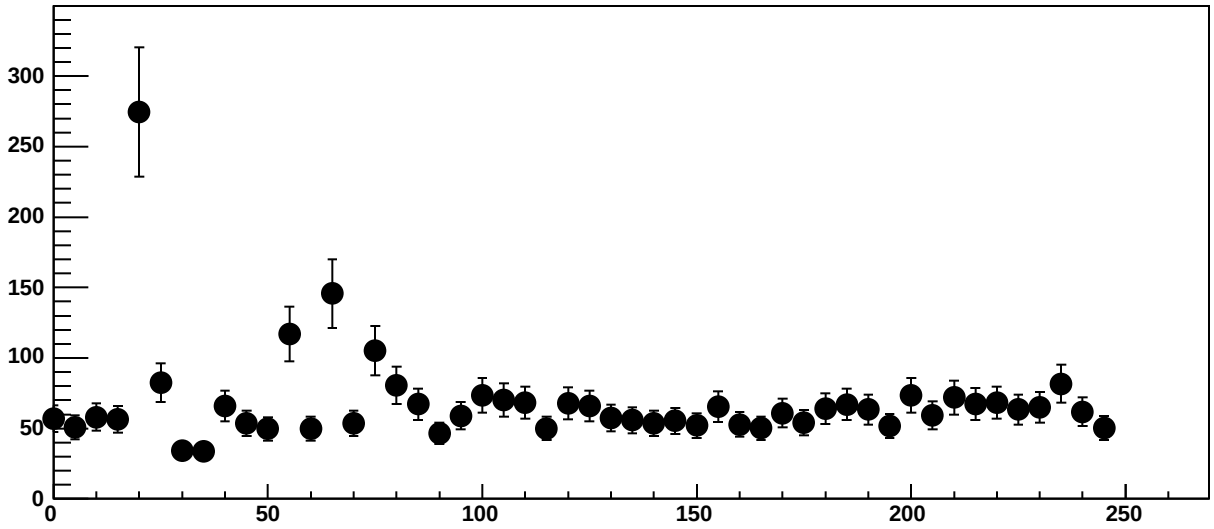

Chip 0, Channel 6, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 0, Channel 6, Enable 1, DAC=1600, ADC Residuals vs Hold
300
200
100
0
-100
0
50
100
150
200
250

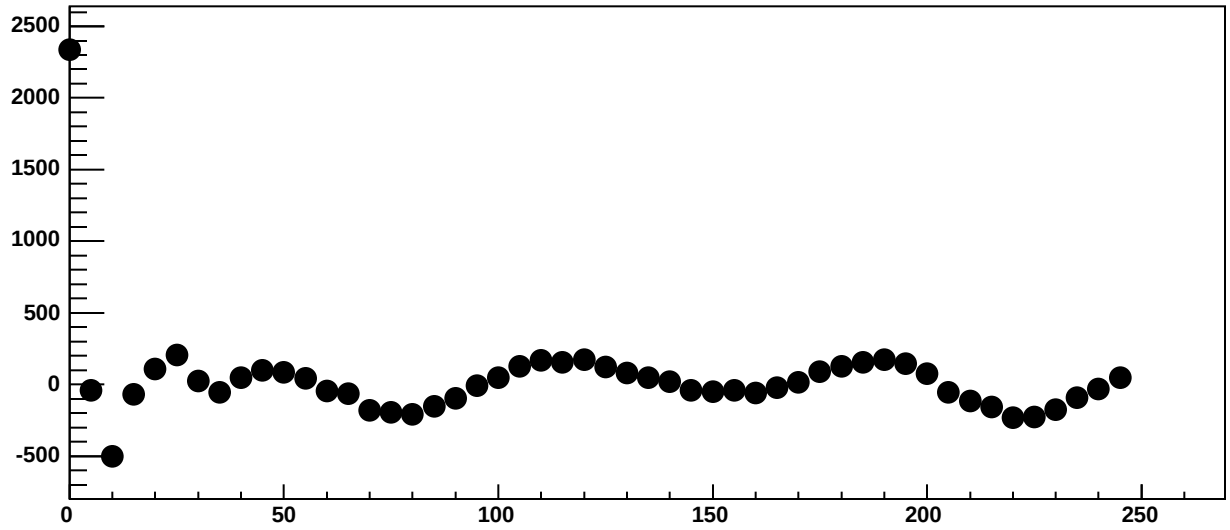
Chip 0, Channel 6, Enable 2!, DAC=1600, ADC Mean vs Hold



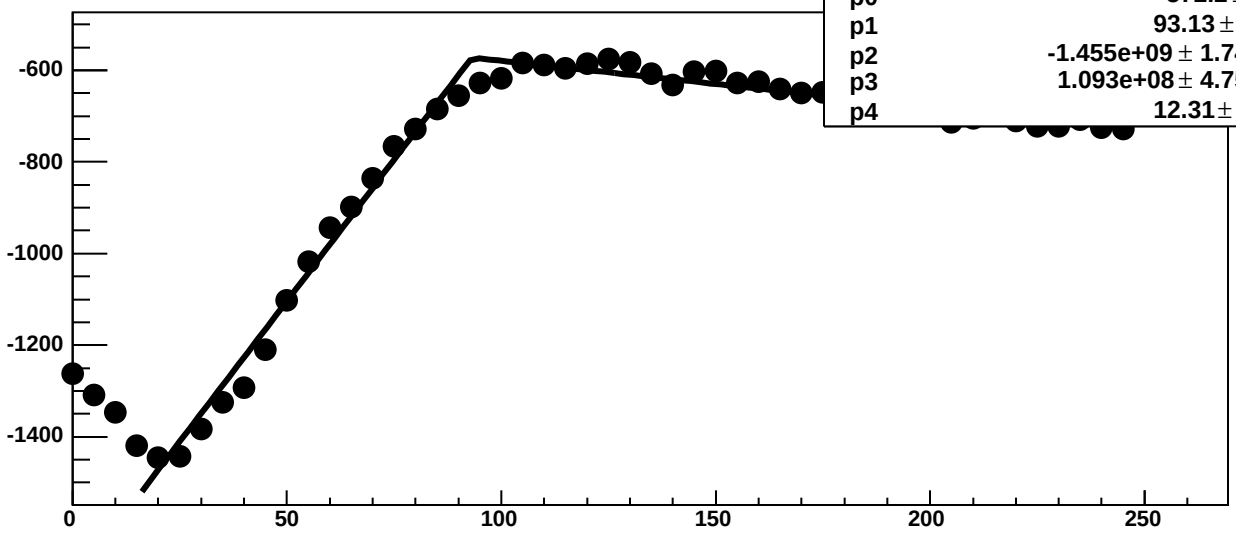
Chip 0, Channel 6, Enable 2!, DAC=1600, ADC Noise vs Hold



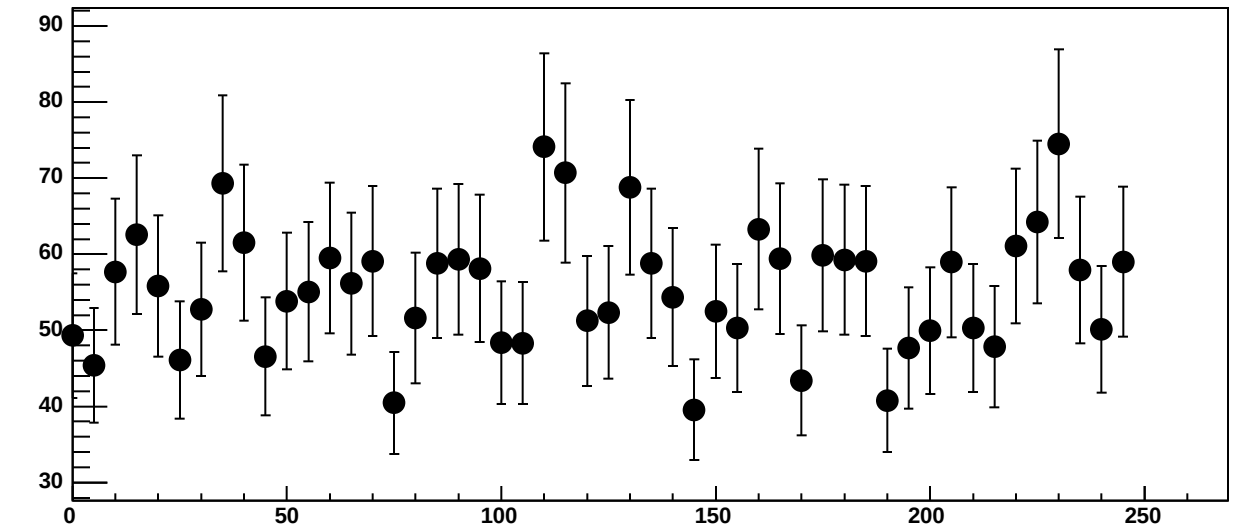
Chip 0, Channel 6, Enable 2!, DAC=1600, ADC Residuals vs Hold



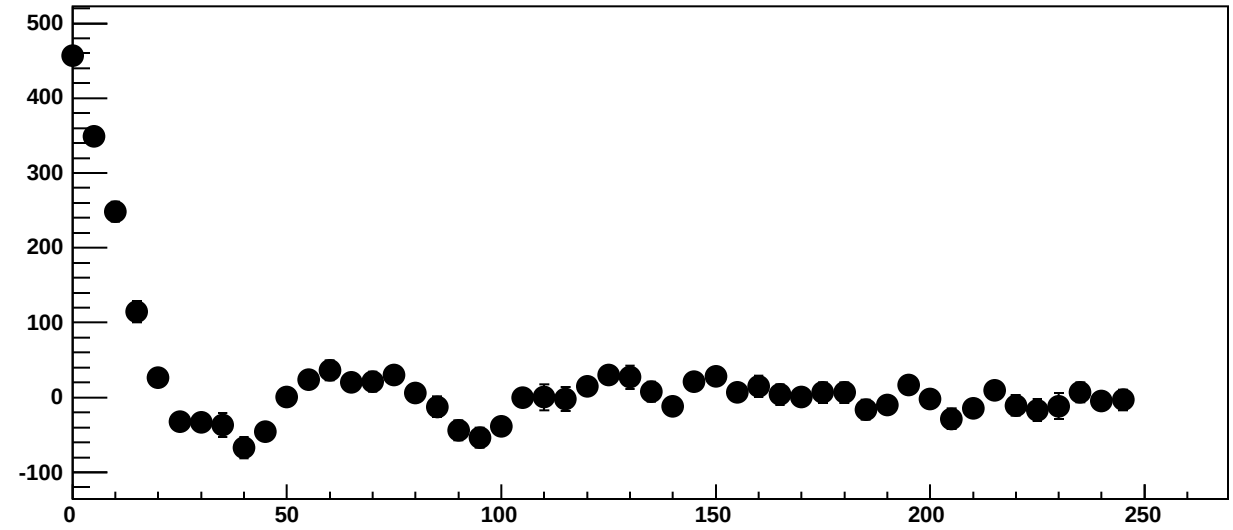
Chip 0, Channel 6, Enable 3, DAC=1600, ADC Mean vs Hold



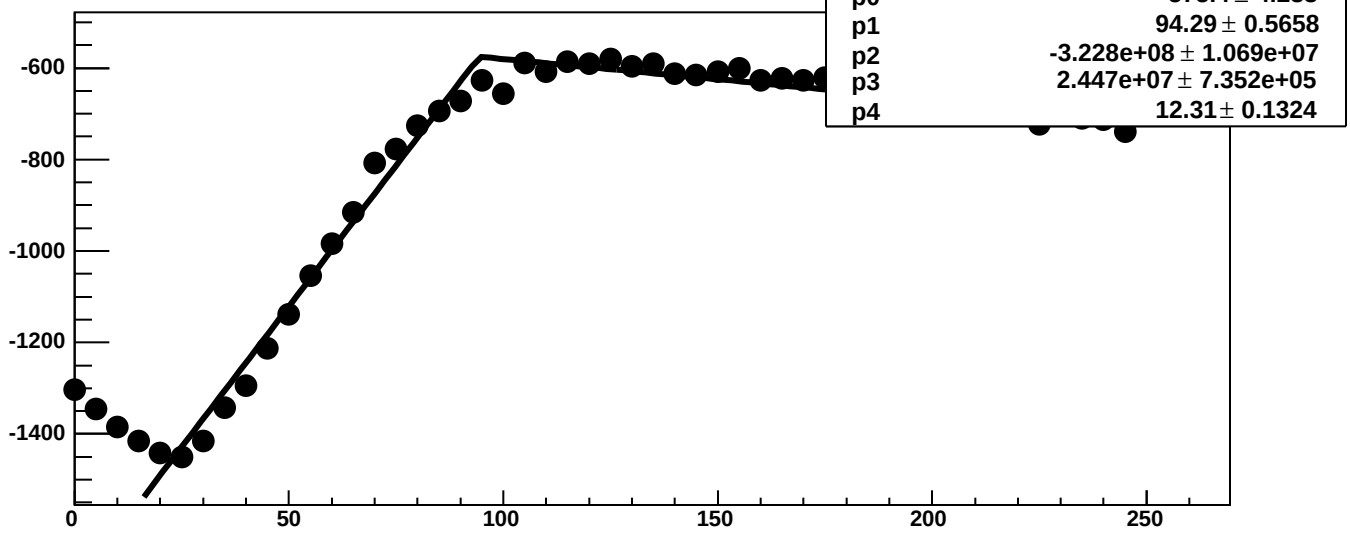
Chip 0, Channel 6, Enable 3, DAC=1600, ADC Noise vs Hold



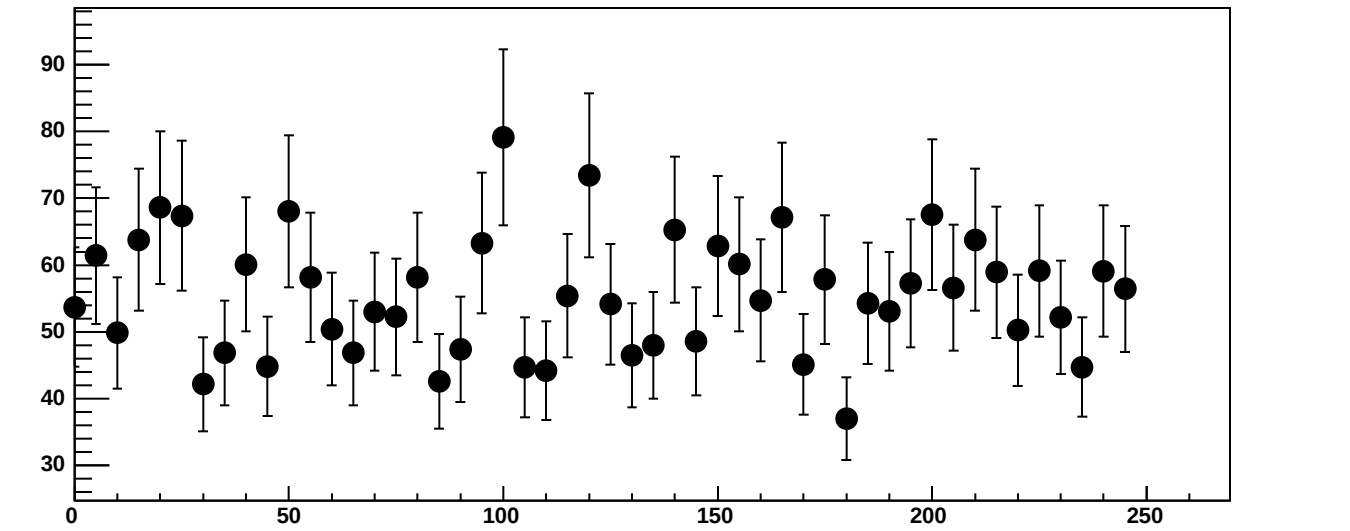
Chip 0, Channel 6, Enable 3, DAC=1600, ADC Residuals vs Hold



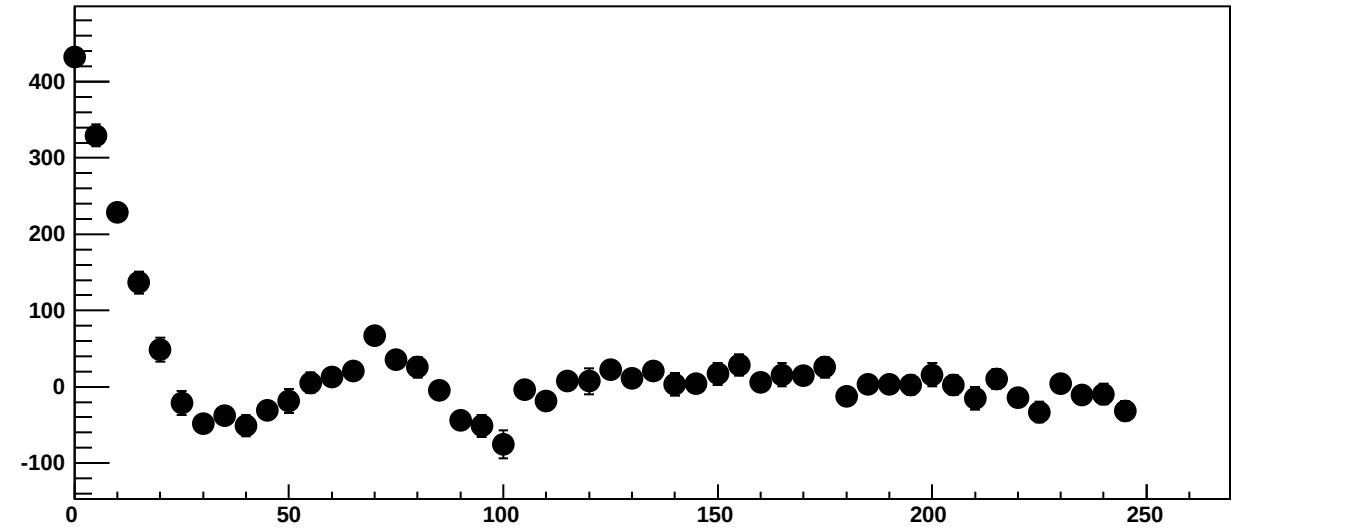
Chip 0, Channel 6, Enable 4, DAC=1600, ADC Mean vs Hold

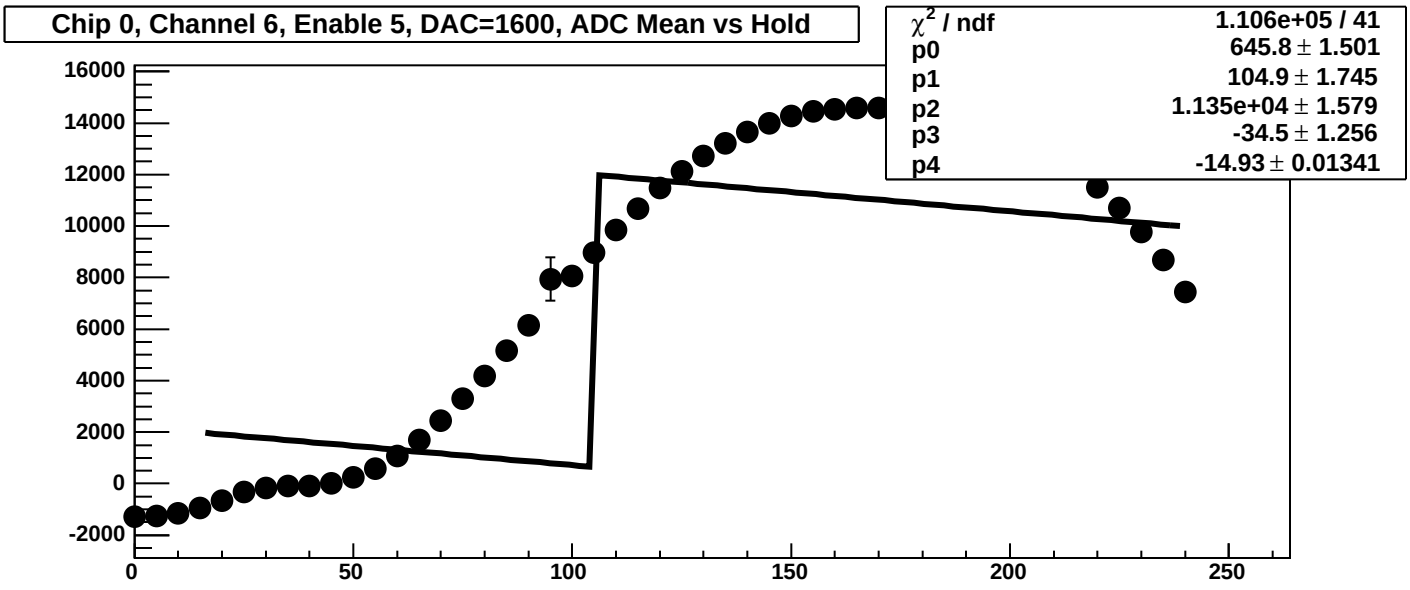


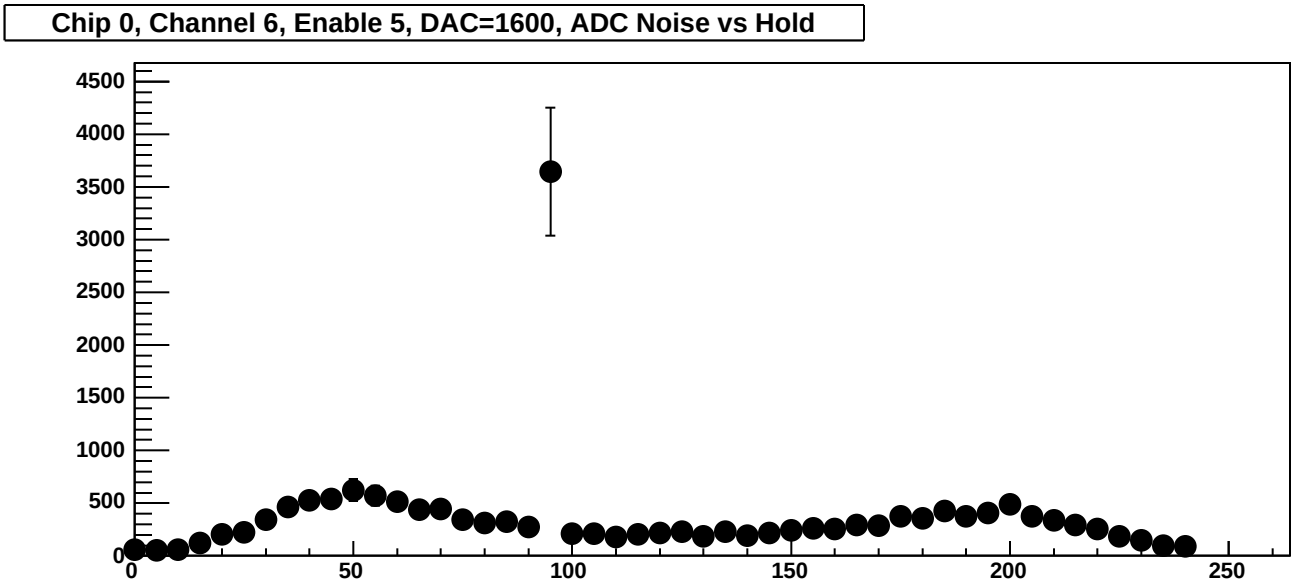
Chip 0, Channel 6, Enable 4, DAC=1600, ADC Noise vs Hold

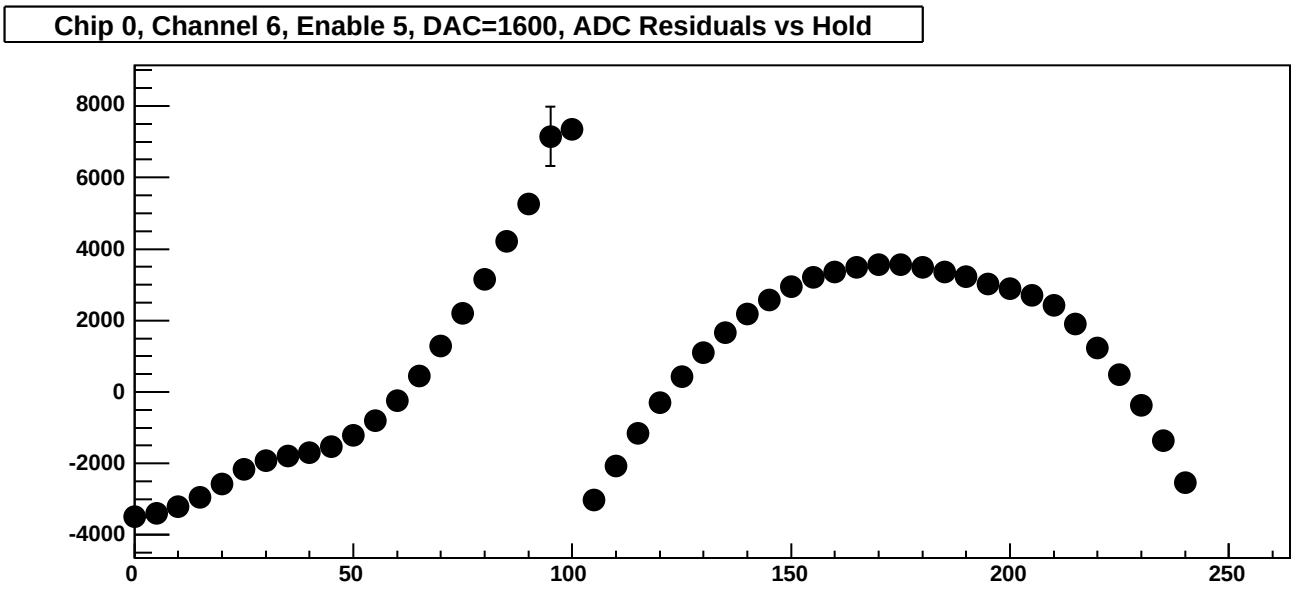


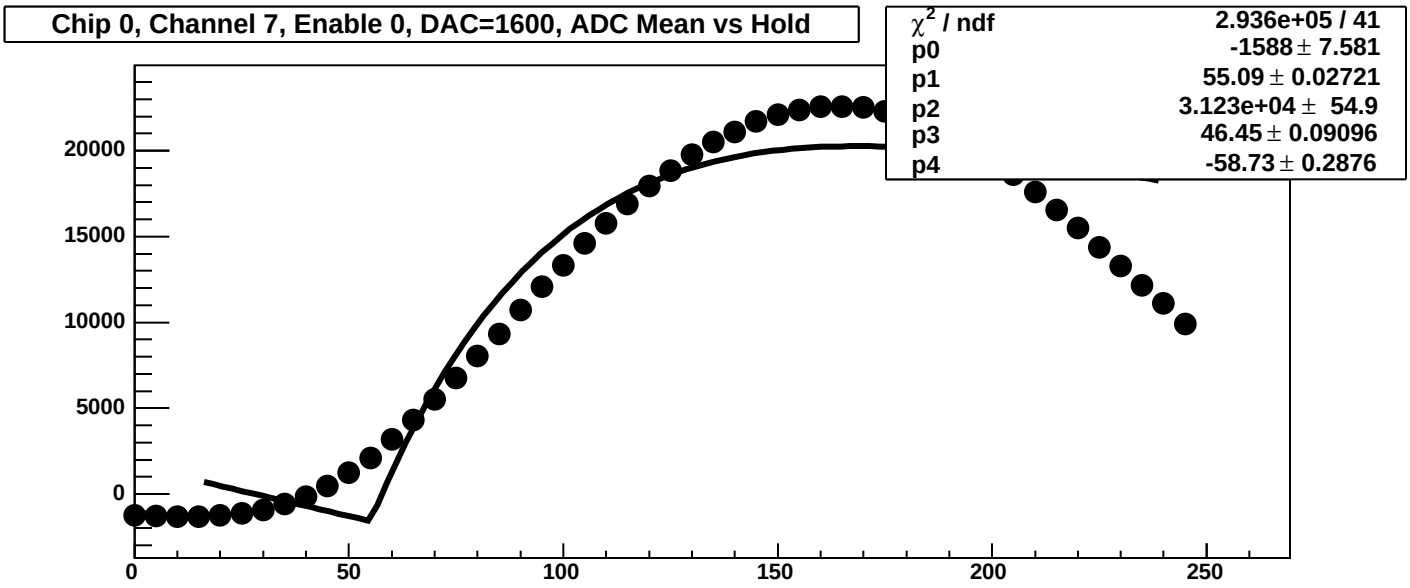
Chip 0, Channel 6, Enable 4, DAC=1600, ADC Residuals vs Hold

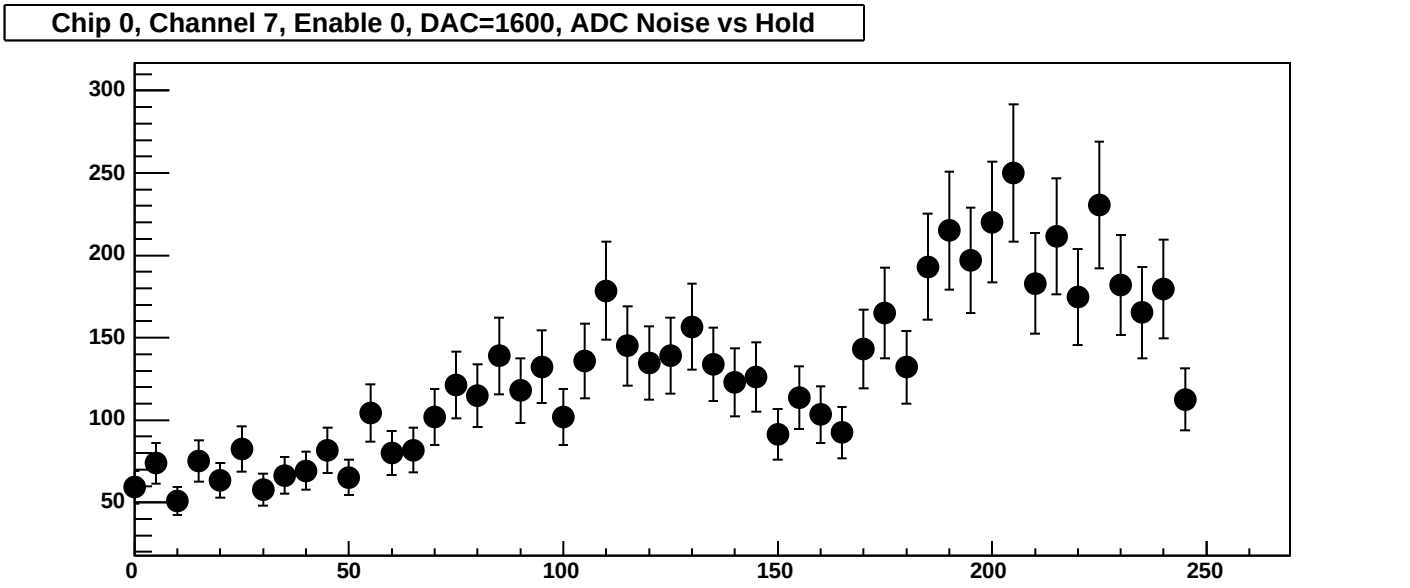


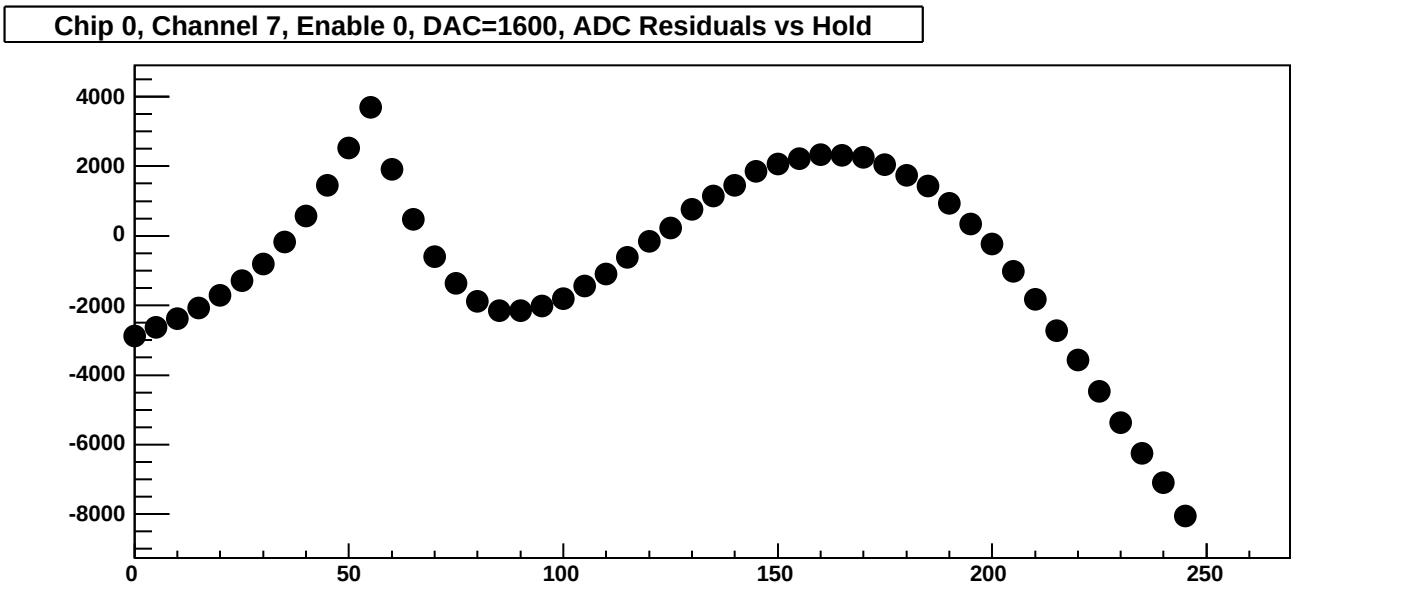

Chip 0, Channel 6, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.106e+05 / 41
p0 645.8 ± 1.501
p1 104.9 ± 1.745
p2 1.135e+04 ± 1.579
p3 -34.5 ± 1.256
p4 -14.93 ± 0.01341
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250


Chip 0, Channel 6, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250

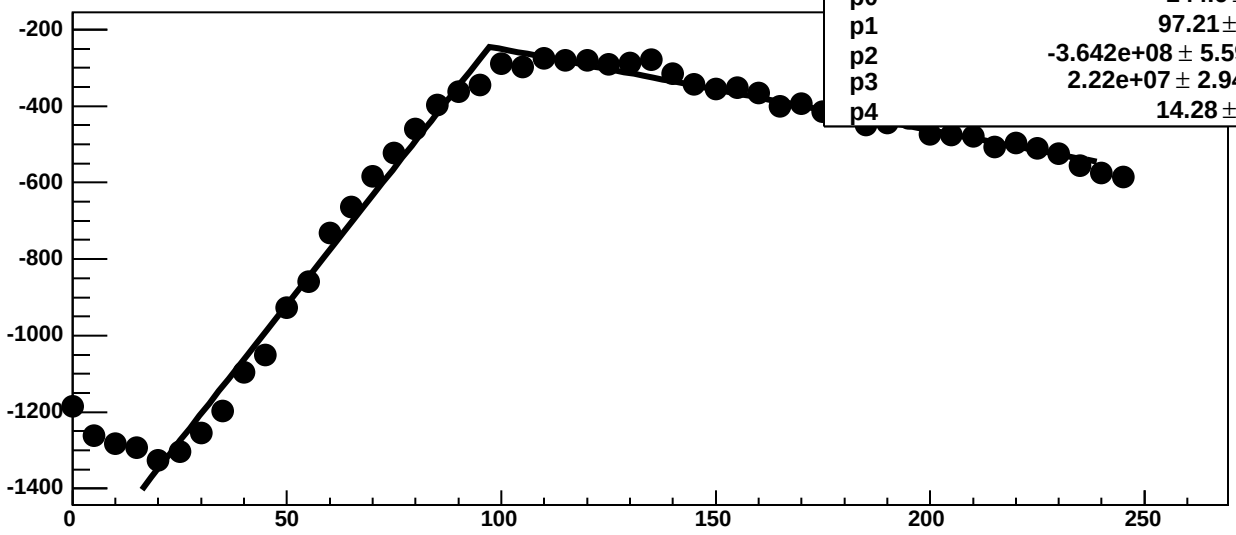

Chip 0, Channel 6, Enable 5, DAC=1600, ADC Residuals vs Hold
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250


Chip 0, Channel 7, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.936e+05 / 41
p0 -1588 ± 7.581
p1 55.09 ± 0.02721
p2 3.123e+04 ± 54.9
p3 46.45 ± 0.09096
p4 -58.73 ± 0.2876
20000
15000
10000
5000
0
0
50
100
150
200
250

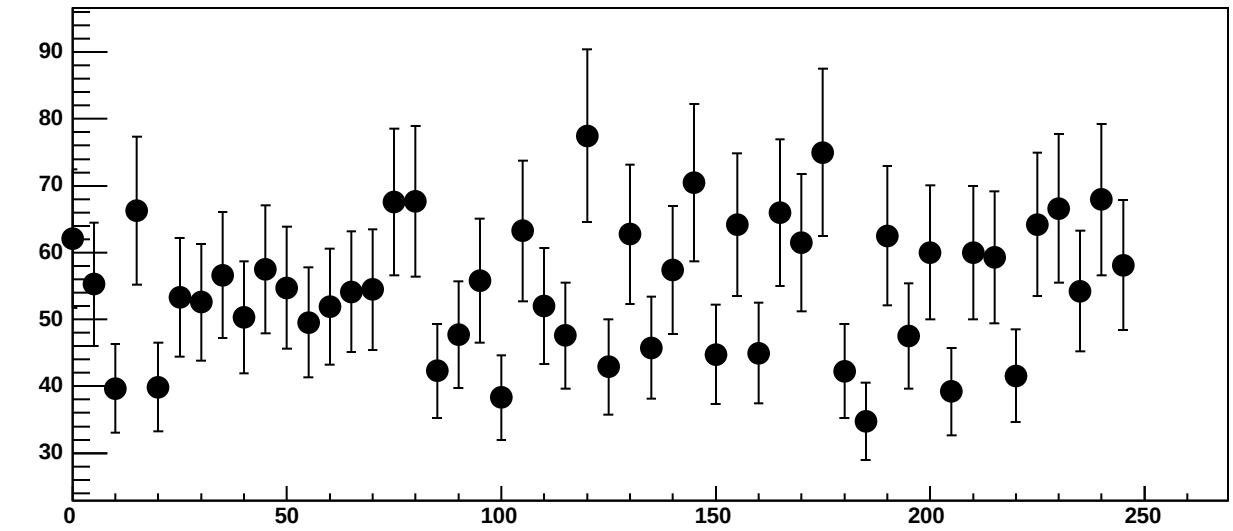

Chip 0, Channel 7, Enable 0, DAC=1600, ADC Noise vs Hold
300
250
200
150
100
50
0
50
100
150
200
250


Chip 0, Channel 7, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250

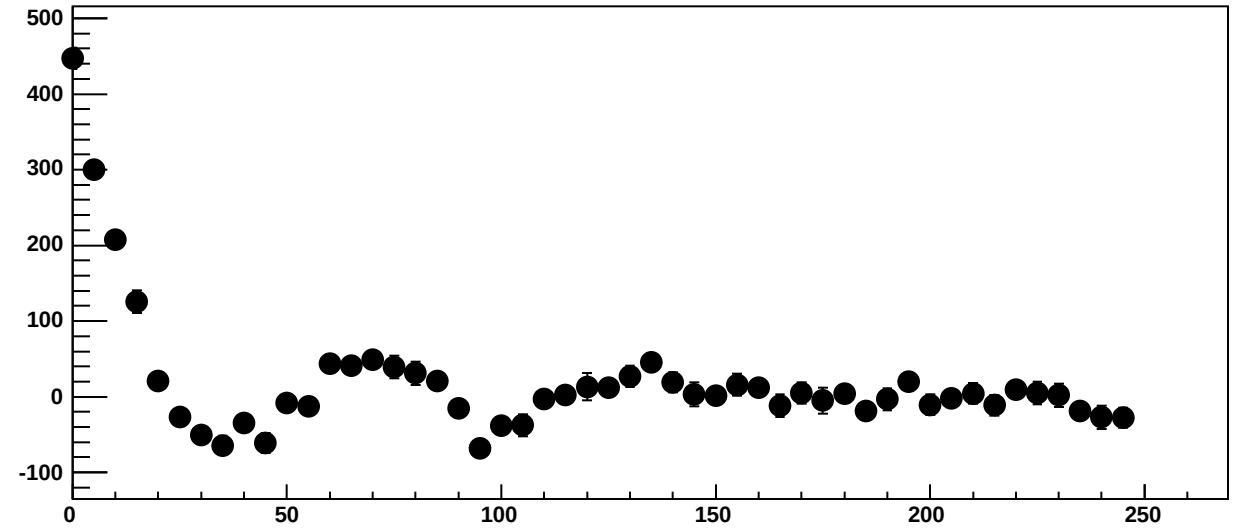
Chip 0, Channel 7, Enable 1, DAC=1600, ADC Mean vs Hold



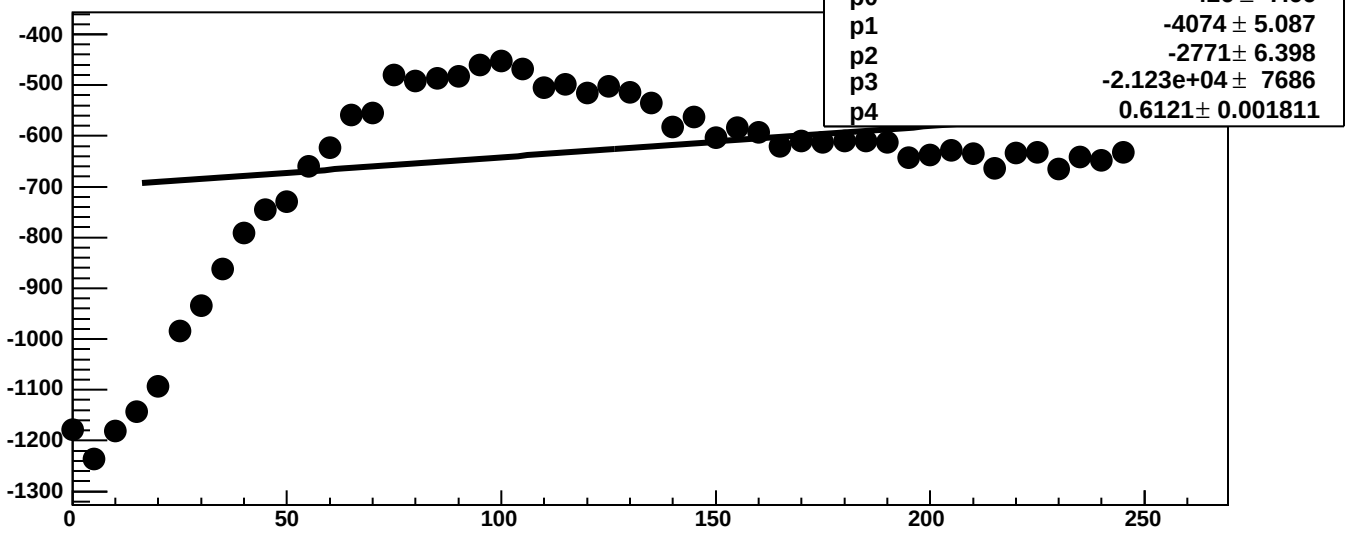
Chip 0, Channel 7, Enable 1, DAC=1600, ADC Noise vs Hold



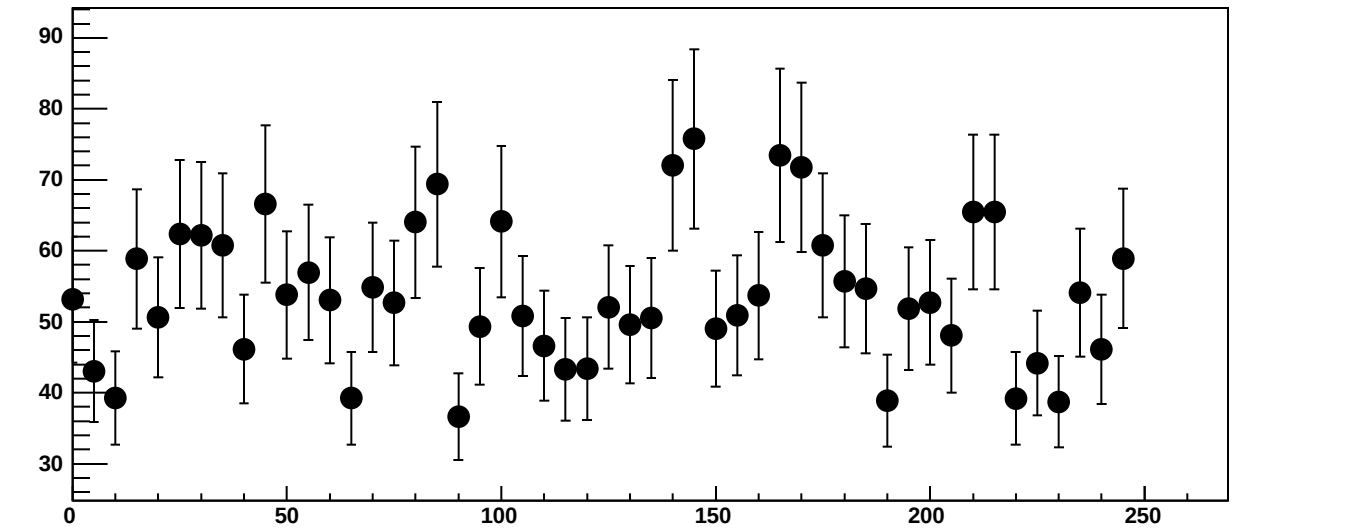
Chip 0, Channel 7, Enable 1, DAC=1600, ADC Residuals vs Hold



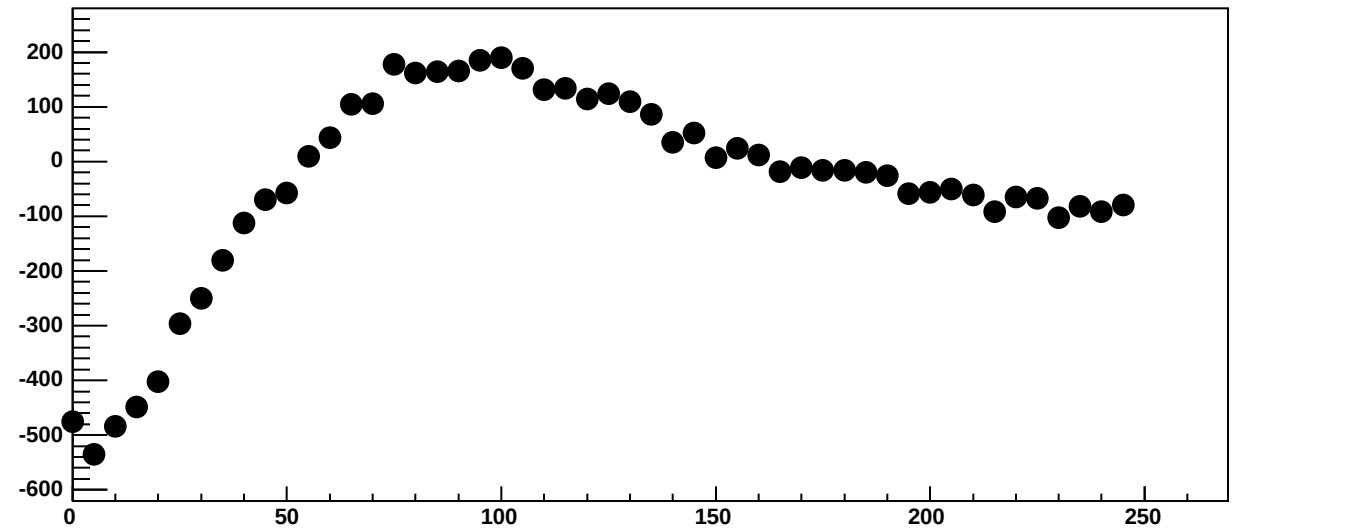
Chip 0, Channel 7, Enable 2, DAC=1600, ADC Mean vs Hold

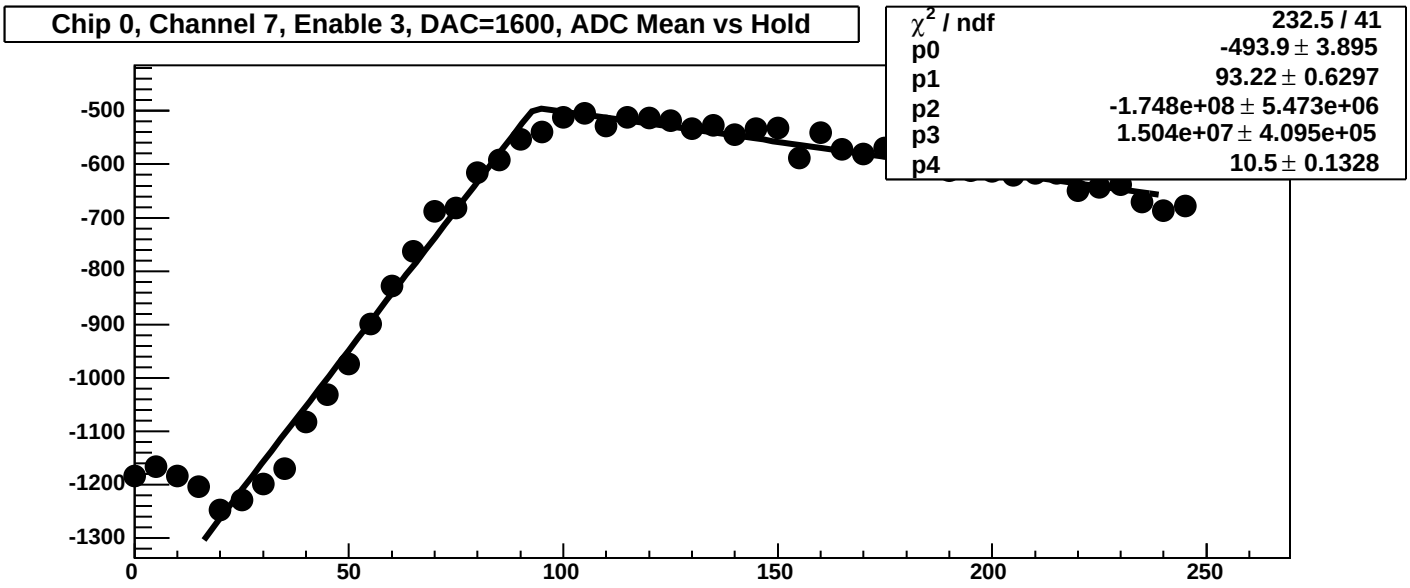


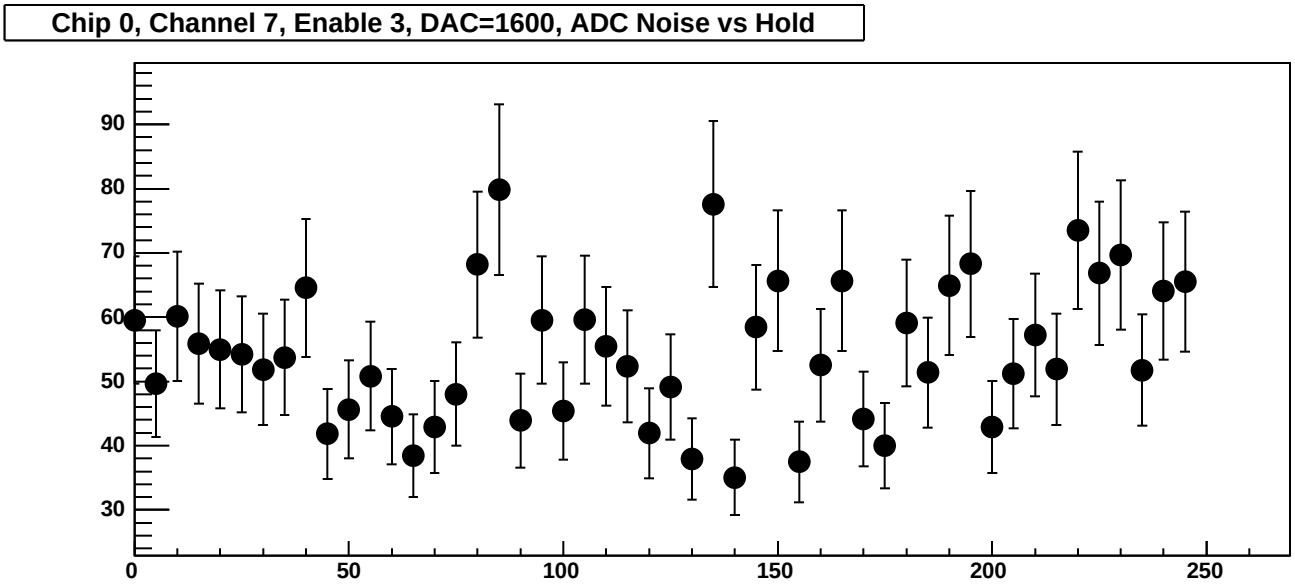
Chip 0, Channel 7, Enable 2, DAC=1600, ADC Noise vs Hold

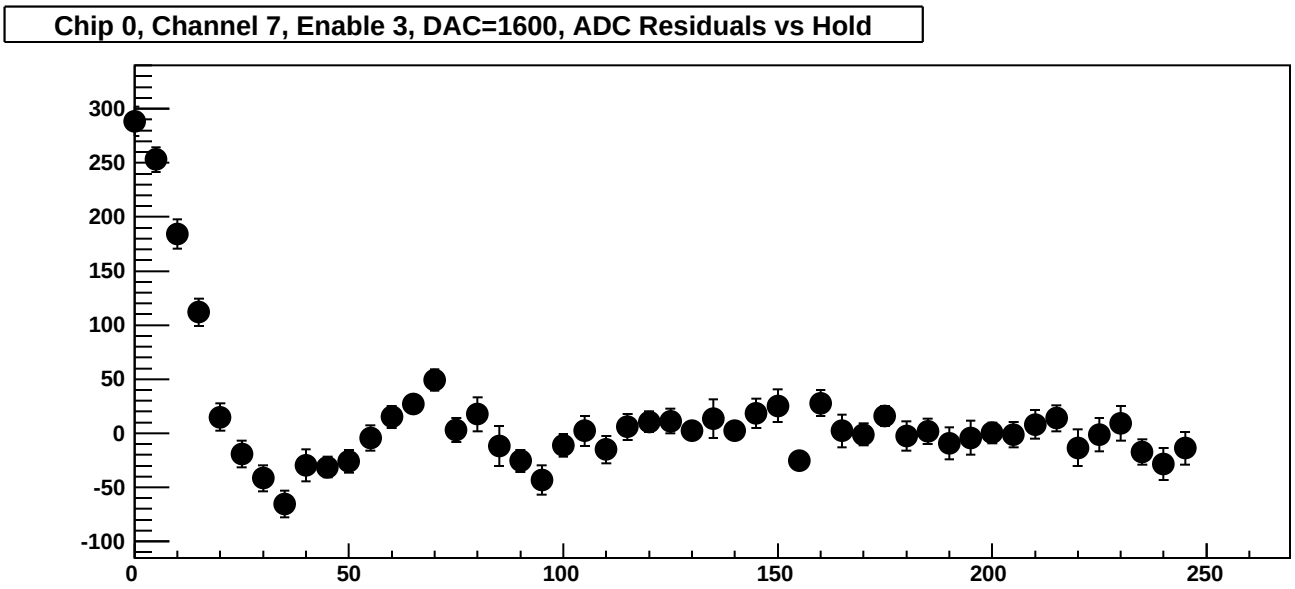


Chip 0, Channel 7, Enable 2, DAC=1600, ADC Residuals vs Hold

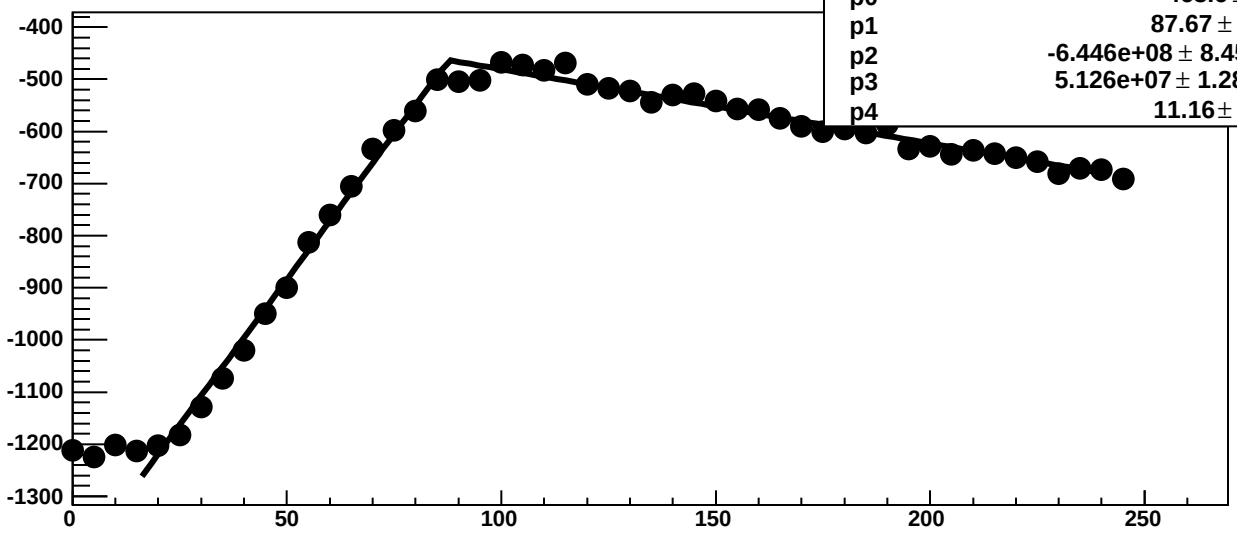



Chip 0, Channel 7, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 232.5 / 41
p0 -493.9 ± 3.895
p1 93.22 ± 0.6297
p2 -1.748e+08 ± 5.473e+06
p3 1.504e+07 ± 4.095e+05
p4 10.5 ± 0.1328
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
50
100
150
200
250

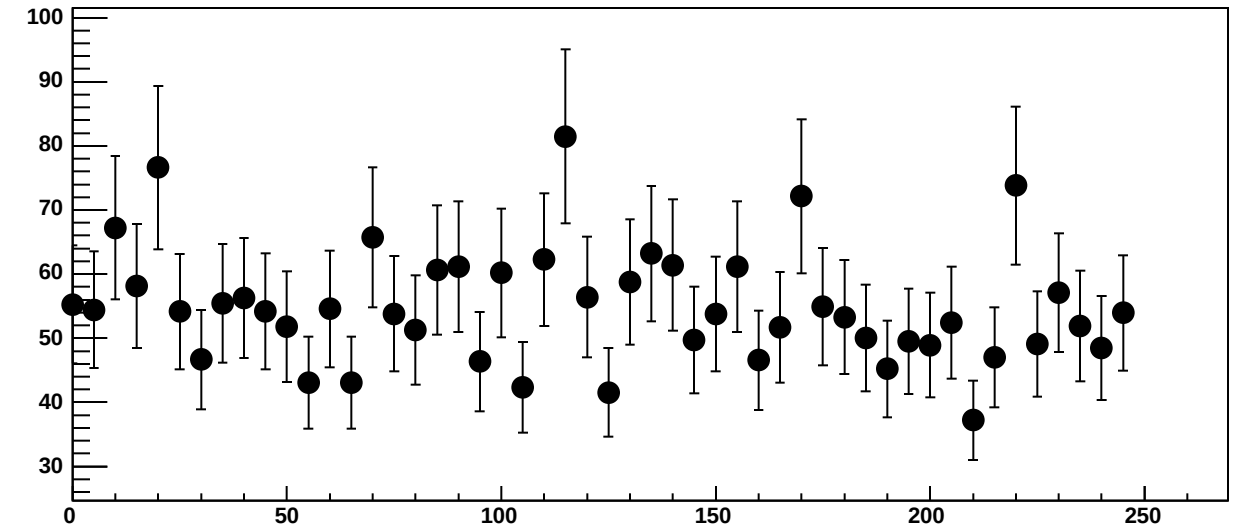

Chip 0, Channel 7, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 0, Channel 7, Enable 3, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

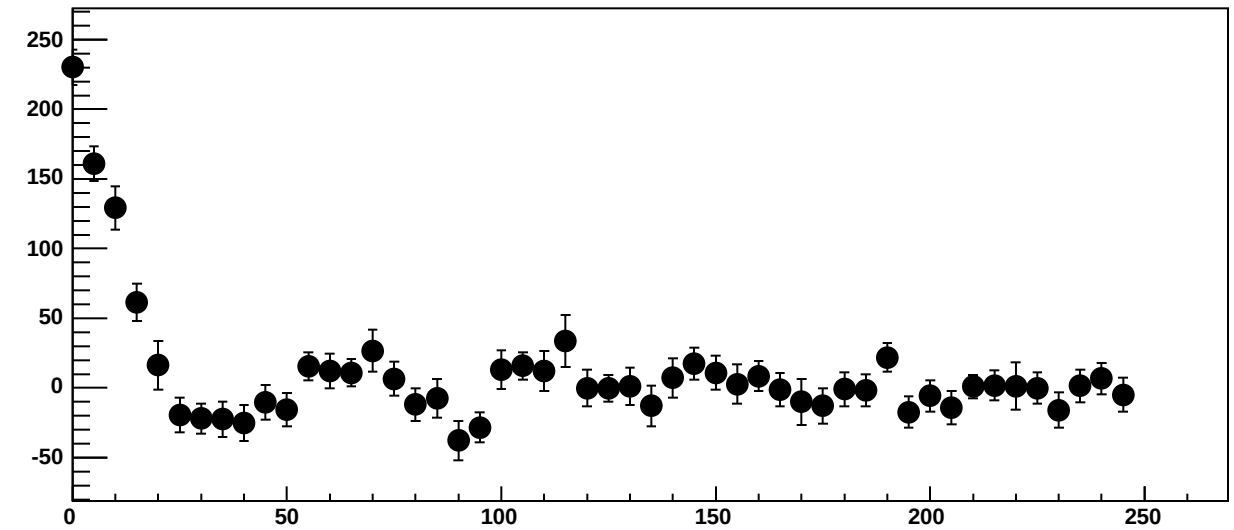
Chip 0, Channel 7, Enable 4, DAC=1600, ADC Mean vs Hold



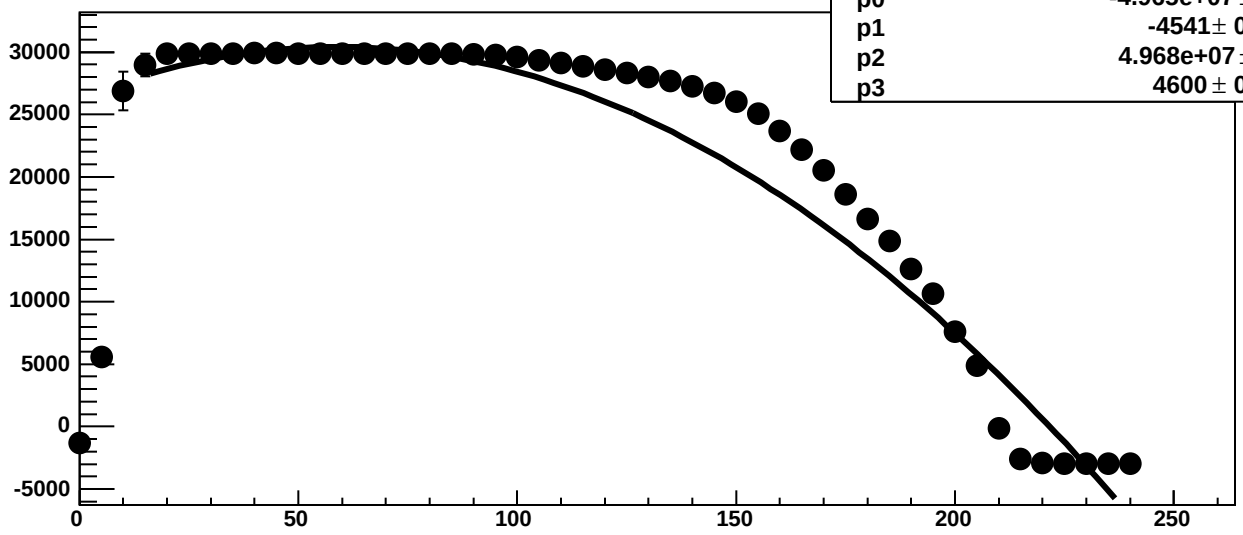
Chip 0, Channel 7, Enable 4, DAC=1600, ADC Noise vs Hold



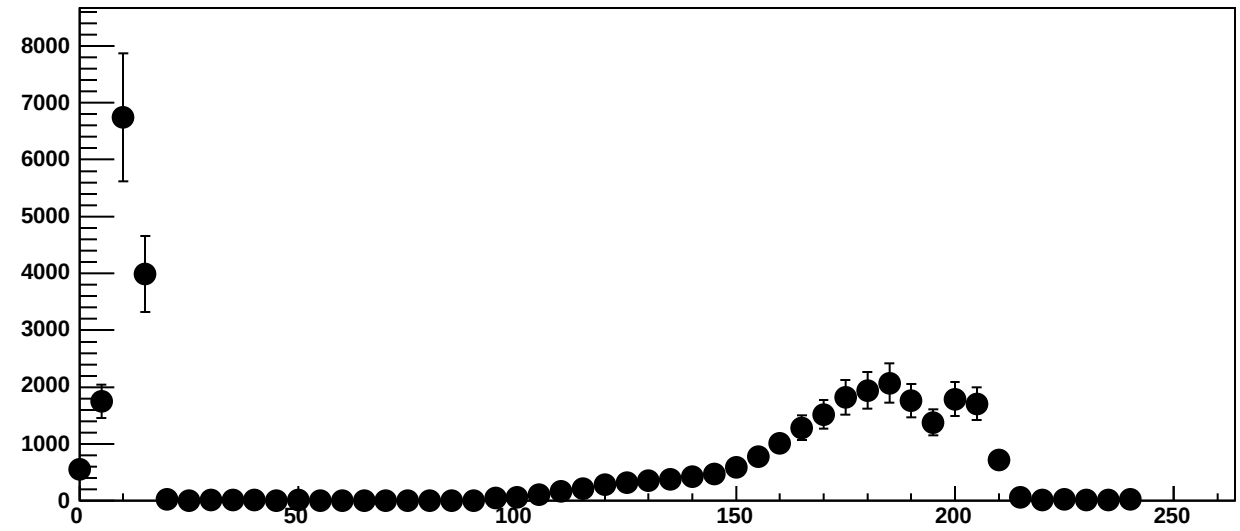
Chip 0, Channel 7, Enable 4, DAC=1600, ADC Residuals vs Hold



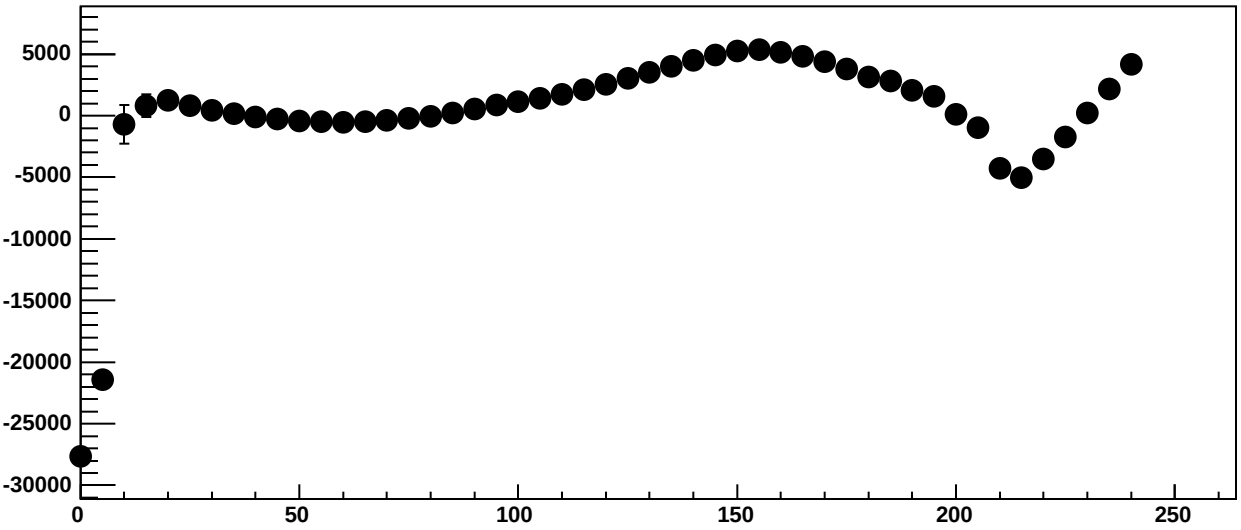
Chip 0, Channel 7, Enable 5!, DAC=1600, ADC Mean vs Hold

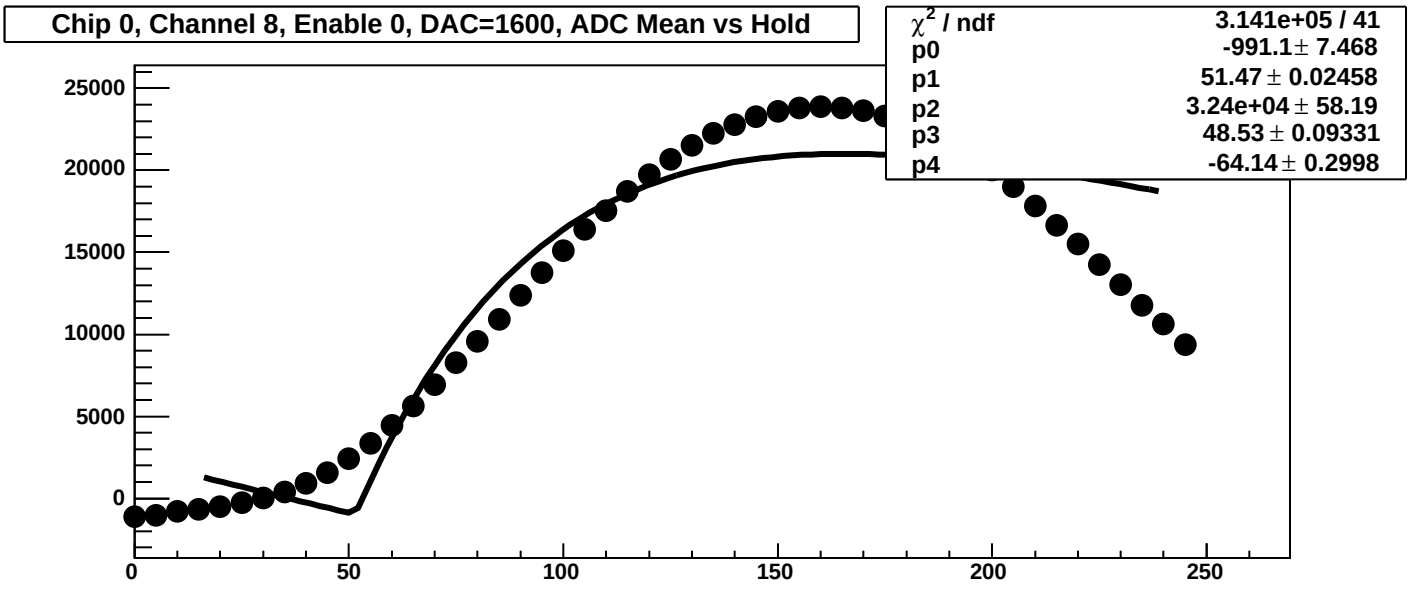


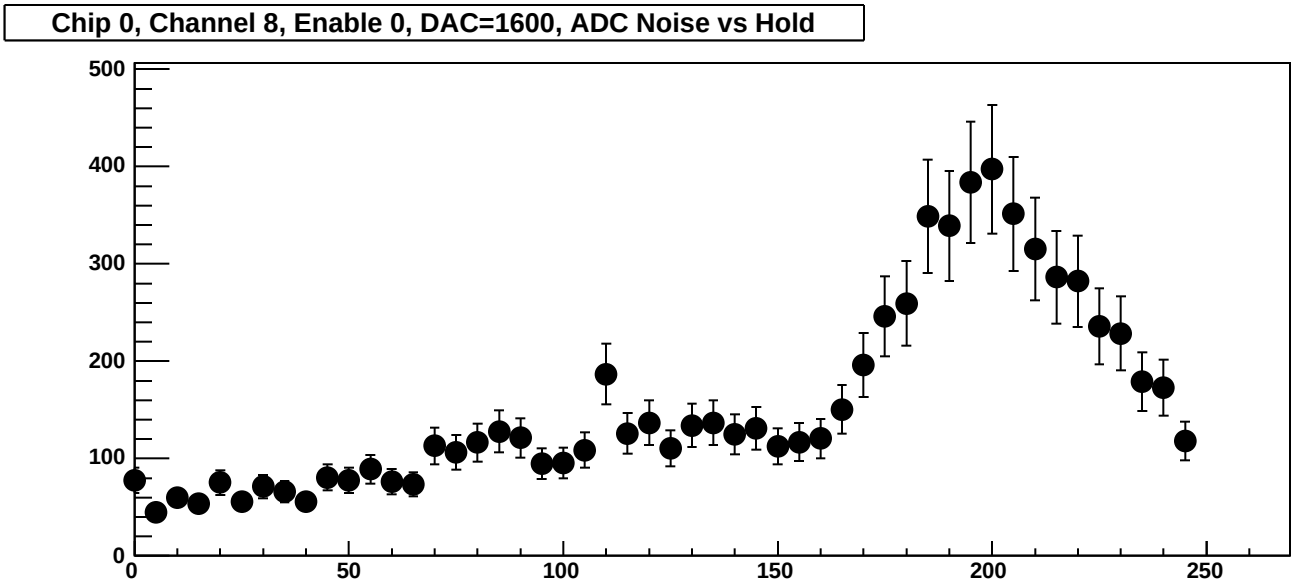
Chip 0, Channel 7, Enable 5!, DAC=1600, ADC Noise vs Hold

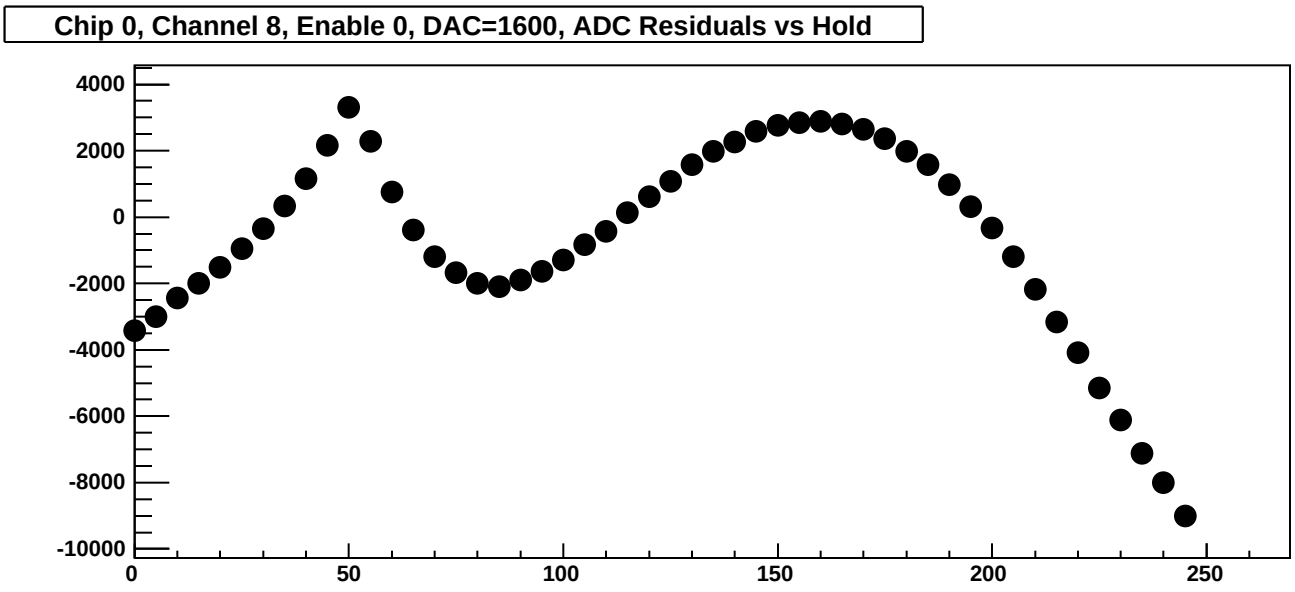


Chip 0, Channel 7, Enable 5!, DAC=1600, ADC Residuals vs Hold

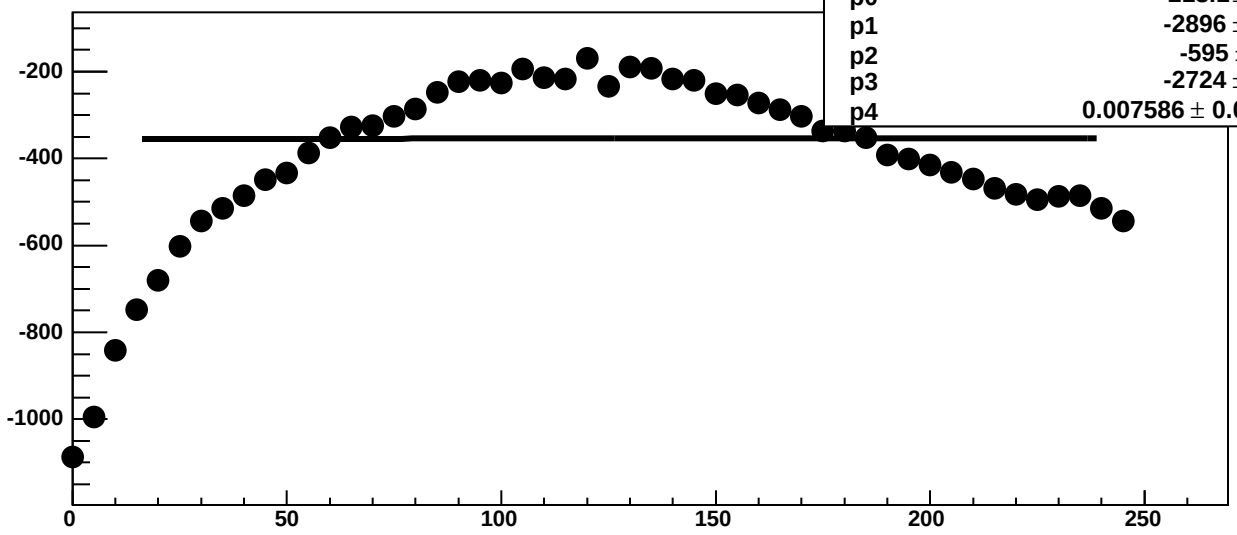



Chip 0, Channel 8, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.141e+05 / 41
p0 -991.1 ± 7.468
p1 51.47 ± 0.02458
p2 3.24e+04 ± 58.19
p3 48.53 ± 0.09331
p4 -64.14 ± 0.2998
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

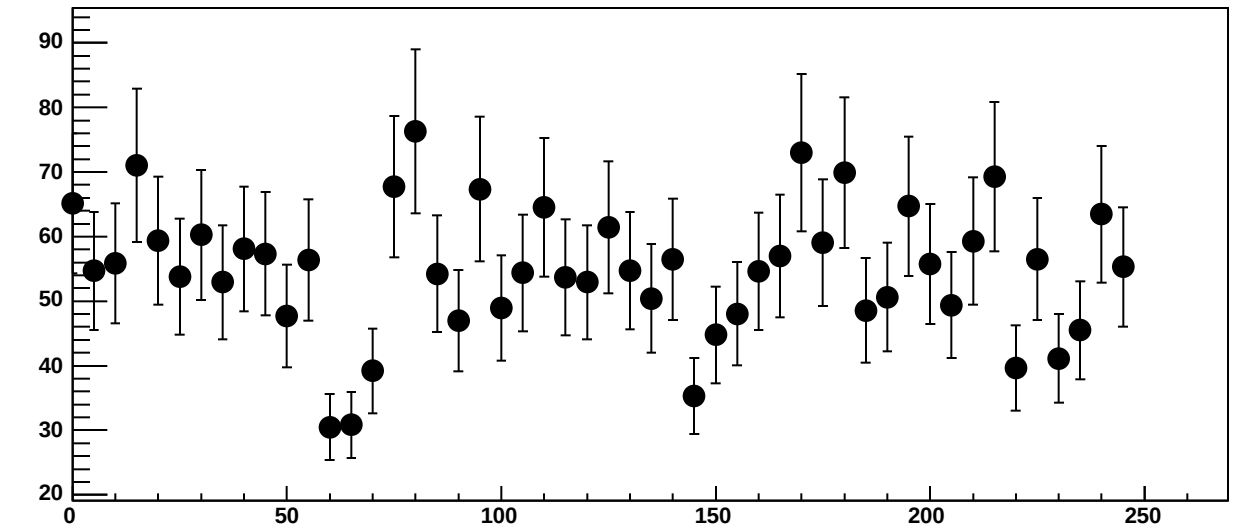

Chip 0, Channel 8, Enable 0, DAC=1600, ADC Noise vs Hold
500
400
300
200
100
0
0
50
100
150
200
250


Chip 0, Channel 8, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

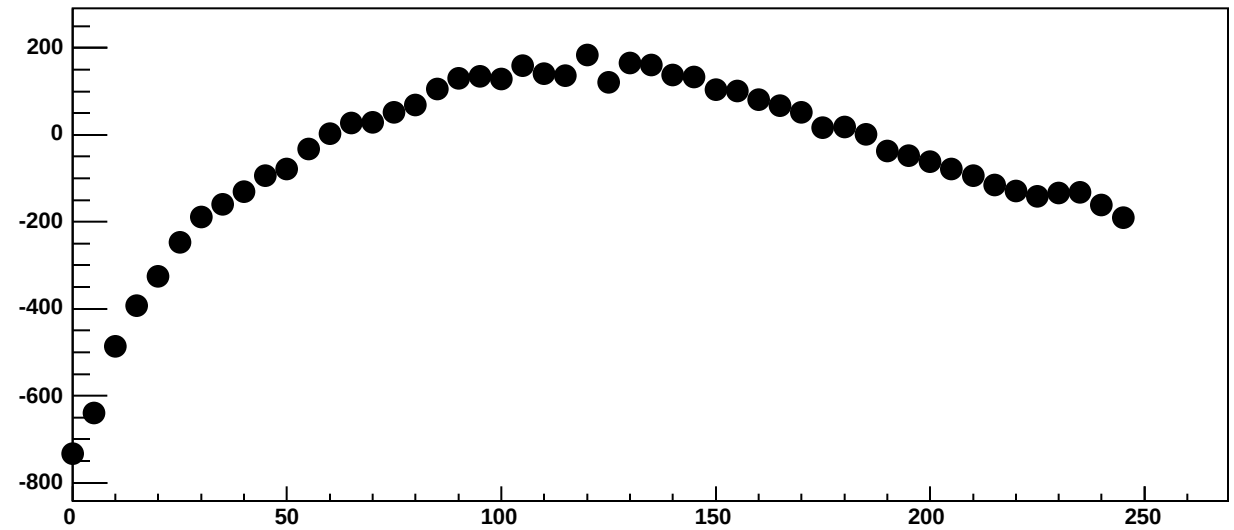
Chip 0, Channel 8, Enable 1, DAC=1600, ADC Mean vs Hold

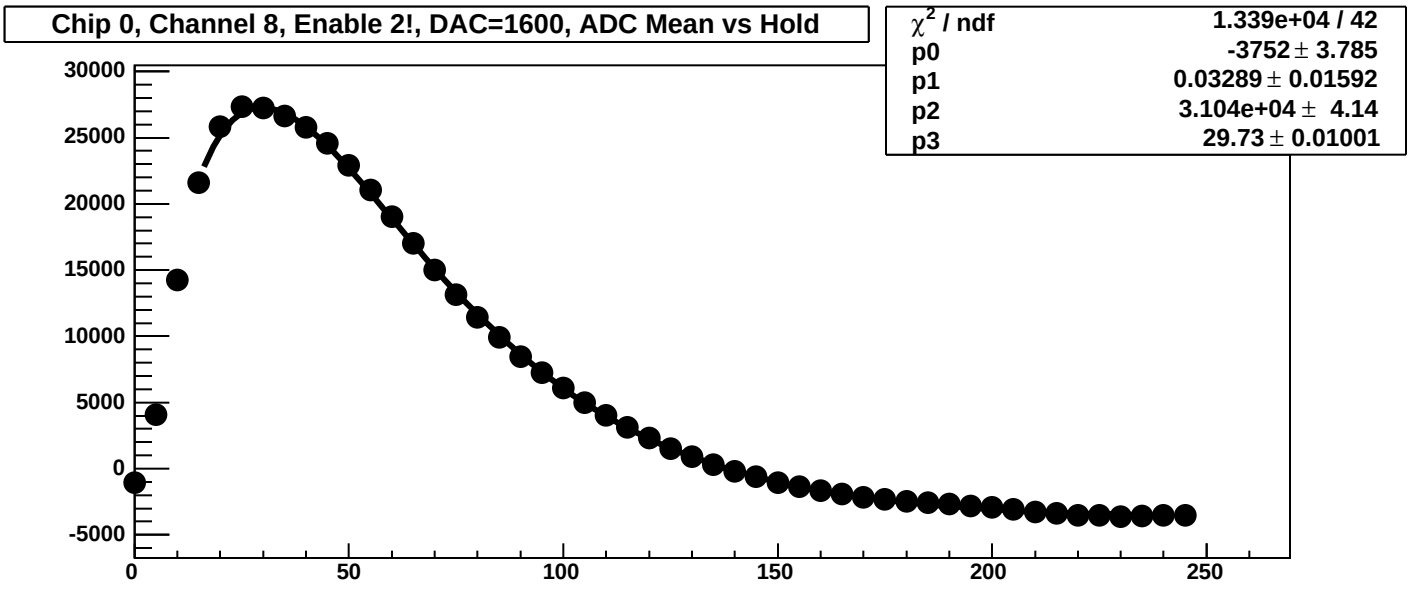


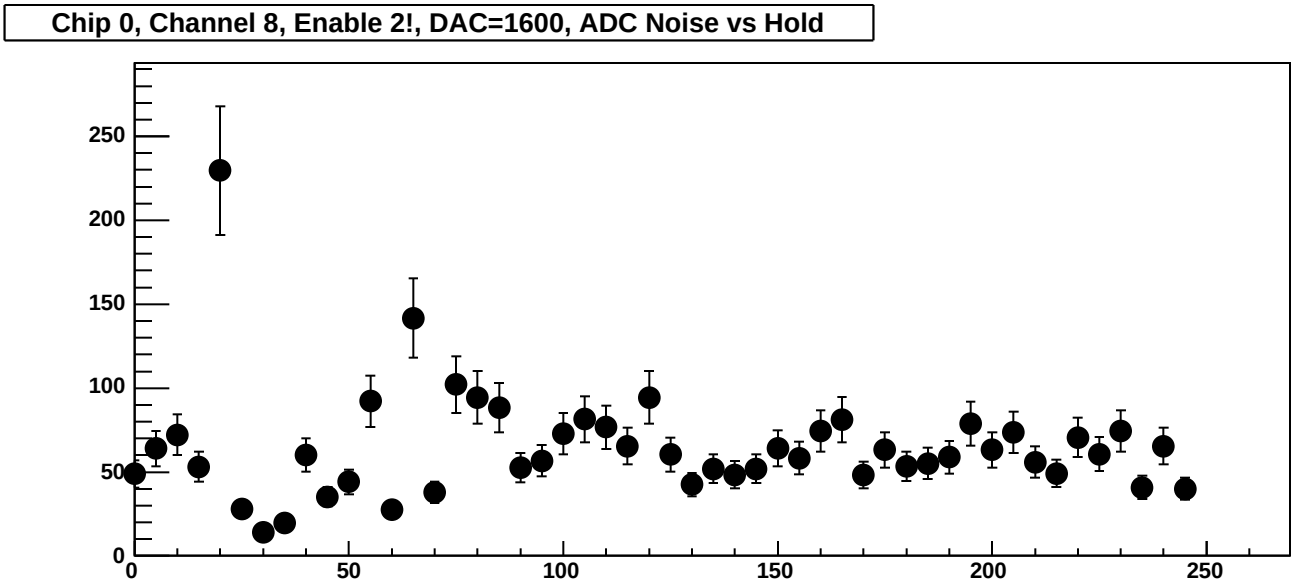
Chip 0, Channel 8, Enable 1, DAC=1600, ADC Noise vs Hold

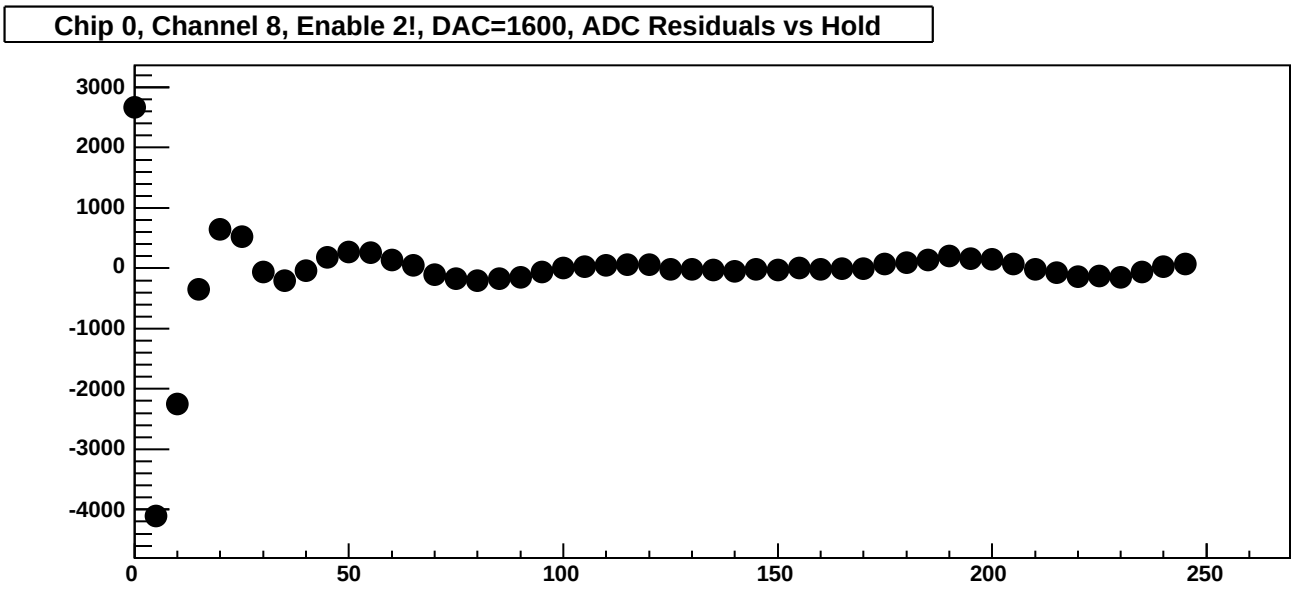


Chip 0, Channel 8, Enable 1, DAC=1600, ADC Residuals vs Hold

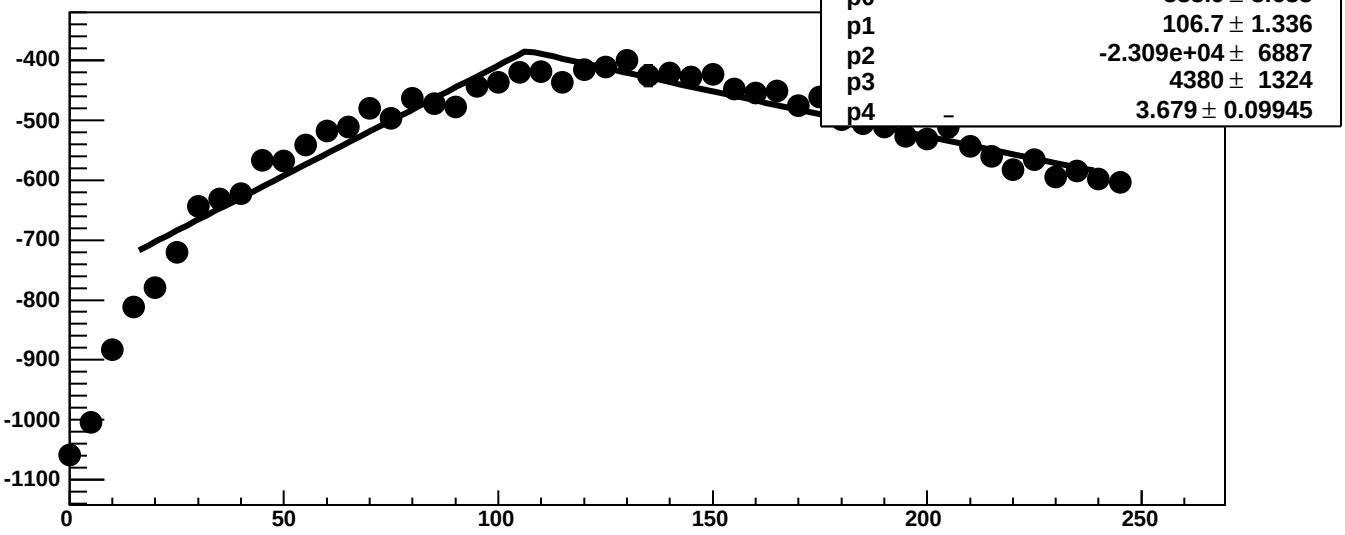



Chip 0, Channel 8, Enable 2!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.339e+04 / 42
p0 -3752 ± 3.785
p1 0.03289 ± 0.01592
p2 3.104e+04 ± 4.14
p3 29.73 ± 0.01001
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

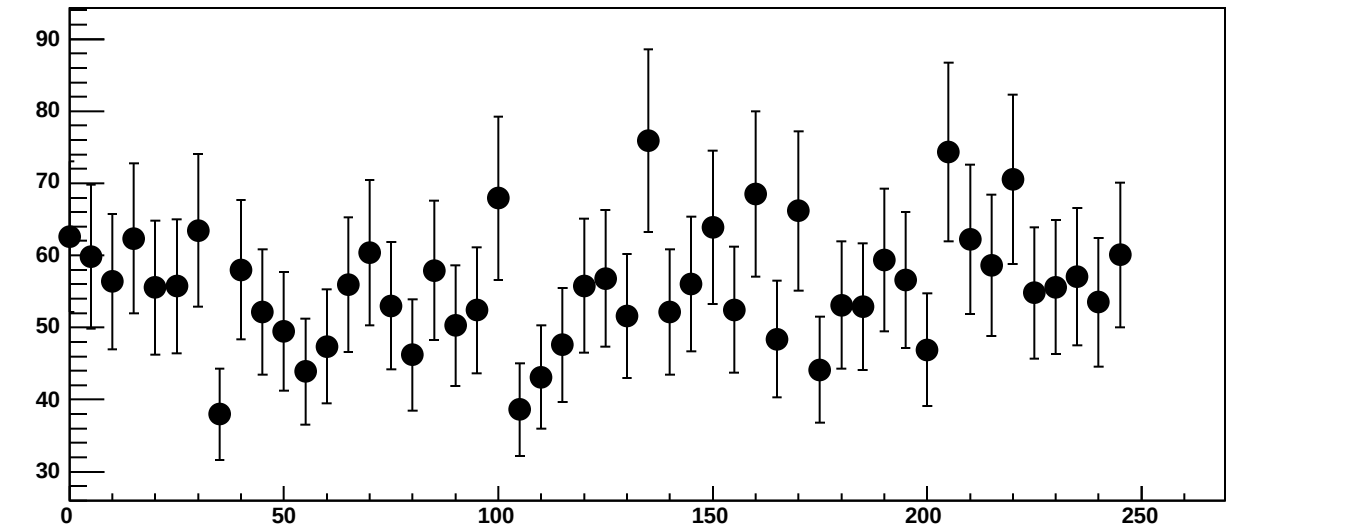

Chip 0, Channel 8, Enable 2!, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250


Chip 0, Channel 8, Enable 2!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

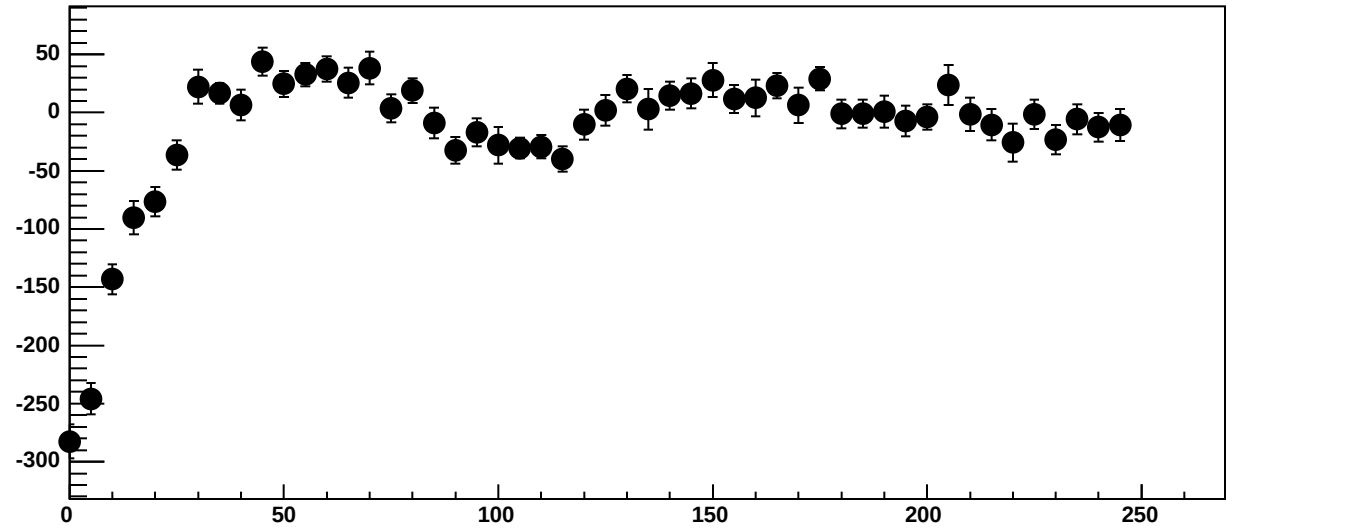
Chip 0, Channel 8, Enable 3, DAC=1600, ADC Mean vs Hold

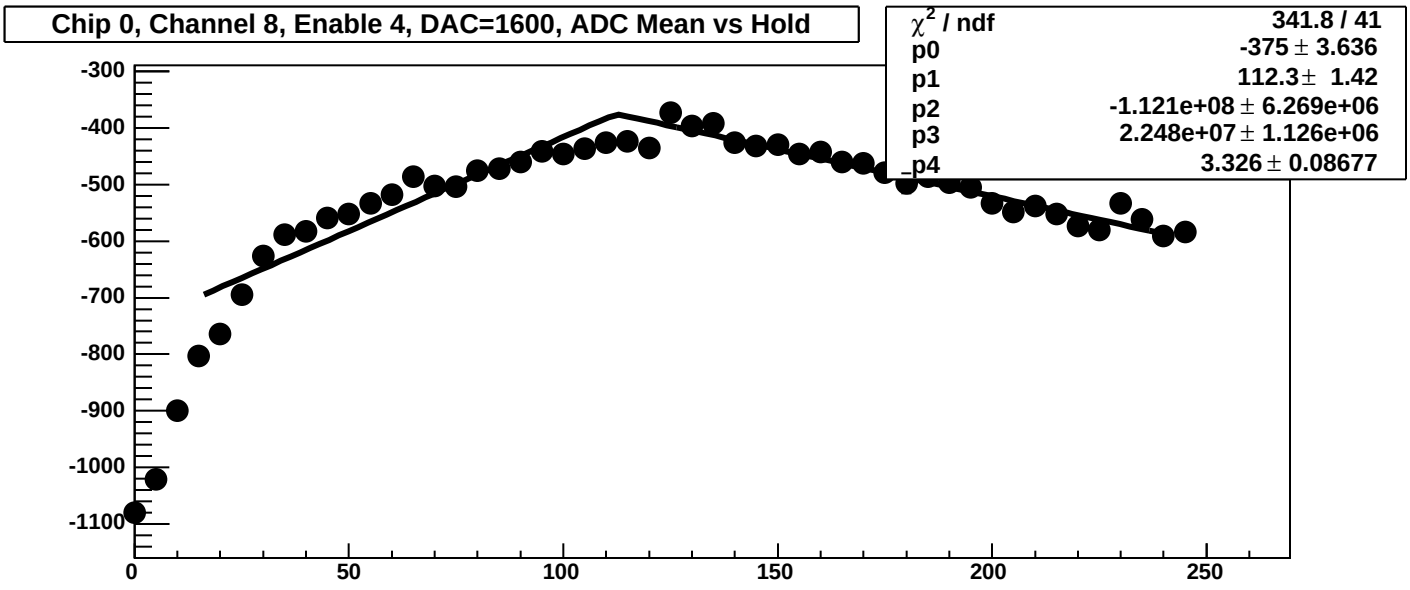


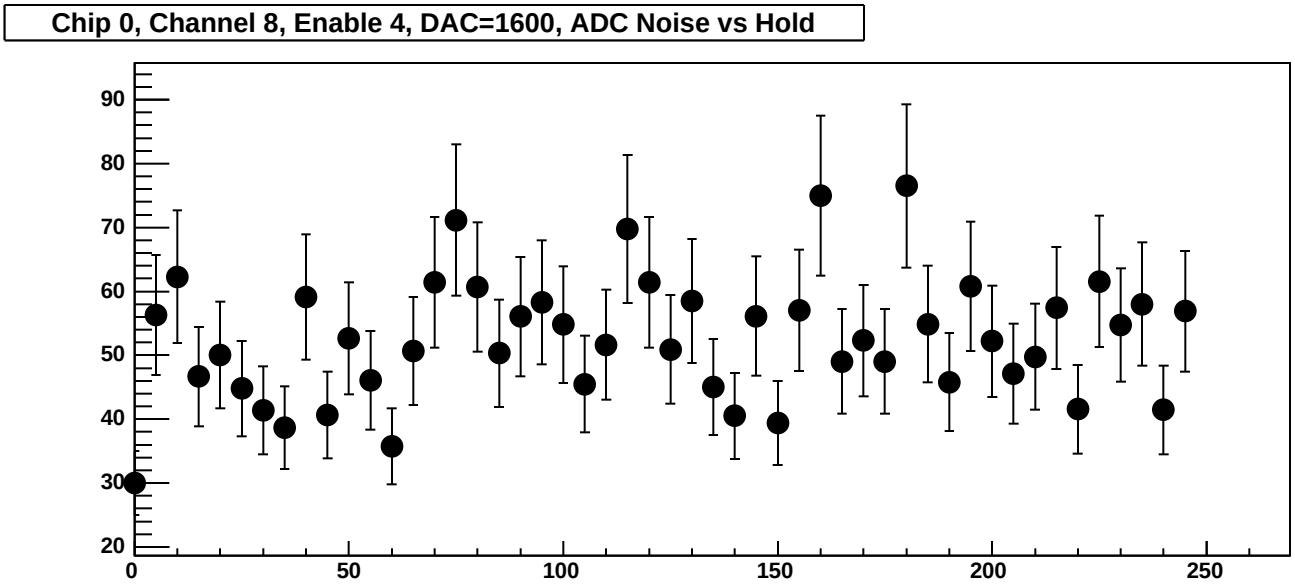
Chip 0, Channel 8, Enable 3, DAC=1600, ADC Noise vs Hold

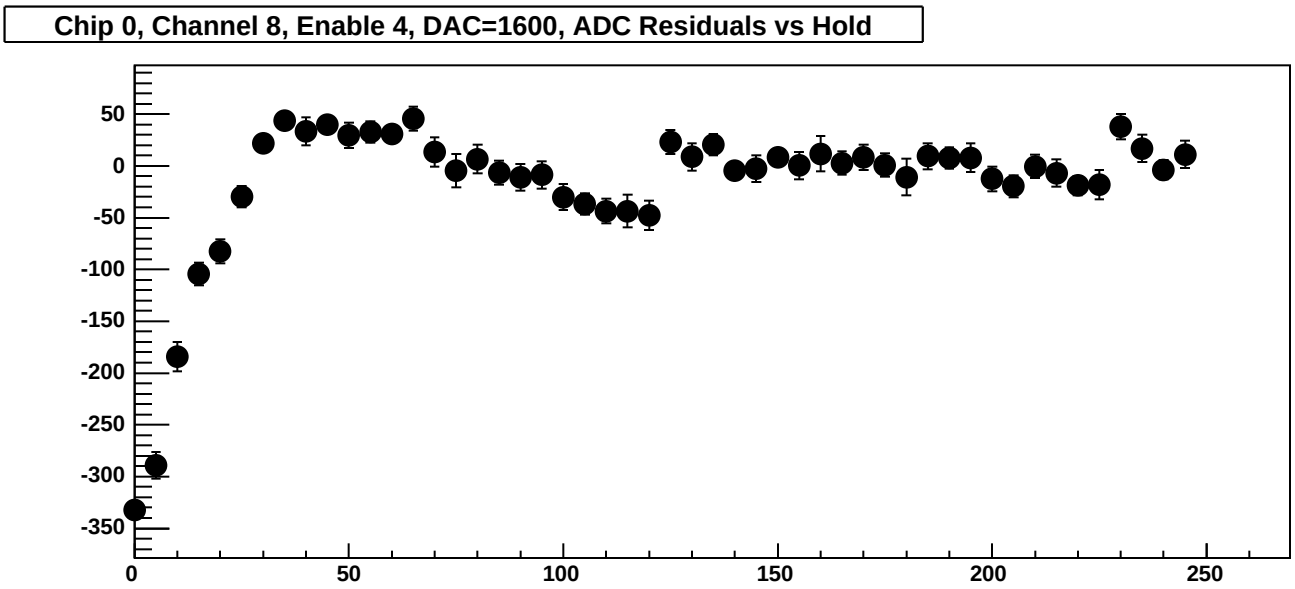


Chip 0, Channel 8, Enable 3, DAC=1600, ADC Residuals vs Hold

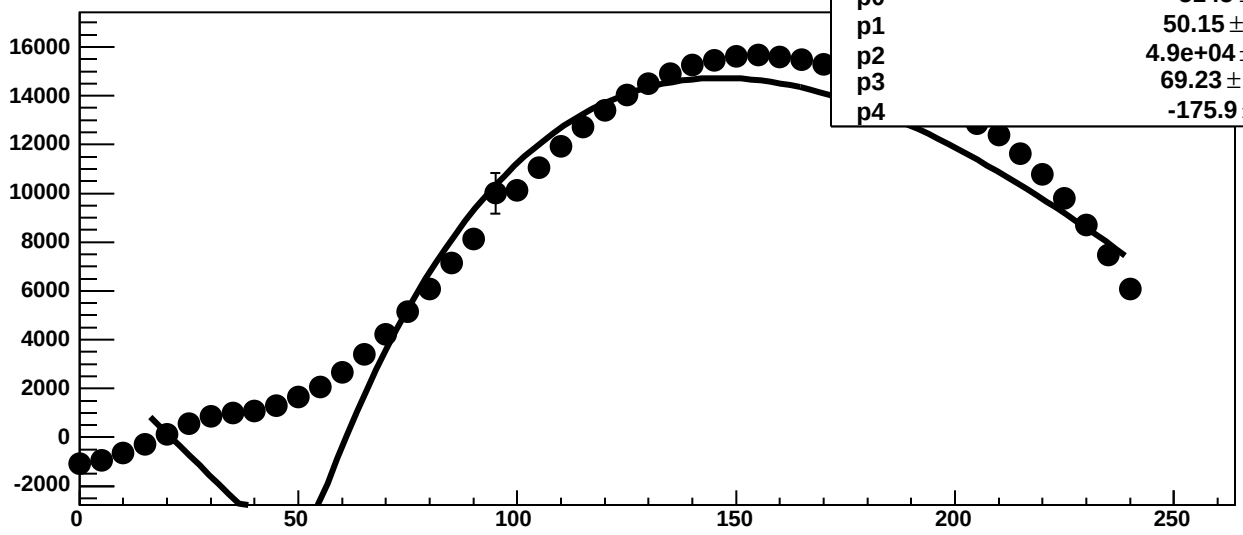



Chip 0, Channel 8, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 341.8 / 41
p0 -375 ± 3.636
p1 112.3 ± 1.42
p2 -1.121e+08 ± 6.269e+06
p3 2.248e+07 ± 1.126e+06
p4 3.326 ± 0.08677
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
50
100
150
200
250

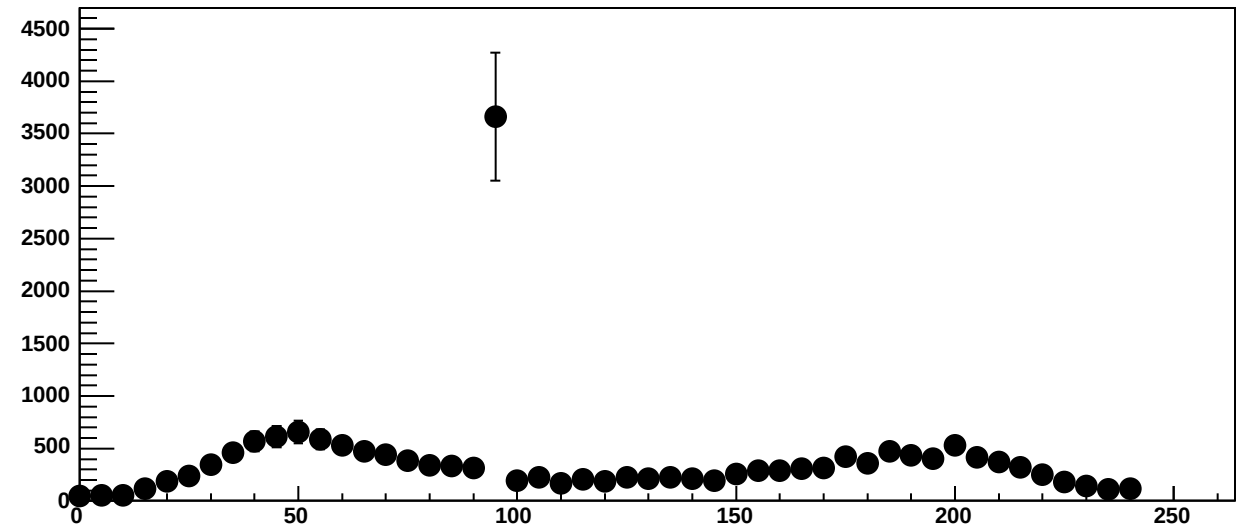

Chip 0, Channel 8, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 0, Channel 8, Enable 4, DAC=1600, ADC Residuals vs Hold
50
0
-50
-100
-150
-200
-250
-300
-350
0
50
100
150
200
250

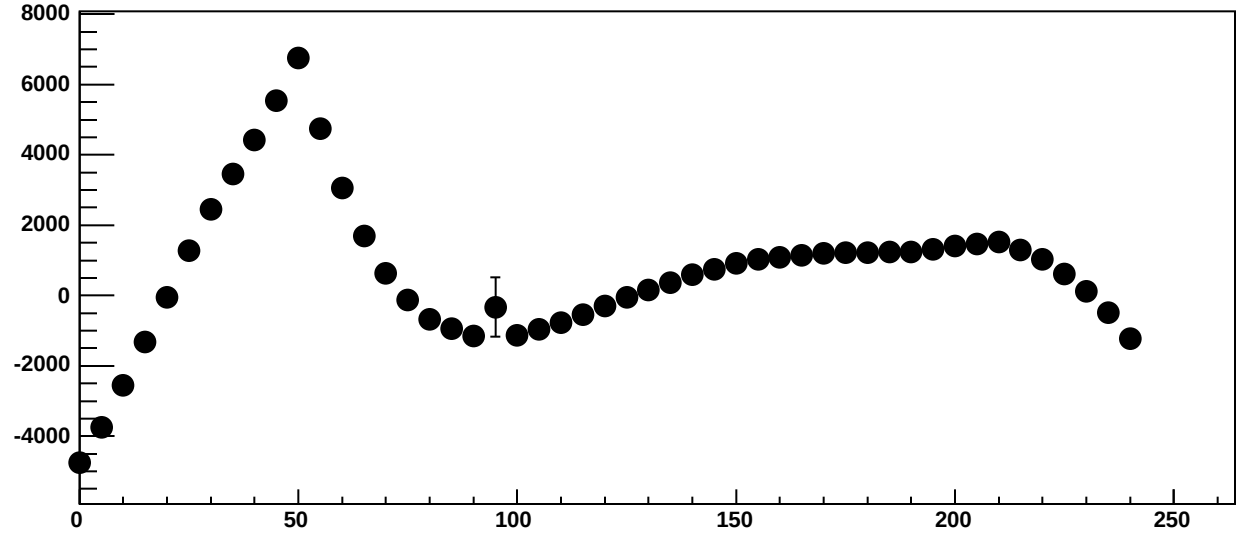
Chip 0, Channel 8, Enable 5, DAC=1600, ADC Mean vs Hold

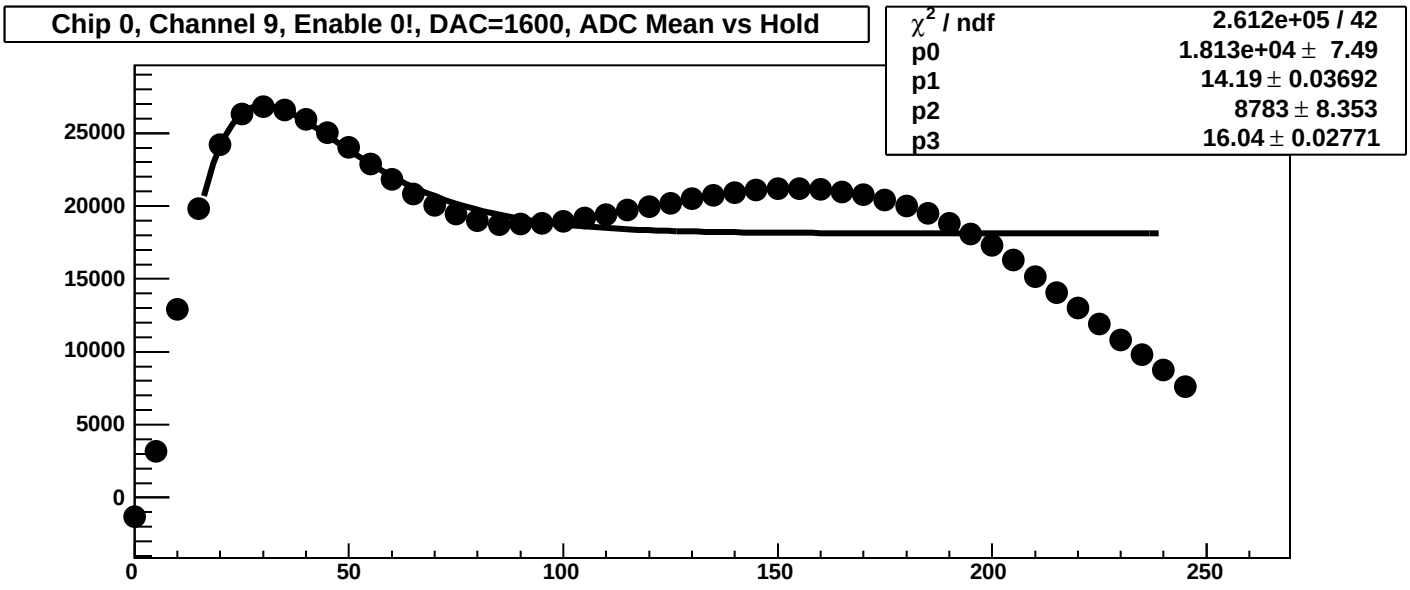


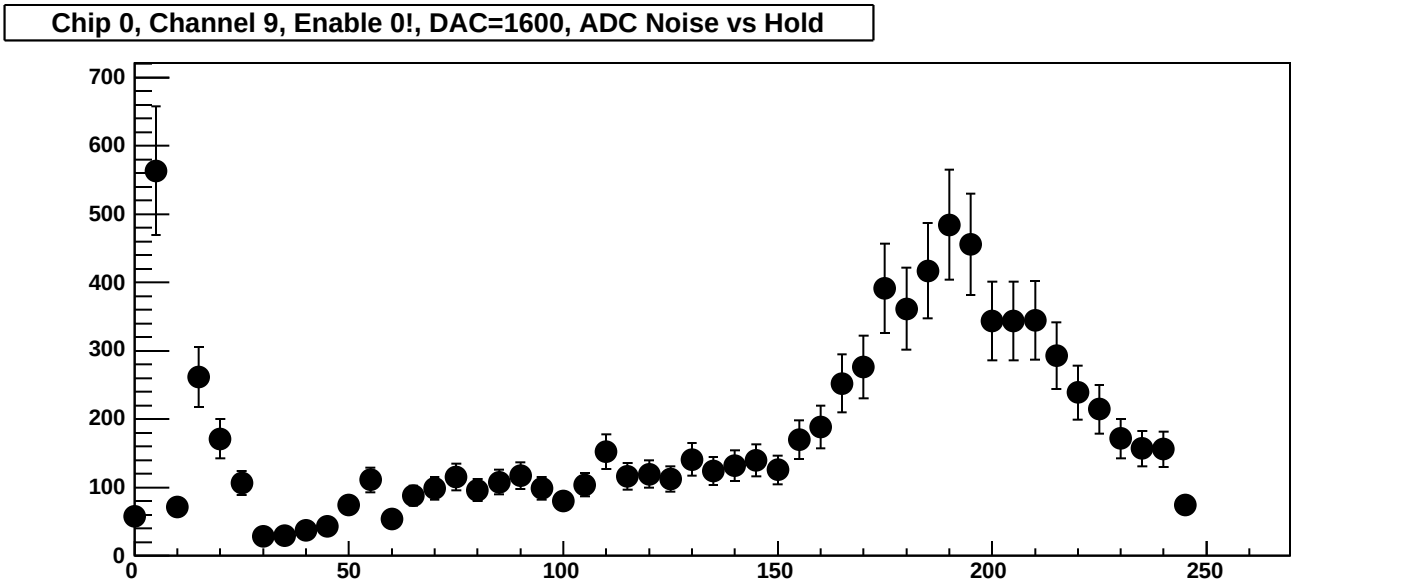
Chip 0, Channel 8, Enable 5, DAC=1600, ADC Noise vs Hold

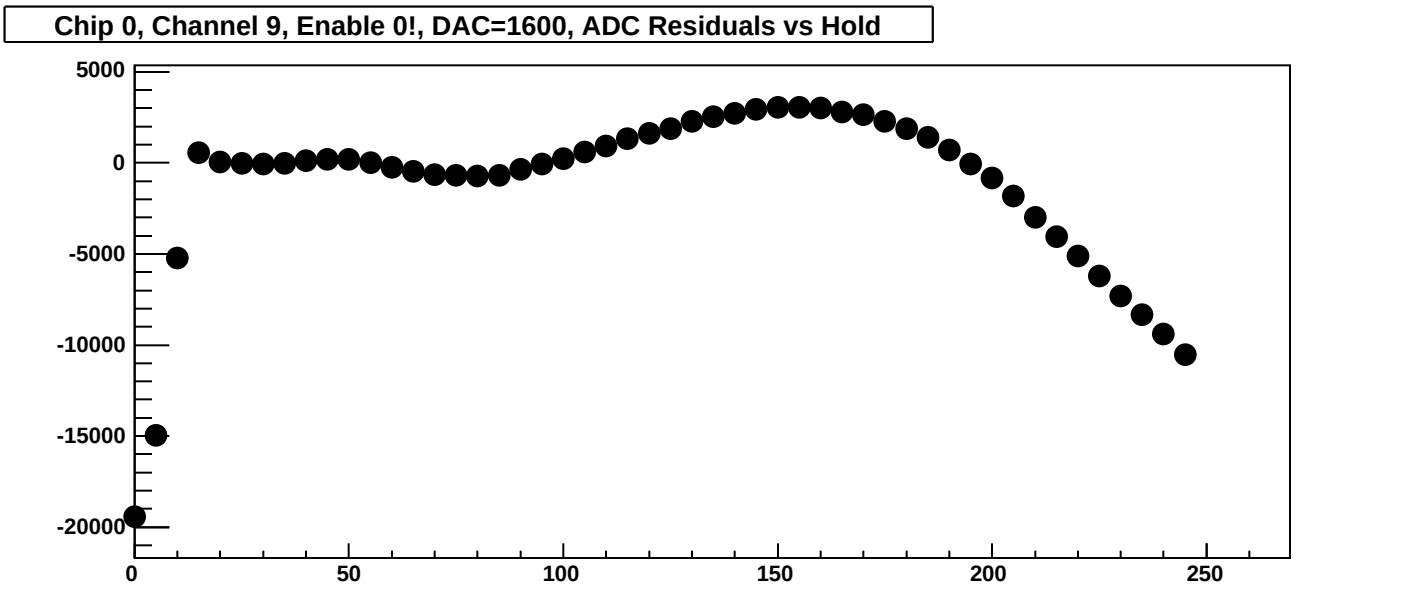


Chip 0, Channel 8, Enable 5, DAC=1600, ADC Residuals vs Hold

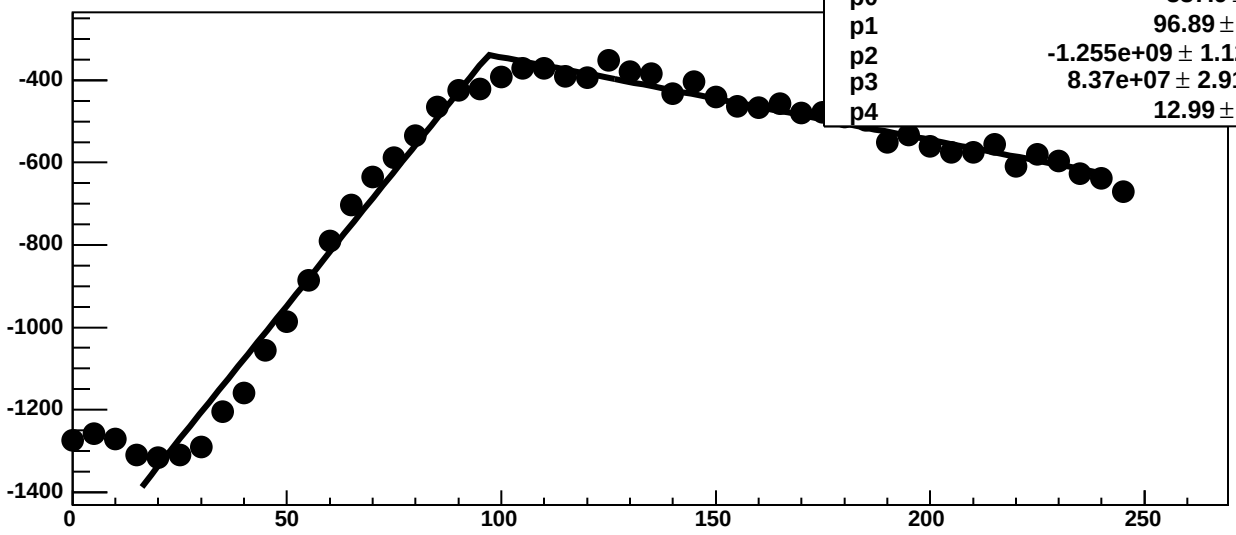



Chip 0, Channel 9, Enable 0!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.612e+05 / 42
p0 1.813e+04 ± 7.49
p1 14.19 ± 0.03692
p2 8783 ± 8.353
p3 16.04 ± 0.02771
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

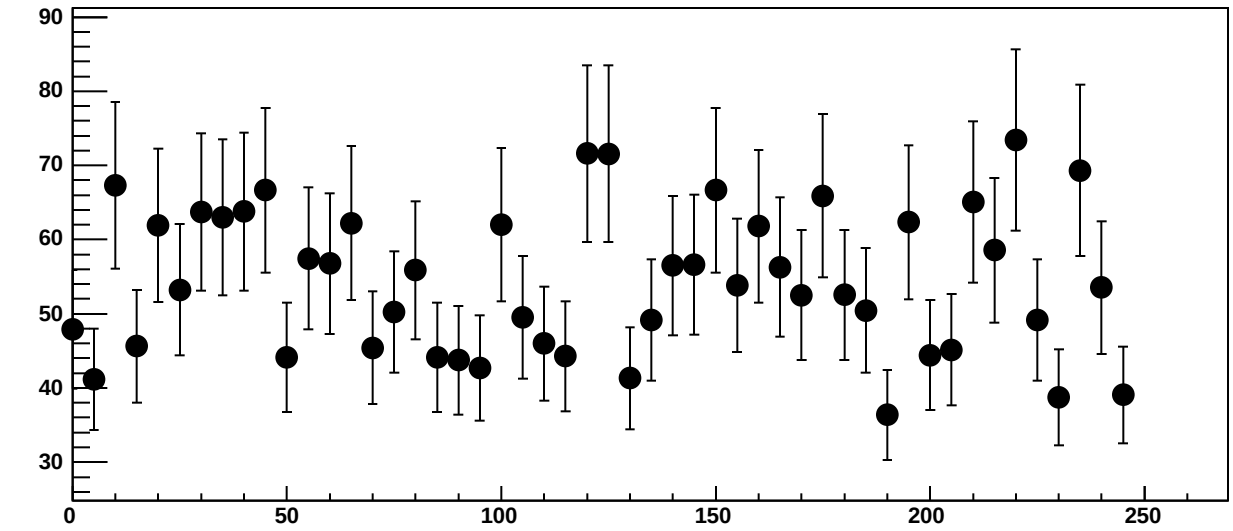

Chip 0, Channel 9, Enable 0!, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 0, Channel 9, Enable 0!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

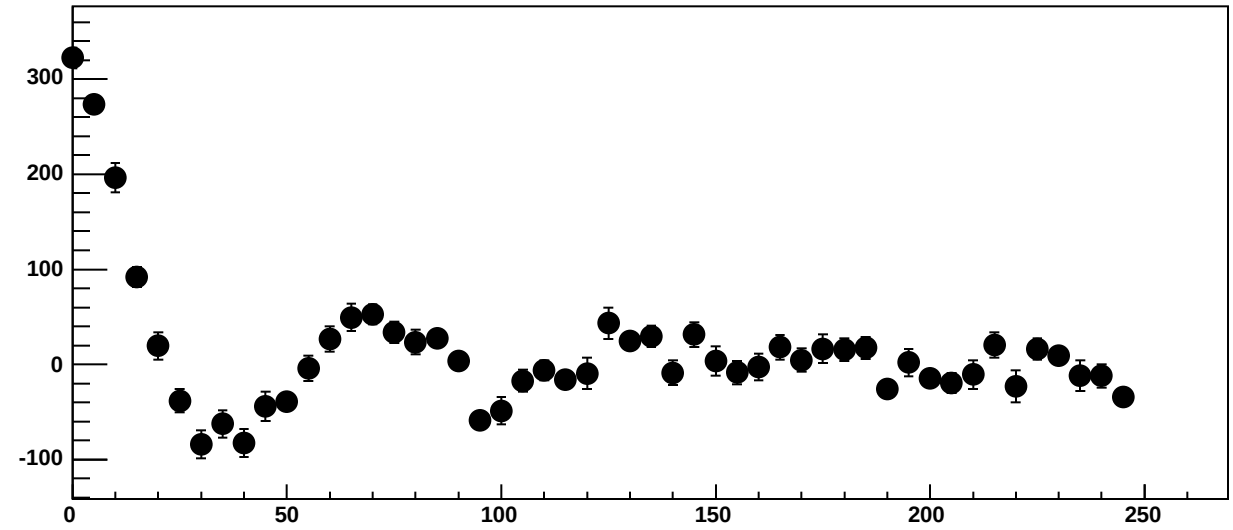
Chip 0, Channel 9, Enable 1, DAC=1600, ADC Mean vs Hold



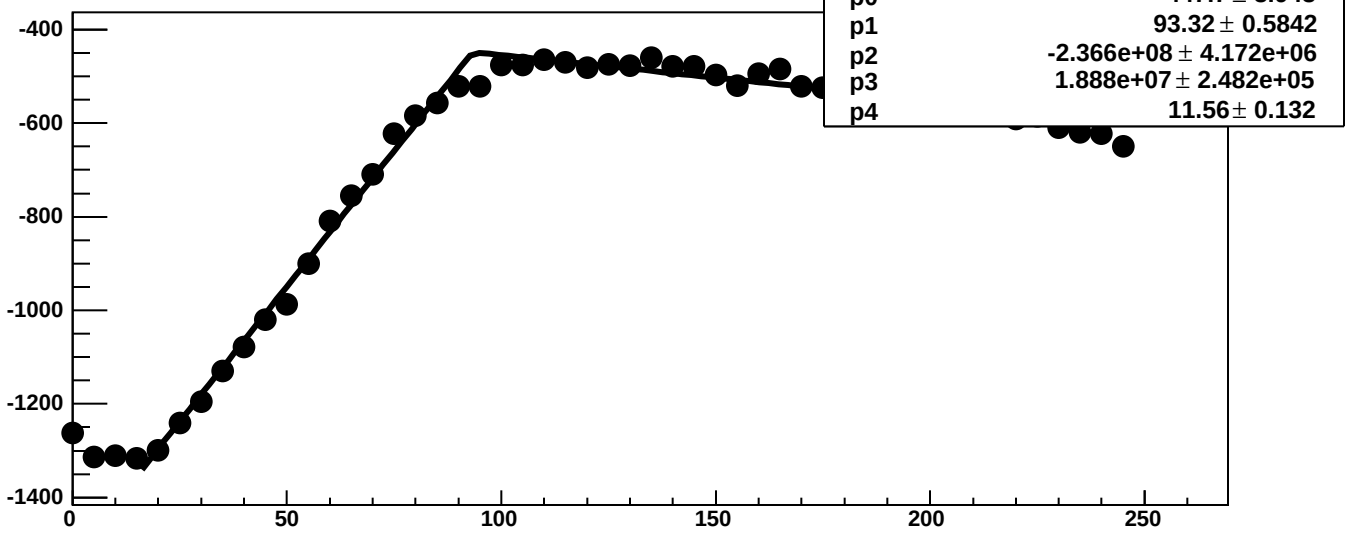
Chip 0, Channel 9, Enable 1, DAC=1600, ADC Noise vs Hold



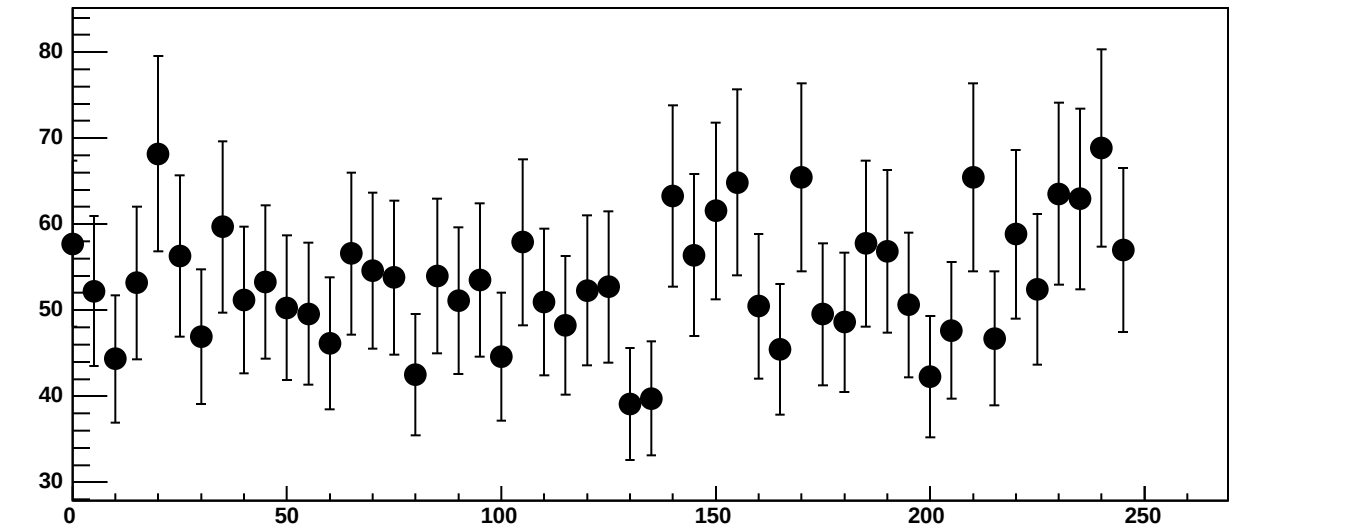
Chip 0, Channel 9, Enable 1, DAC=1600, ADC Residuals vs Hold



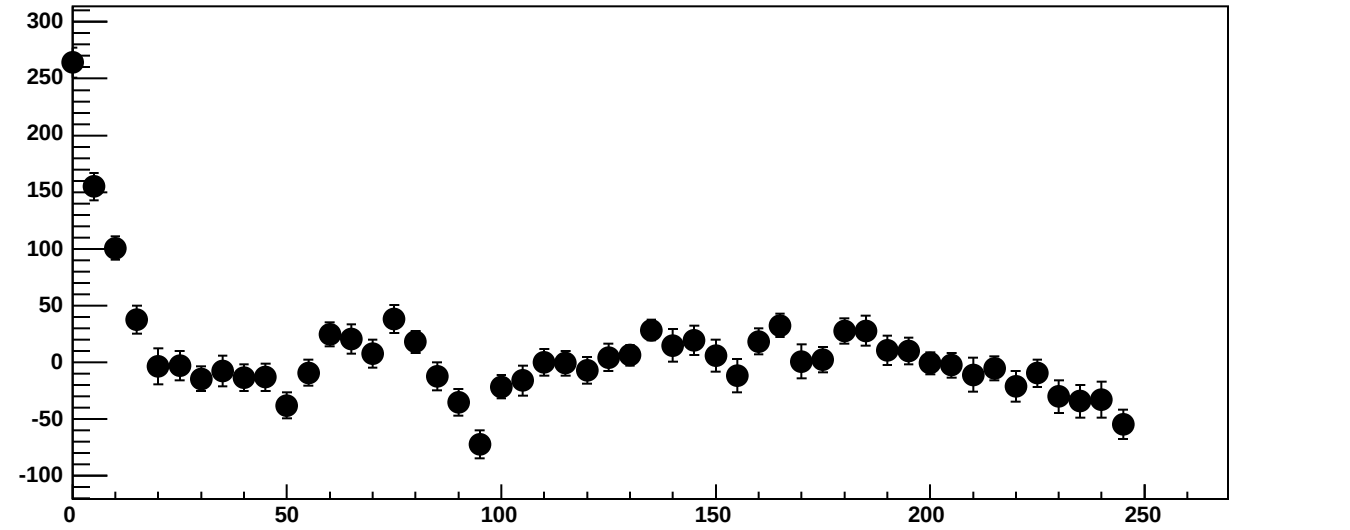
Chip 0, Channel 9, Enable 2, DAC=1600, ADC Mean vs Hold

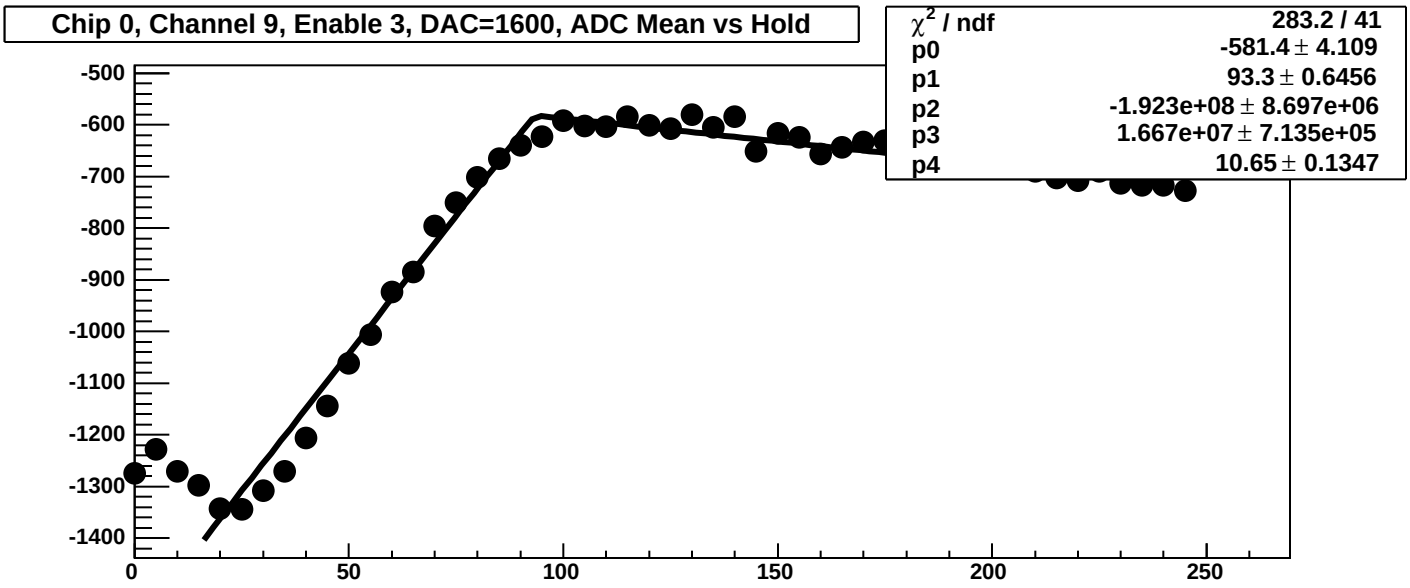


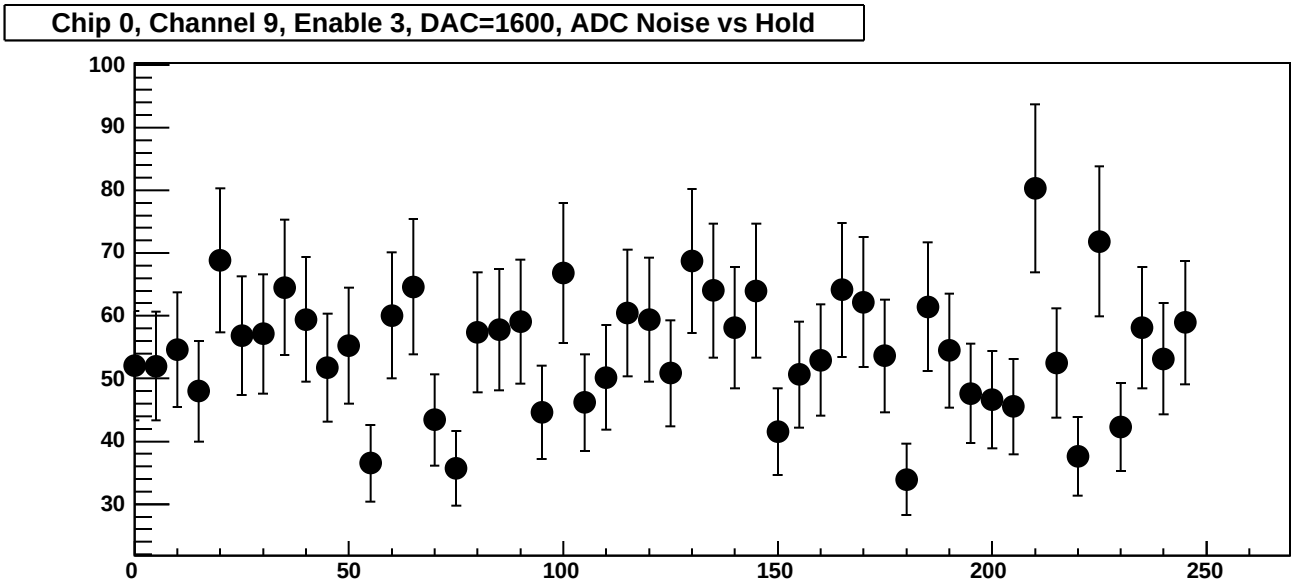
Chip 0, Channel 9, Enable 2, DAC=1600, ADC Noise vs Hold

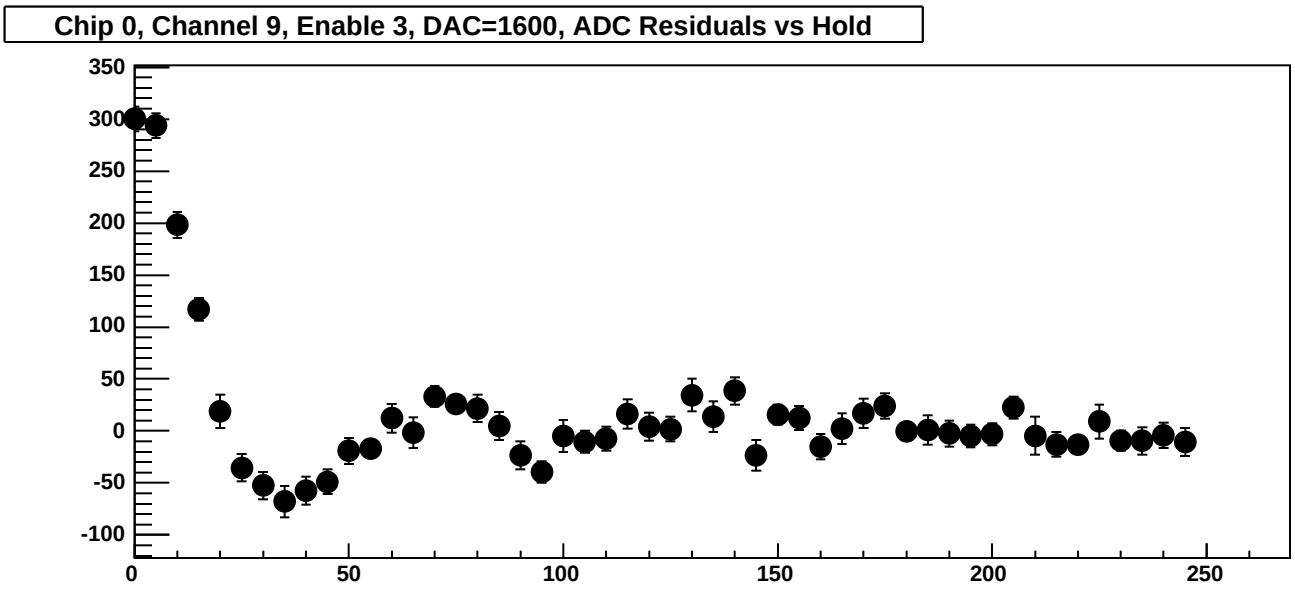


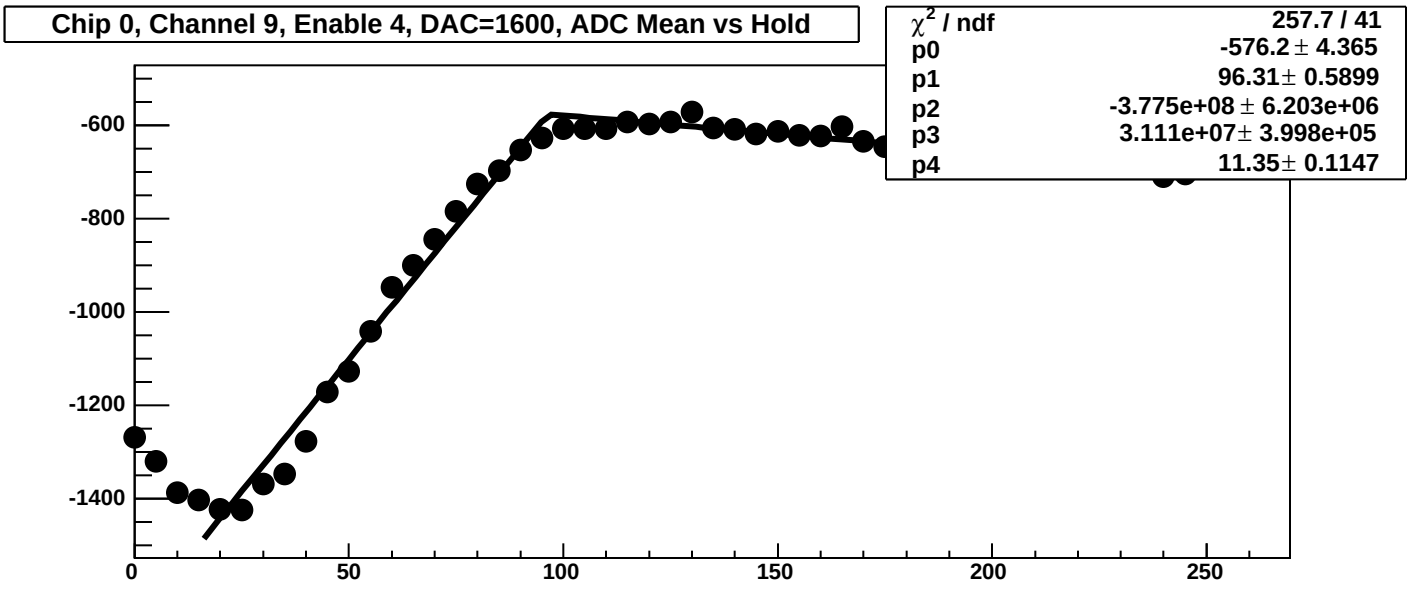
Chip 0, Channel 9, Enable 2, DAC=1600, ADC Residuals vs Hold

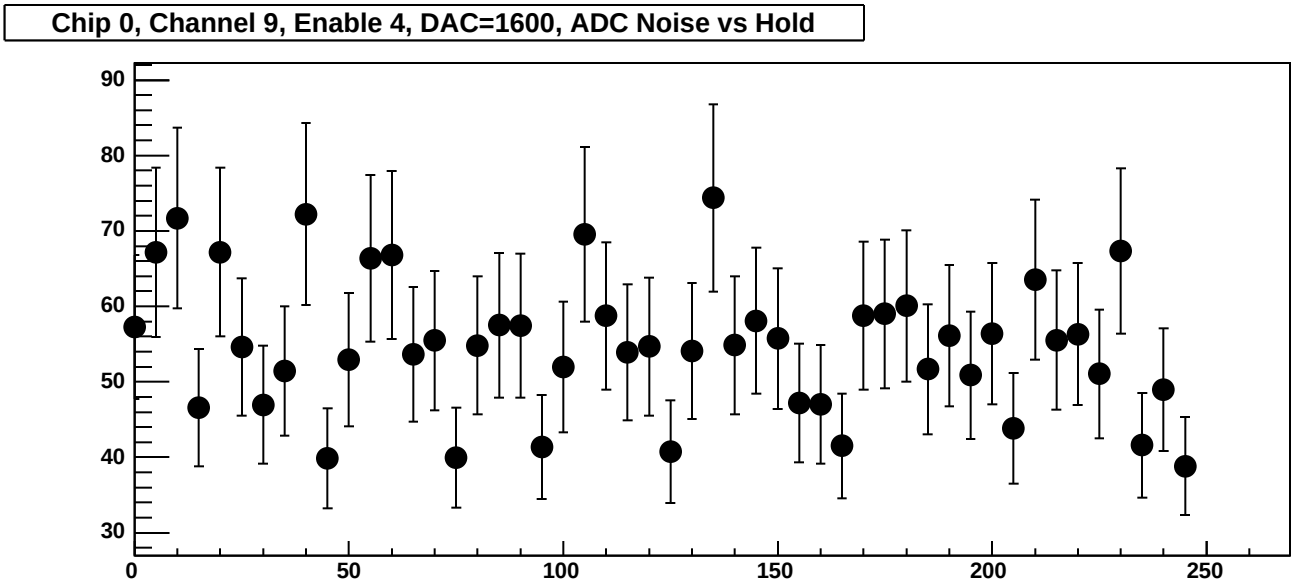


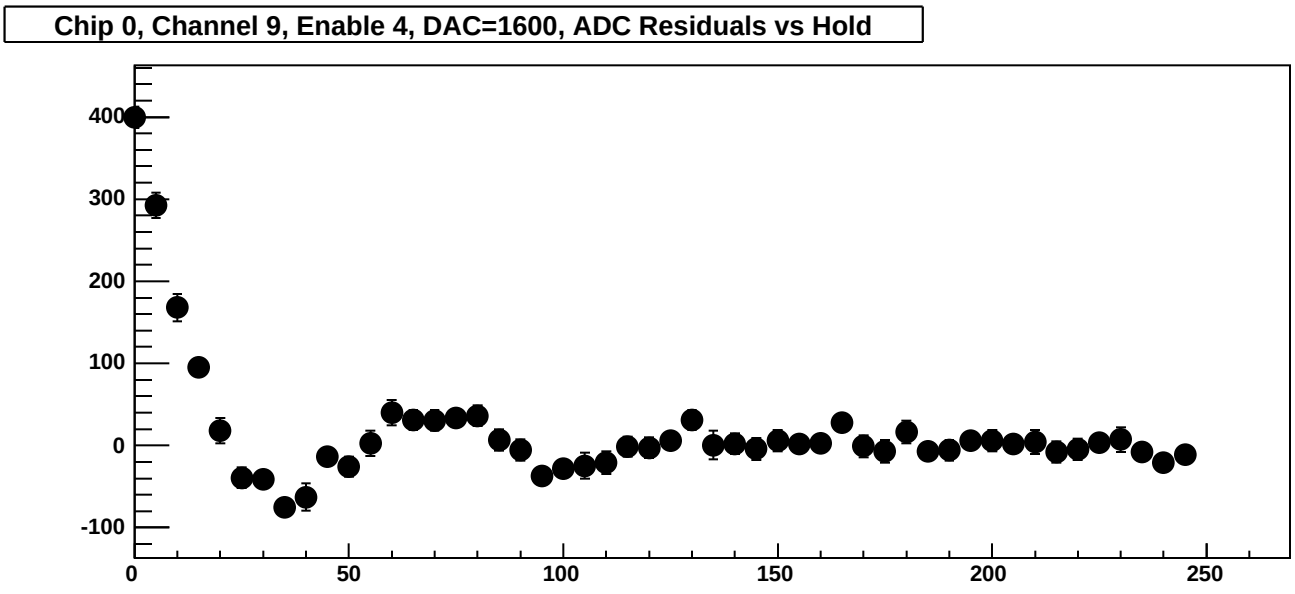

Chip 0, Channel 9, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 283.2 / 41
p0 -581.4 ± 4.109
p1 93.3 ± 0.6456
p2 -1.923e+08 ± 8.697e+06
p3 1.667e+07 ± 7.135e+05
p4 10.65 ± 0.1347
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
50
100
150
200
250


Chip 0, Channel 9, Enable 3, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250

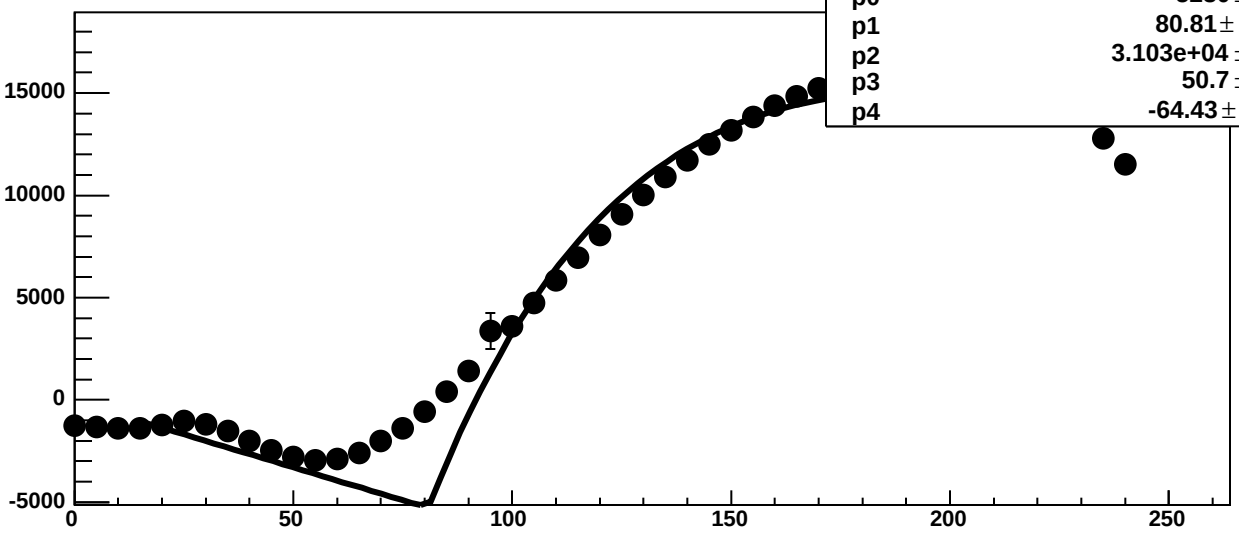

Chip 0, Channel 9, Enable 3, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250


Chip 0, Channel 9, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 257.7 / 41
p0 -576.2 ± 4.365
p1 96.31 ± 0.5899
p2 -3.775e+08 ± 6.203e+06
p3 3.111e+07 ± 3.998e+05
p4 11.35 ± 0.1147
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

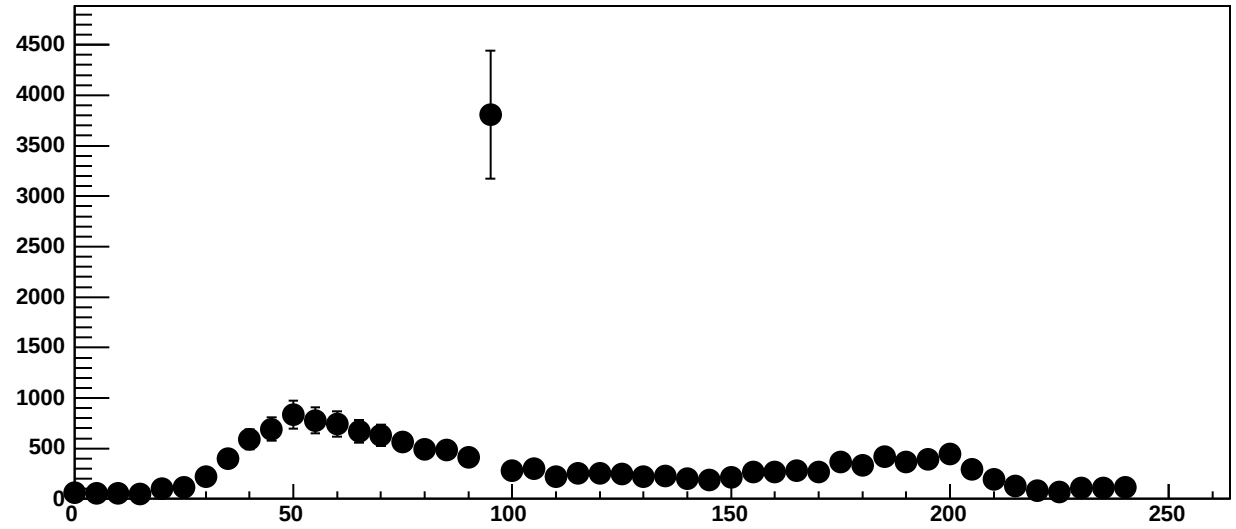

Chip 0, Channel 9, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 0, Channel 9, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

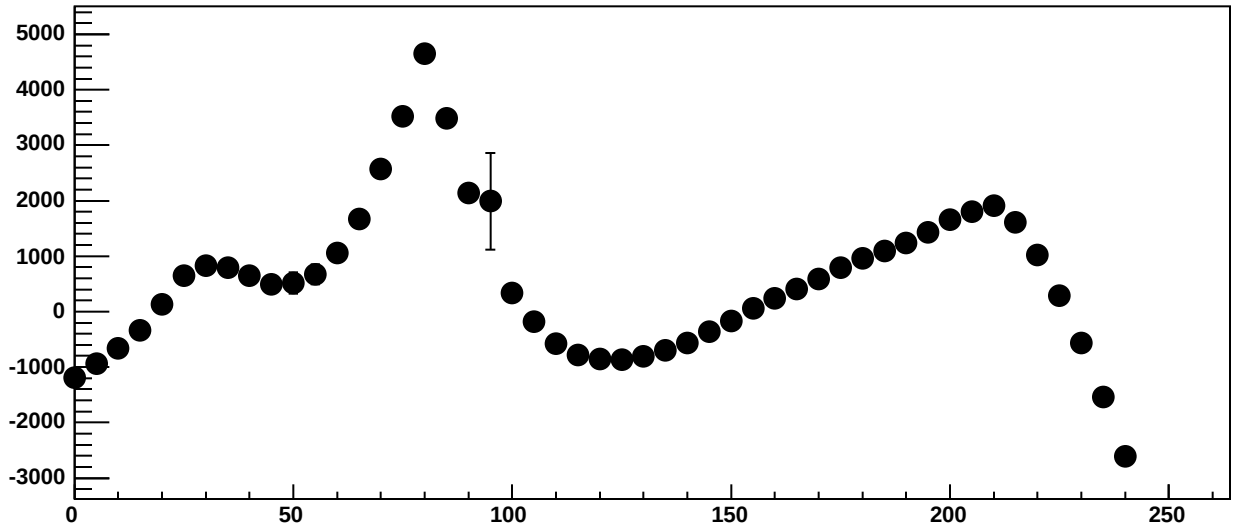
Chip 0, Channel 9, Enable 5, DAC=1600, ADC Mean vs Hold

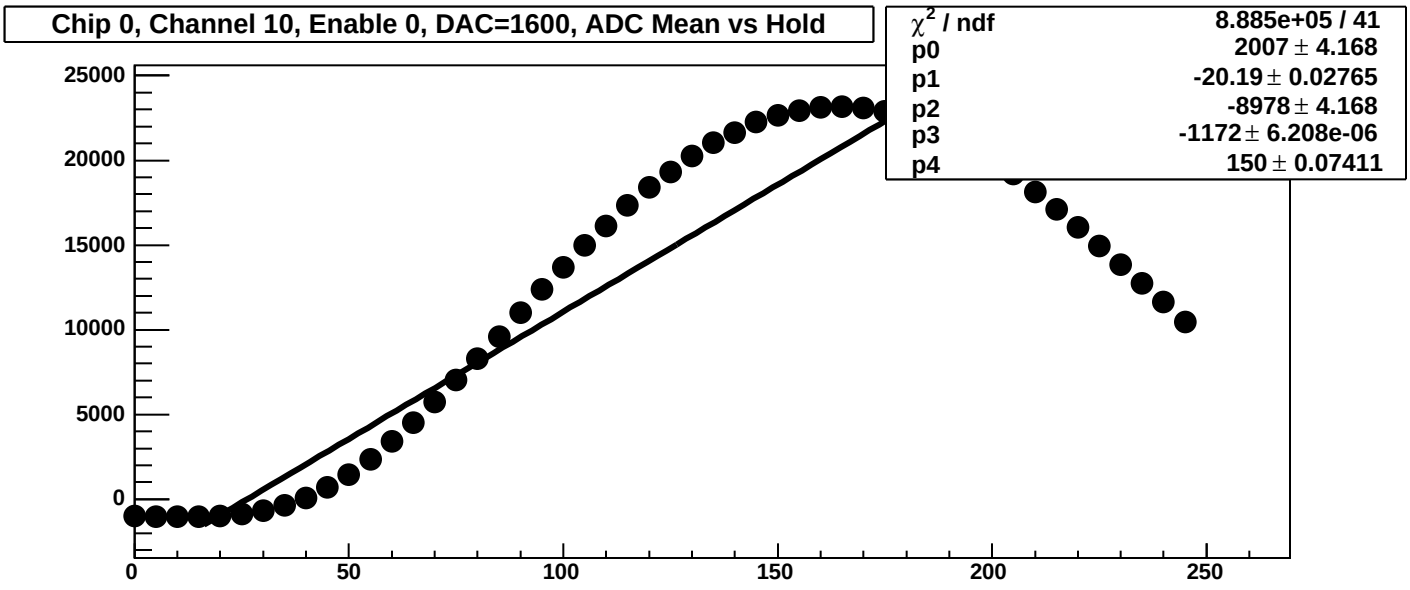


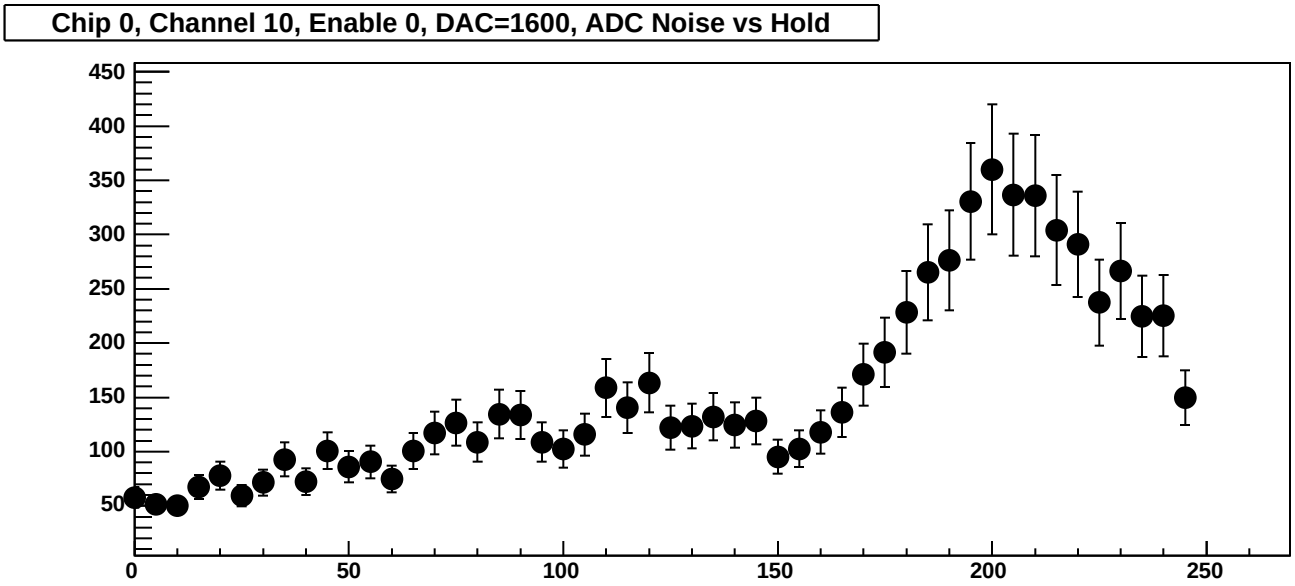
Chip 0, Channel 9, Enable 5, DAC=1600, ADC Noise vs Hold

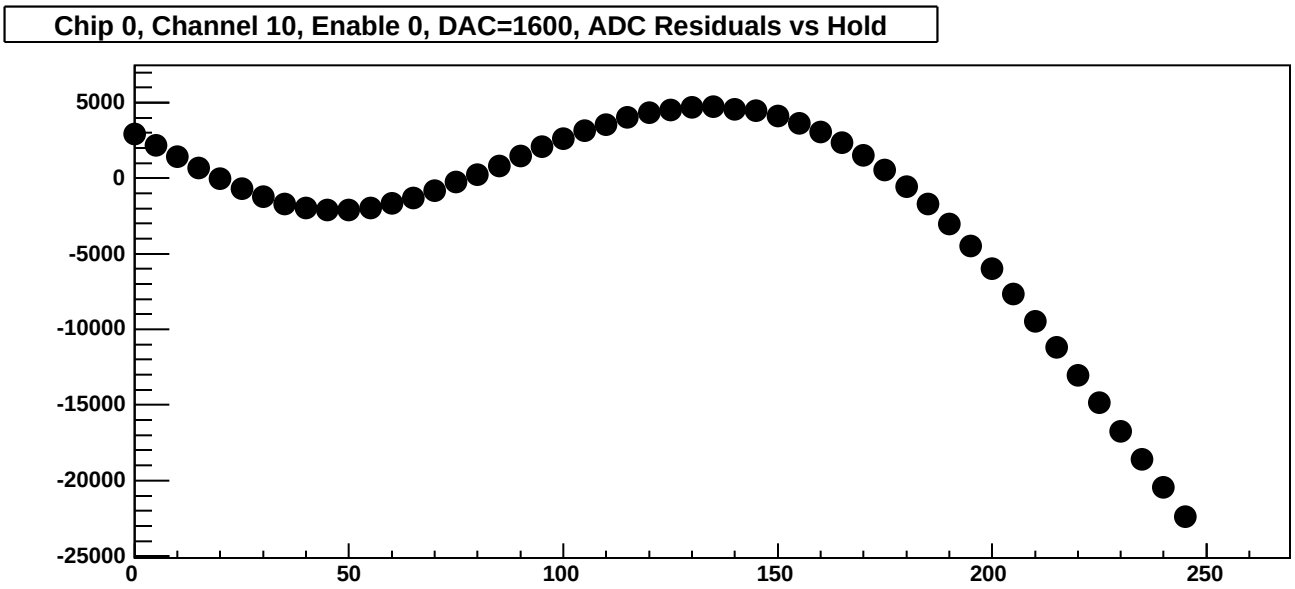


Chip 0, Channel 9, Enable 5, DAC=1600, ADC Residuals vs Hold

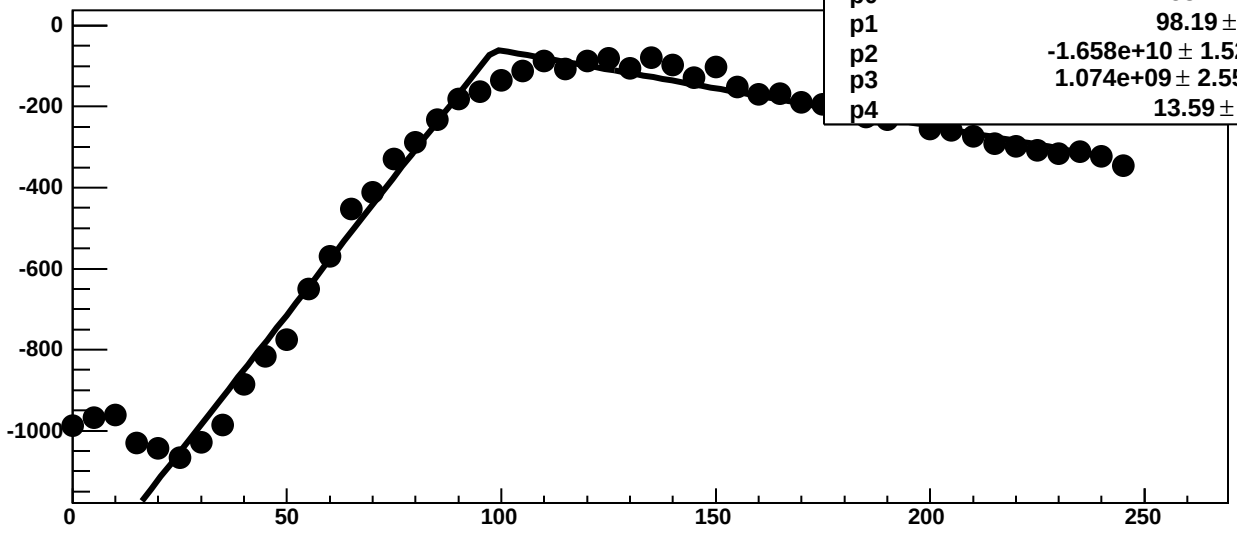



Chip 0, Channel 10, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 8.885e+05 / 41
p0 2007 ± 4.168
p1 -20.19 ± 0.02765
p2 -8978 ± 4.168
p3 -1172 ± 6.208e-06
p4 150 ± 0.07411
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

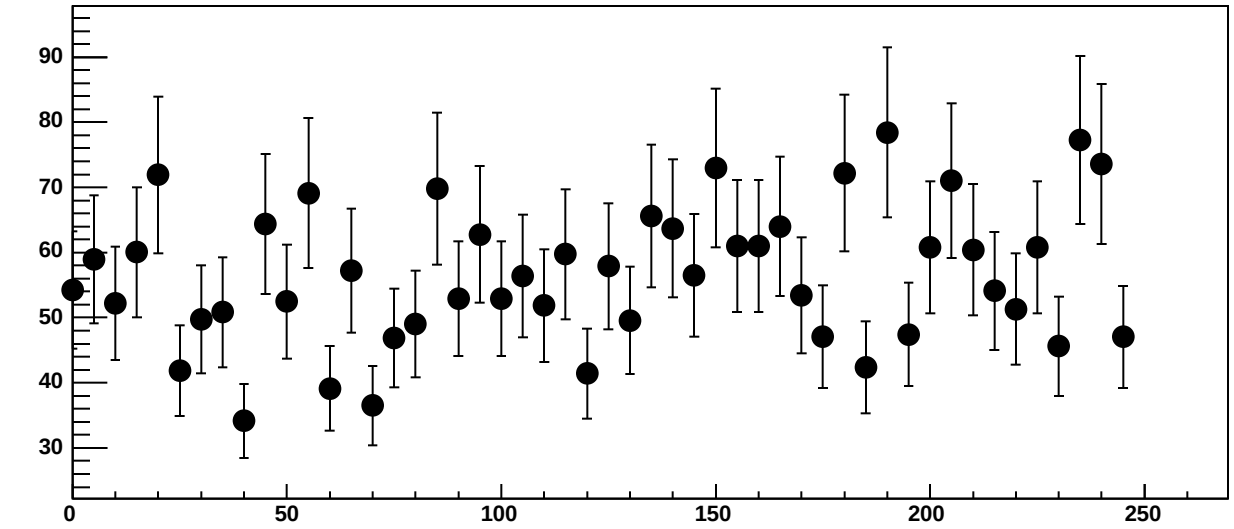

Chip 0, Channel 10, Enable 0, DAC=1600, ADC Noise vs Hold
450
400
350
300
250
200
150
100
50
0
50
100
150
200
250


Chip 0, Channel 10, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
0
50
100
150
200
250

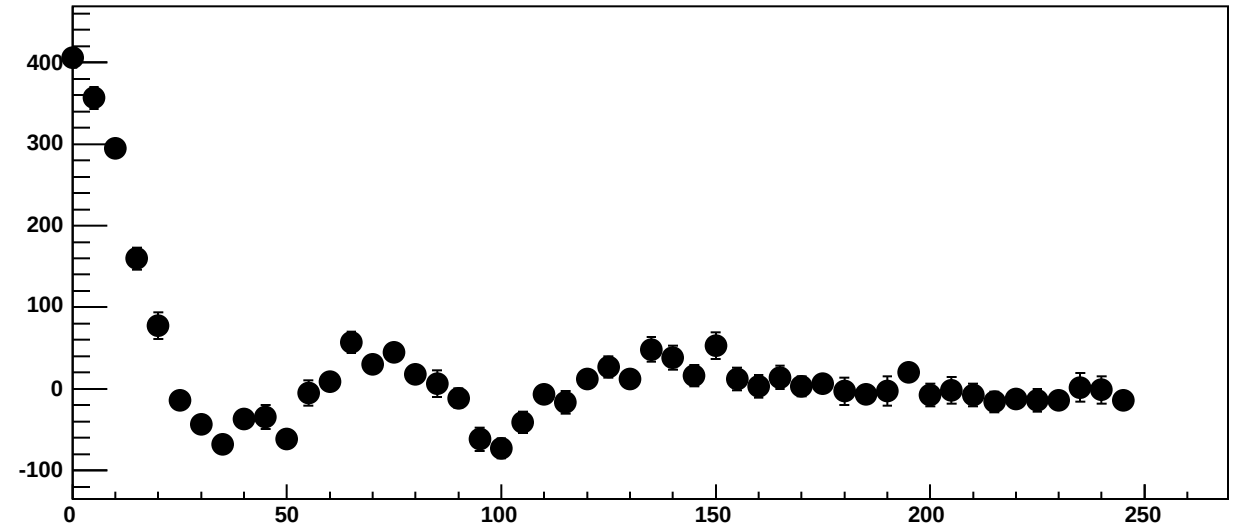
Chip 0, Channel 10, Enable 1, DAC=1600, ADC Mean vs Hold



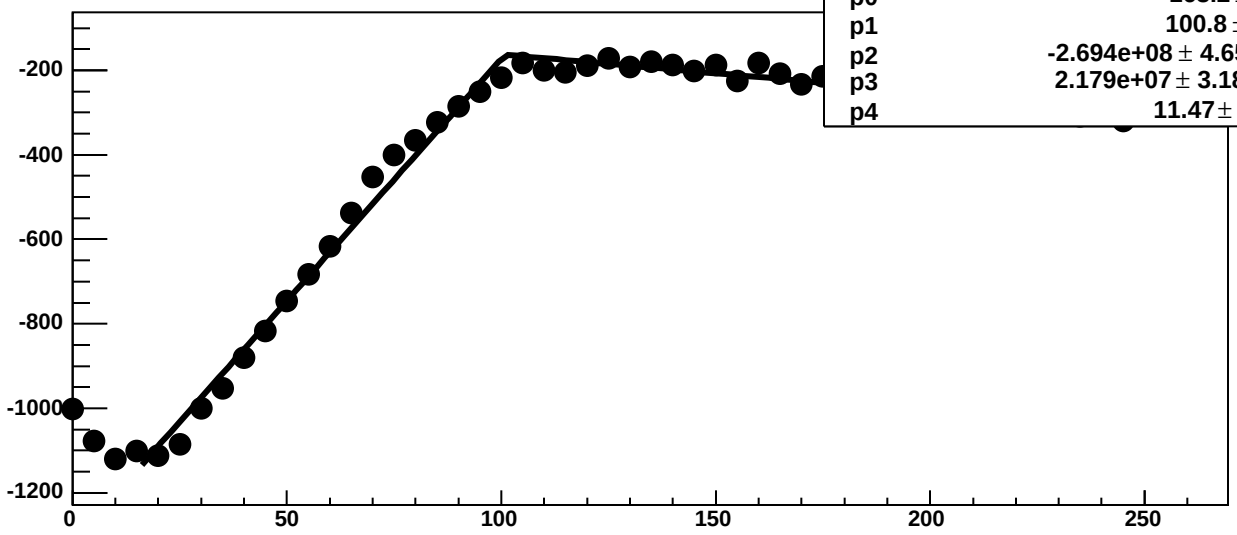
Chip 0, Channel 10, Enable 1, DAC=1600, ADC Noise vs Hold



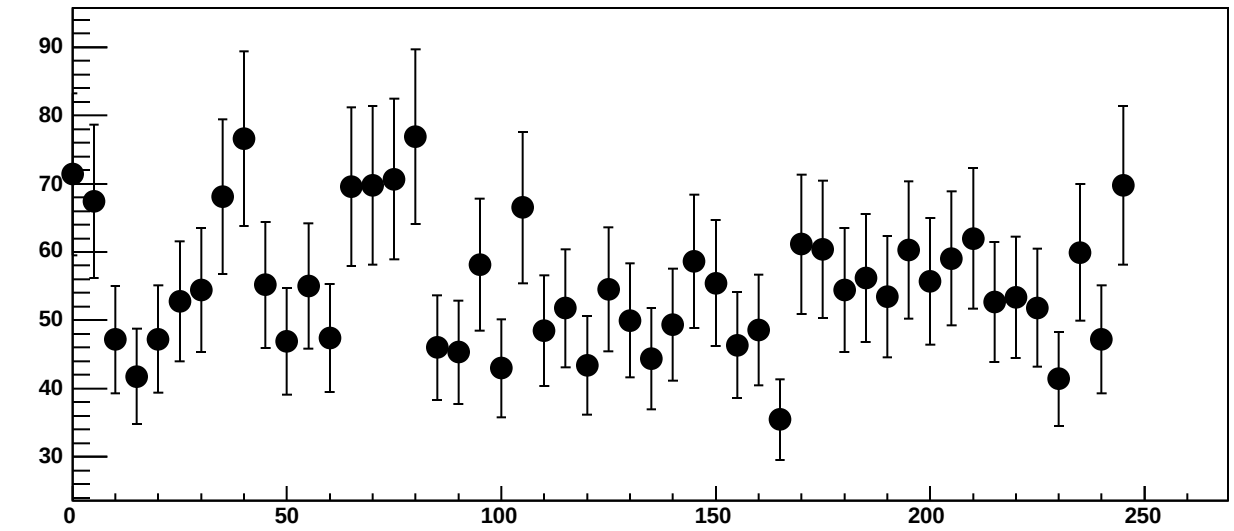
Chip 0, Channel 10, Enable 1, DAC=1600, ADC Residuals vs Hold



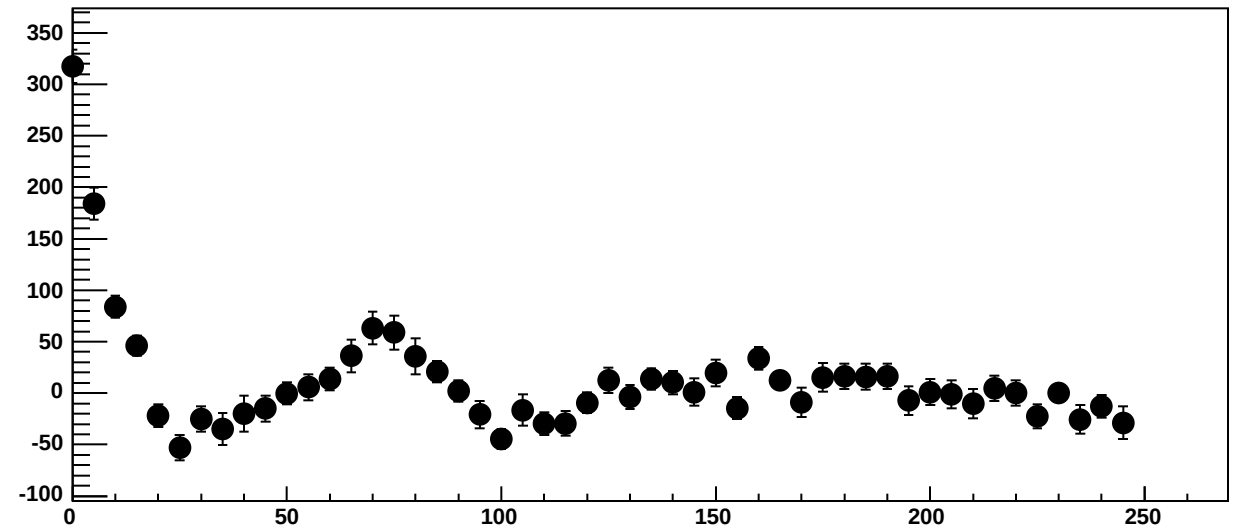
Chip 0, Channel 10, Enable 2, DAC=1600, ADC Mean vs Hold



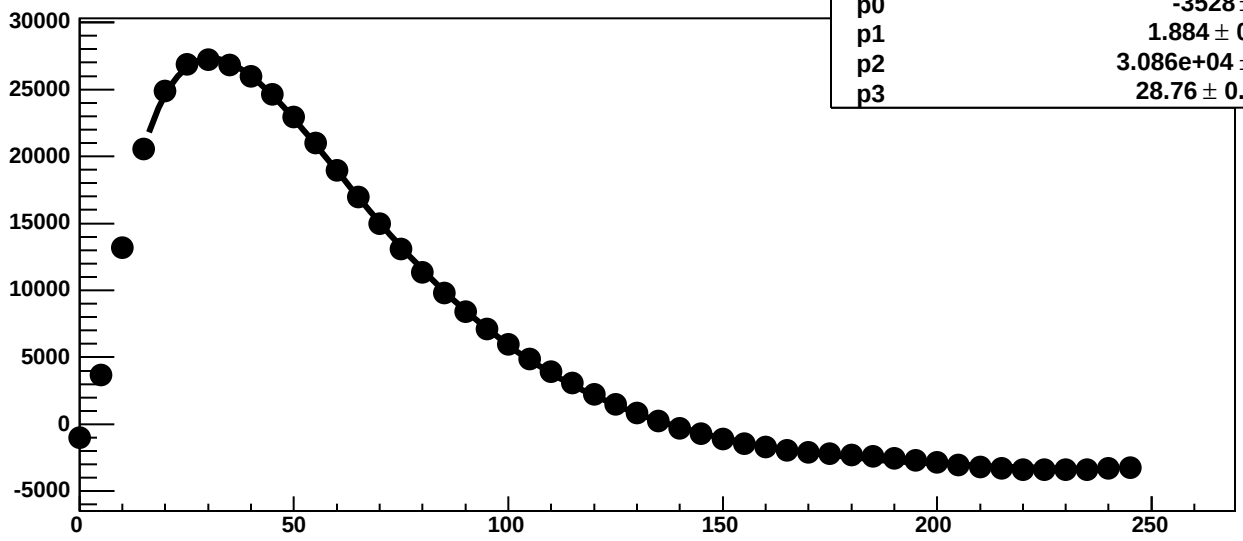
Chip 0, Channel 10, Enable 2, DAC=1600, ADC Noise vs Hold



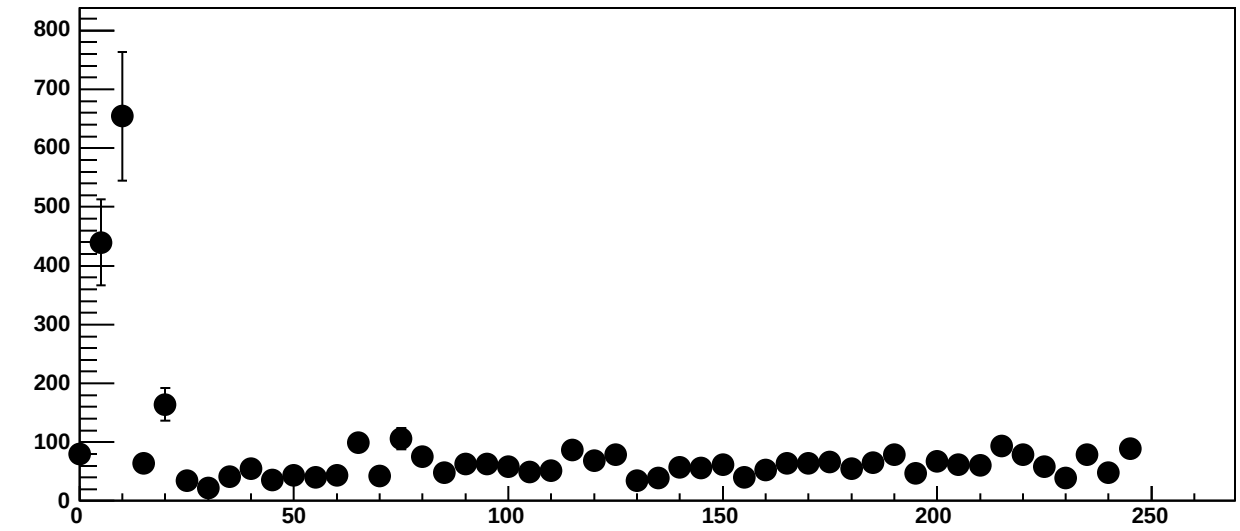
Chip 0, Channel 10, Enable 2, DAC=1600, ADC Residuals vs Hold



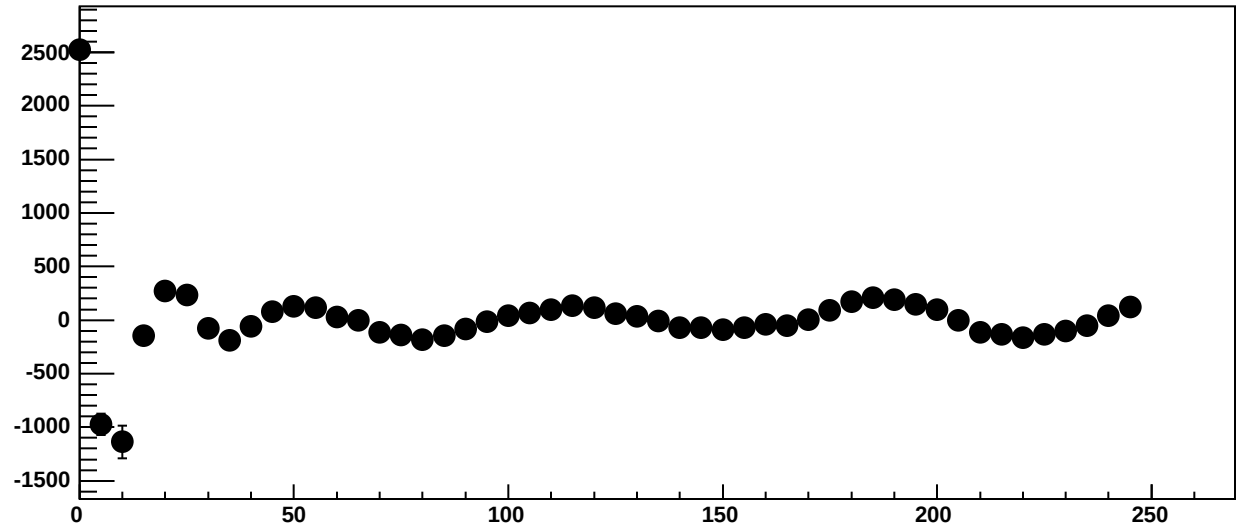
Chip 0, Channel 10, Enable 3!, DAC=1600, ADC Mean vs Hold

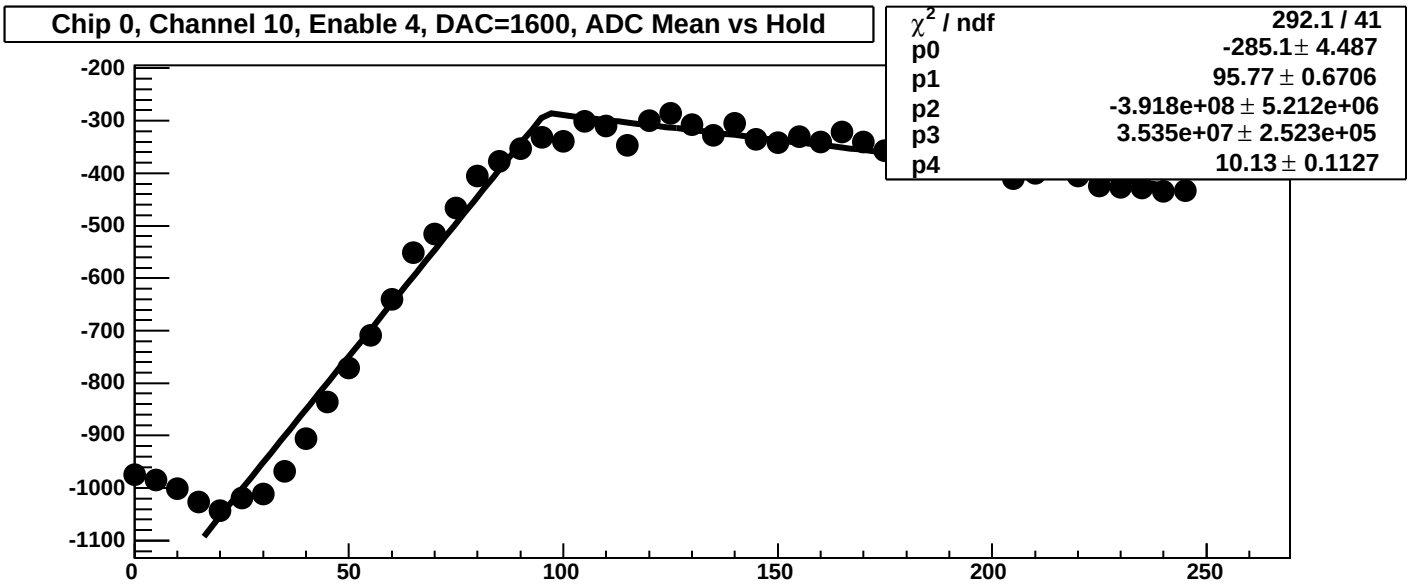


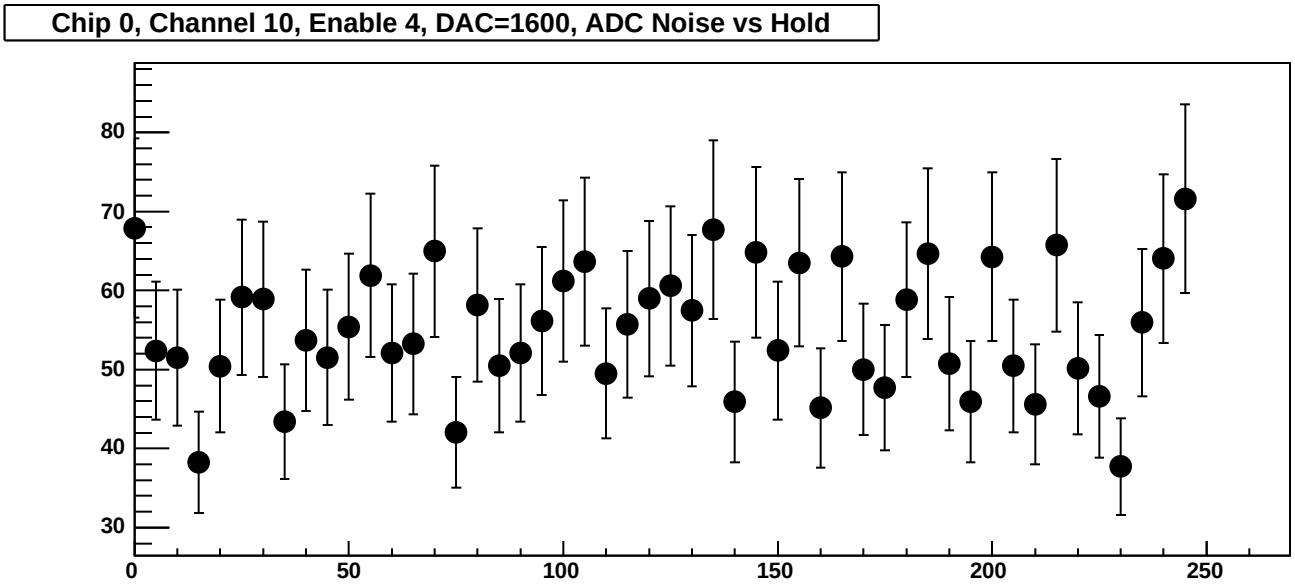
Chip 0, Channel 10, Enable 3!, DAC=1600, ADC Noise vs Hold

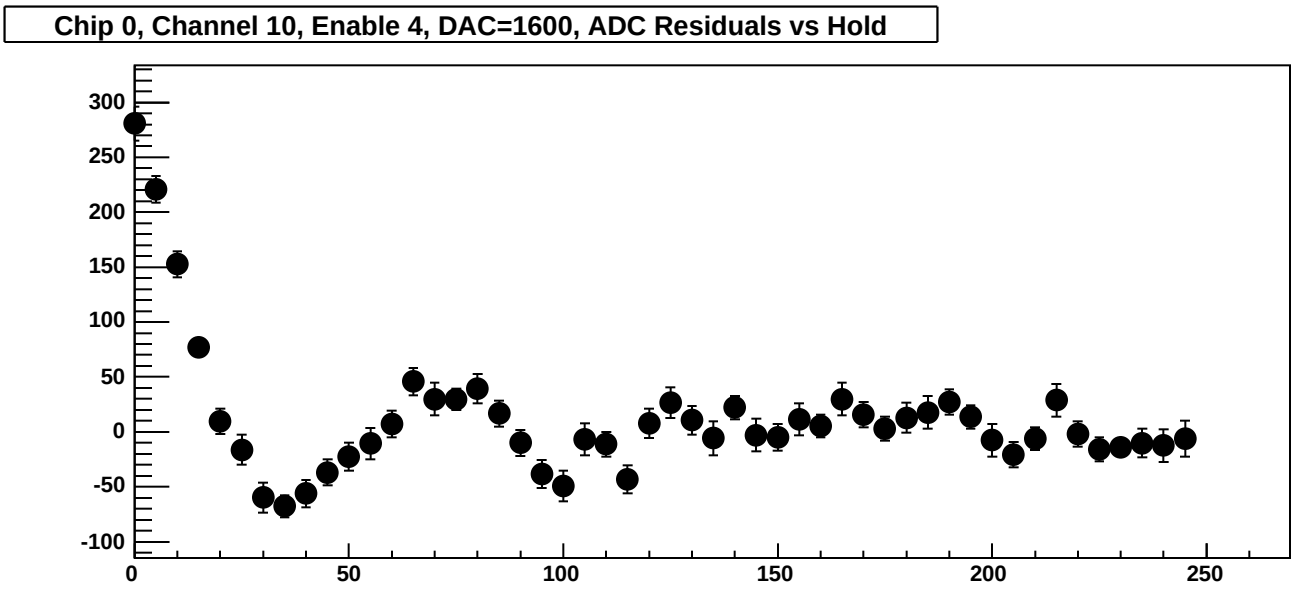


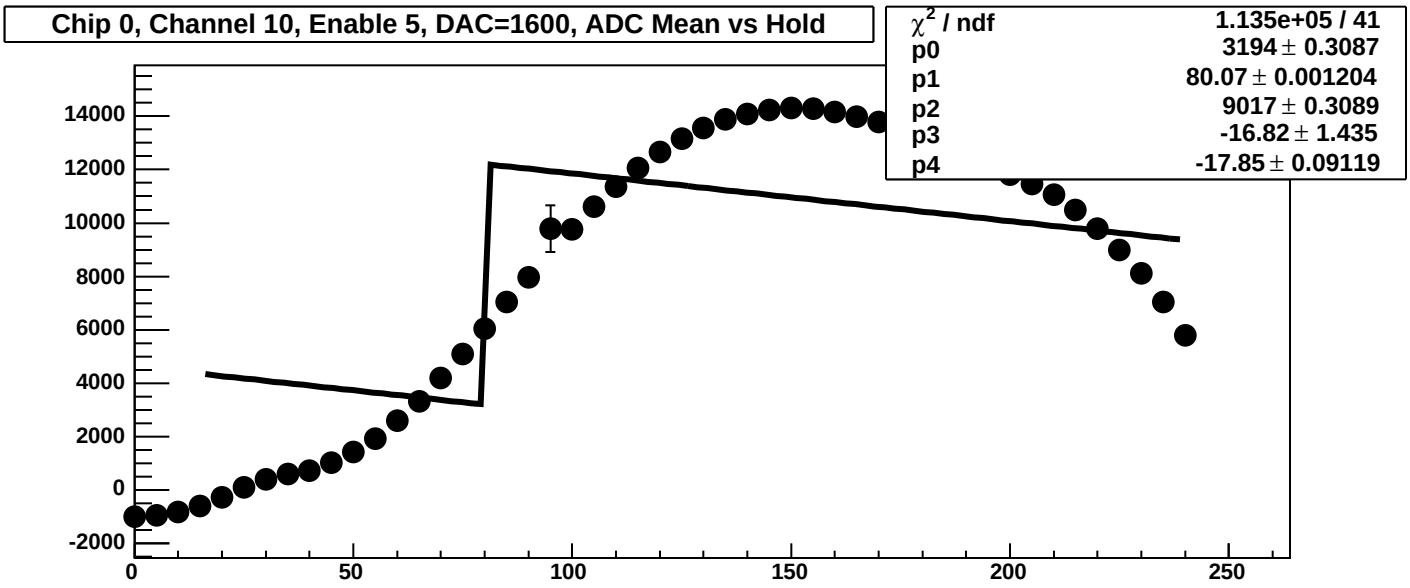
Chip 0, Channel 10, Enable 3!, DAC=1600, ADC Residuals vs Hold

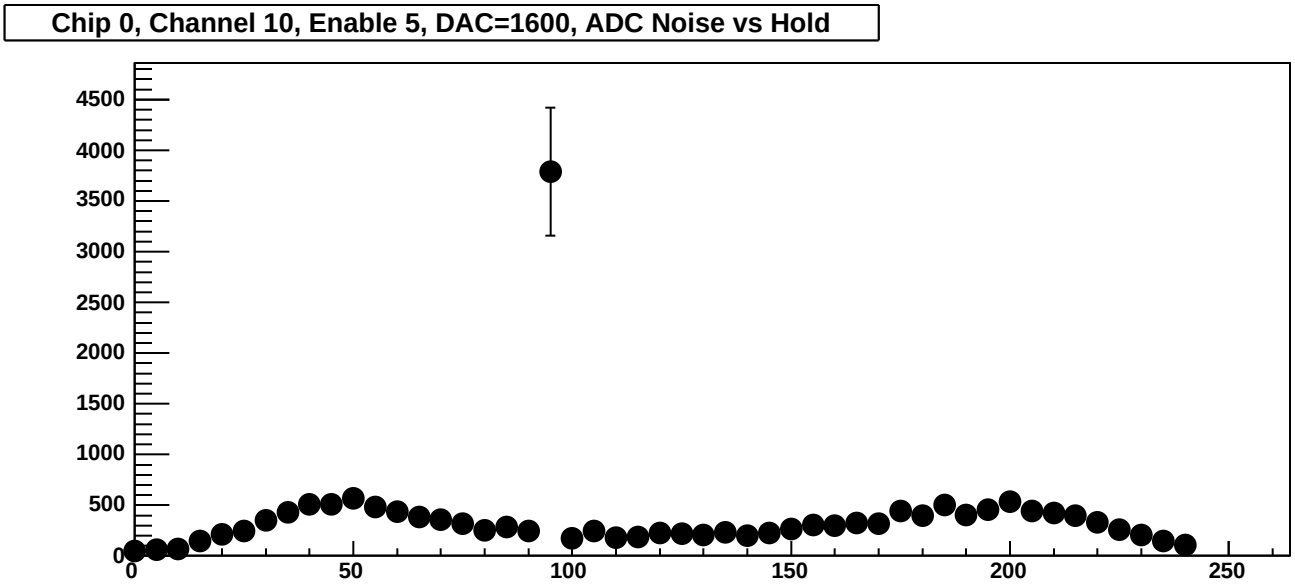


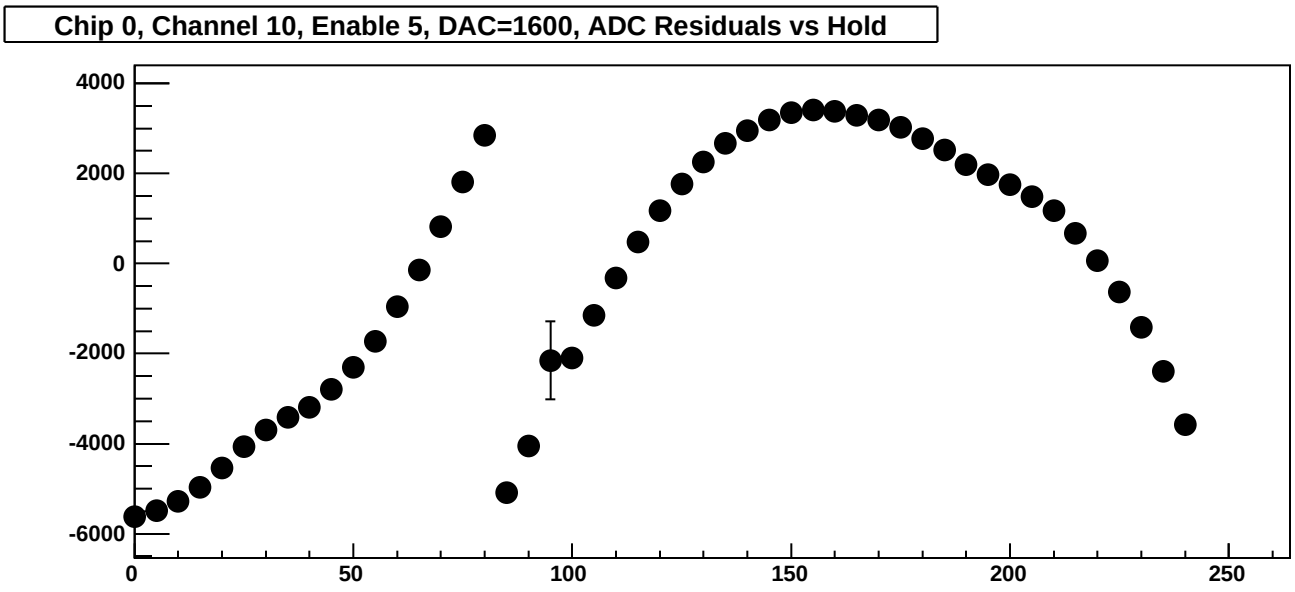

Chip 0, Channel 10, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 292.1 / 41
p0 -285.1 ± 4.487
p1 95.77 ± 0.6706
p2 -3.918e+08 ± 5.212e+06
p3 3.535e+07 ± 2.523e+05
p4 10.13 ± 0.1127
-200
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
50
100
150
200
250

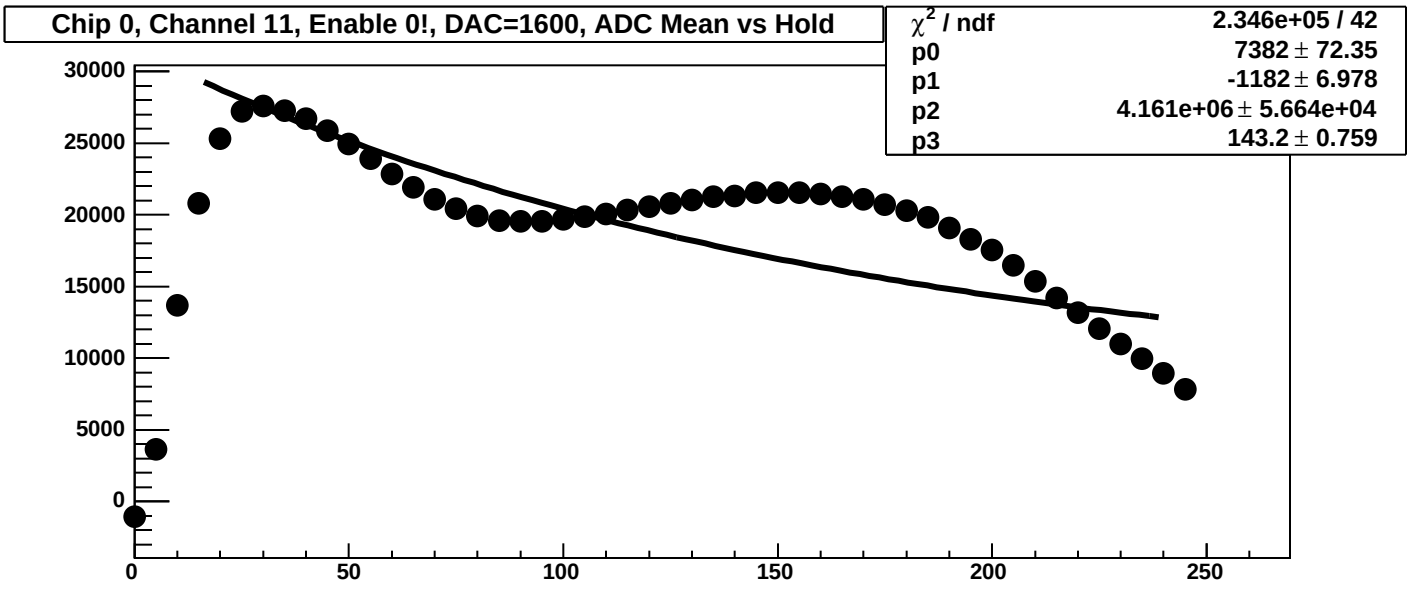

Chip 0, Channel 10, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

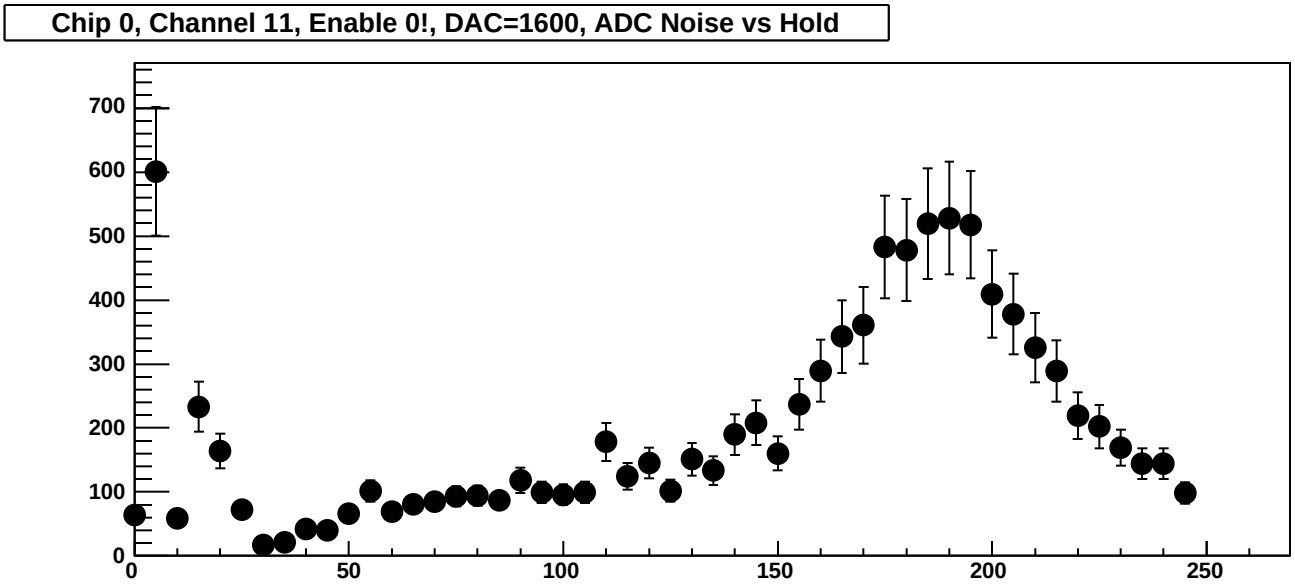

Chip 0, Channel 10, Enable 4, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

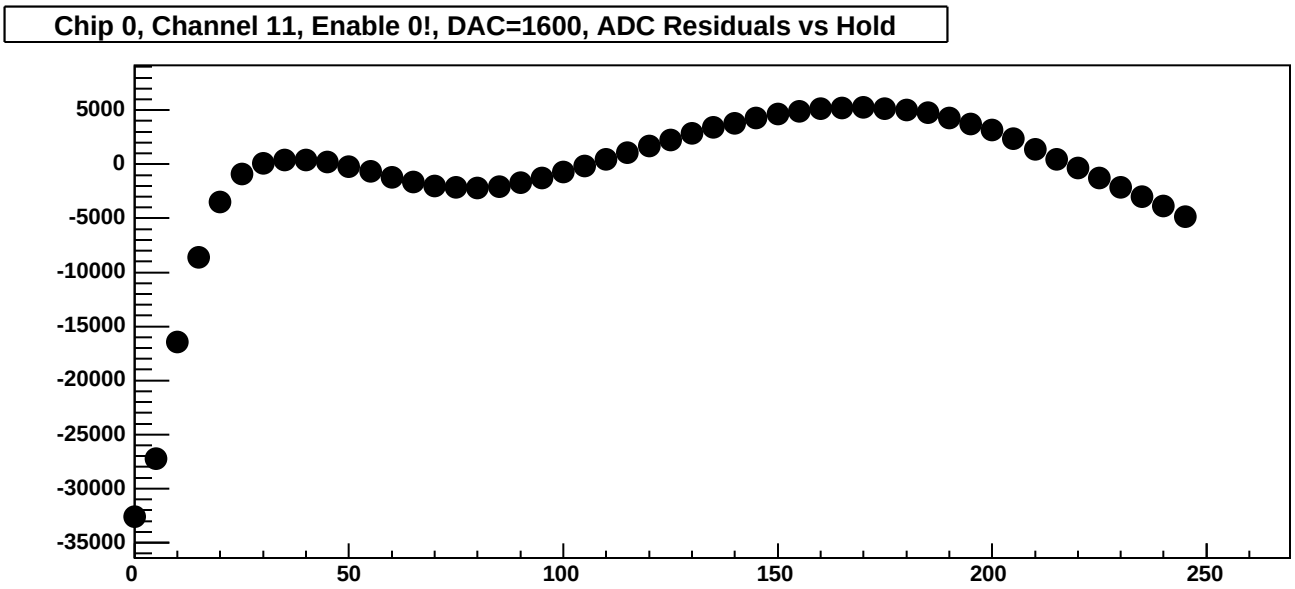

Chip 0, Channel 10, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.135e+05 / 41
p0 3194 ± 0.3087
p1 80.07 ± 0.001204
p2 9017 ± 0.3089
p3 -16.82 ± 1.435
p4 -17.85 ± 0.09119
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

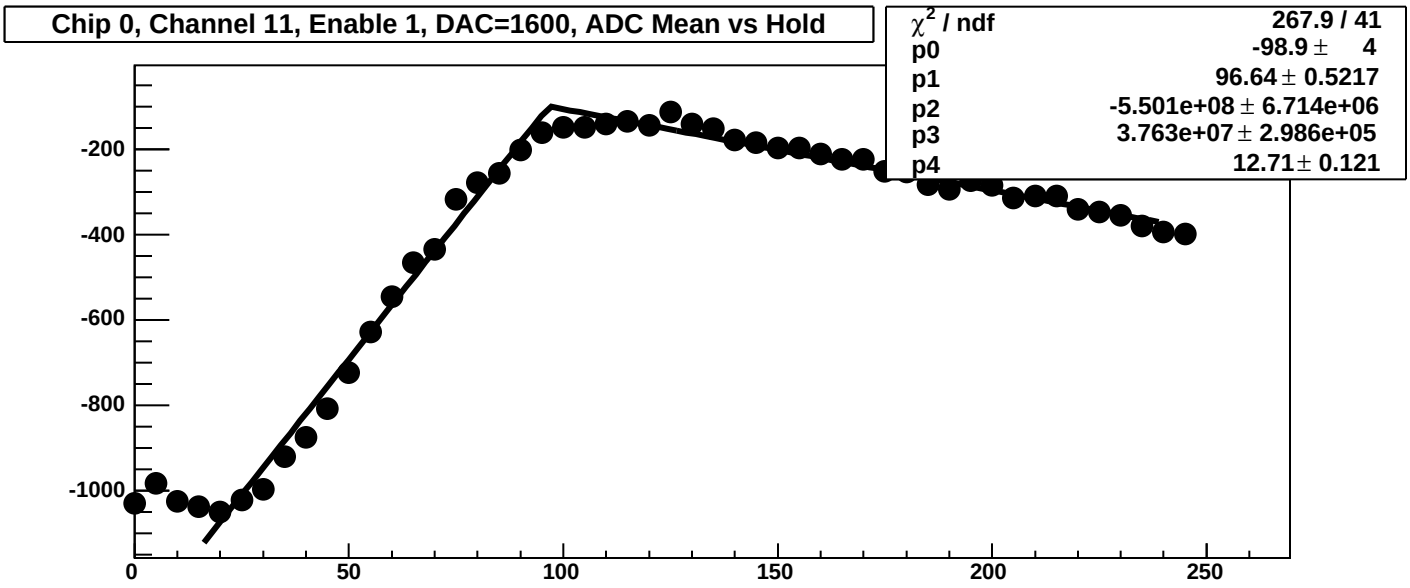

Chip 0, Channel 10, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250

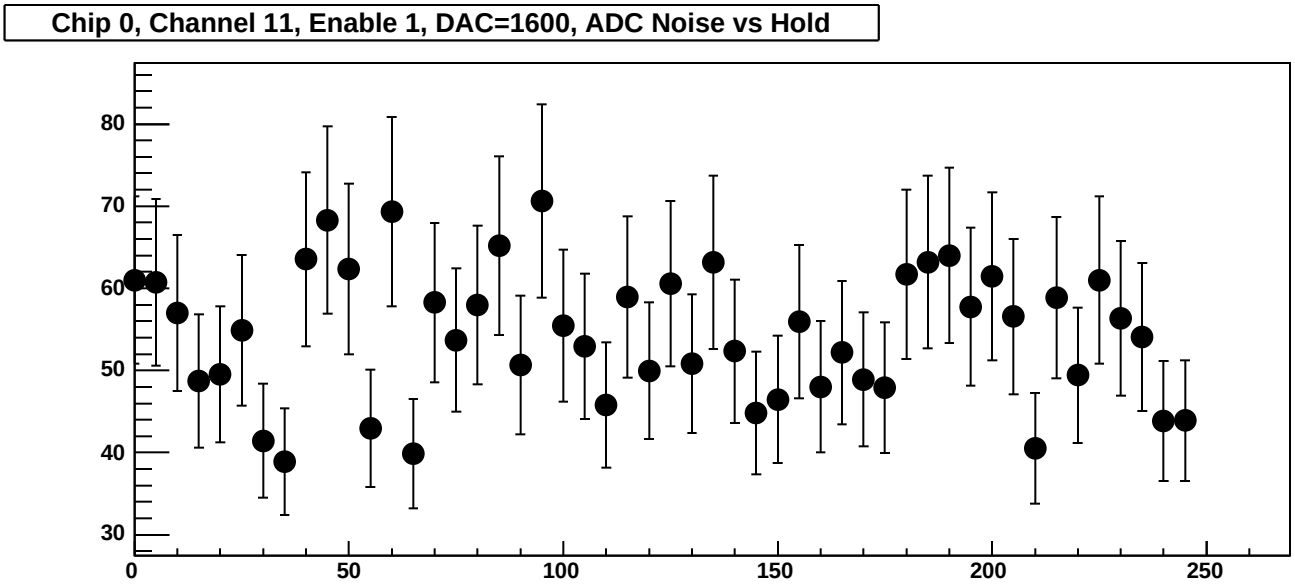

Chip 0, Channel 10, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

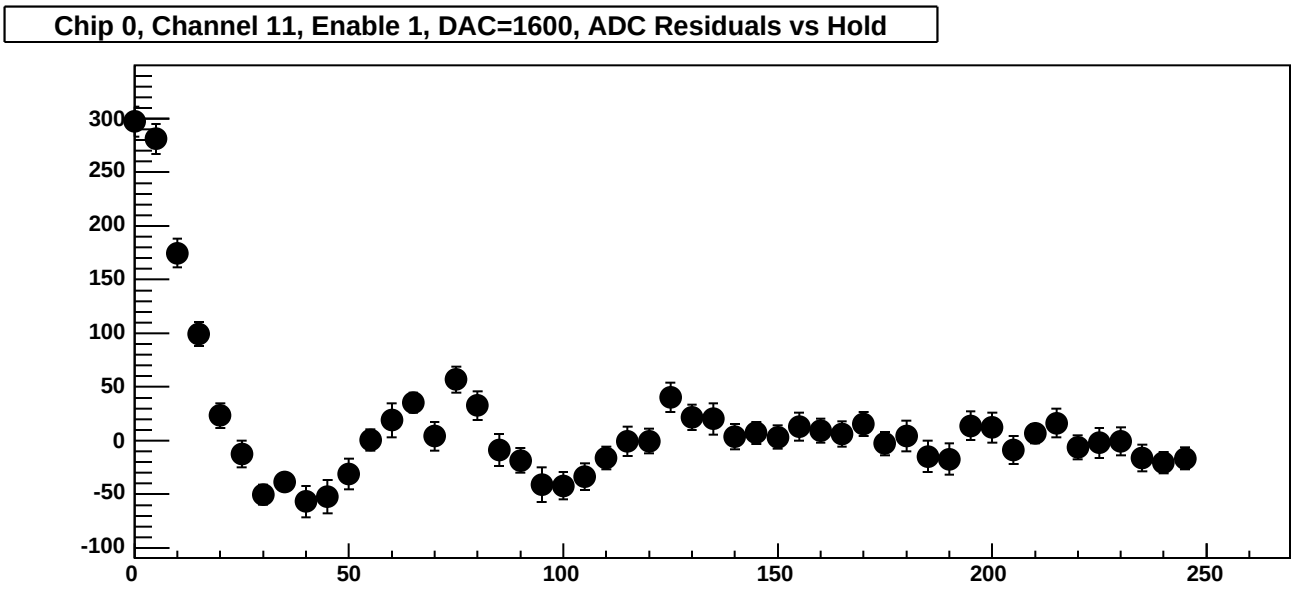

Chip 0, Channel 11, Enable 0!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.346e+05 / 42
p0 7382 ± 72.35
p1 -1182 ± 6.978
p2 4.161e+06 ± 5.664e+04
p3 143.2 ± 0.759
30000
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

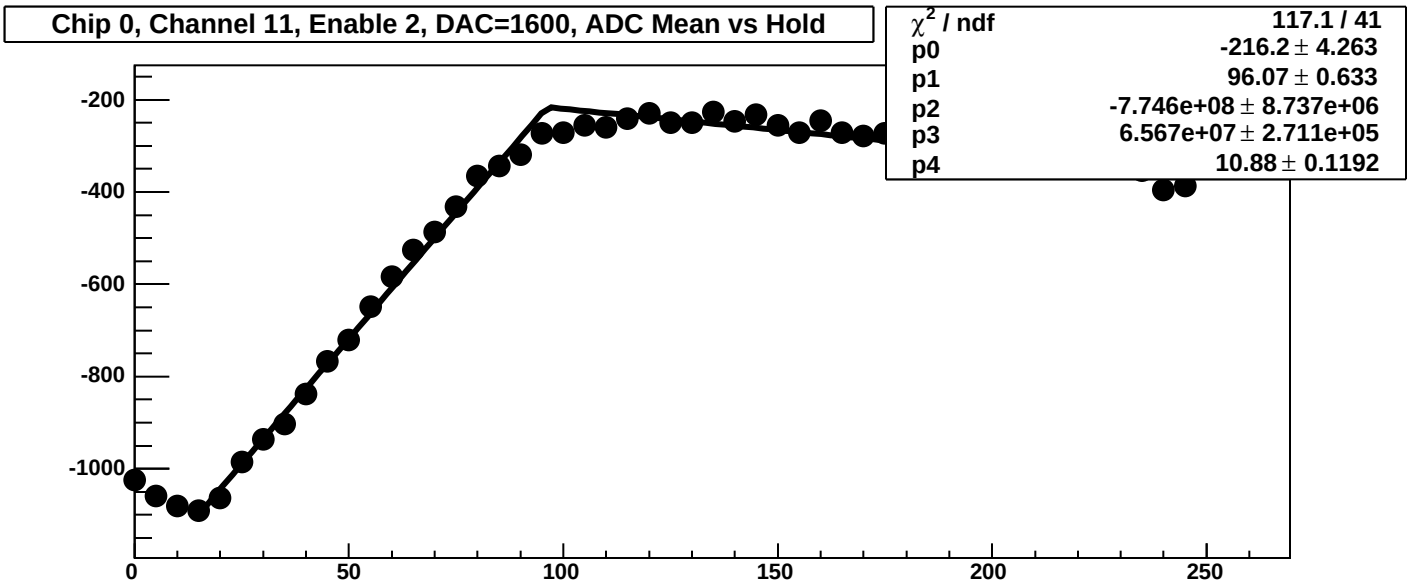

Chip 0, Channel 11, Enable 0!, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250

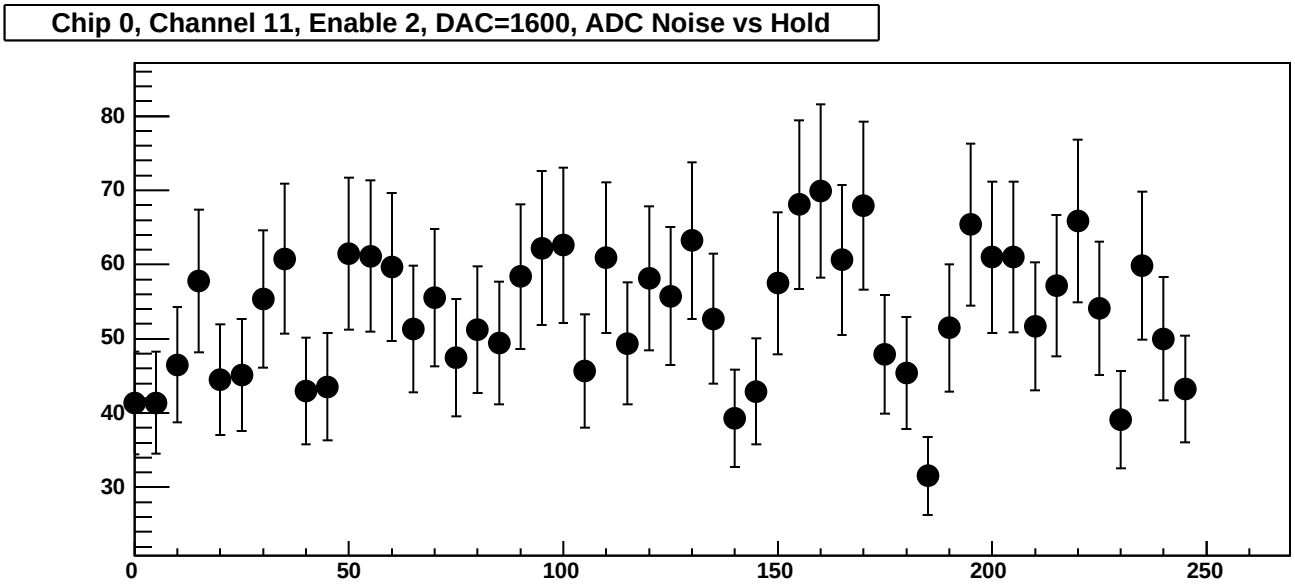

Chip 0, Channel 11, Enable 0!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
-35000
0
50
100
150
200
250

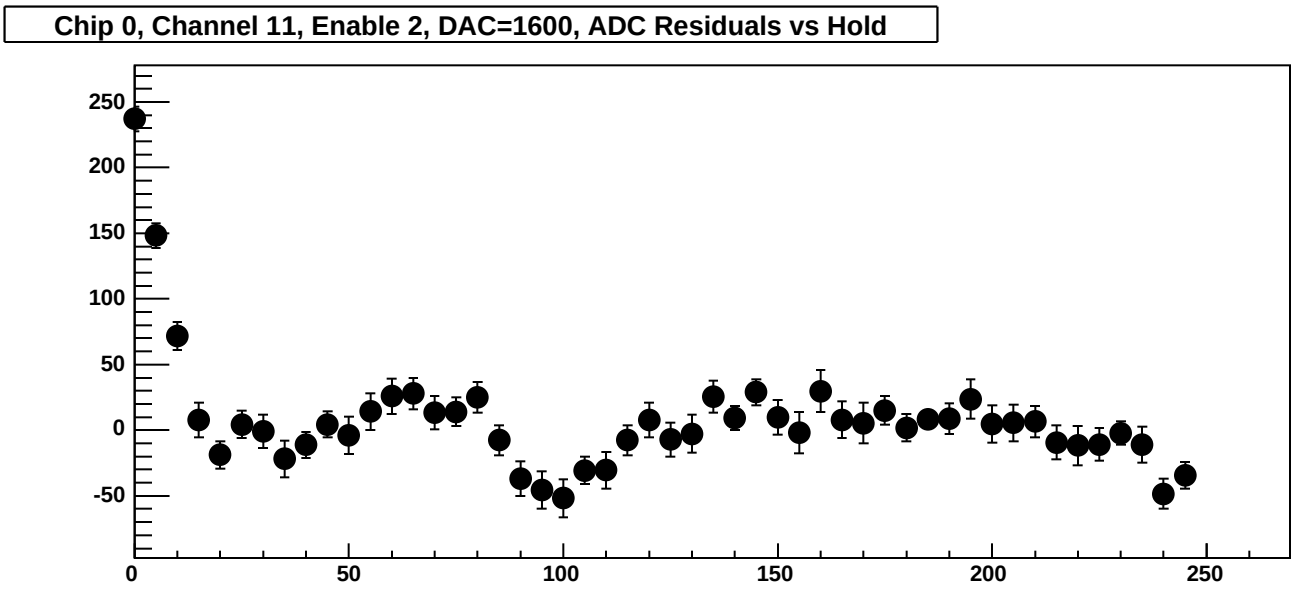

Chip 0, Channel 11, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 267.9 / 41
p0 -98.9 ± 4
p1 96.64 ± 0.5217
p2 -5.501e+08 ± 6.714e+06
p3 3.763e+07 ± 2.986e+05
p4 12.71 ± 0.121
-200
-400
-600
-800
-1000
0
50
100
150
200
250

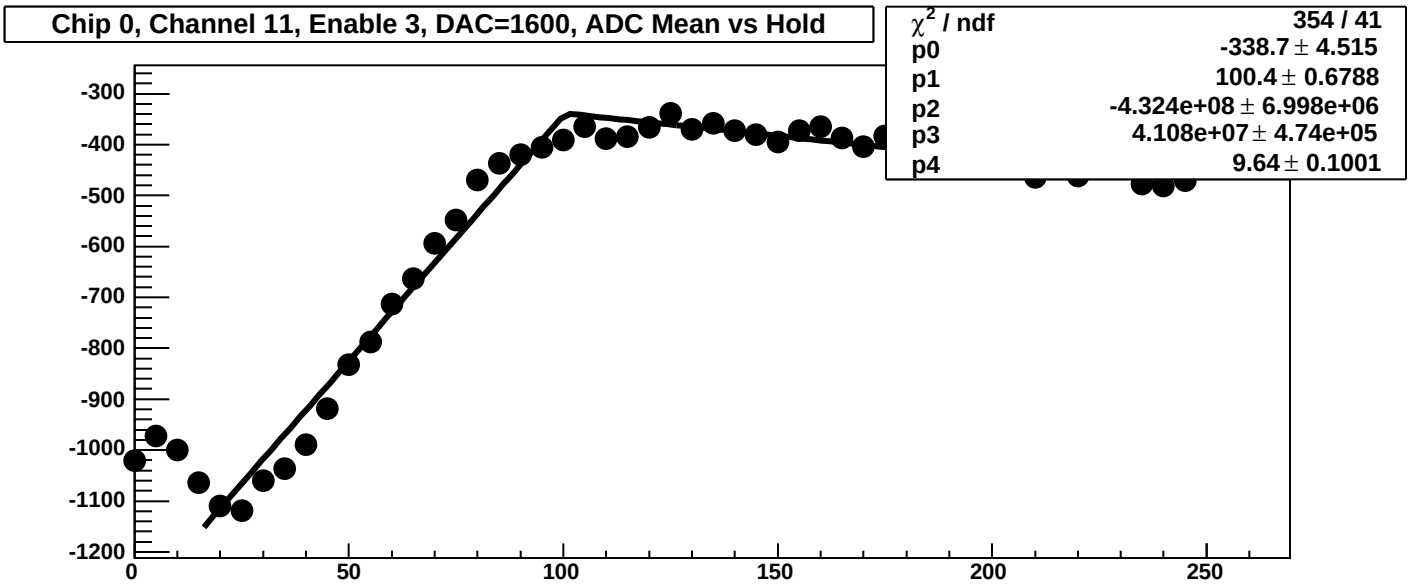

Chip 0, Channel 11, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

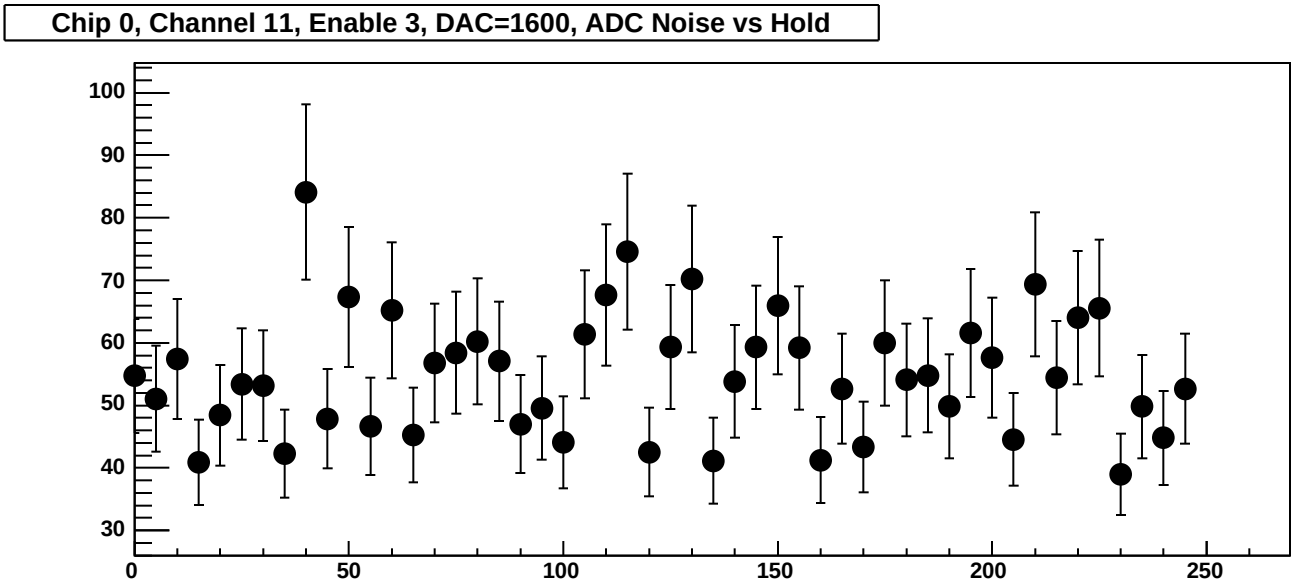

Chip 0, Channel 11, Enable 1, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

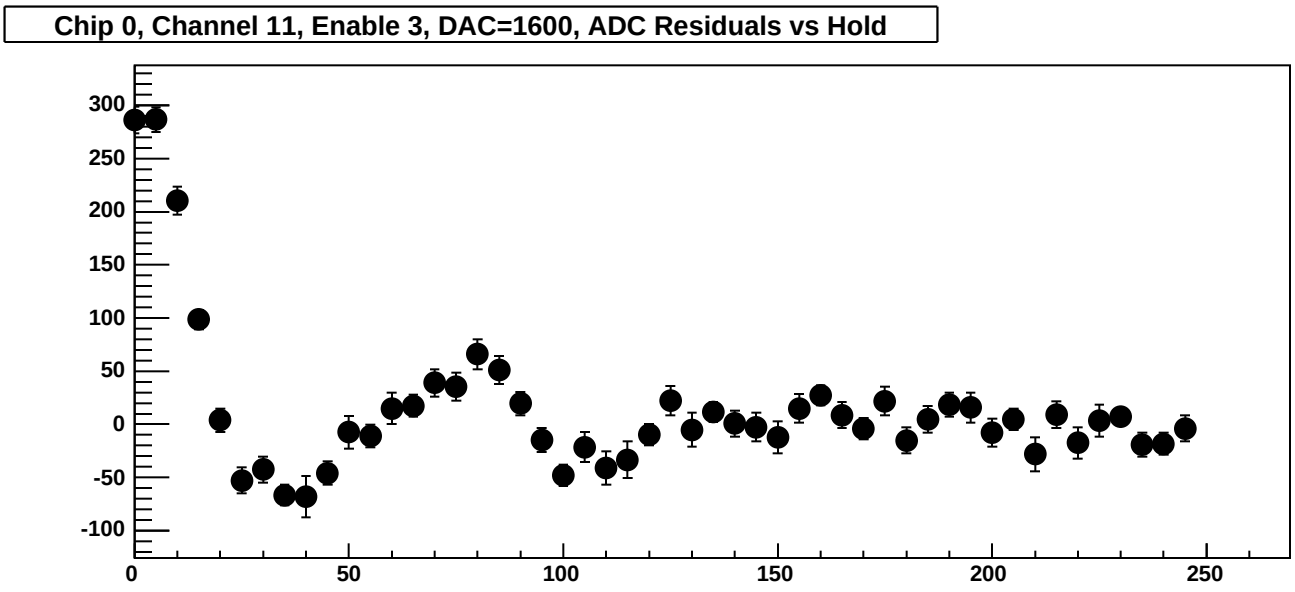

Chip 0, Channel 11, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 117.1 / 41
p0 -216.2 ± 4.263
p1 96.07 ± 0.633
p2 -7.746e+08 ± 8.737e+06
p3 6.567e+07 ± 2.711e+05
p4 10.88 ± 0.1192
-200
-400
-600
-800
-1000
0
50
100
150
200
250


Chip 0, Channel 11, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

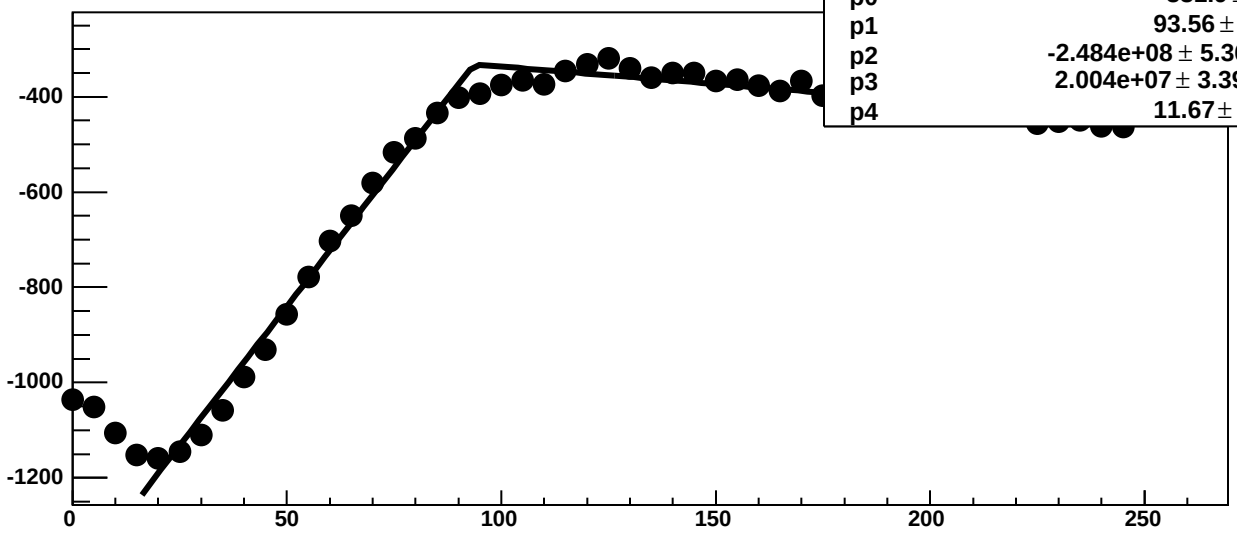

Chip 0, Channel 11, Enable 2, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
0
50
100
150
200
250


Chip 0, Channel 11, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 354 / 41
p0 -338.7 ± 4.515
p1 100.4 ± 0.6788
p2 -4.324e+08 ± 6.998e+06
p3 4.108e+07 ± 4.74e+05
p4 9.64 ± 0.1001
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
50
100
150
200
250

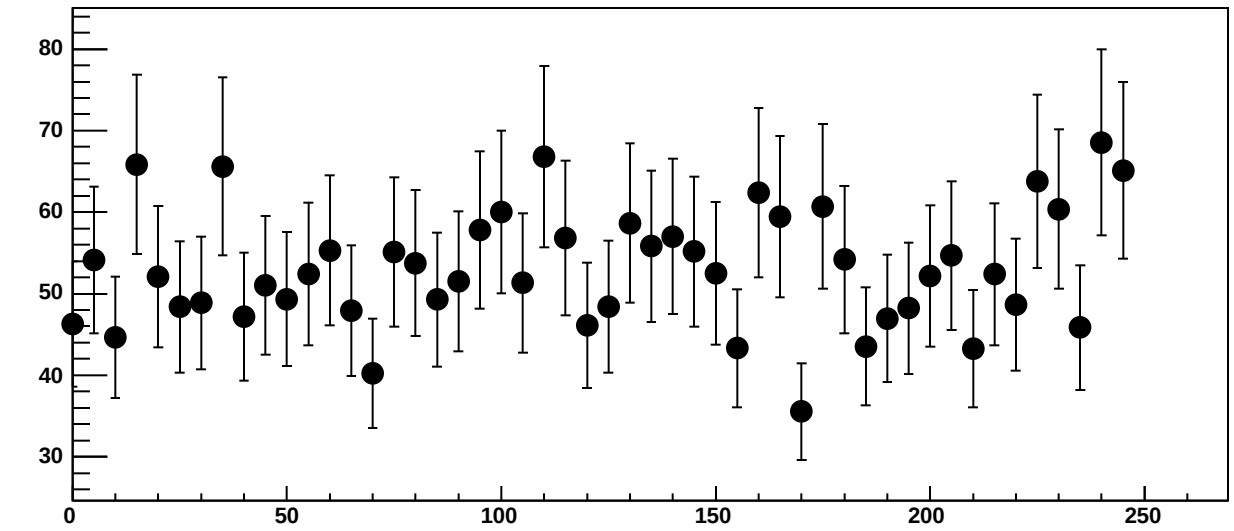

Chip 0, Channel 11, Enable 3, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 0, Channel 11, Enable 3, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

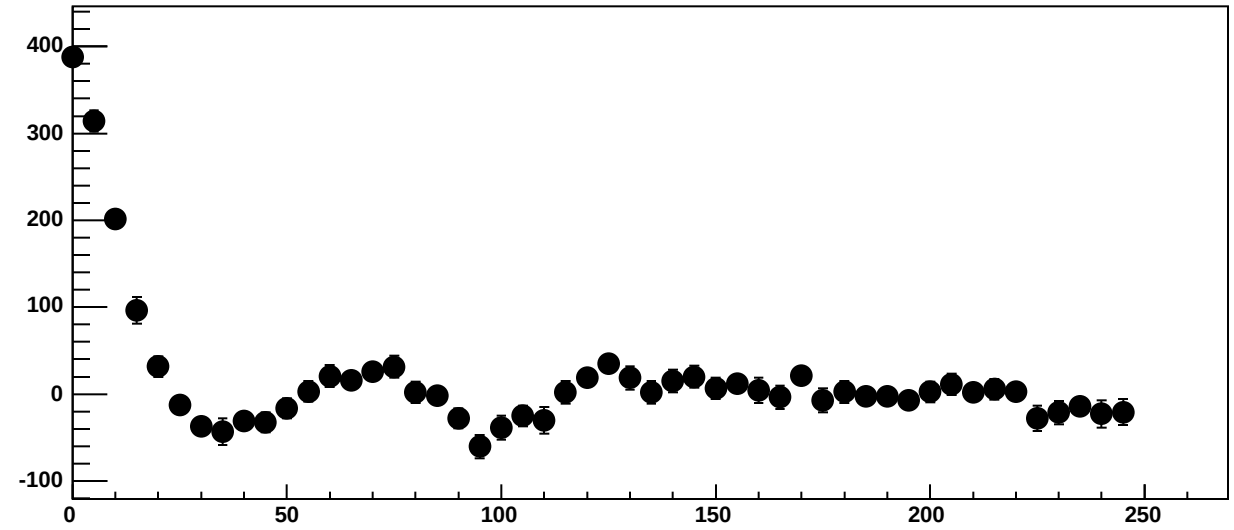
Chip 0, Channel 11, Enable 4, DAC=1600, ADC Mean vs Hold

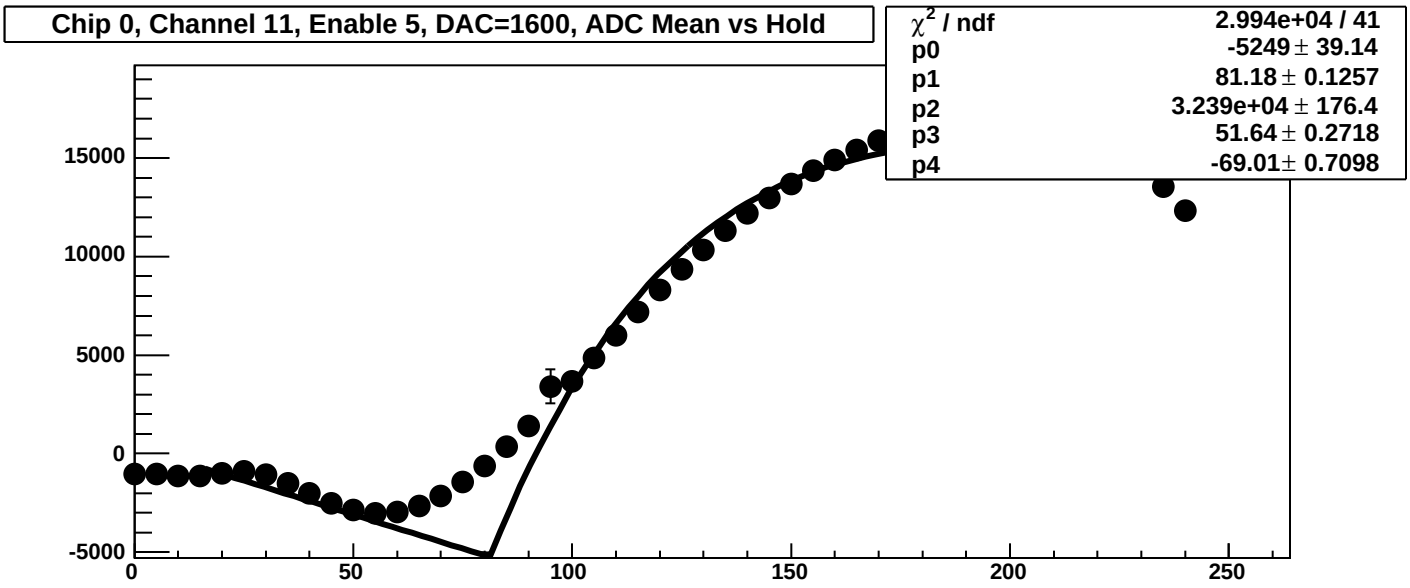


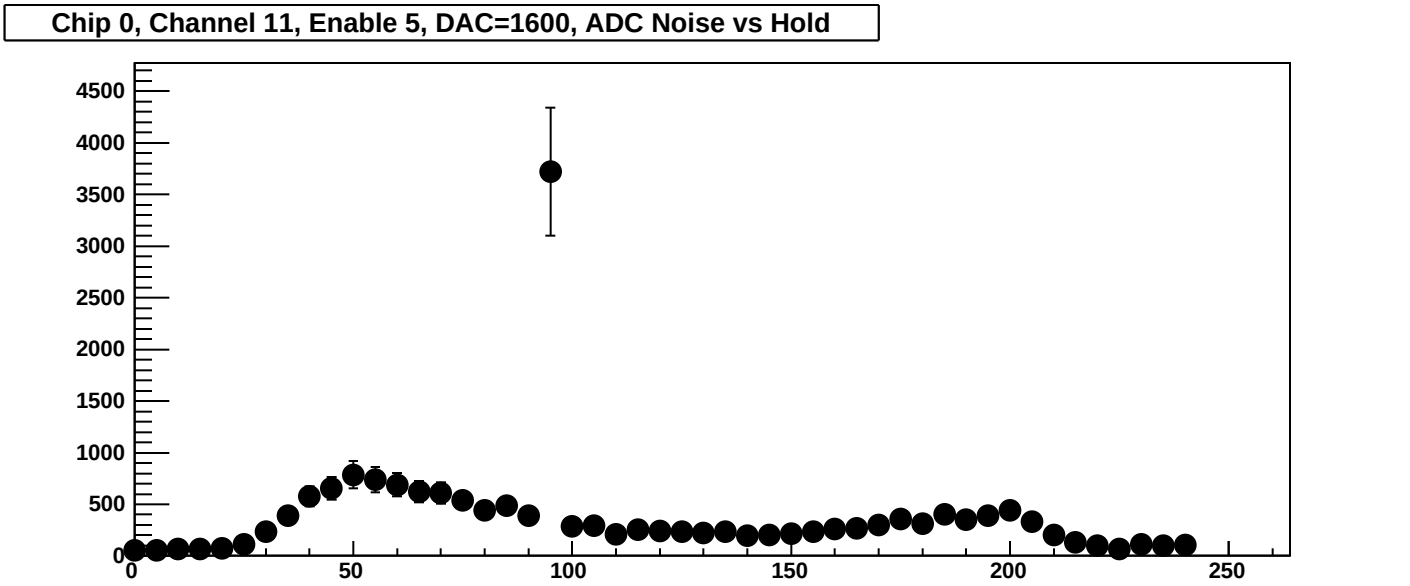
Chip 0, Channel 11, Enable 4, DAC=1600, ADC Noise vs Hold

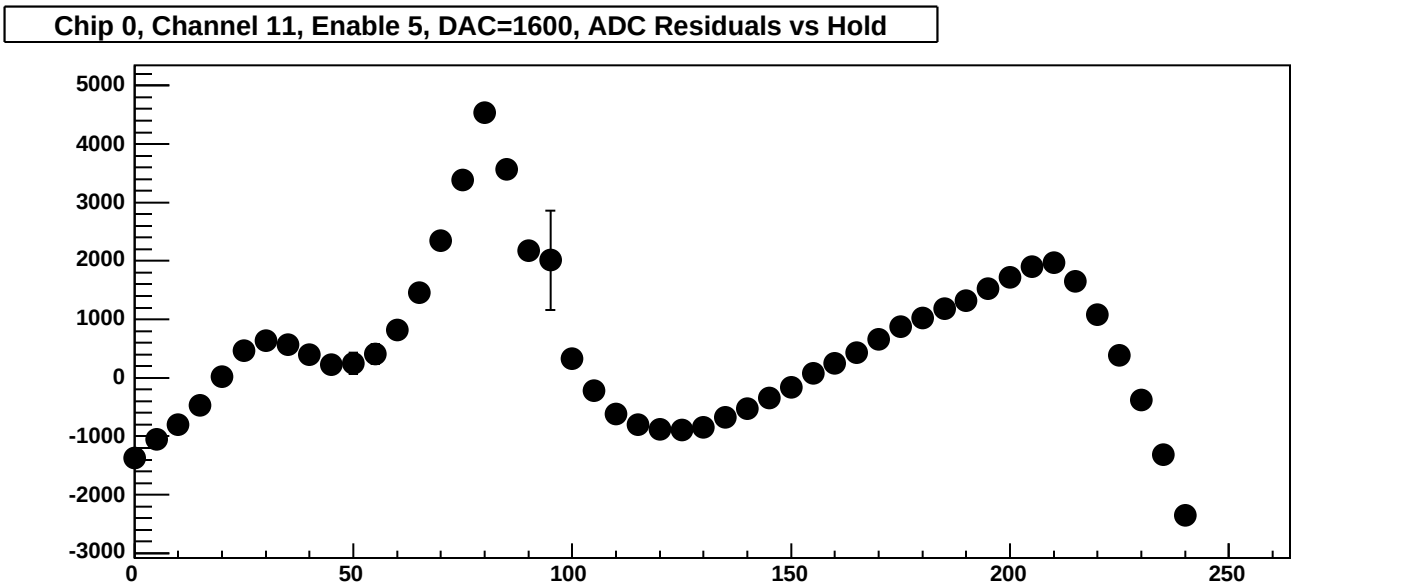


Chip 0, Channel 11, Enable 4, DAC=1600, ADC Residuals vs Hold

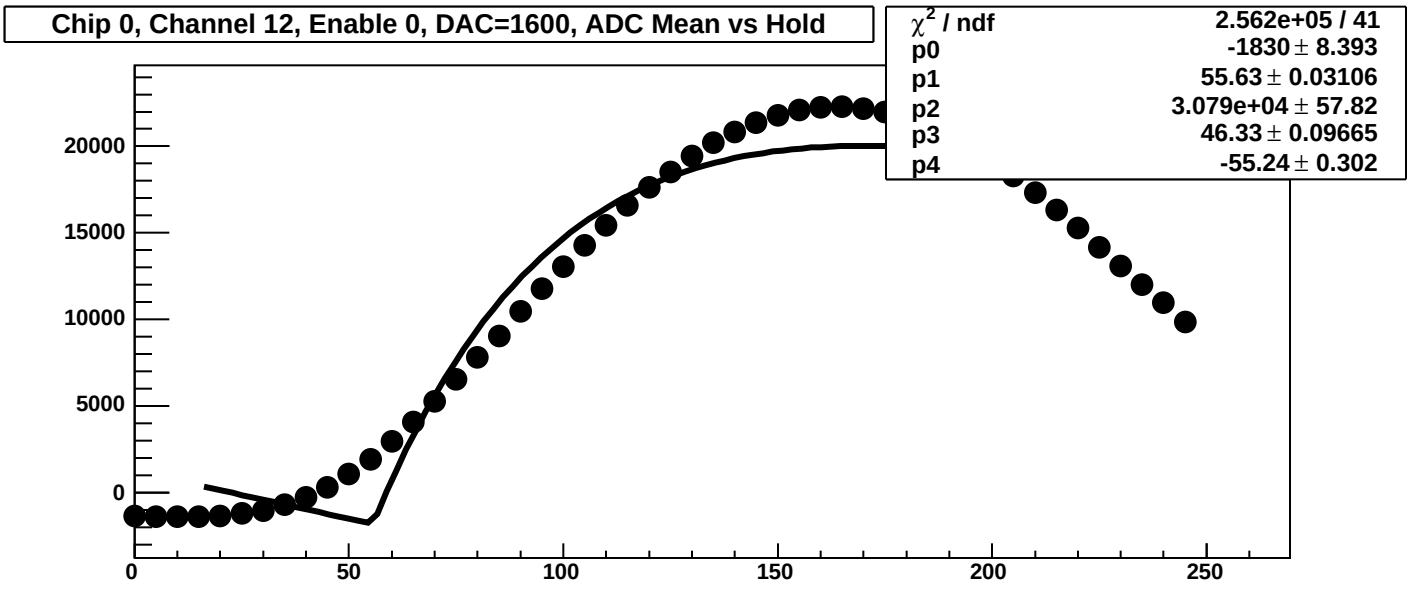



Chip 0, Channel 11, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.994e+04 / 41
p0 -5249 ± 39.14
p1 81.18 ± 0.1257
p2 3.239e+04 ± 176.4
p3 51.64 ± 0.2718
p4 -69.01 ± 0.7098
15000
10000
5000
0
-5000
0
50
100
150
200
250

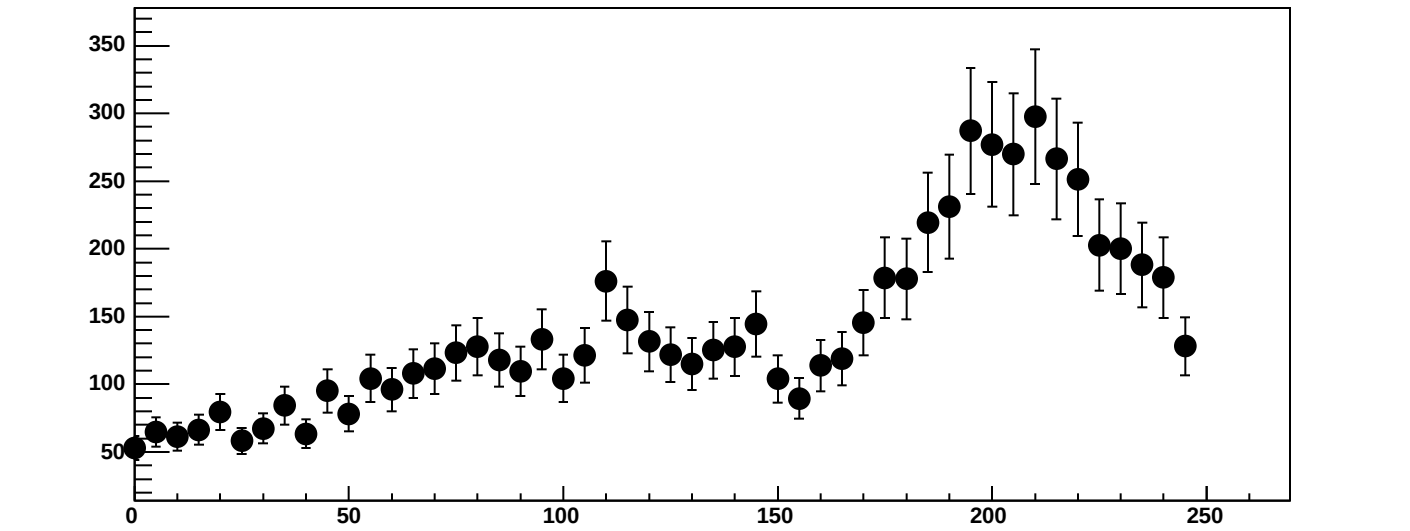

Chip 0, Channel 11, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 0, Channel 11, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
4000
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

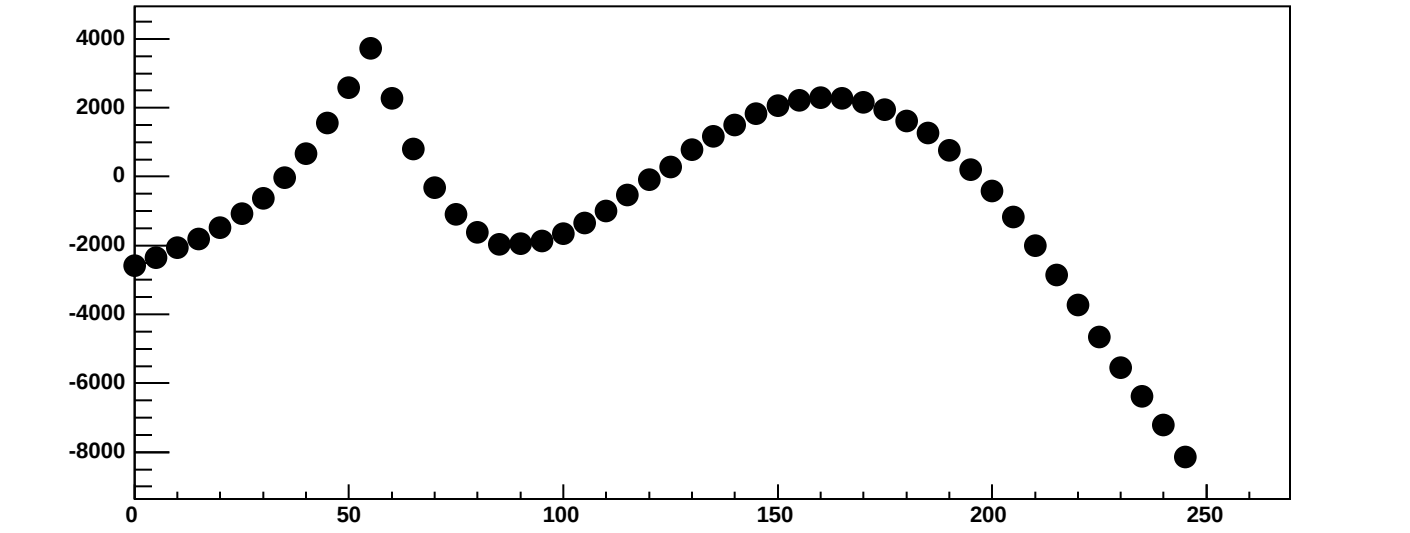
Chip 0, Channel 12, Enable 0, DAC=1600, ADC Mean vs Hold

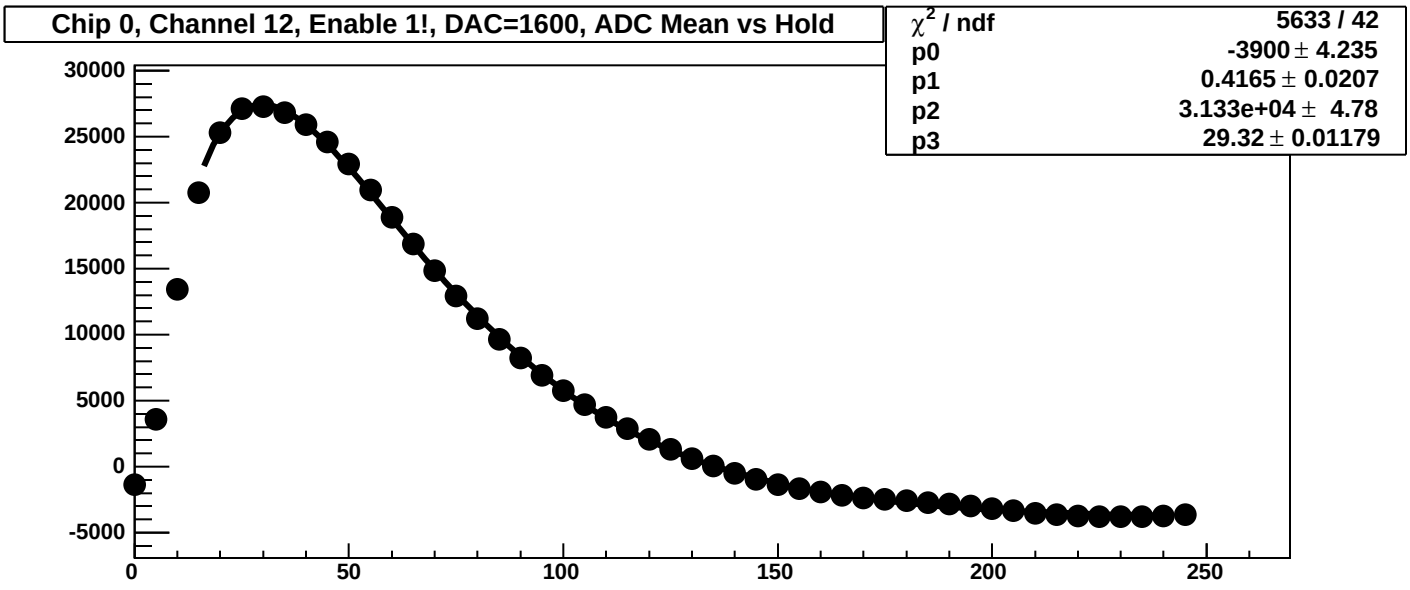


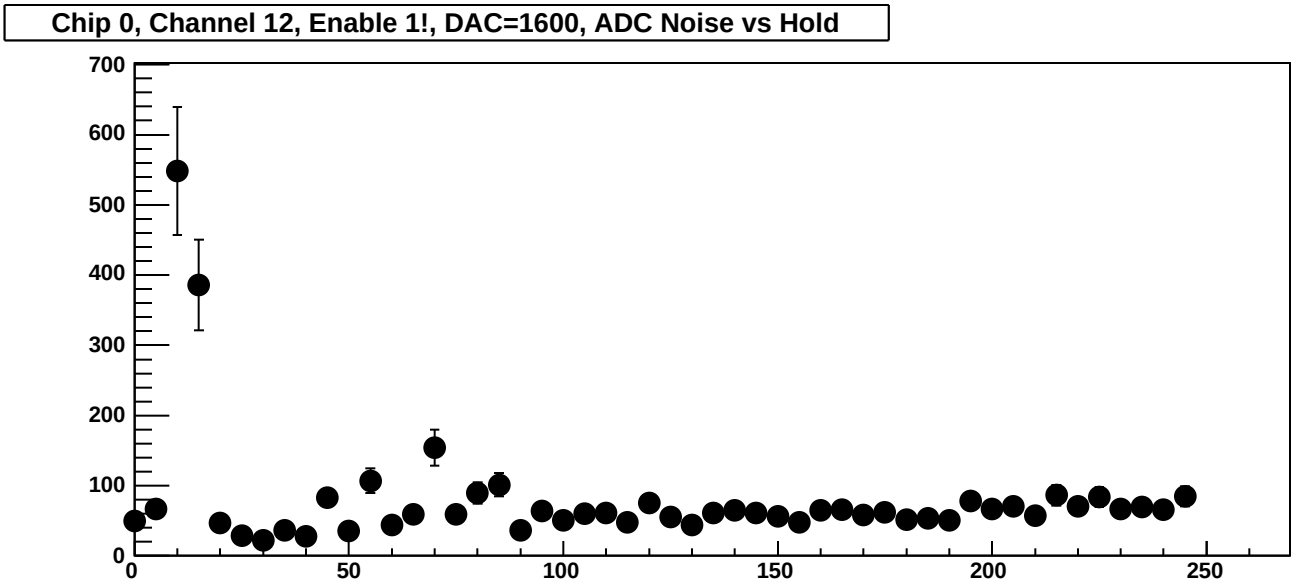
Chip 0, Channel 12, Enable 0, DAC=1600, ADC Noise vs Hold

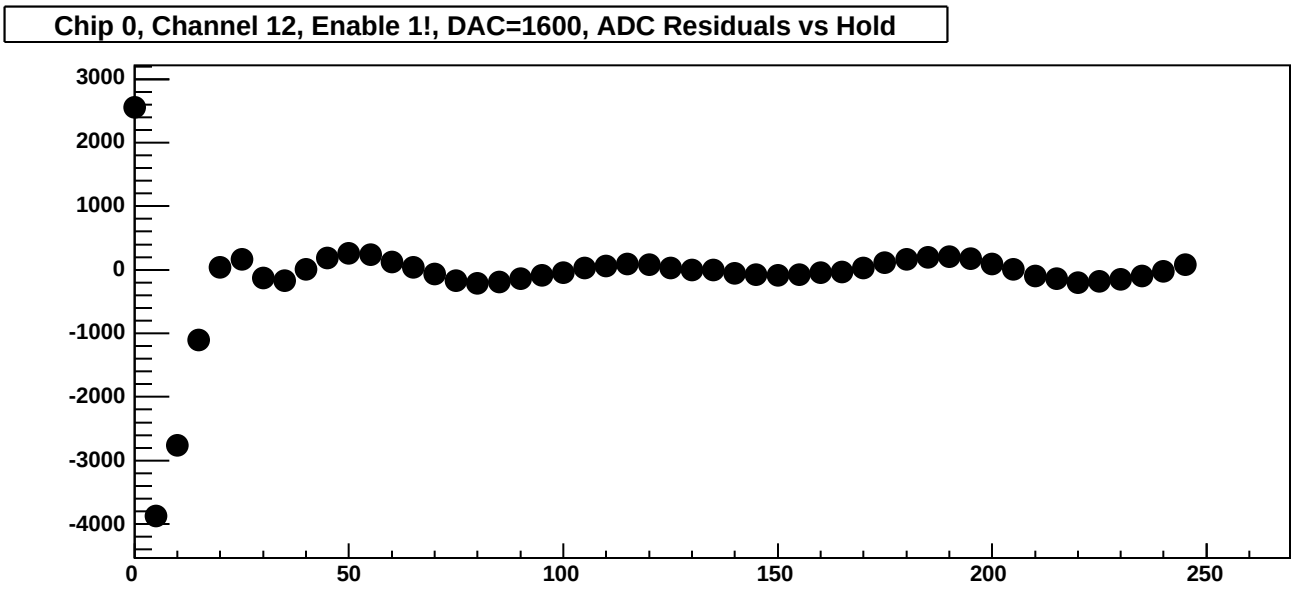


Chip 0, Channel 12, Enable 0, DAC=1600, ADC Residuals vs Hold

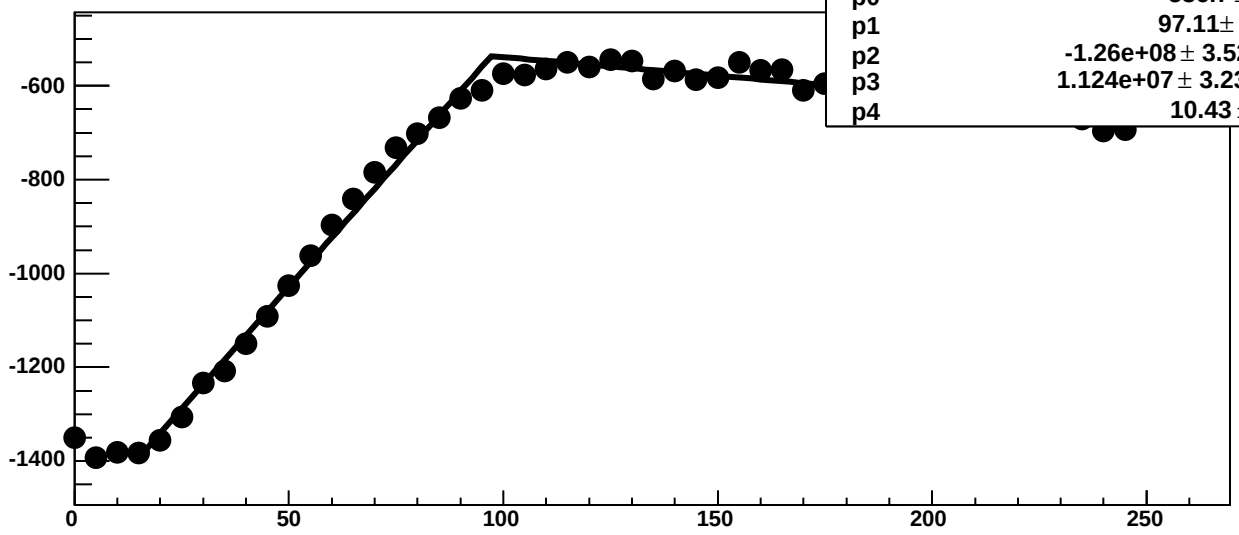



Chip 0, Channel 12, Enable 1!, DAC=1600, ADC Mean vs Hold
χ² / ndf 5633 / 42
p0 -3900 ± 4.235
p1 0.4165 ± 0.0207
p2 3.133e+04 ± 4.78
p3 29.32 ± 0.01179
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

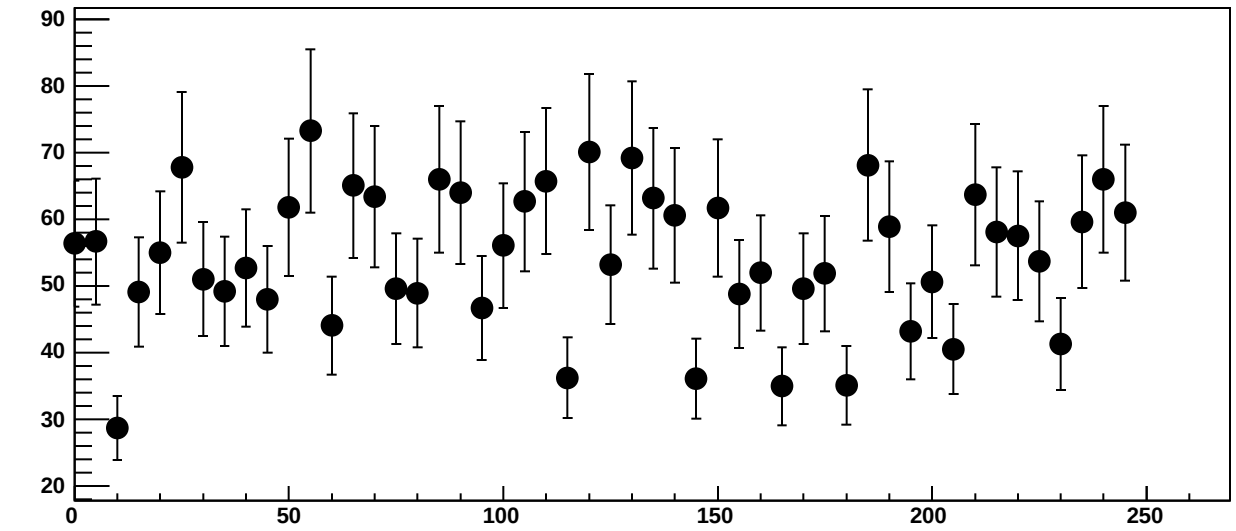

Chip 0, Channel 12, Enable 1!, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 0, Channel 12, Enable 1!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

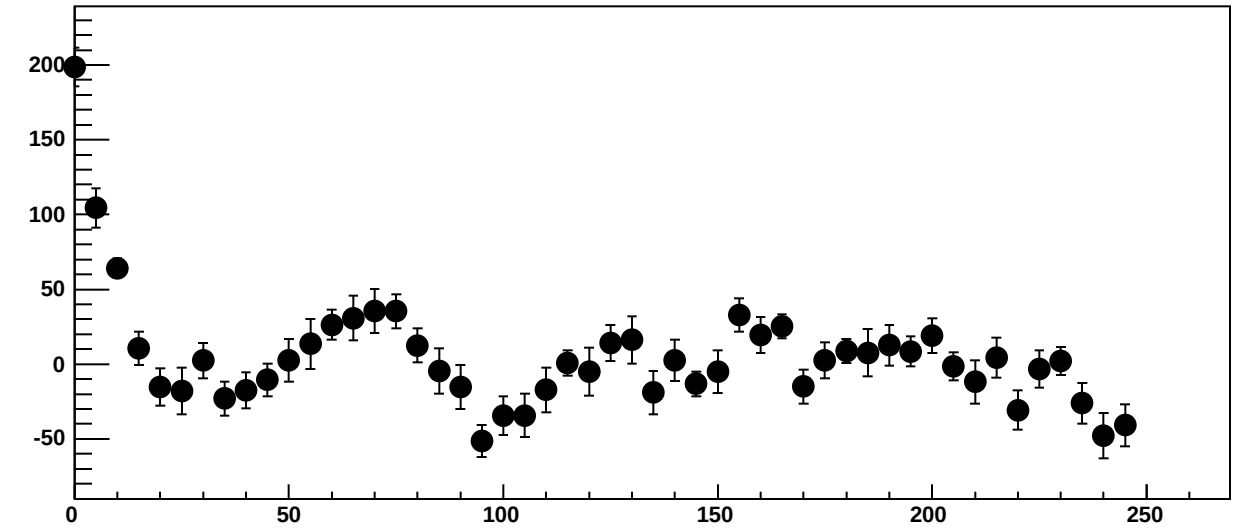
Chip 0, Channel 12, Enable 2, DAC=1600, ADC Mean vs Hold



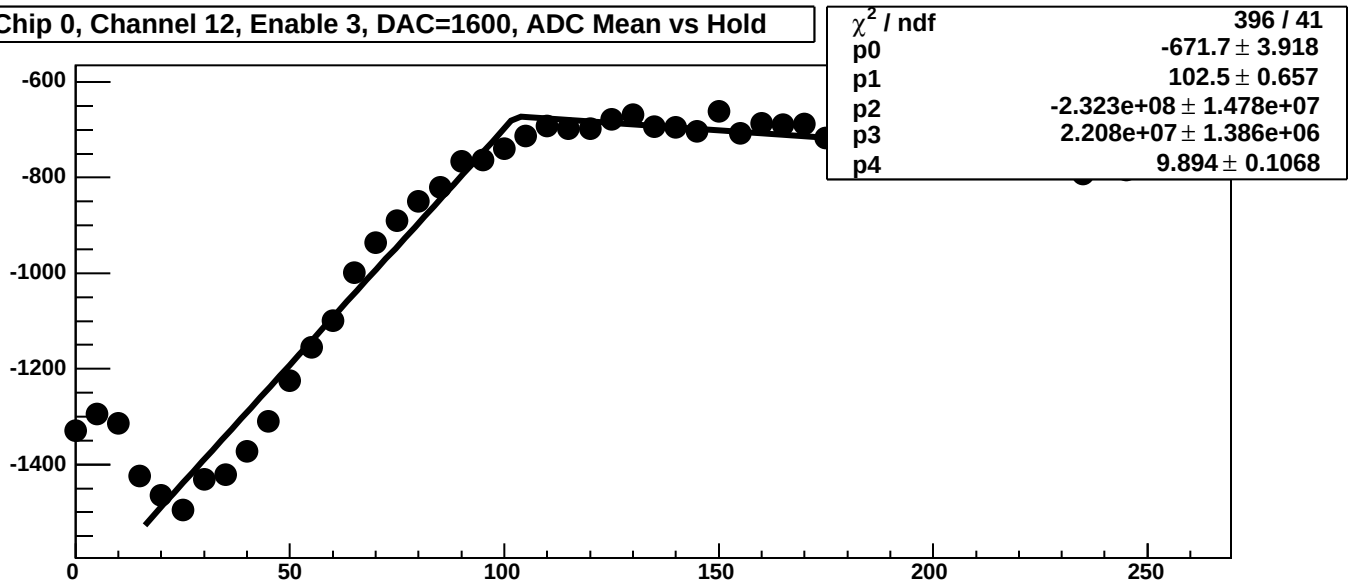
Chip 0, Channel 12, Enable 2, DAC=1600, ADC Noise vs Hold



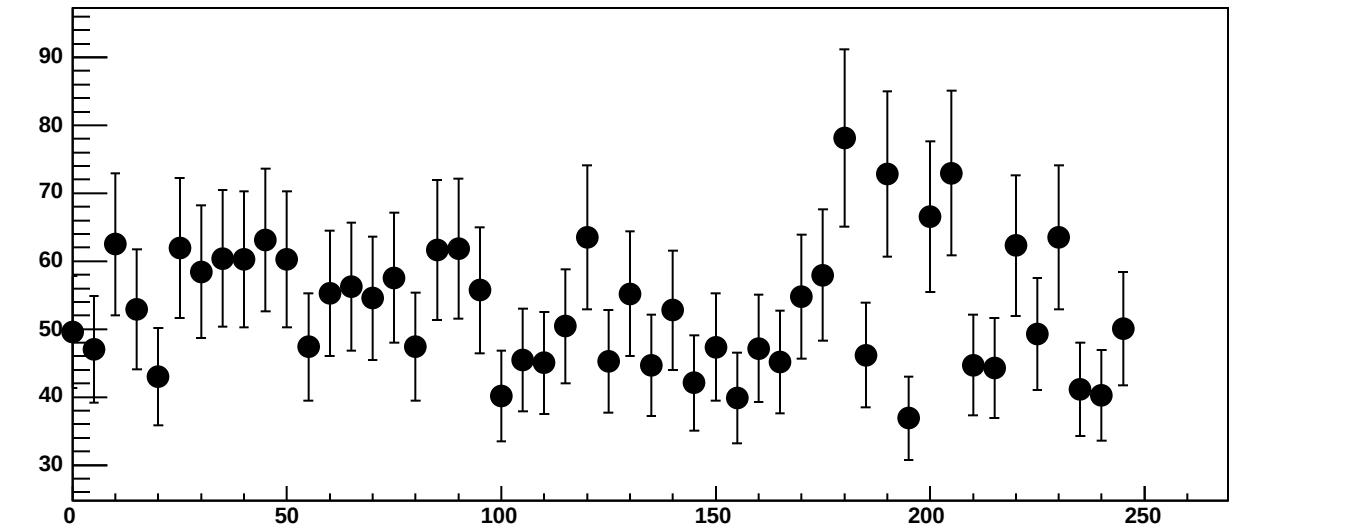
Chip 0, Channel 12, Enable 2, DAC=1600, ADC Residuals vs Hold



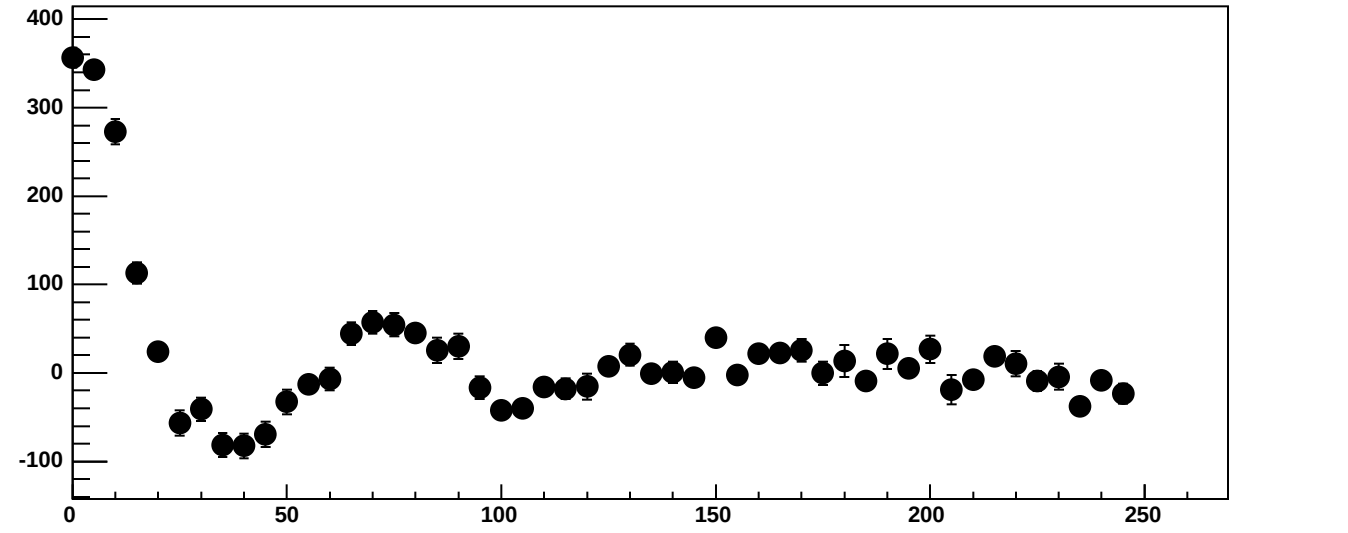
Chip 0, Channel 12, Enable 3, DAC=1600, ADC Mean vs Hold

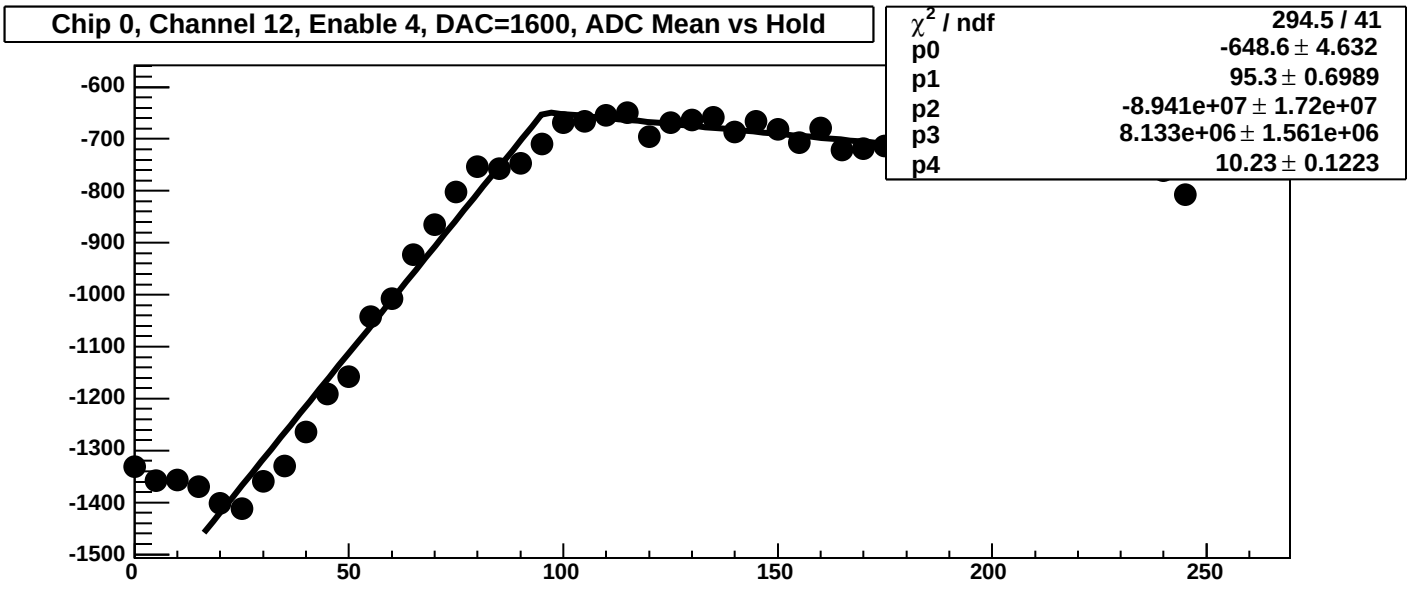


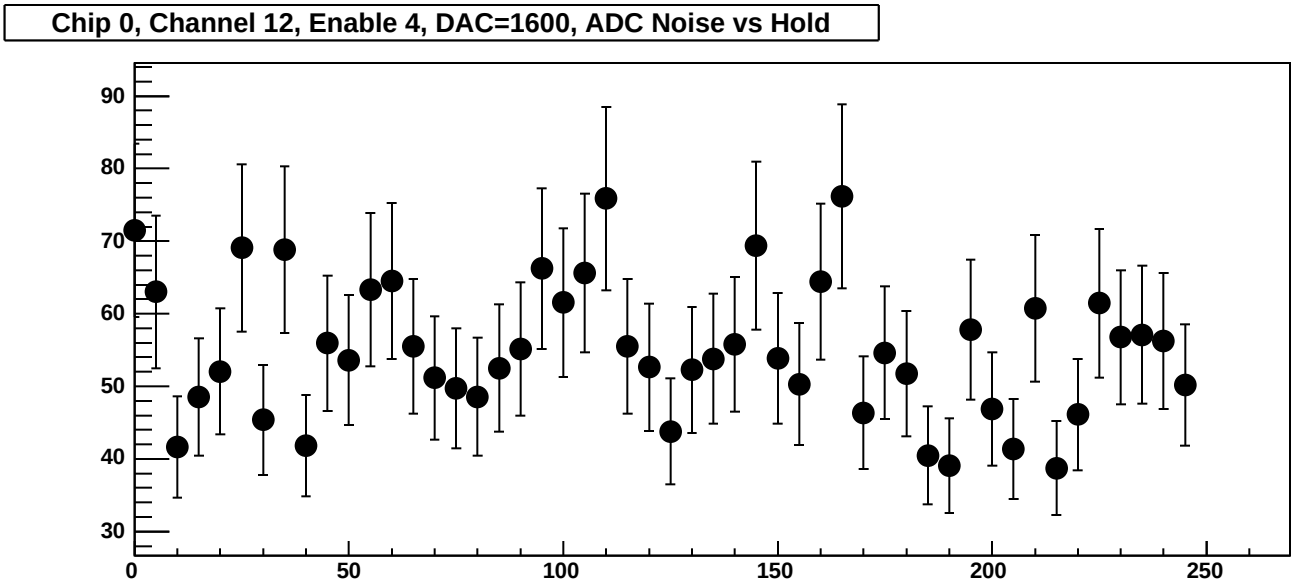
Chip 0, Channel 12, Enable 3, DAC=1600, ADC Noise vs Hold

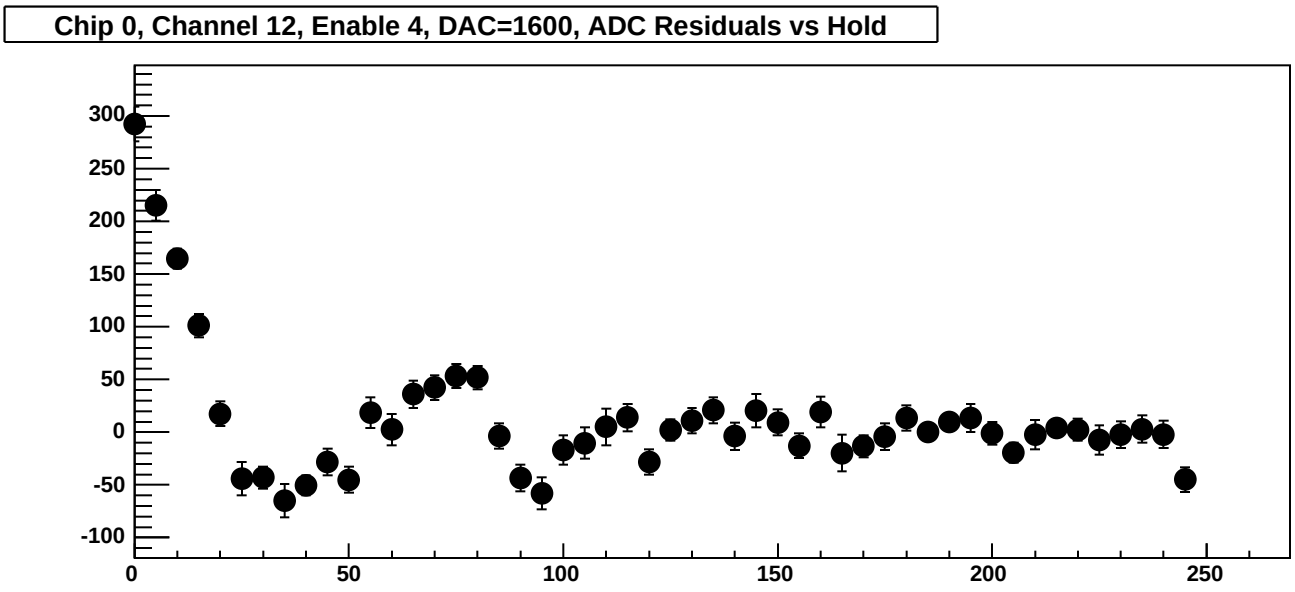


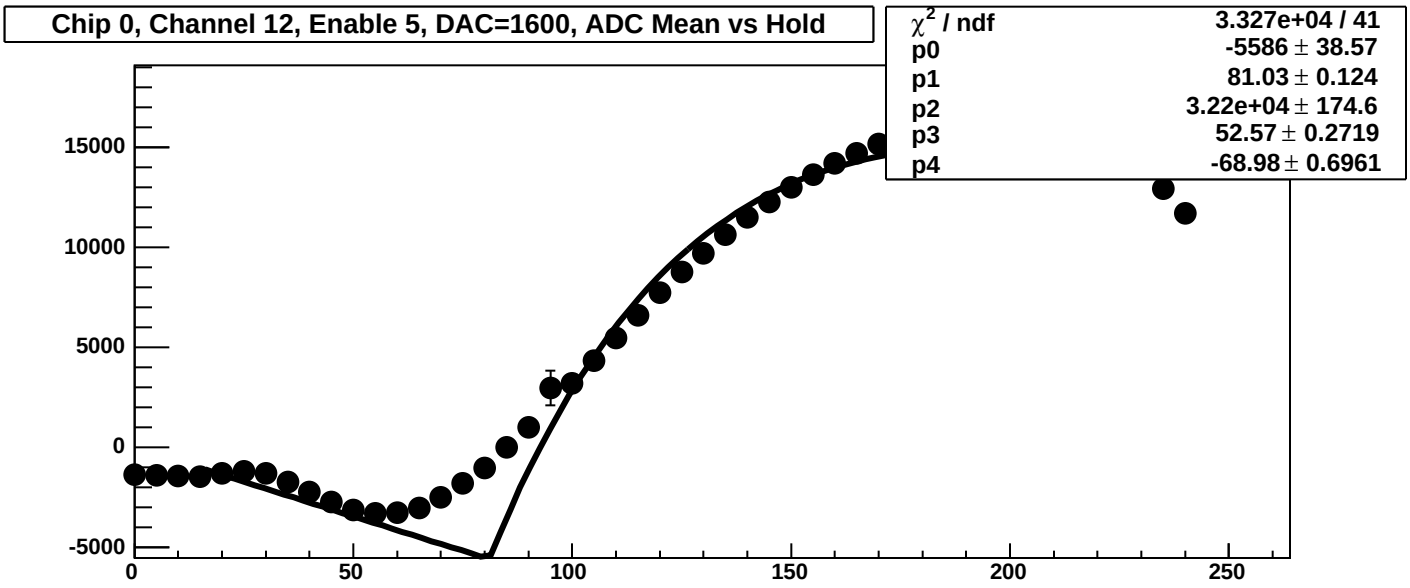
Chip 0, Channel 12, Enable 3, DAC=1600, ADC Residuals vs Hold

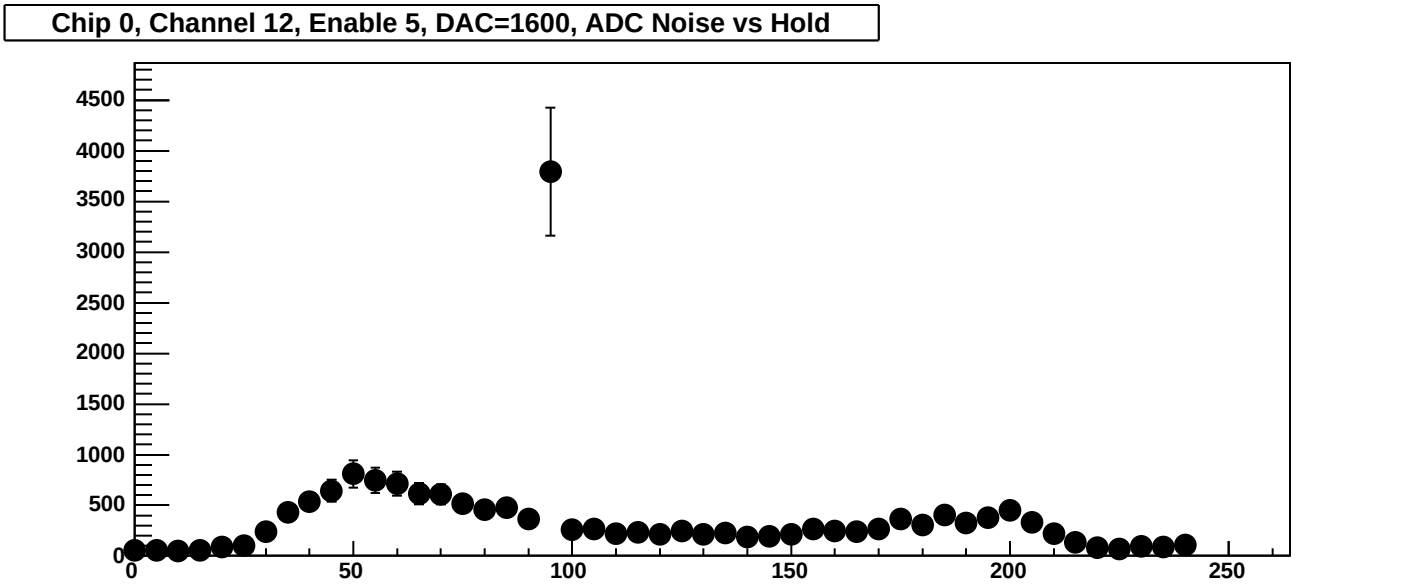


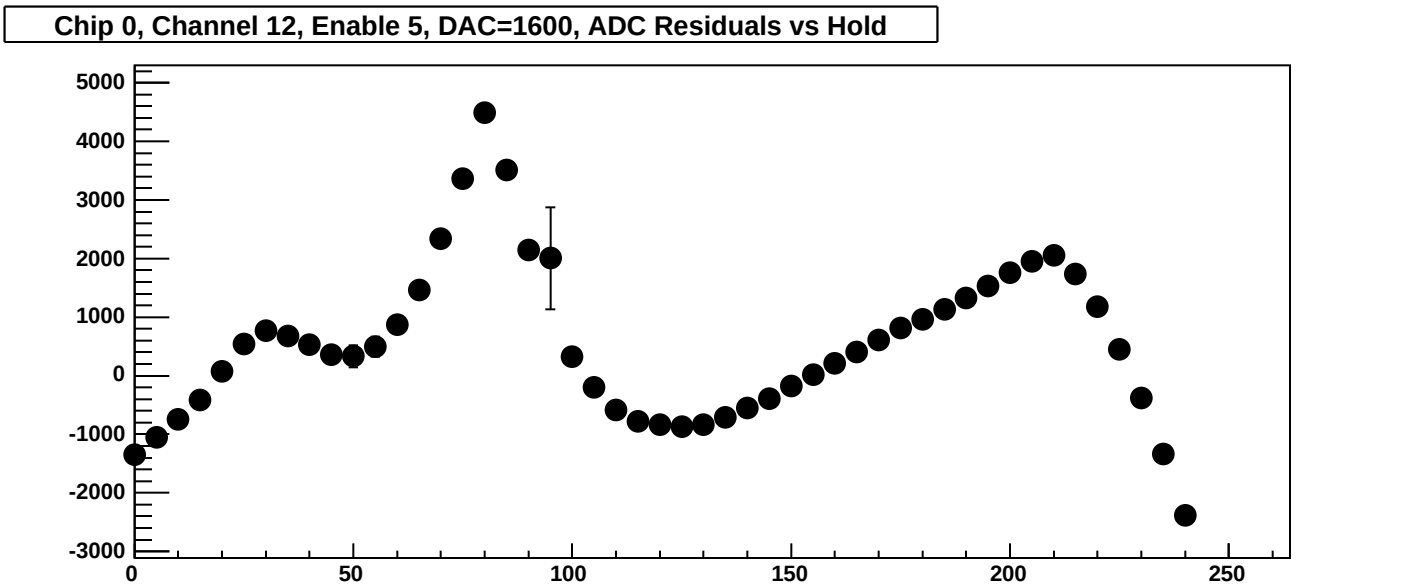

Chip 0, Channel 12, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 294.5 / 41
p0 -648.6 ± 4.632
p1 95.3 ± 0.6989
p2 -8.941e+07 ± 1.72e+07
p3 8.133e+06 ± 1.561e+06
p4 10.23 ± 0.1223
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250

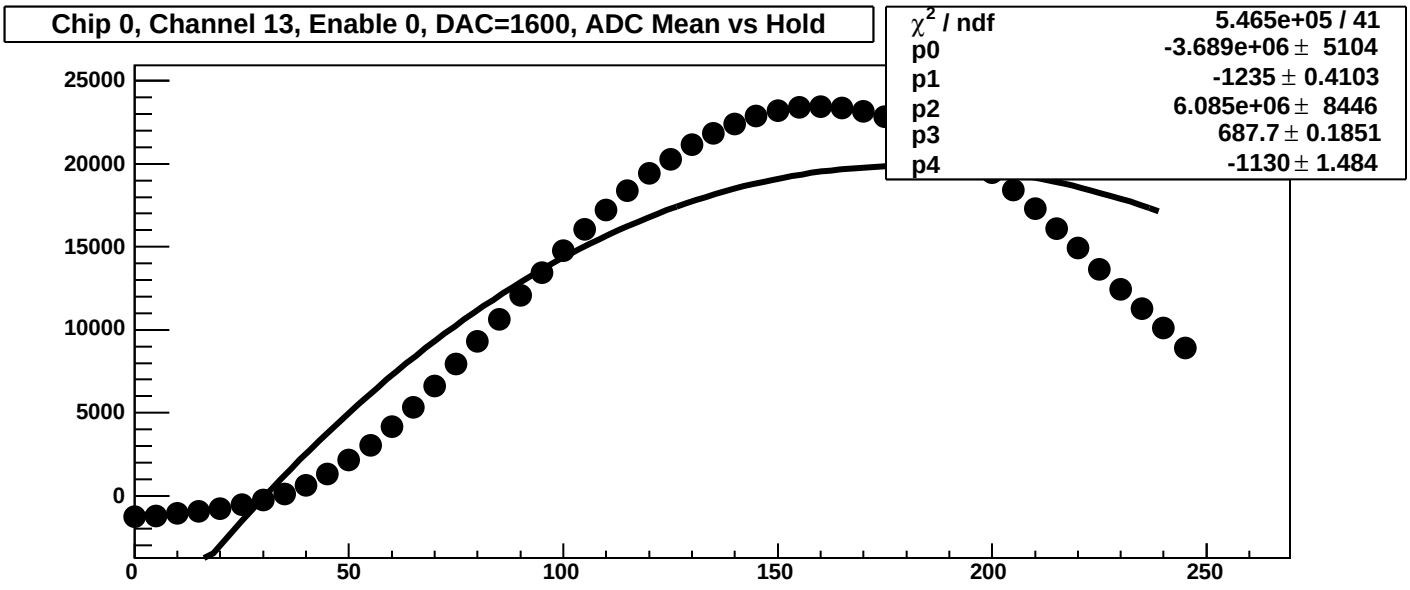

Chip 0, Channel 12, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

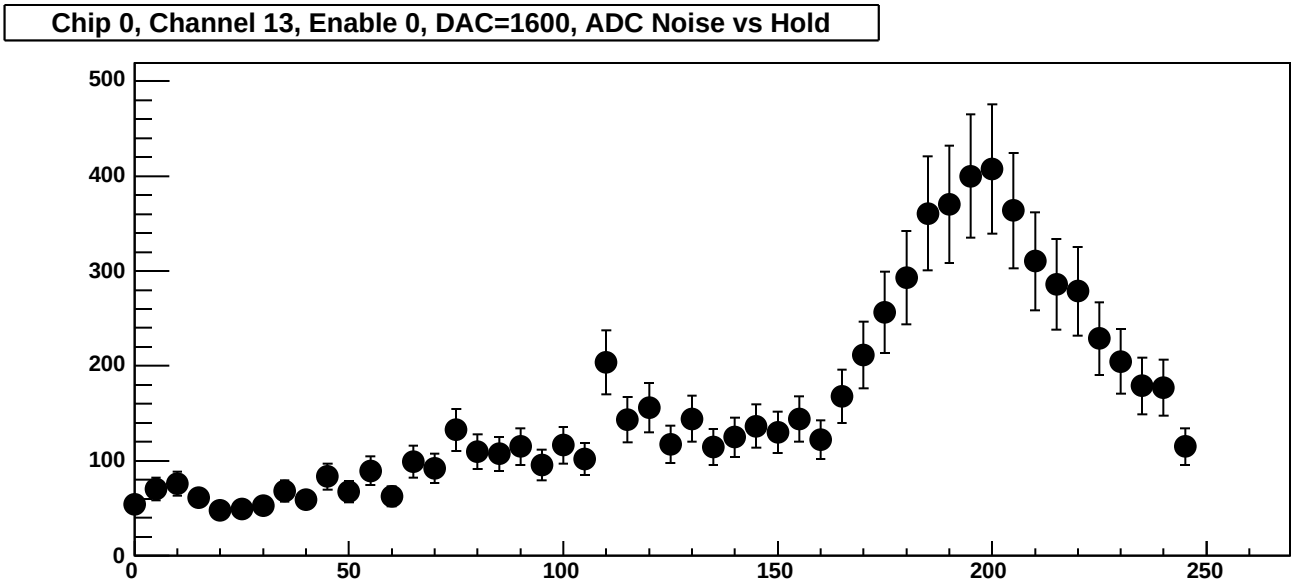

Chip 0, Channel 12, Enable 4, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

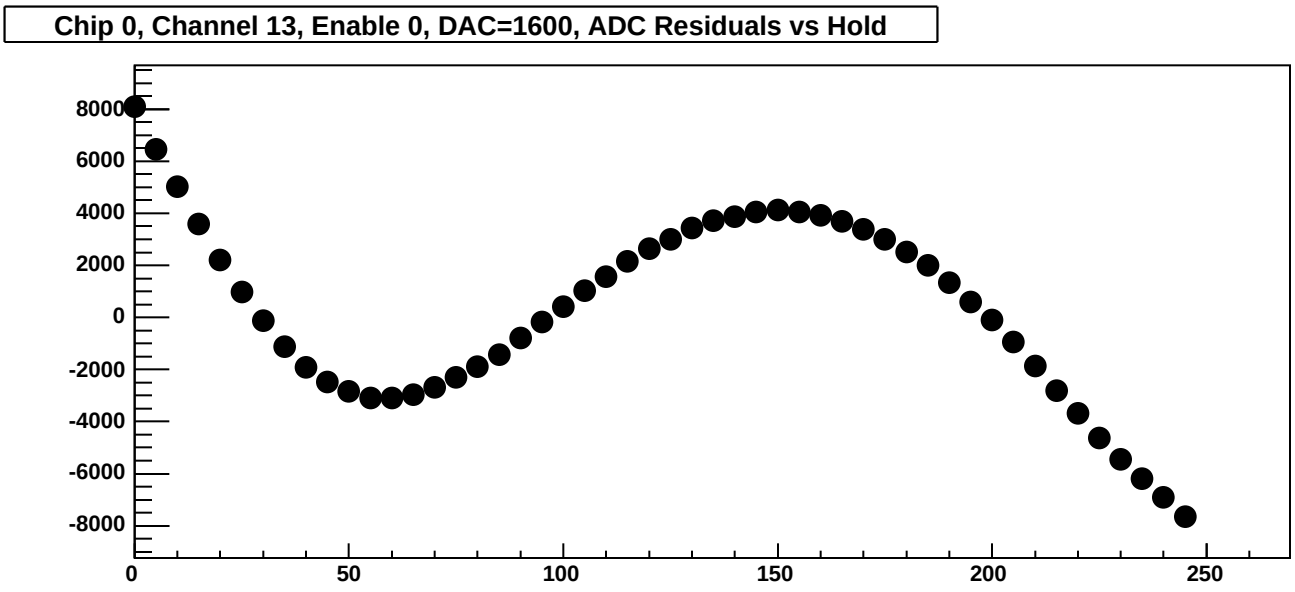

Chip 0, Channel 12, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.327e+04 / 41
p0 -5586 ± 38.57
p1 81.03 ± 0.124
p2 3.22e+04 ± 174.6
p3 52.57 ± 0.2719
p4 -68.98 ± 0.6961
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 0, Channel 12, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250

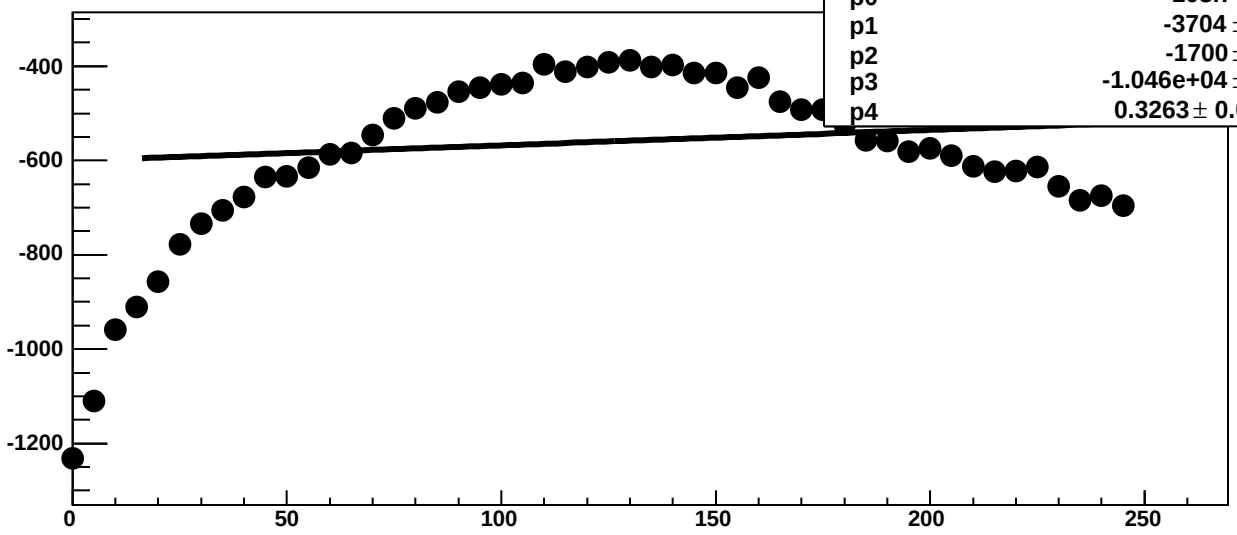

Chip 0, Channel 12, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
4000
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250


Chip 0, Channel 13, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.465e+05 / 41
p0 -3.689e+06 ± 5104
p1 -1235 ± 0.4103
p2 6.085e+06 ± 8446
p3 687.7 ± 0.1851
p4 -1130 ± 1.484
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

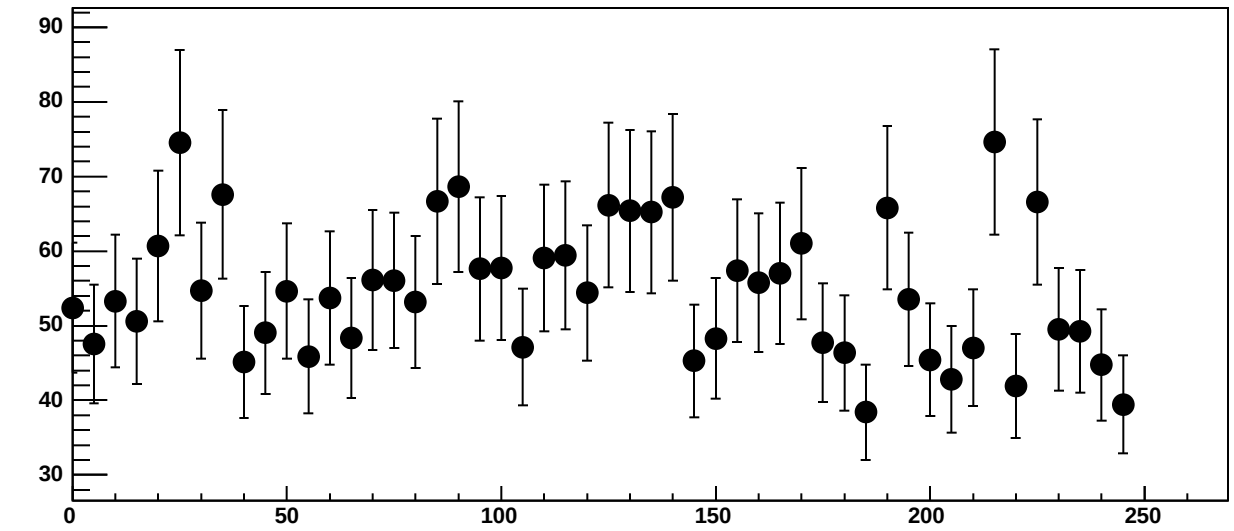

Chip 0, Channel 13, Enable 0, DAC=1600, ADC Noise vs Hold
500
400
300
200
100
0
0
50
100
150
200
250


Chip 0, Channel 13, Enable 0, DAC=1600, ADC Residuals vs Hold
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250

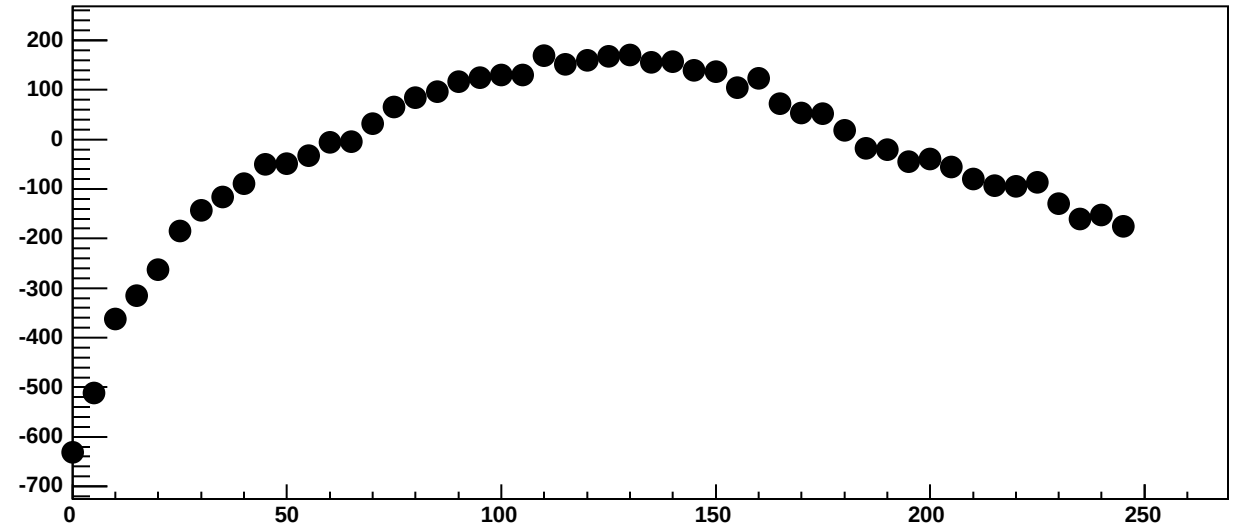
Chip 0, Channel 13, Enable 1, DAC=1600, ADC Mean vs Hold



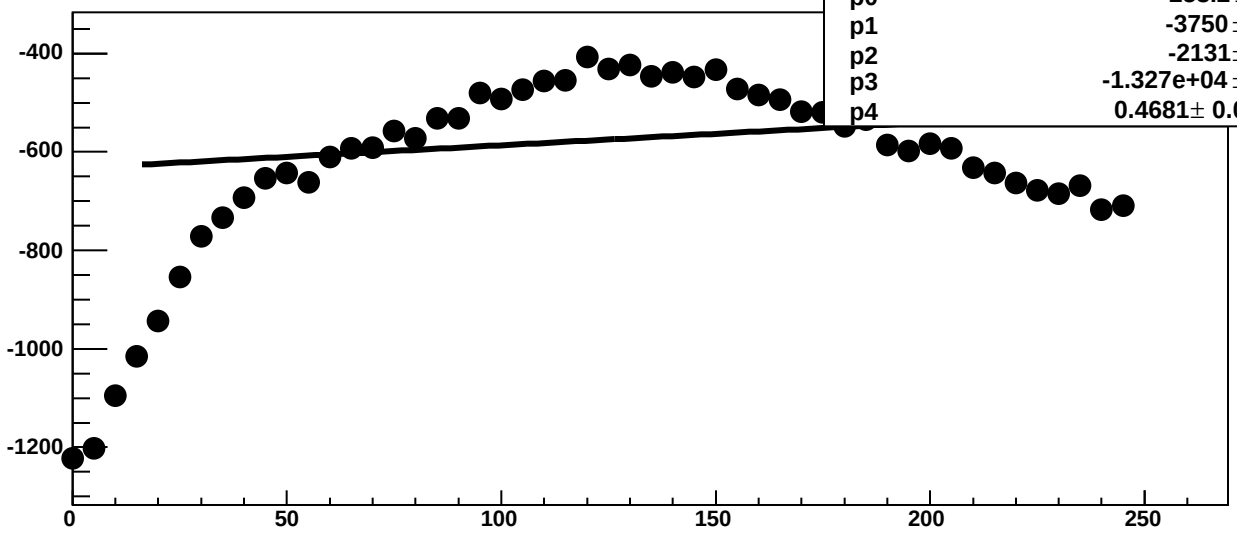
Chip 0, Channel 13, Enable 1, DAC=1600, ADC Noise vs Hold



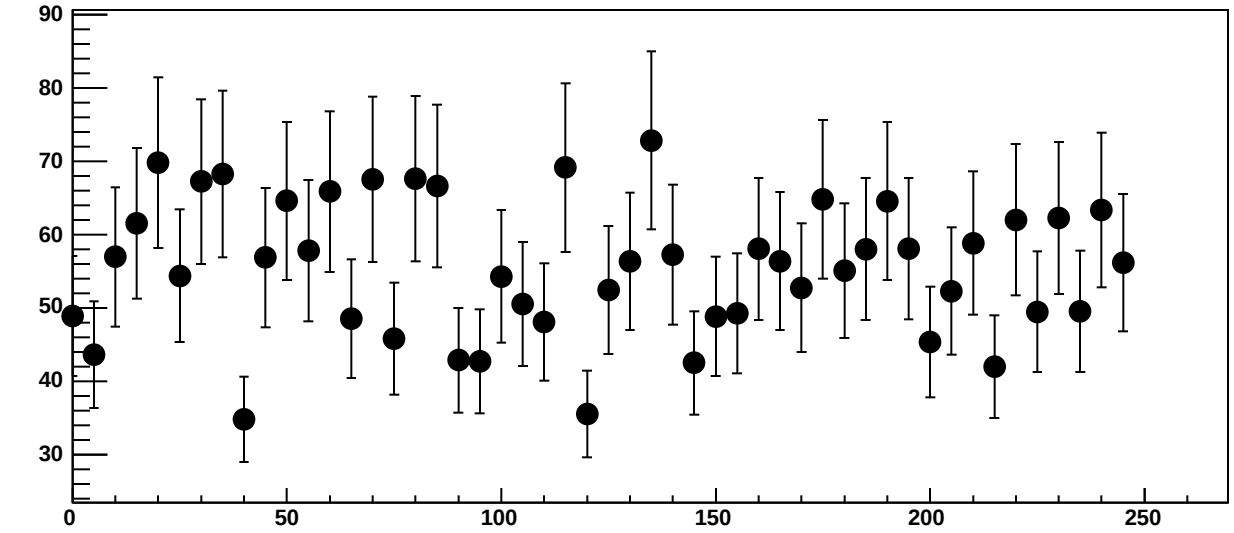
Chip 0, Channel 13, Enable 1, DAC=1600, ADC Residuals vs Hold



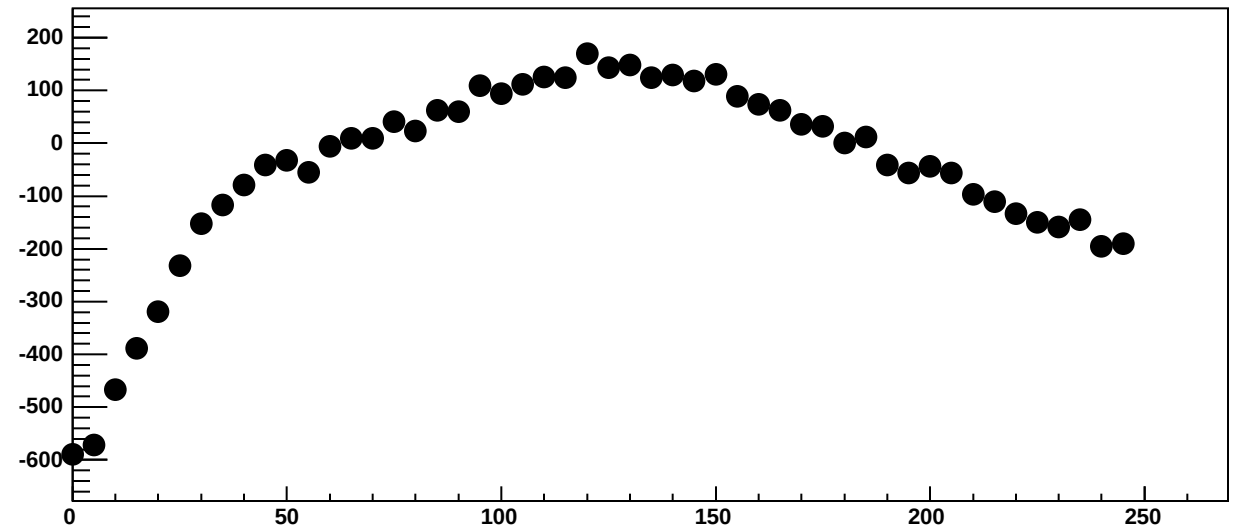
Chip 0, Channel 13, Enable 2, DAC=1600, ADC Mean vs Hold



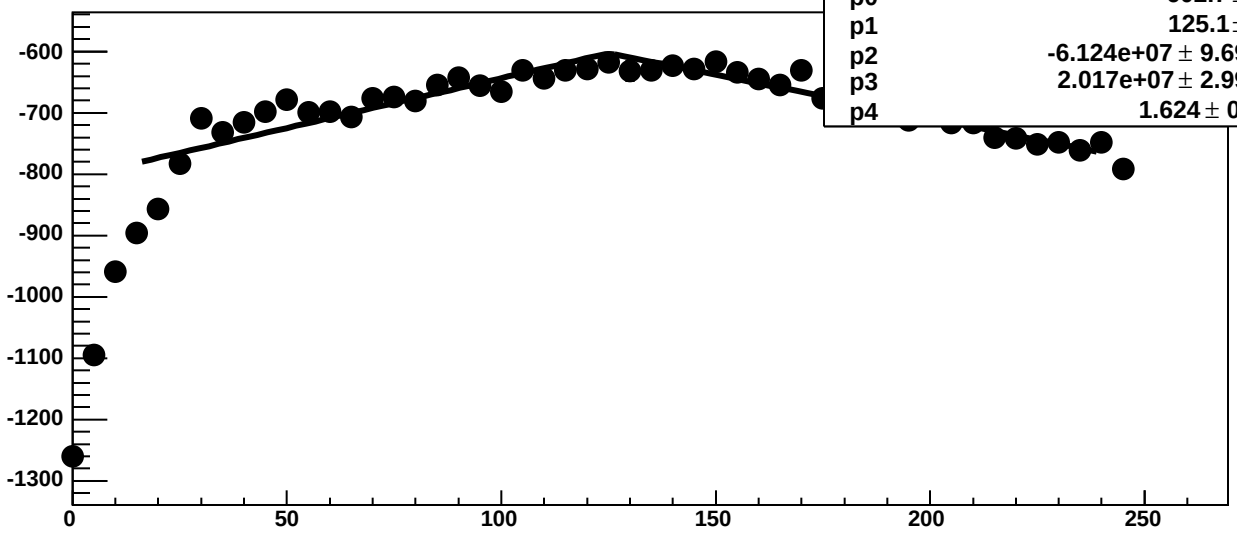
Chip 0, Channel 13, Enable 2, DAC=1600, ADC Noise vs Hold



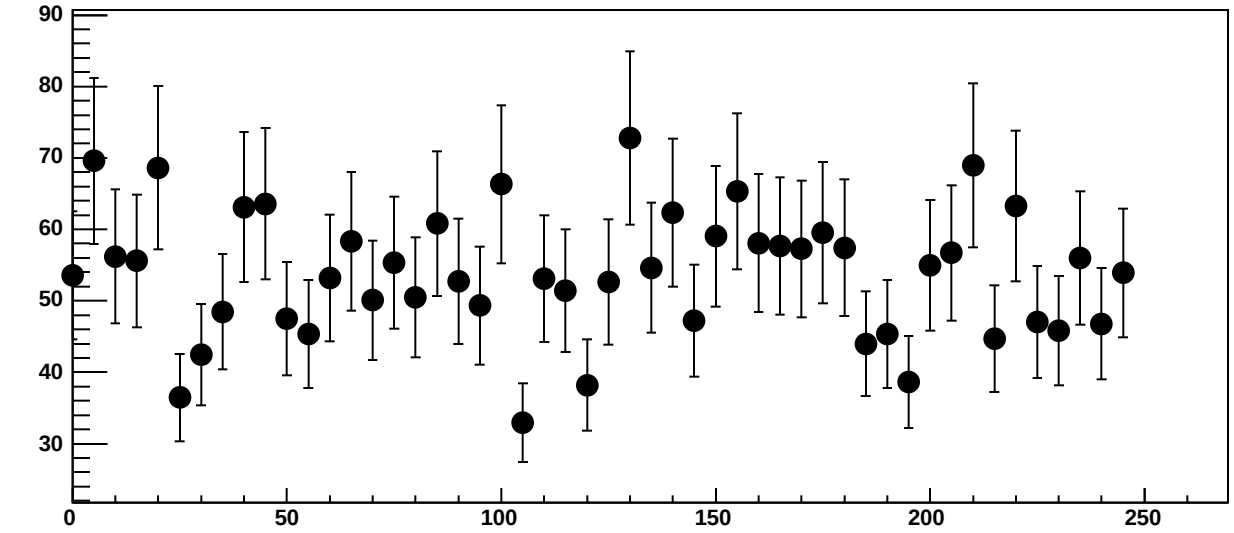
Chip 0, Channel 13, Enable 2, DAC=1600, ADC Residuals vs Hold



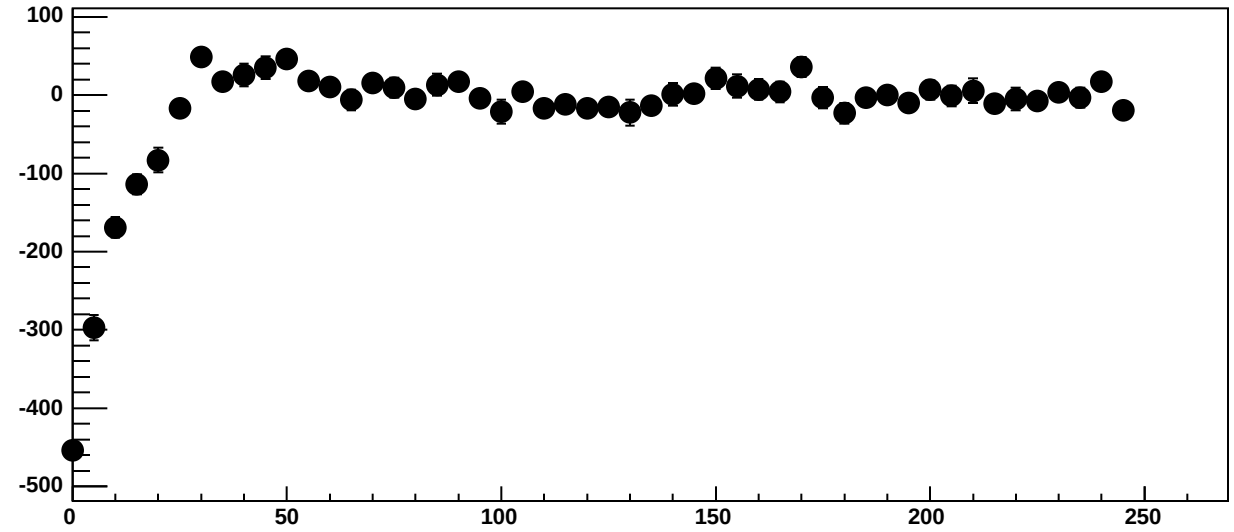
Chip 0, Channel 13, Enable 3, DAC=1600, ADC Mean vs Hold

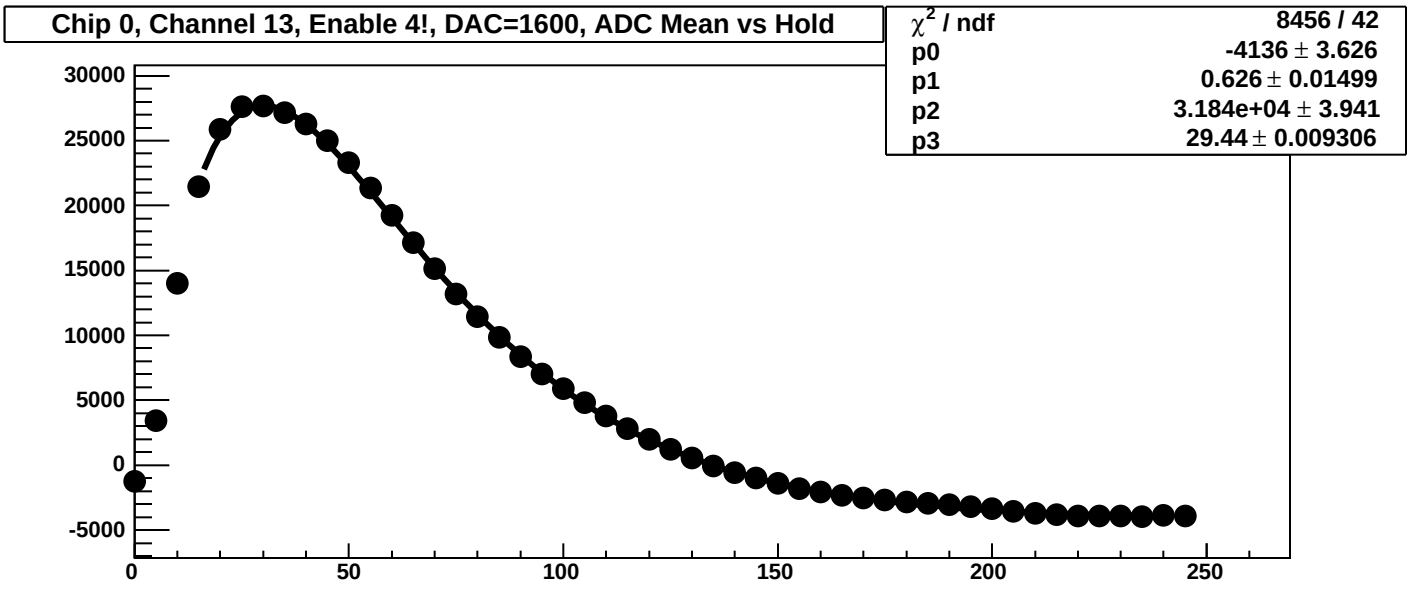


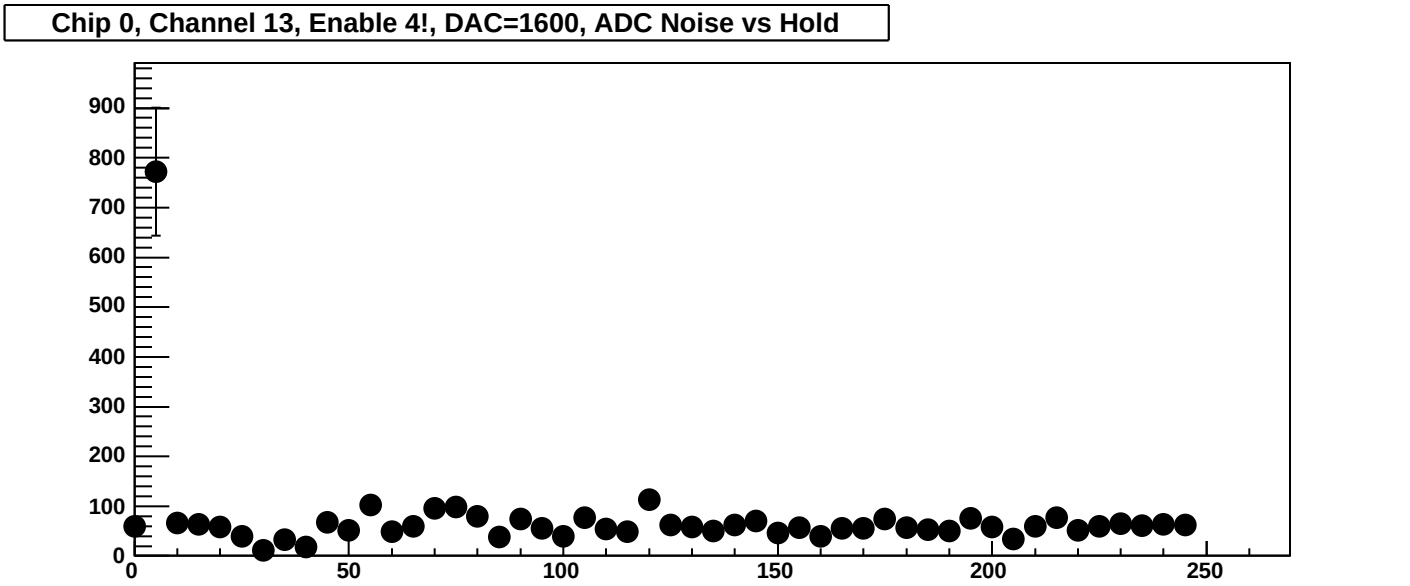
Chip 0, Channel 13, Enable 3, DAC=1600, ADC Noise vs Hold

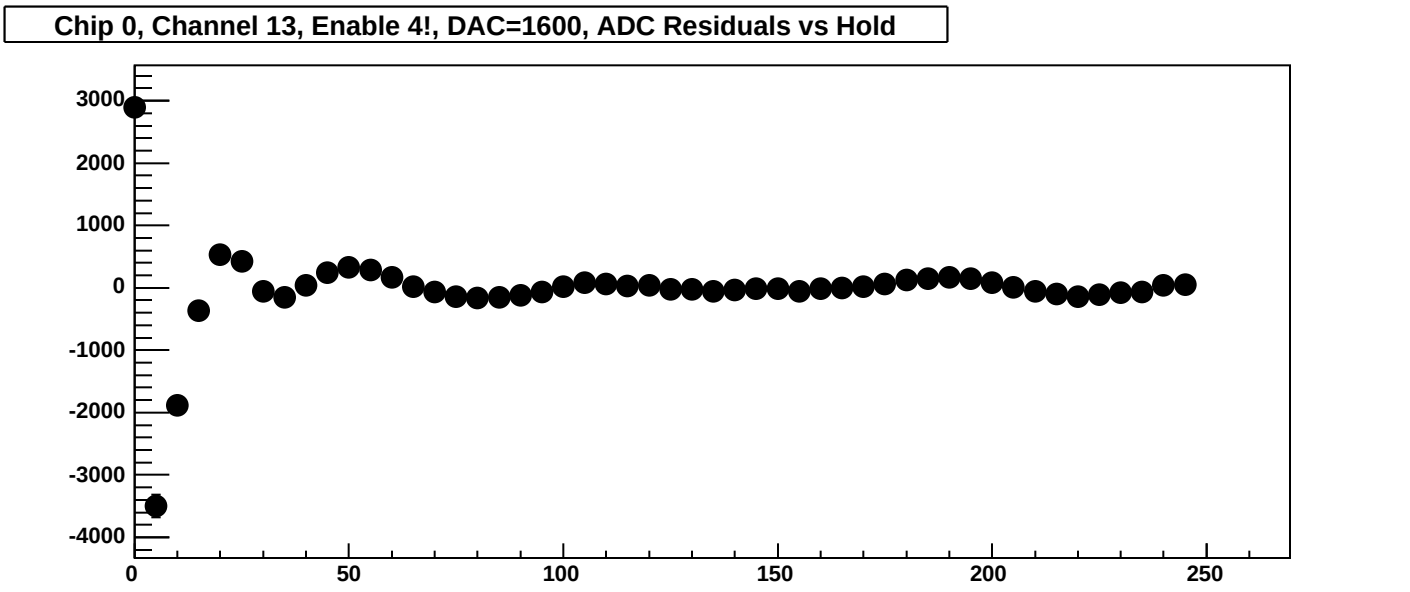


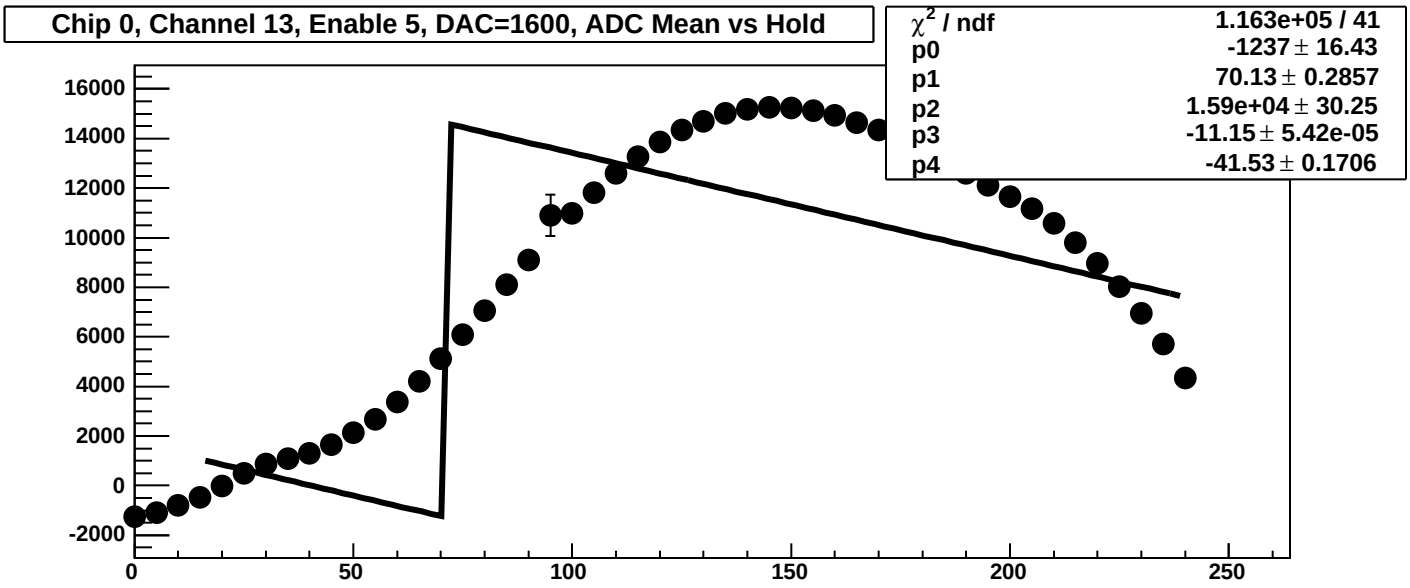
Chip 0, Channel 13, Enable 3, DAC=1600, ADC Residuals vs Hold

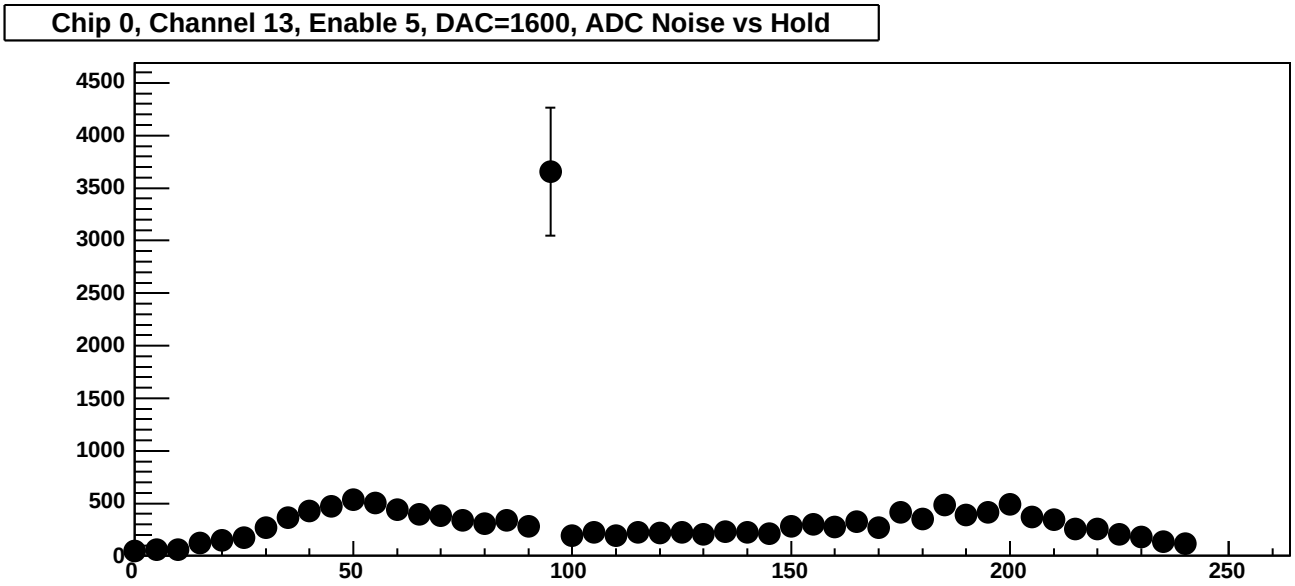


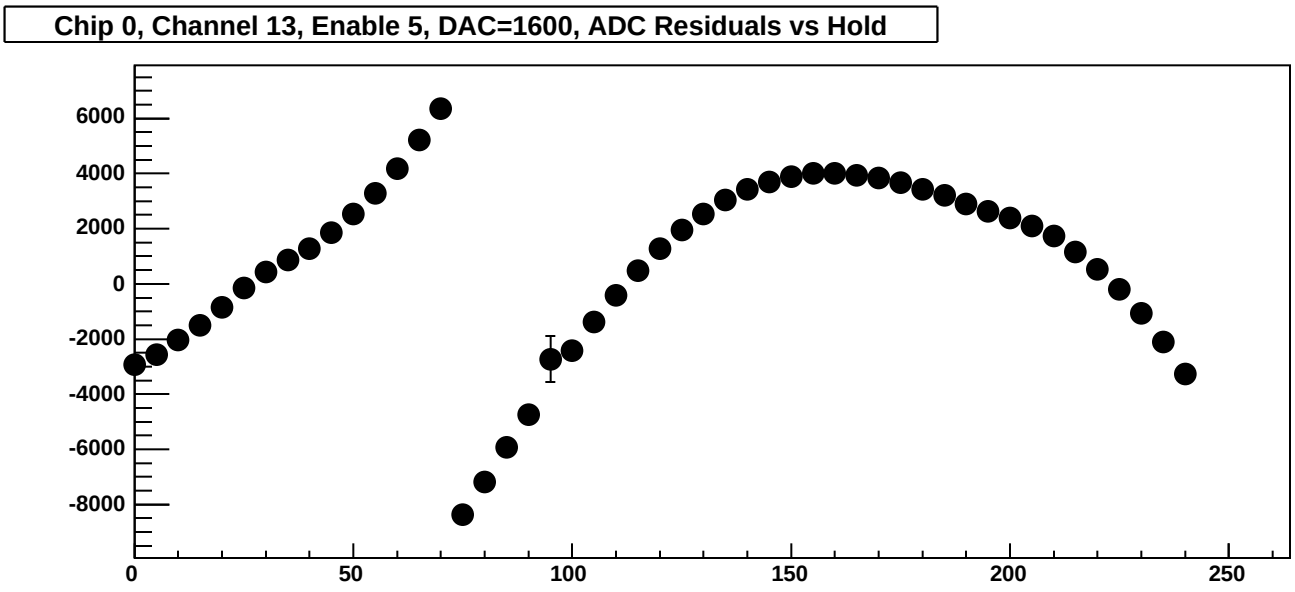

Chip 0, Channel 13, Enable 4!, DAC=1600, ADC Mean vs Hold
χ² / ndf 8456 / 42
p0 -4136 ± 3.626
p1 0.626 ± 0.01499
p2 3.184e+04 ± 3.941
p3 29.44 ± 0.009306
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

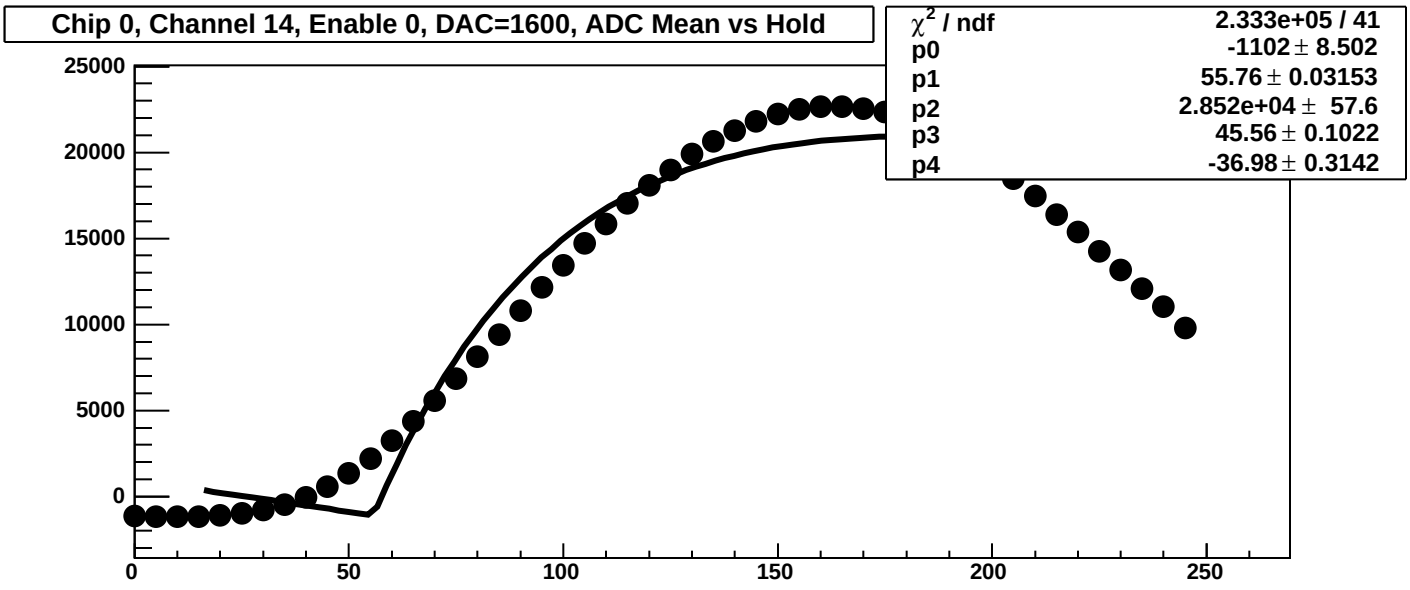

Chip 0, Channel 13, Enable 4!, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250

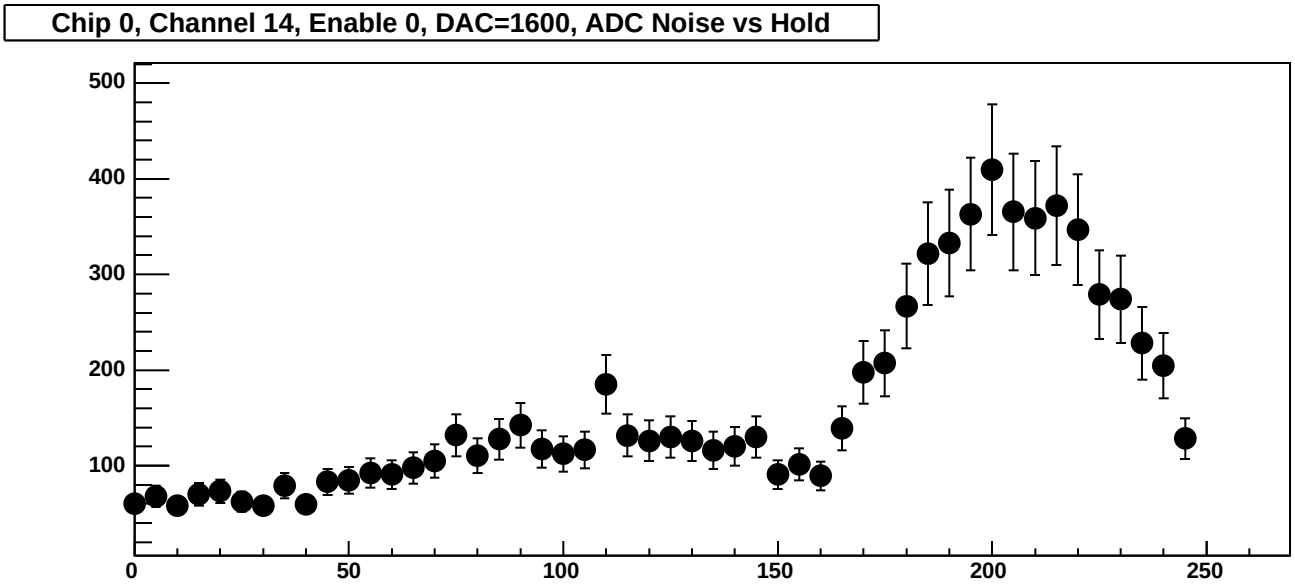

Chip 0, Channel 13, Enable 4!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

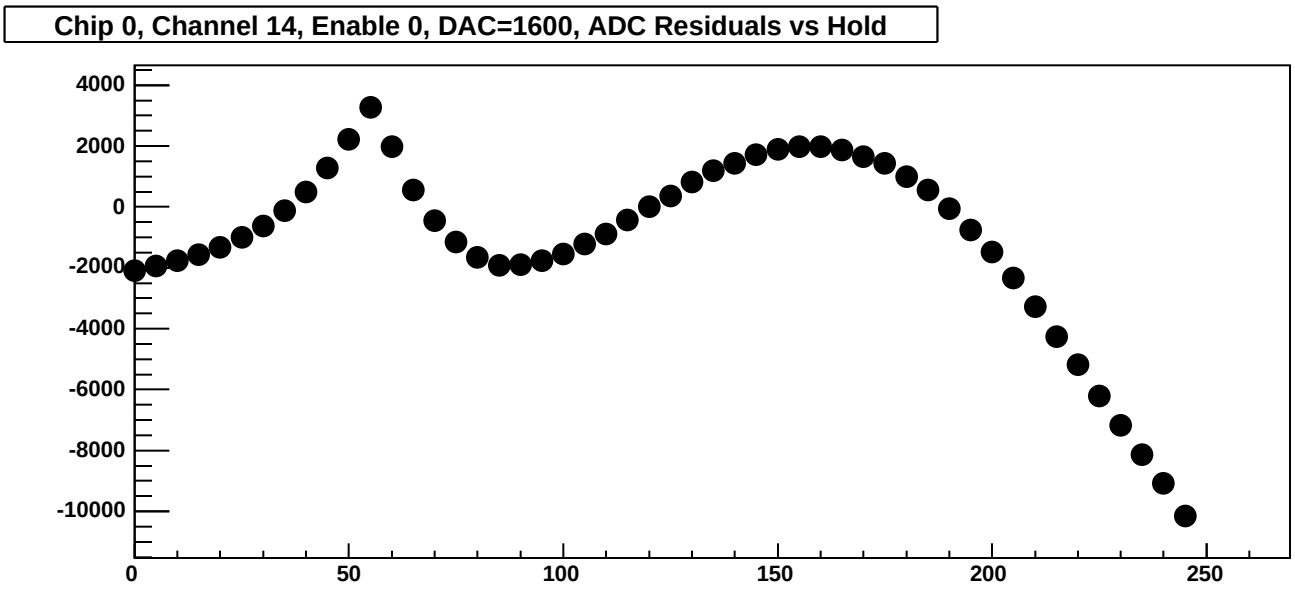

Chip 0, Channel 13, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.163e+05 / 41
p0 -1237 ± 16.43
p1 70.13 ± 0.2857
p2 1.59e+04 ± 30.25
p3 -11.15 ± 5.42e-05
p4 -41.53 ± 0.1706
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250


Chip 0, Channel 13, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250

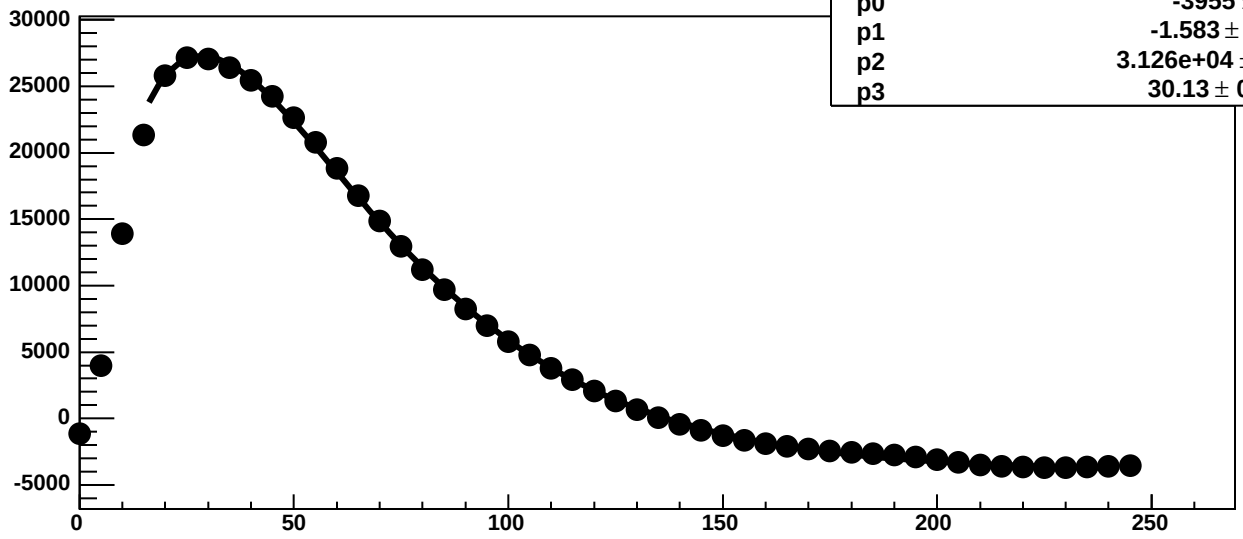

Chip 0, Channel 13, Enable 5, DAC=1600, ADC Residuals vs Hold
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250


Chip 0, Channel 14, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.333e+05 / 41
p0 -1102 ± 8.502
p1 55.76 ± 0.03153
p2 2.852e+04 ± 57.6
p3 45.56 ± 0.1022
p4 -36.98 ± 0.3142
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

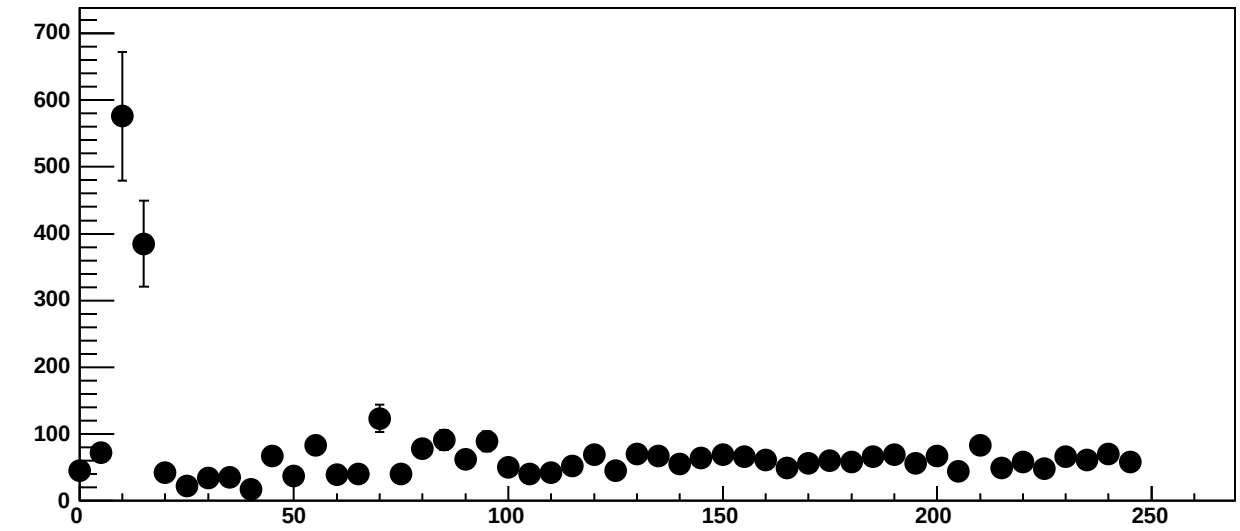

Chip 0, Channel 14, Enable 0, DAC=1600, ADC Noise vs Hold
500
400
300
200
100
0
50
100
150
200
250


Chip 0, Channel 14, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

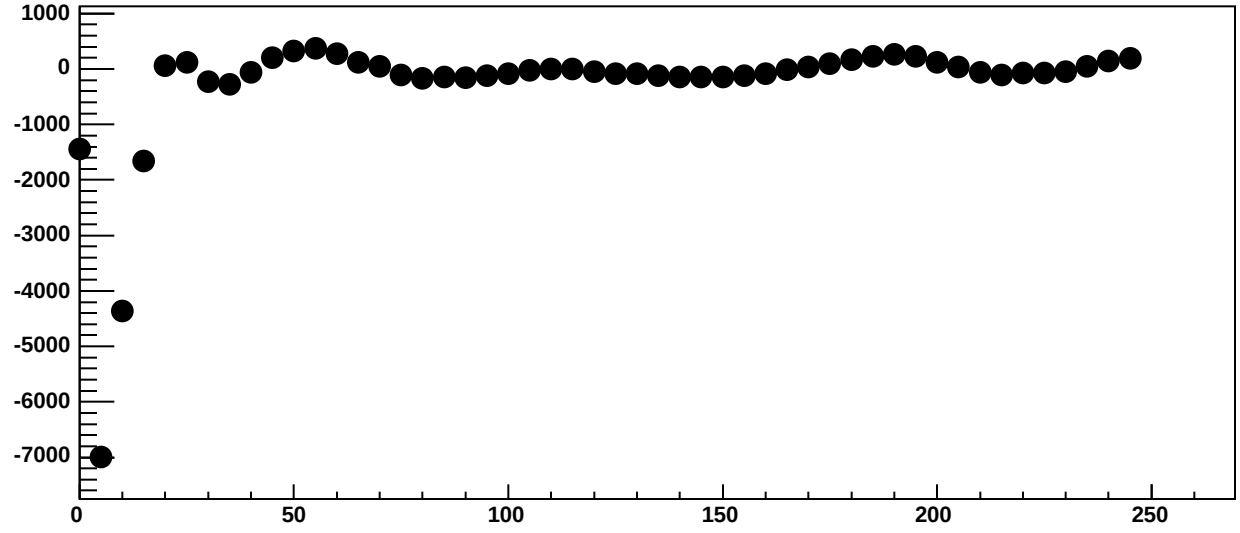
Chip 0, Channel 14, Enable 1!, DAC=1600, ADC Mean vs Hold

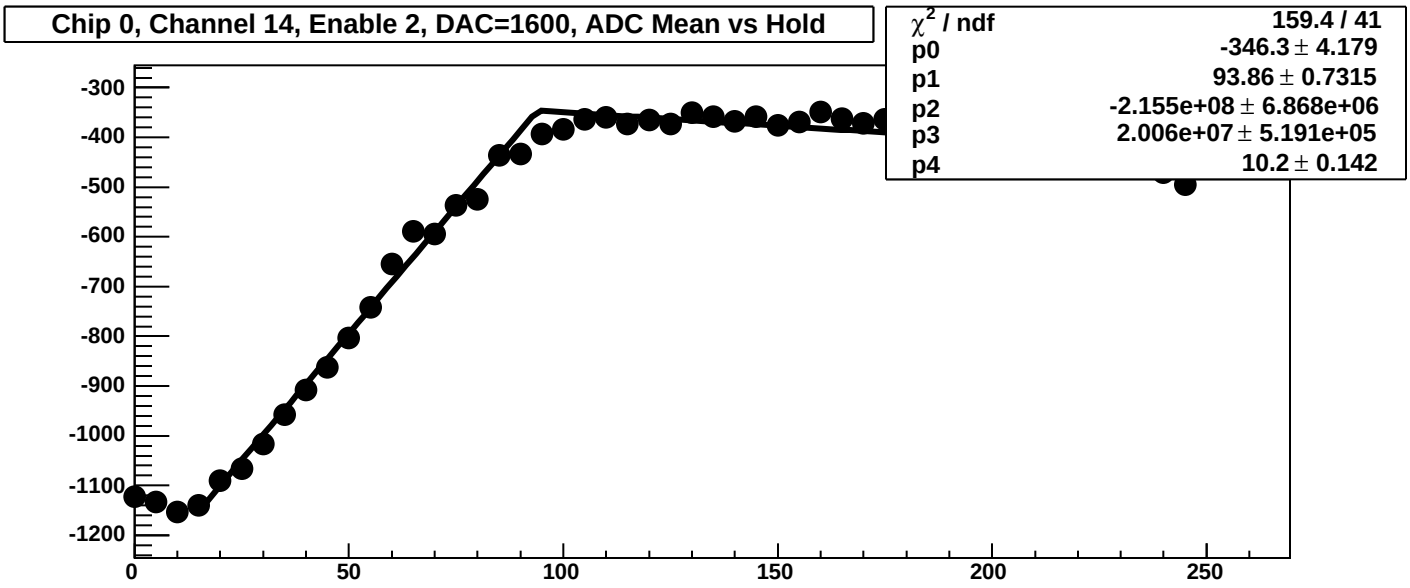


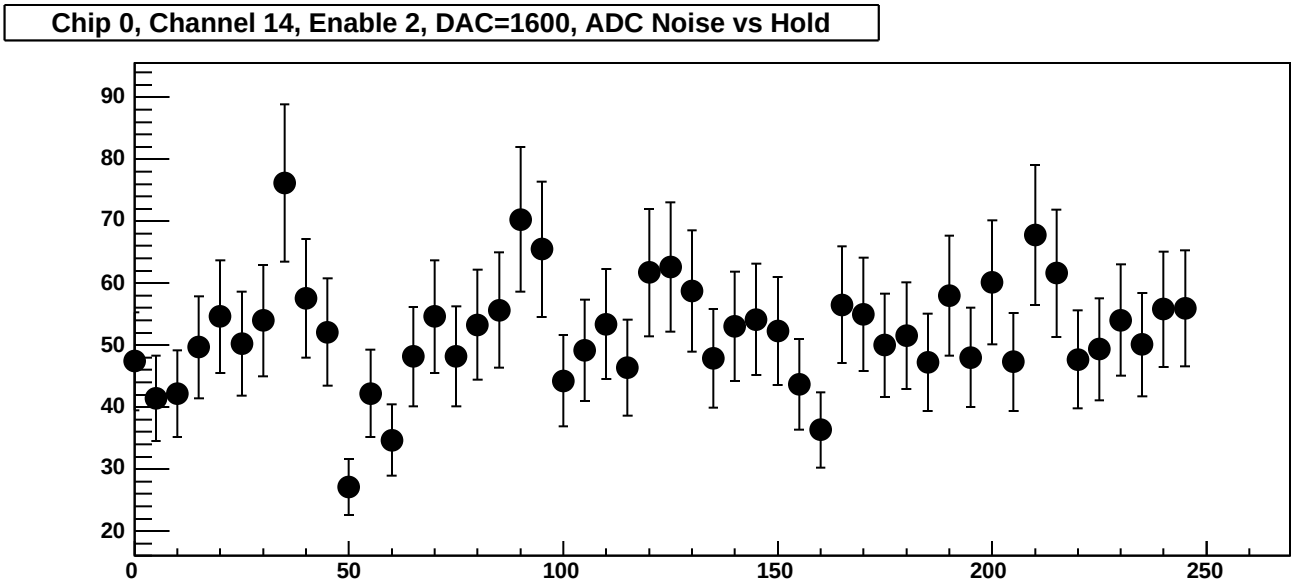
Chip 0, Channel 14, Enable 1!, DAC=1600, ADC Noise vs Hold

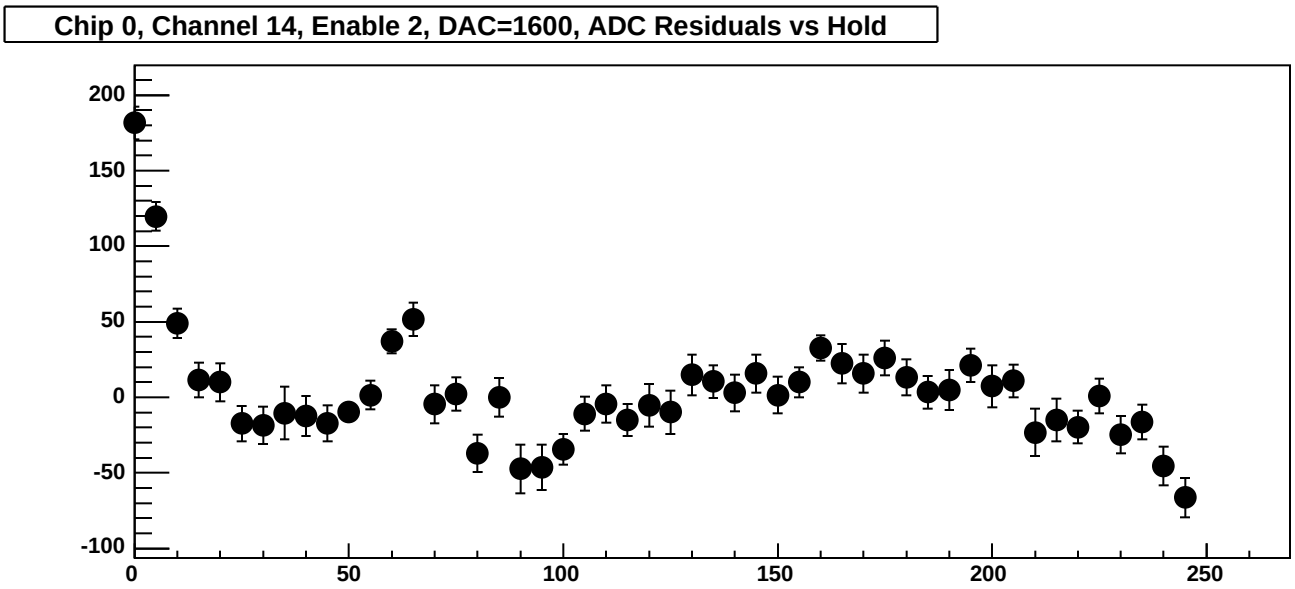


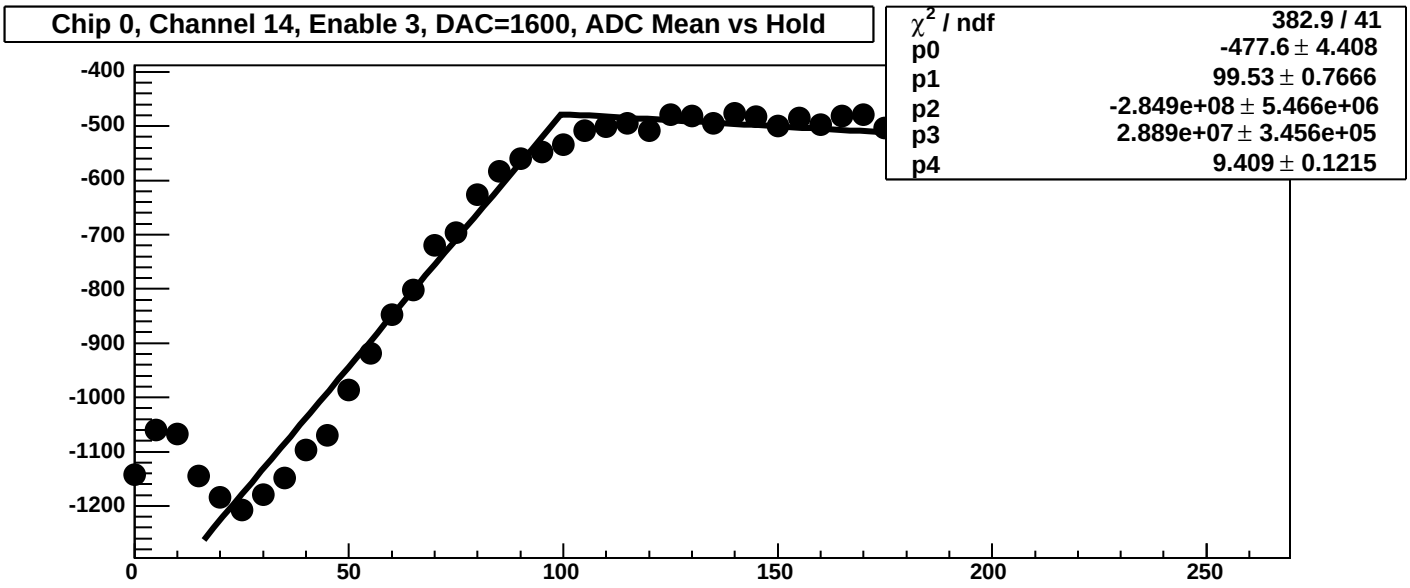
Chip 0, Channel 14, Enable 1!, DAC=1600, ADC Residuals vs Hold

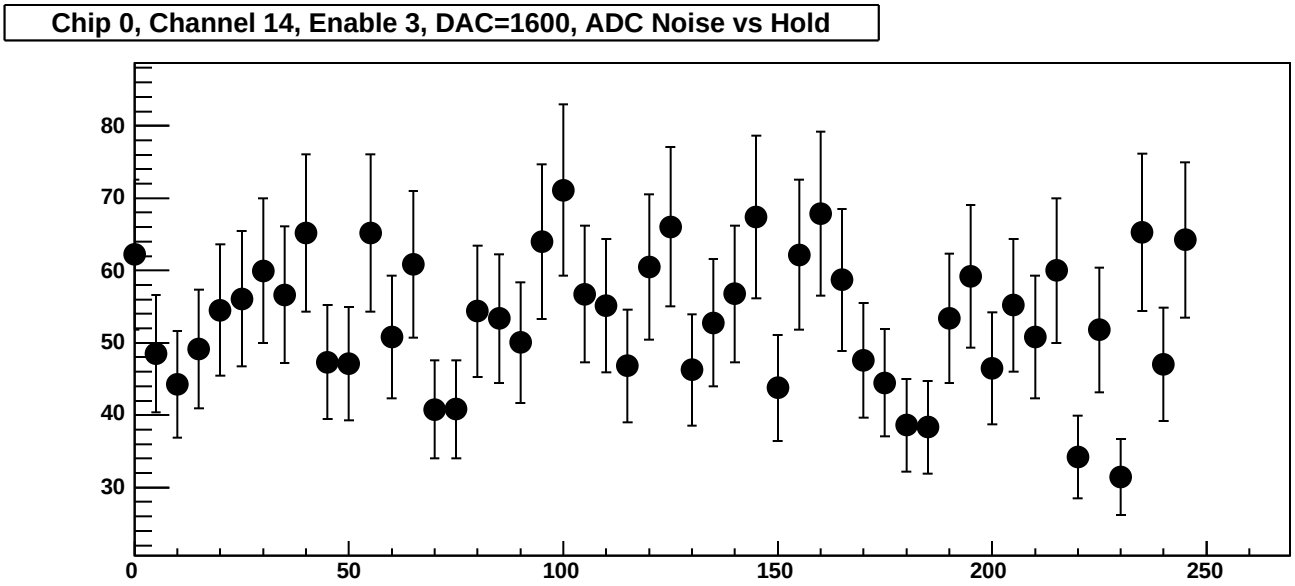


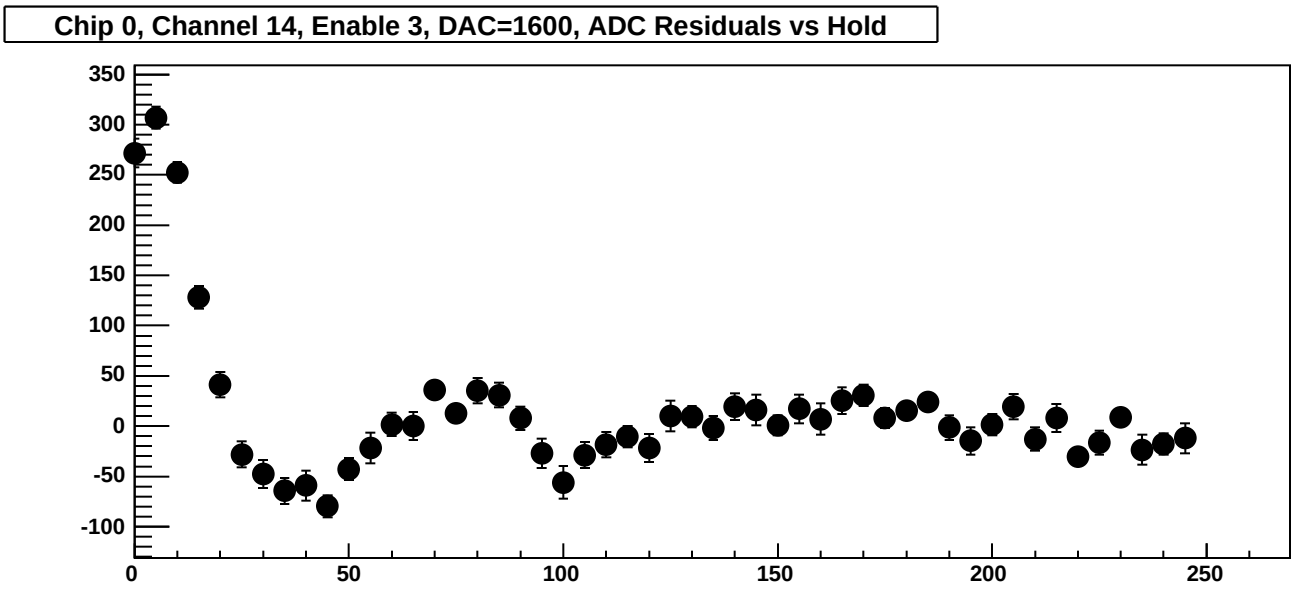

Chip 0, Channel 14, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 159.4 / 41
p0 -346.3 ± 4.179
p1 93.86 ± 0.7315
p2 -2.155e+08 ± 6.868e+06
p3 2.006e+07 ± 5.191e+05
p4 10.2 ± 0.142
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
50
100
150
200
250

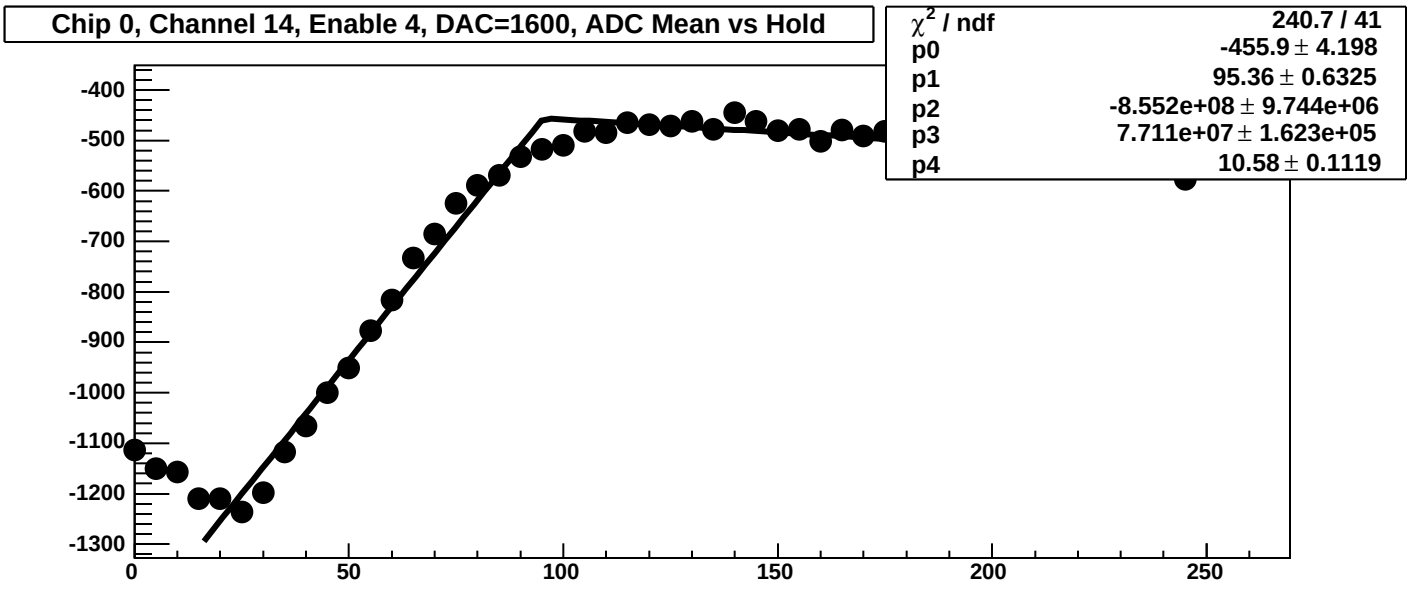

Chip 0, Channel 14, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
20
0
50
100
150
200
250

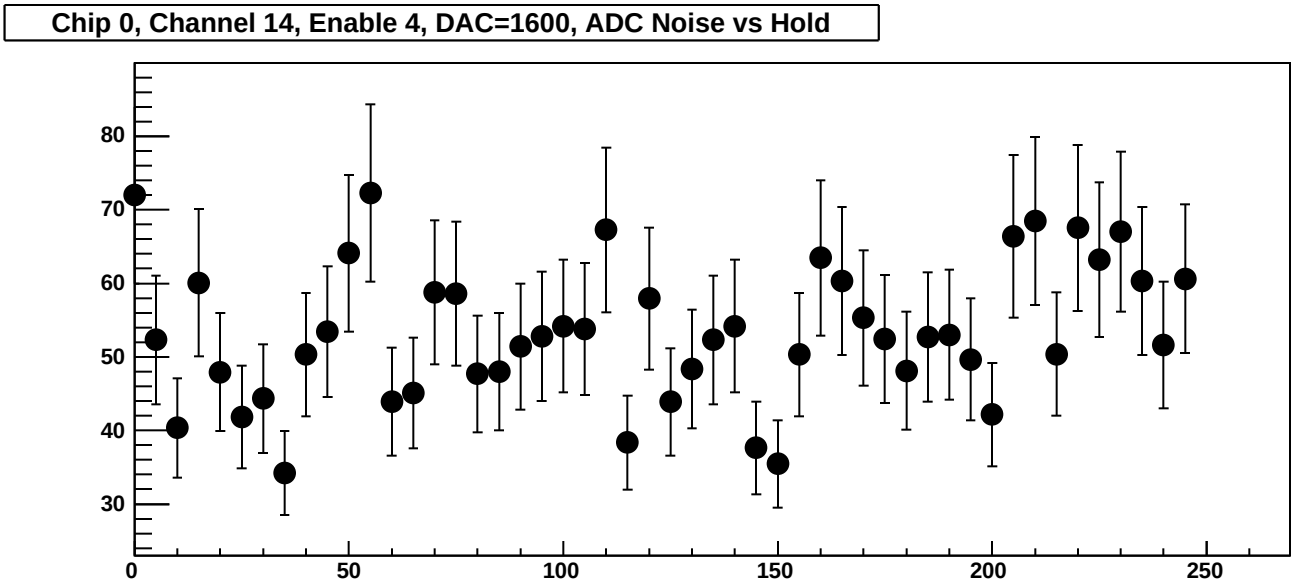

Chip 0, Channel 14, Enable 2, DAC=1600, ADC Residuals vs Hold
200
150
100
50
0
-50
-100
0
50
100
150
200
250

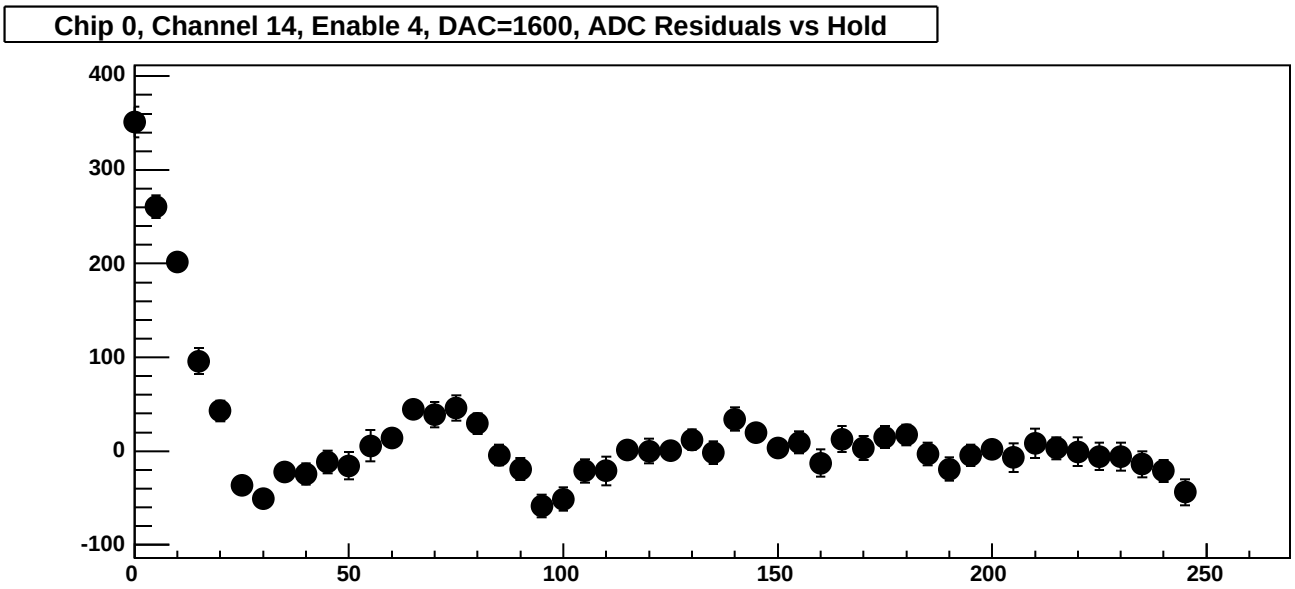

Chip 0, Channel 14, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 382.9 / 41
p0 -477.6 ± 4.408
p1 99.53 ± 0.7666
p2 -2.849e+08 ± 5.466e+06
p3 2.889e+07 ± 3.456e+05
p4 9.409 ± 0.1215
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
50
100
150
200
250

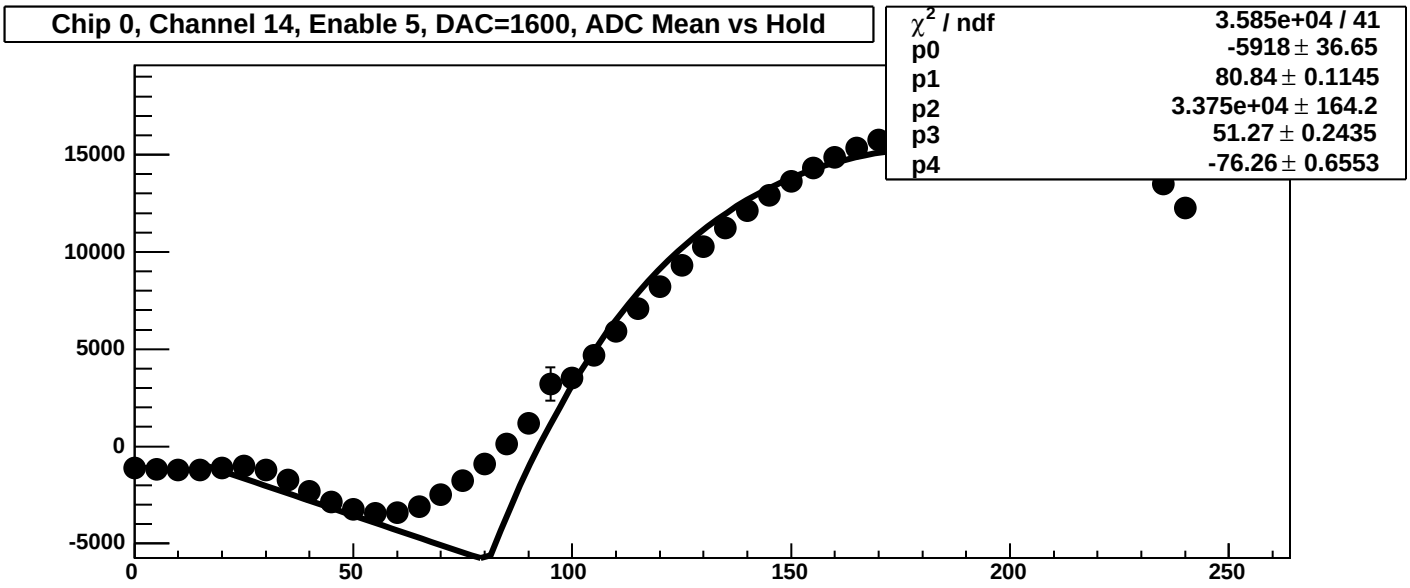

Chip 0, Channel 14, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

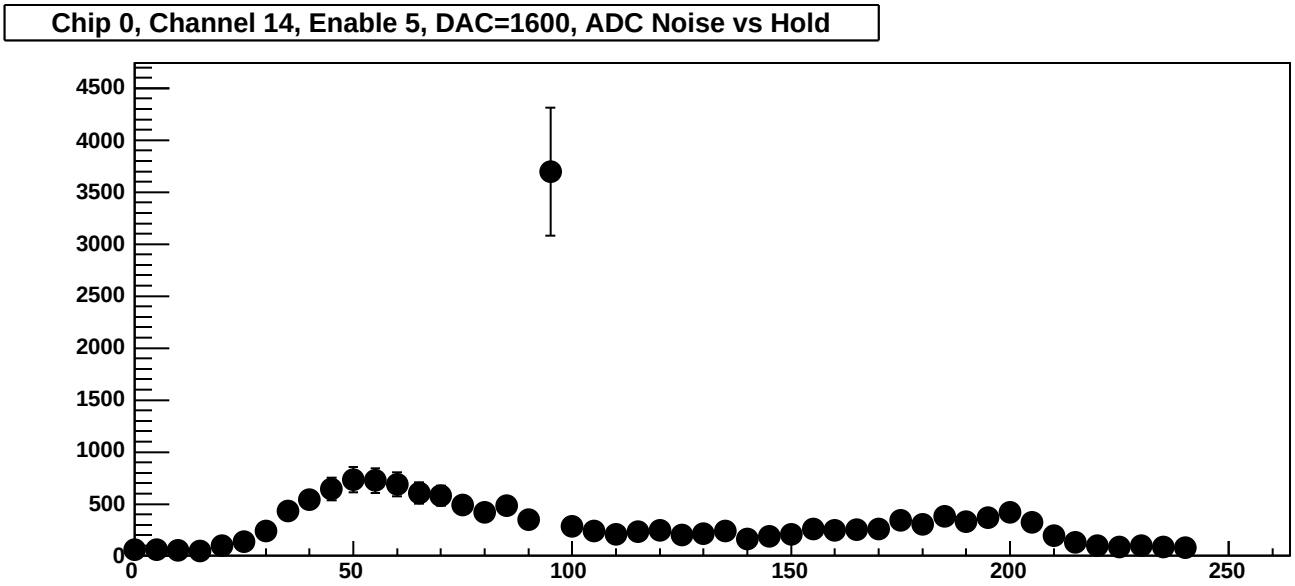

Chip 0, Channel 14, Enable 3, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

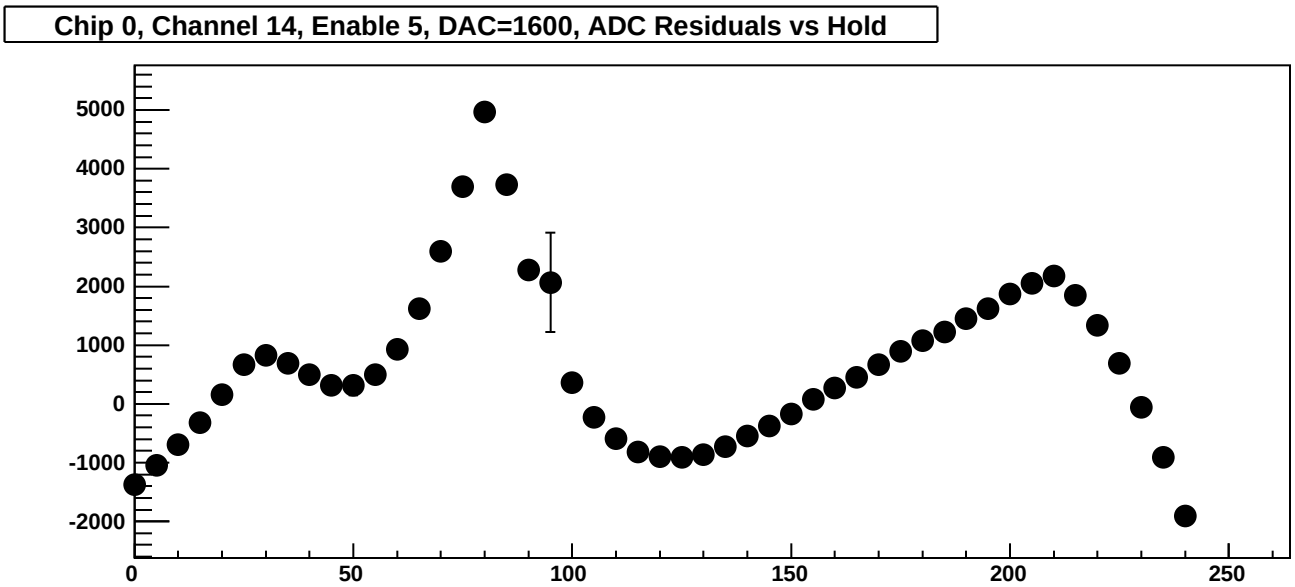

Chip 0, Channel 14, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 240.7 / 41
p0 -455.9 ± 4.198
p1 95.36 ± 0.6325
p2 -8.552e+08 ± 9.744e+06
p3 7.711e+07 ± 1.623e+05
p4 10.58 ± 0.1119
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
50
100
150
200
250

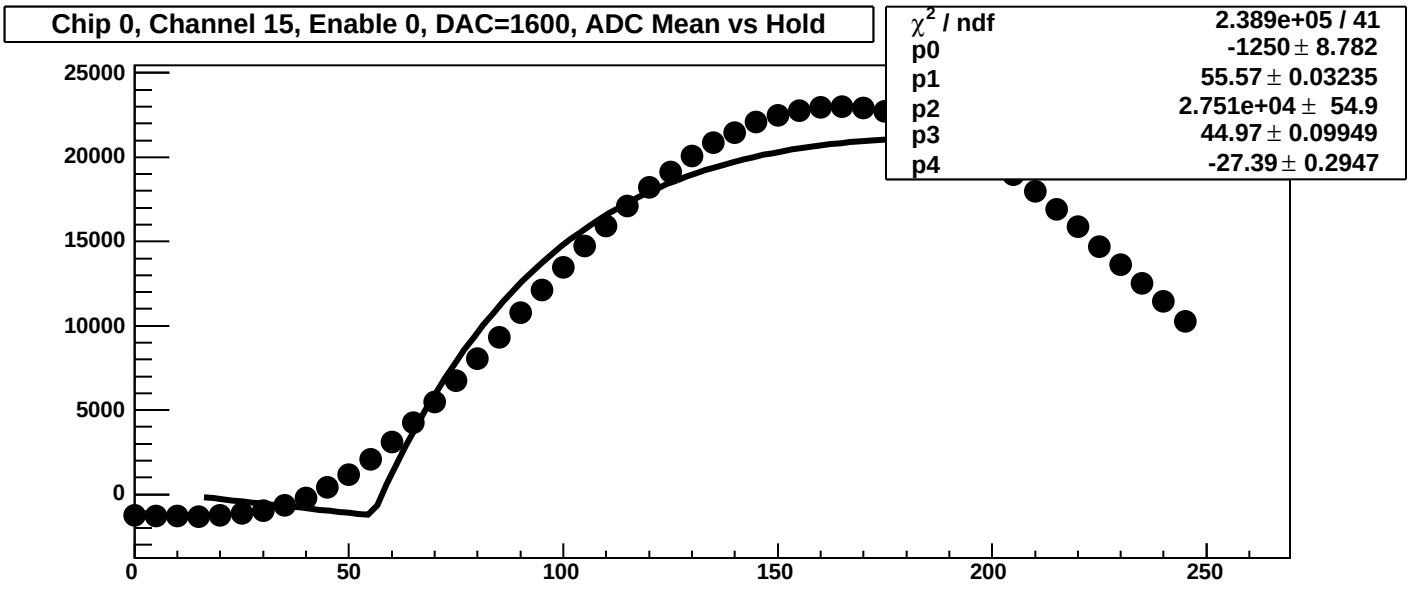

Chip 0, Channel 14, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

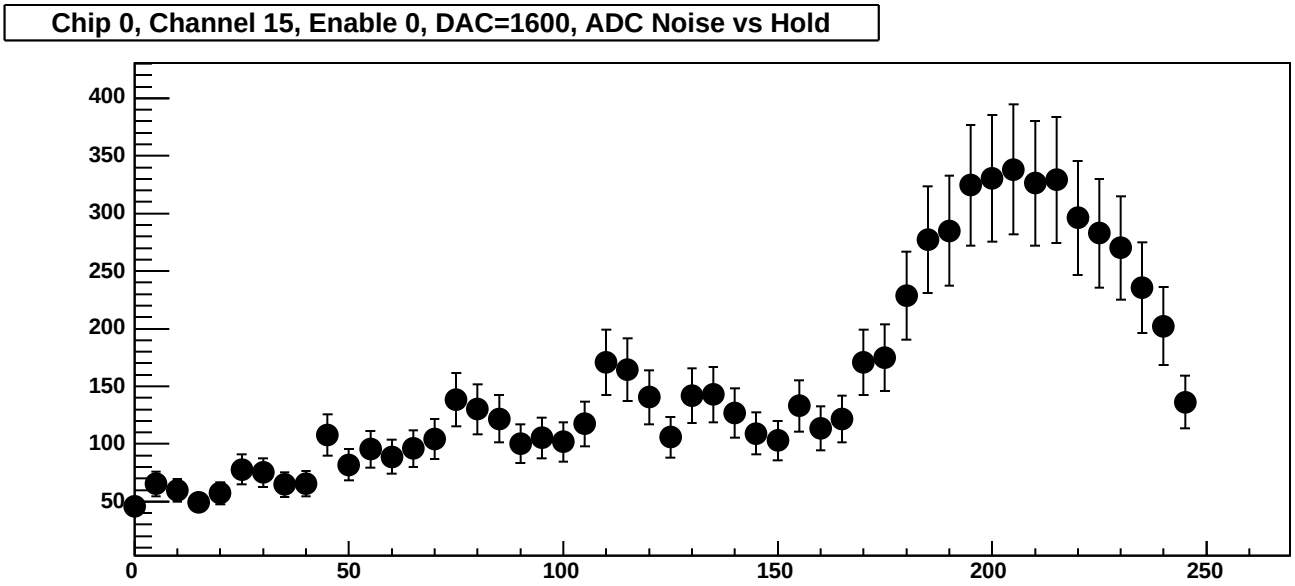

Chip 0, Channel 14, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

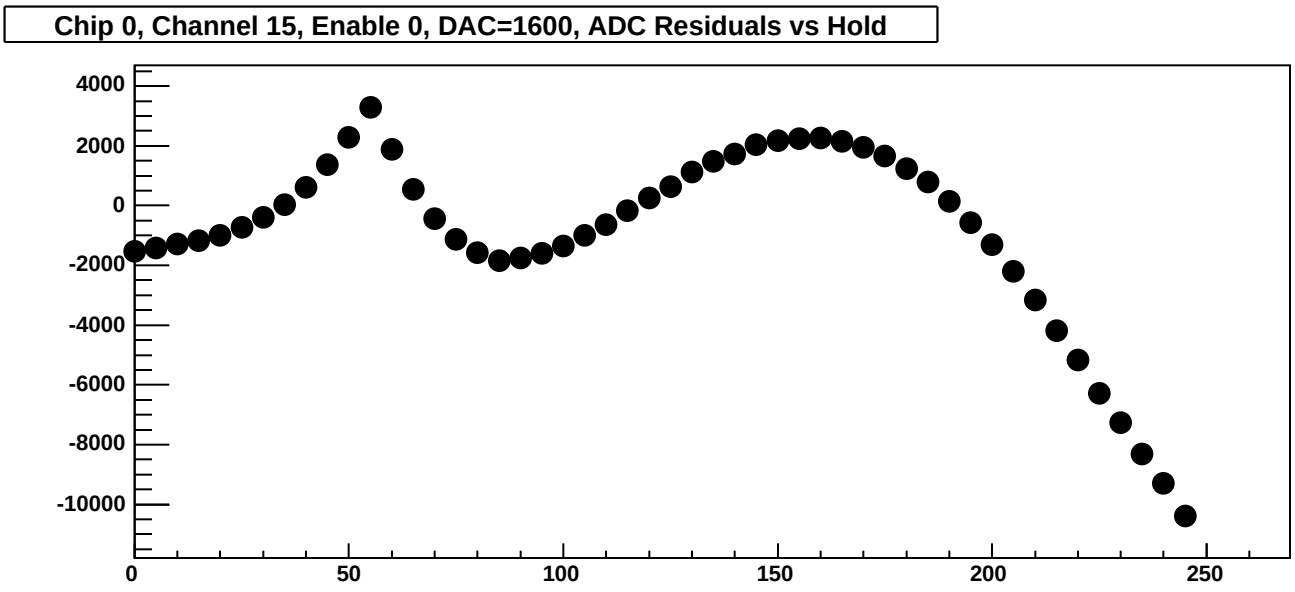

Chip 0, Channel 14, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.585e+04 / 41
p0 -5918 ± 36.65
p1 80.84 ± 0.1145
p2 3.375e+04 ± 164.2
p3 51.27 ± 0.2435
p4 -76.26 ± 0.6553
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 0, Channel 14, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250

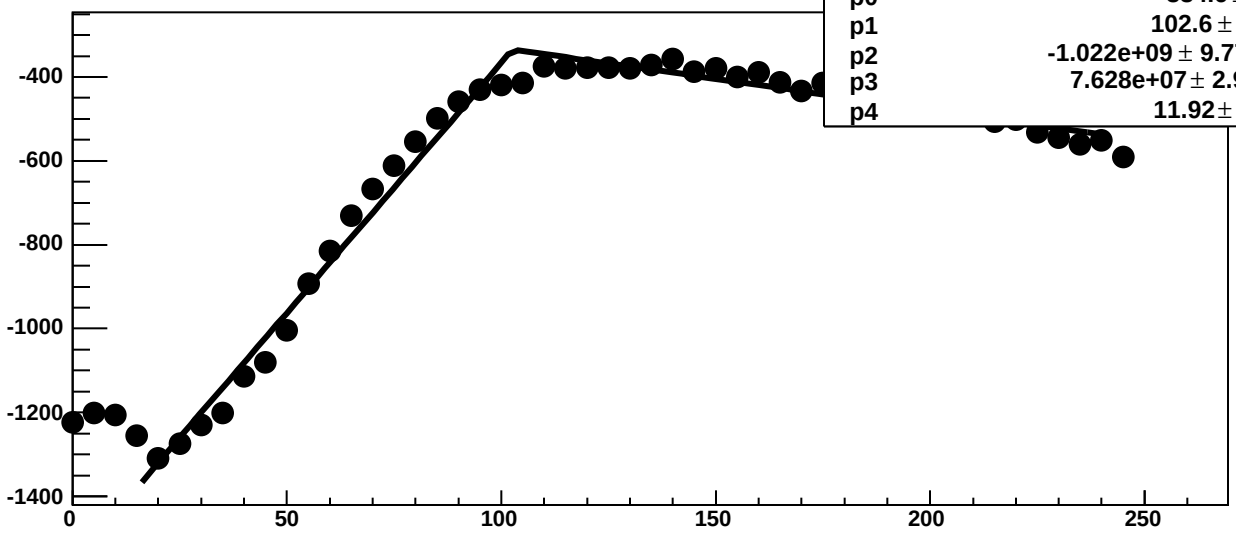

Chip 0, Channel 14, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
4000
3000
2000
1000
0
-1000
-2000
0
50
100
150
200
250


Chip 0, Channel 15, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.389e+05 / 41
p0 -1250 ± 8.782
p1 55.57 ± 0.03235
p2 2.751e+04 ± 54.9
p3 44.97 ± 0.09949
p4 -27.39 ± 0.2947
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

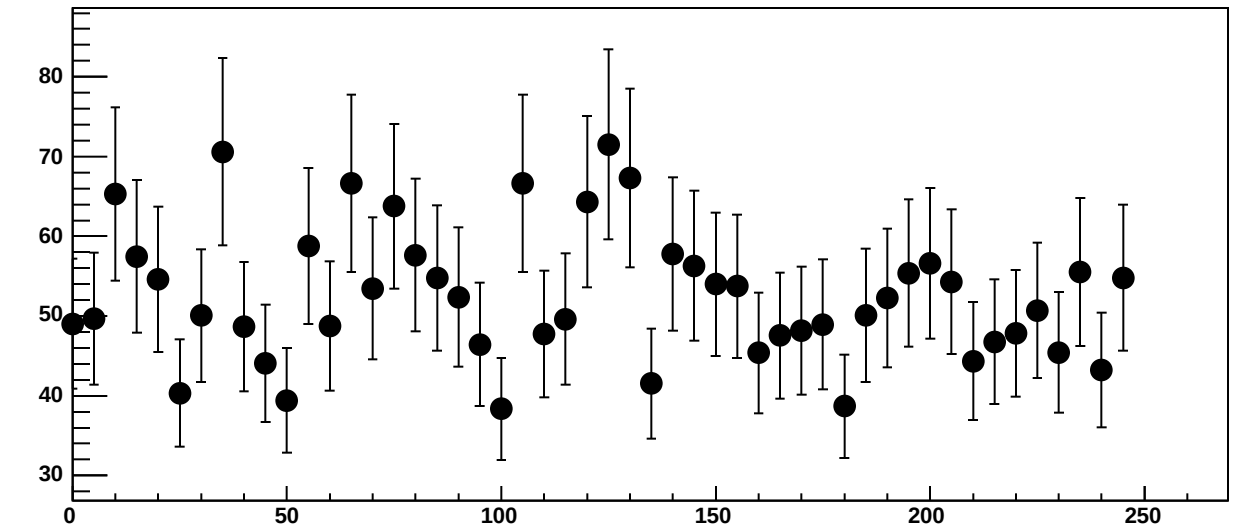

Chip 0, Channel 15, Enable 0, DAC=1600, ADC Noise vs Hold
400
350
300
250
200
150
100
50
0
50
100
150
200
250


Chip 0, Channel 15, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

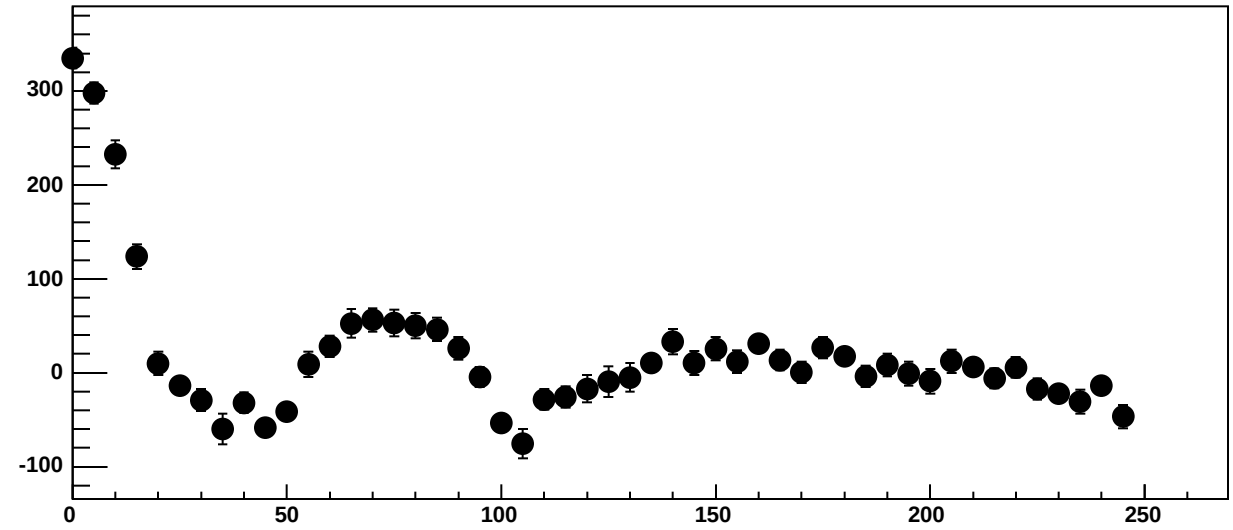
Chip 0, Channel 15, Enable 1, DAC=1600, ADC Mean vs Hold

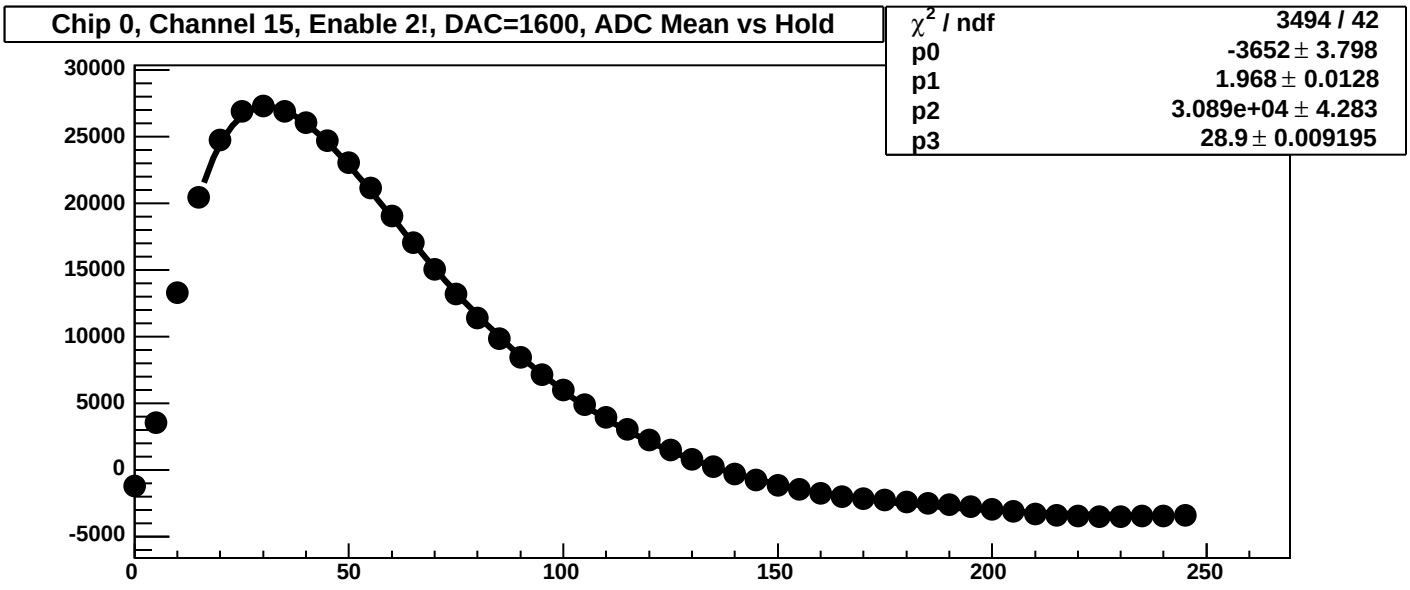


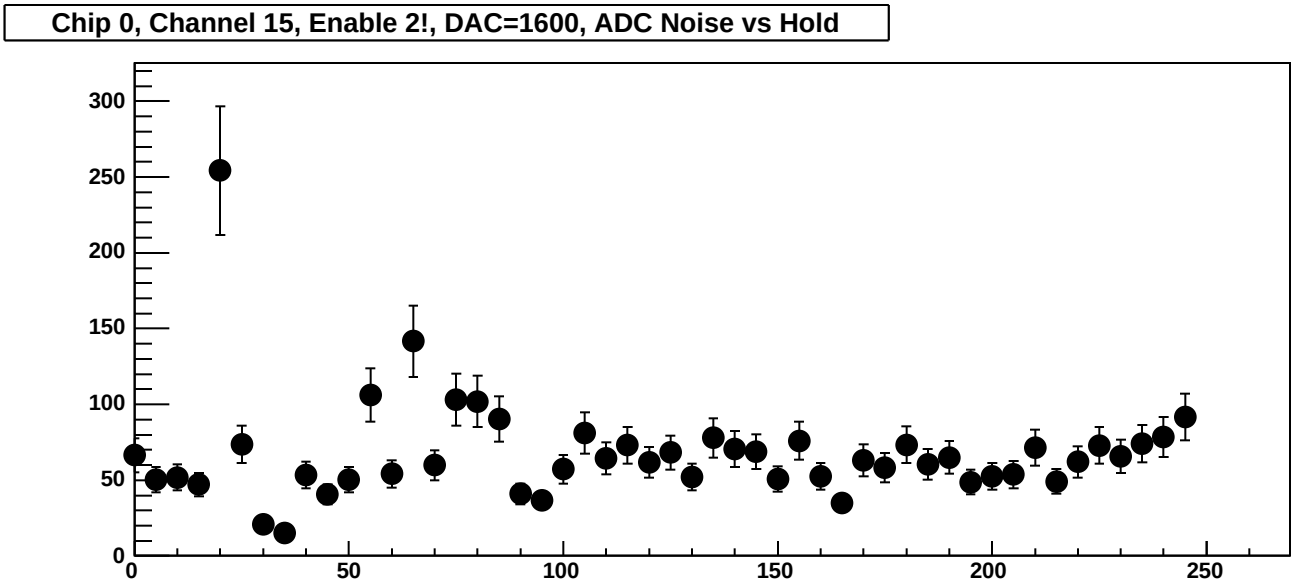
Chip 0, Channel 15, Enable 1, DAC=1600, ADC Noise vs Hold

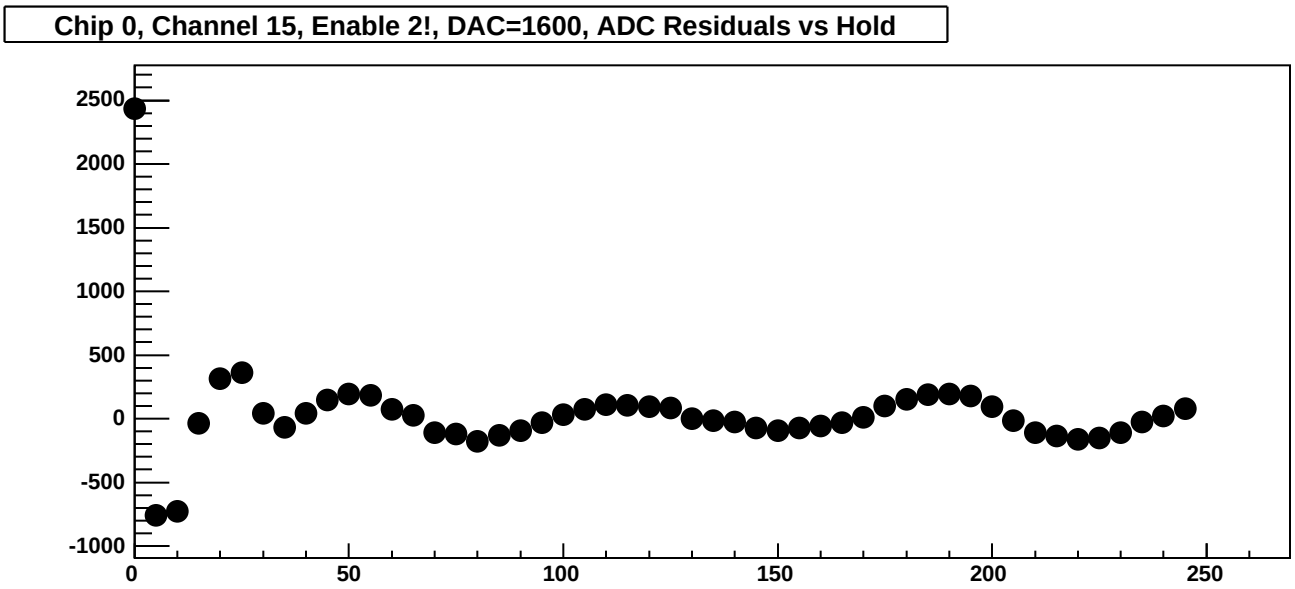


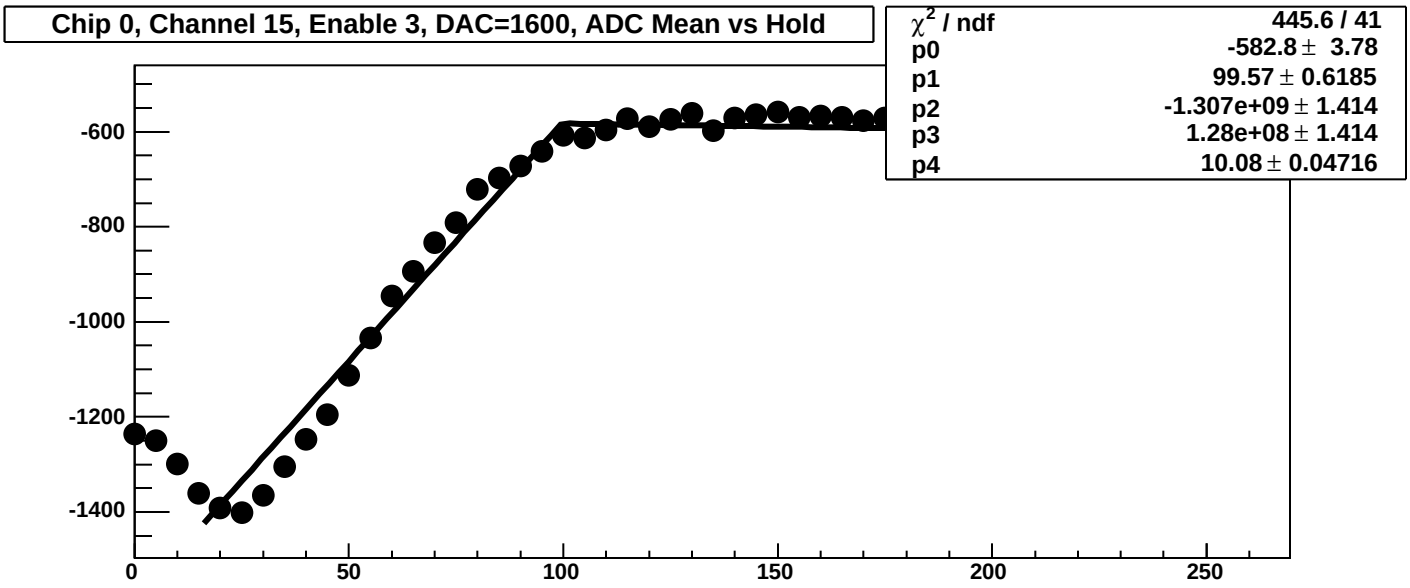
Chip 0, Channel 15, Enable 1, DAC=1600, ADC Residuals vs Hold

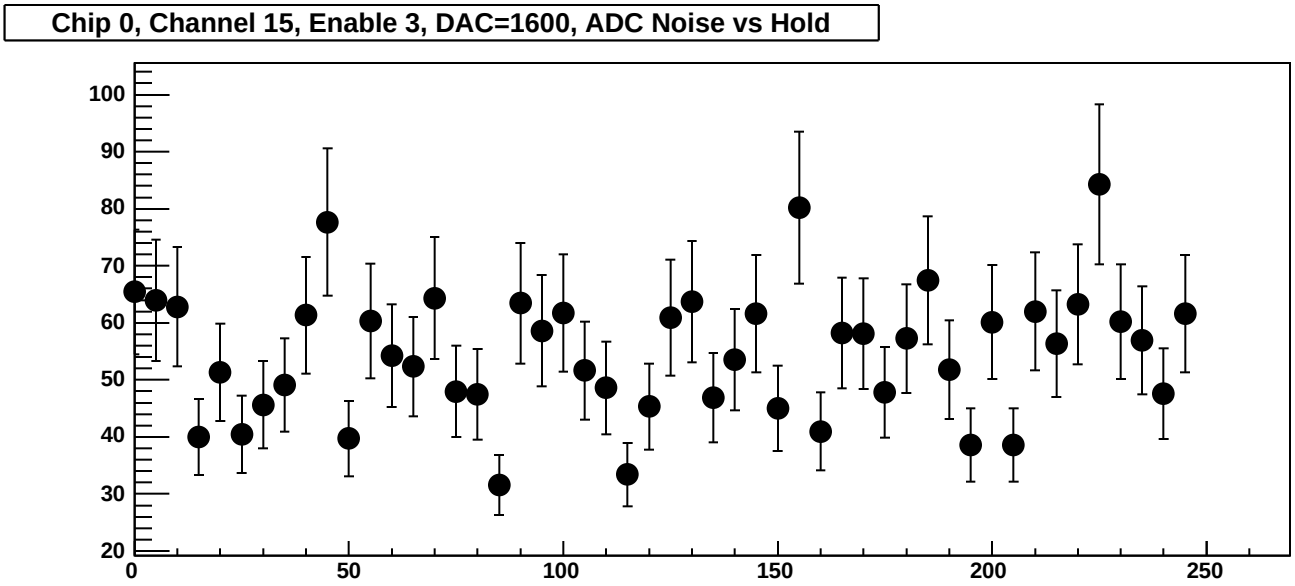


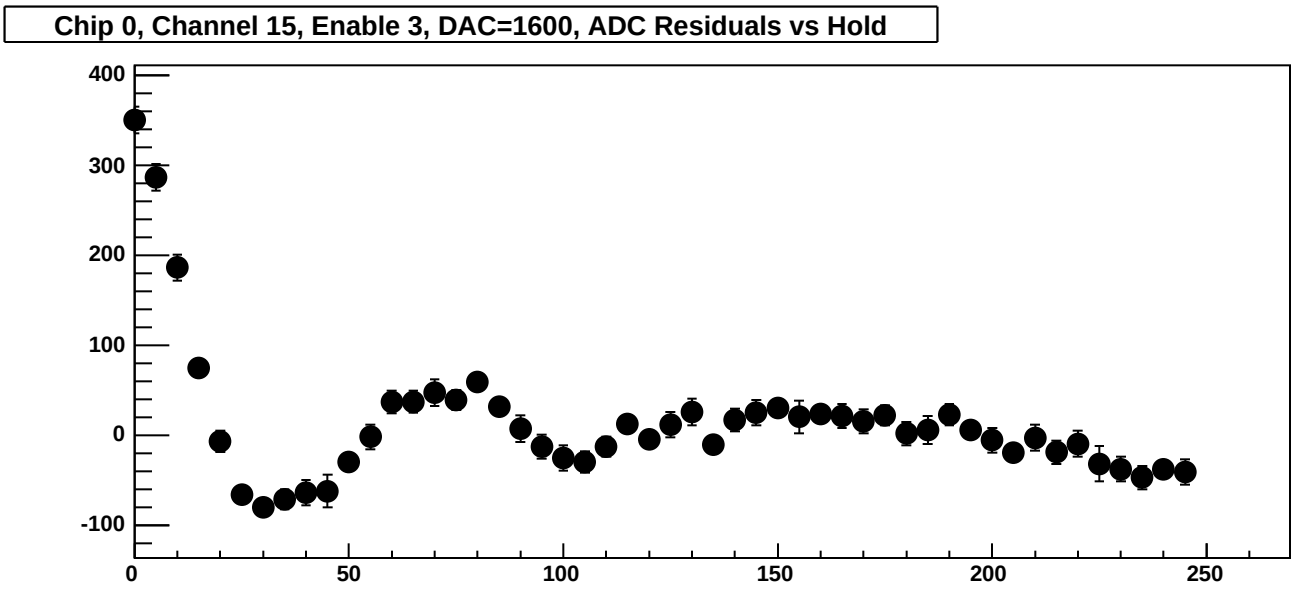

Chip 0, Channel 15, Enable 2!, DAC=1600, ADC Mean vs Hold
χ² / ndf 3494 / 42
p0 -3652 ± 3.798
p1 1.968 ± 0.0128
p2 3.089e+04 ± 4.283
p3 28.9 ± 0.009195
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 0, Channel 15, Enable 2!, DAC=1600, ADC Noise vs Hold
300
250
200
150
100
50
0
0
50
100
150
200
250

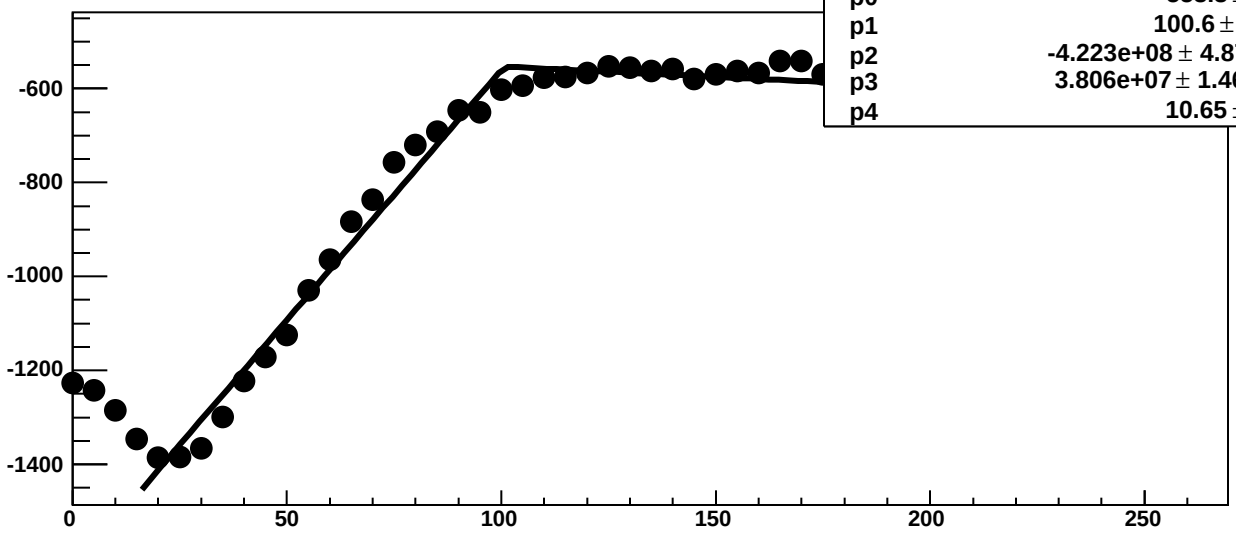

Chip 0, Channel 15, Enable 2!, DAC=1600, ADC Residuals vs Hold
2500
2000
1500
1000
500
0
-500
-1000
0
50
100
150
200
250


Chip 0, Channel 15, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 445.6 / 41
p0 -582.8 ± 3.78
p1 99.57 ± 0.6185
p2 -1.307e+09 ± 1.414
p3 1.28e+08 ± 1.414
p4 10.08 ± 0.04716
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

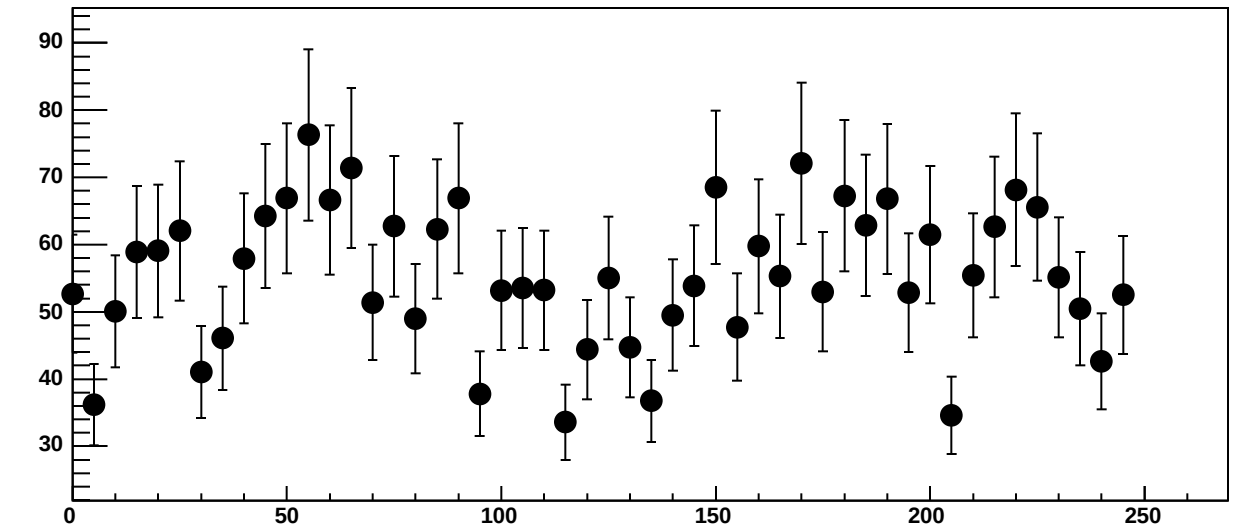

Chip 0, Channel 15, Enable 3, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 0, Channel 15, Enable 3, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

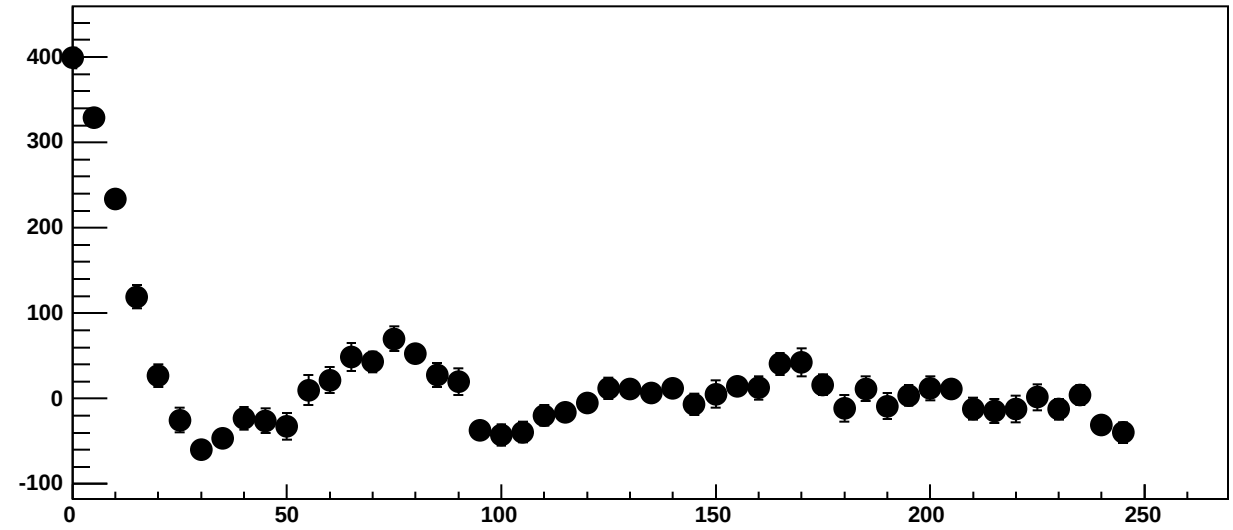
Chip 0, Channel 15, Enable 4, DAC=1600, ADC Mean vs Hold

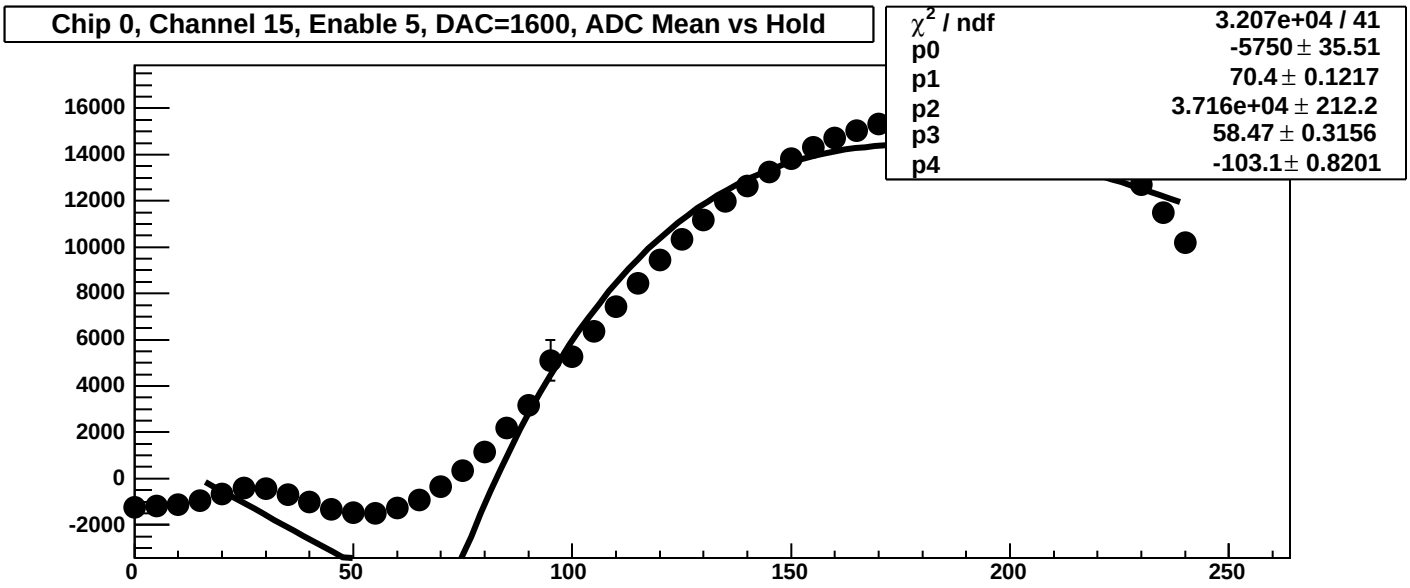


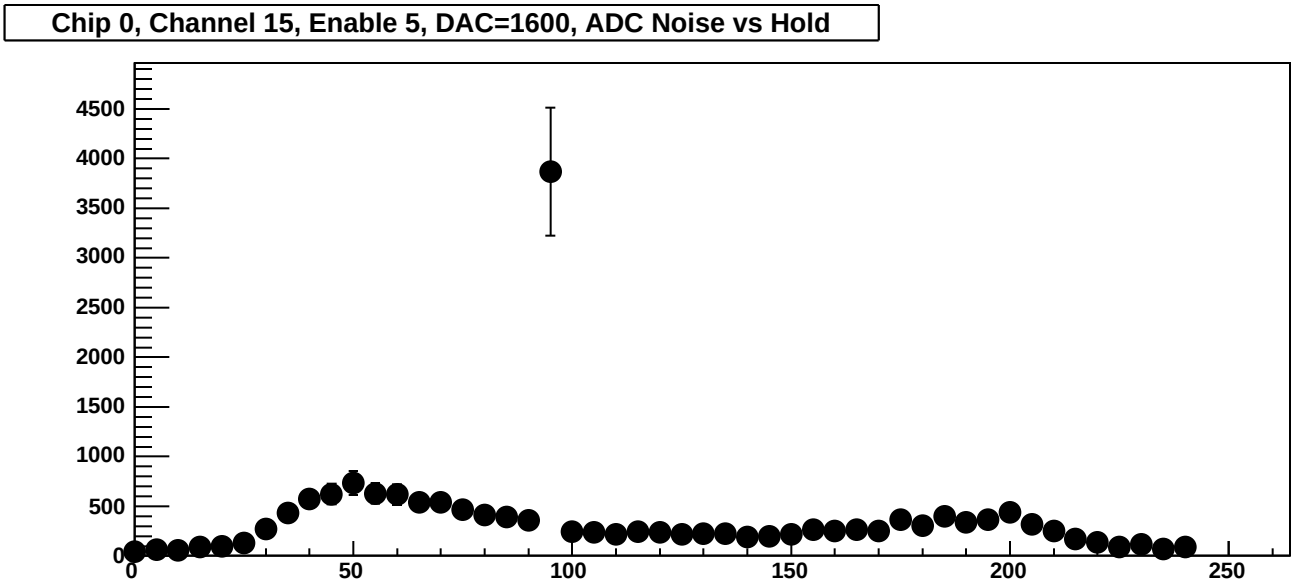
Chip 0, Channel 15, Enable 4, DAC=1600, ADC Noise vs Hold

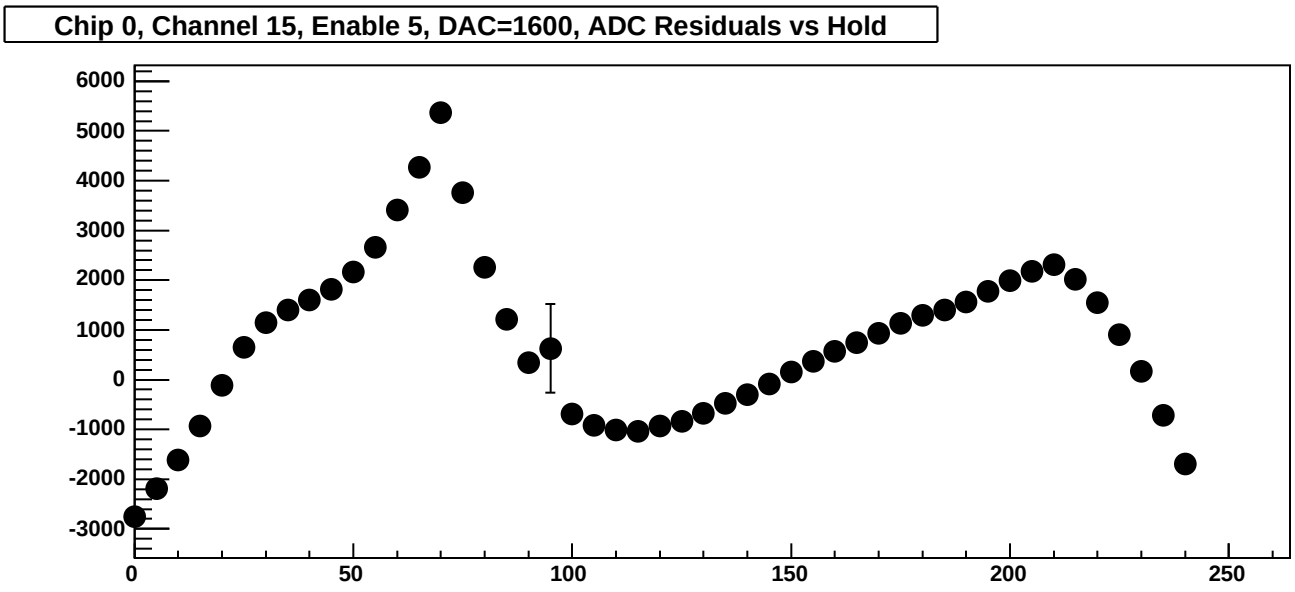


Chip 0, Channel 15, Enable 4, DAC=1600, ADC Residuals vs Hold

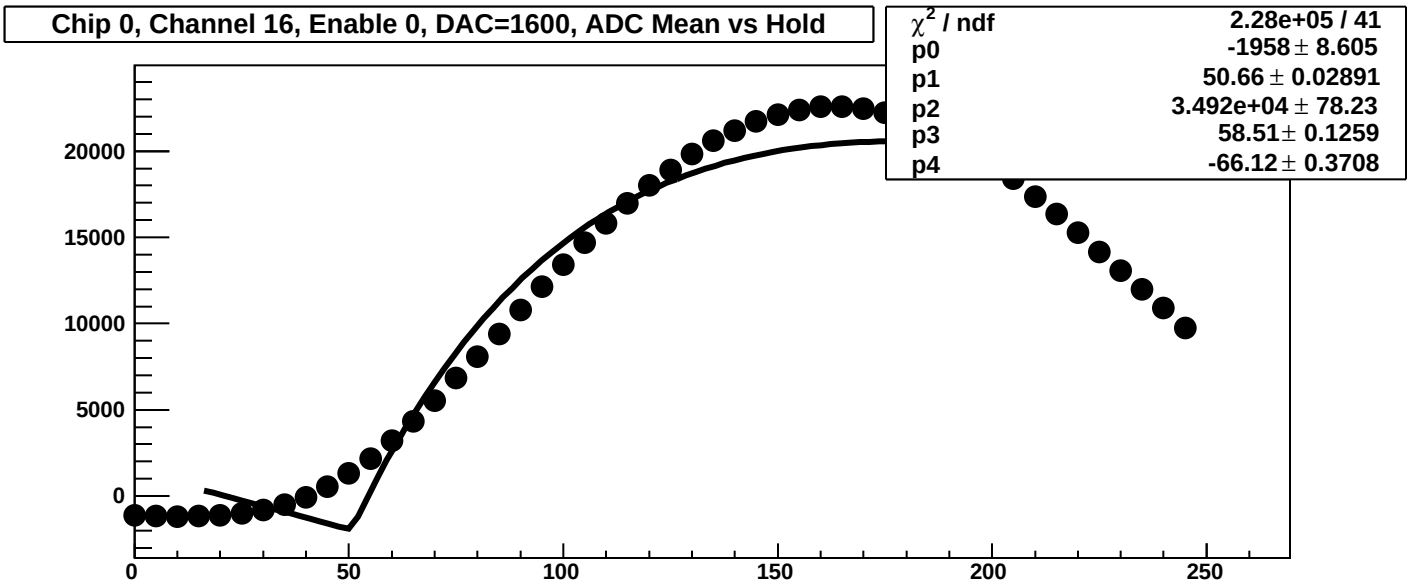



Chip 0, Channel 15, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.207e+04 / 41
p0 -5750 ± 35.51
p1 70.4 ± 0.1217
p2 3.716e+04 ± 212.2
p3 58.47 ± 0.3156
p4 -103.1 ± 0.8201
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

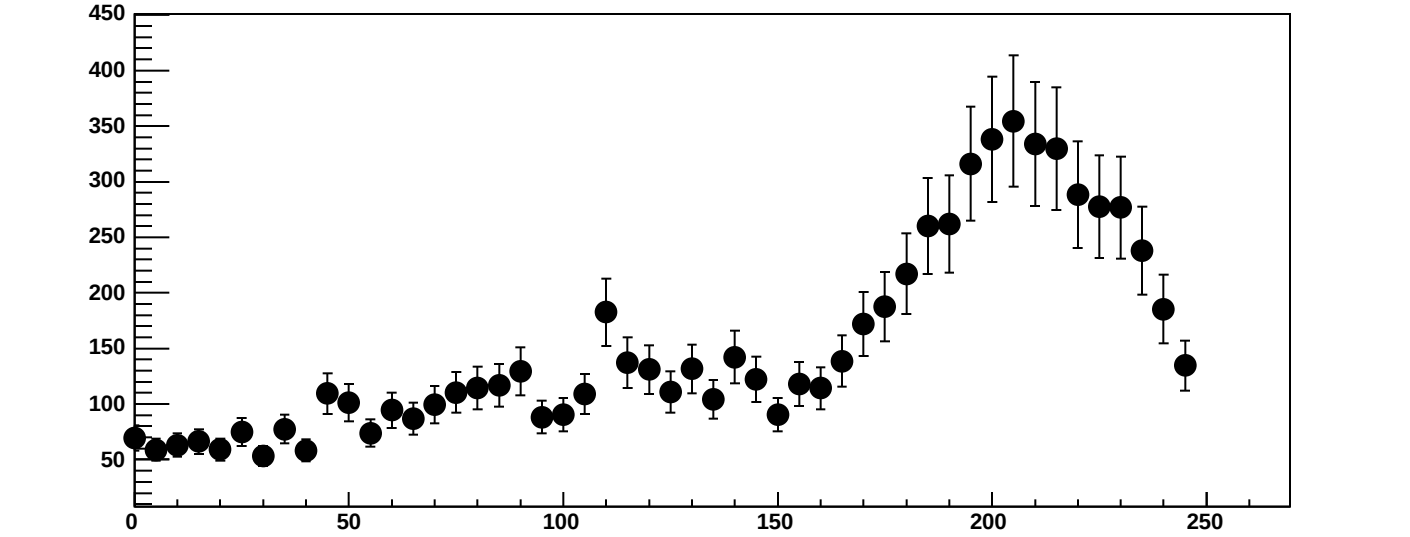

Chip 0, Channel 15, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 0, Channel 15, Enable 5, DAC=1600, ADC Residuals vs Hold
6000
5000
4000
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

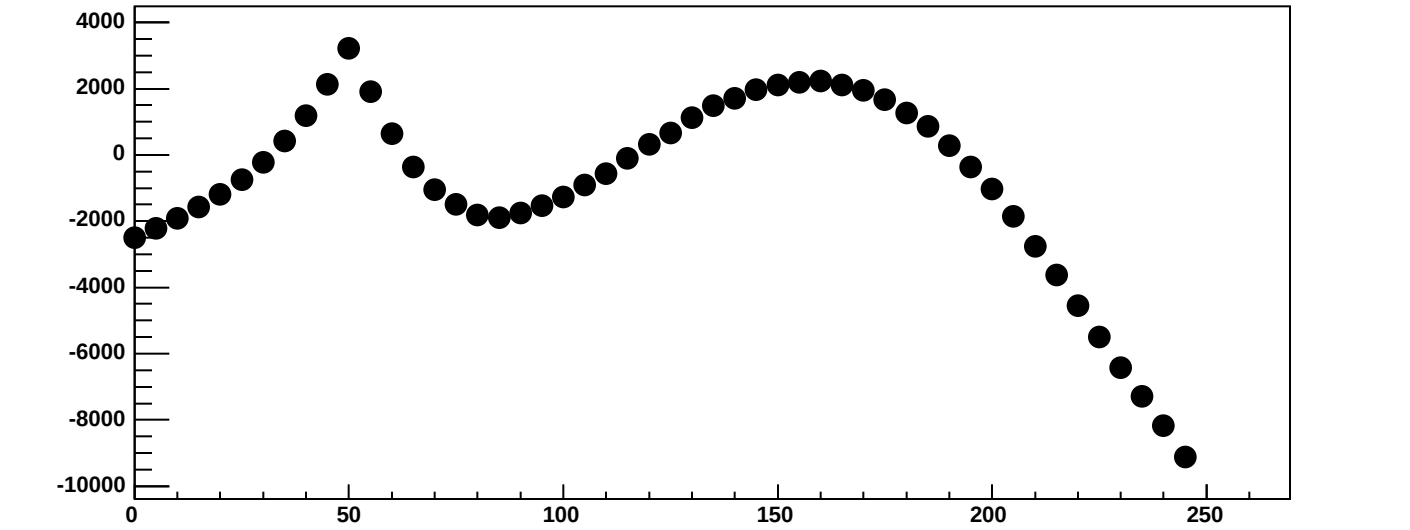
Chip 0, Channel 16, Enable 0, DAC=1600, ADC Mean vs Hold



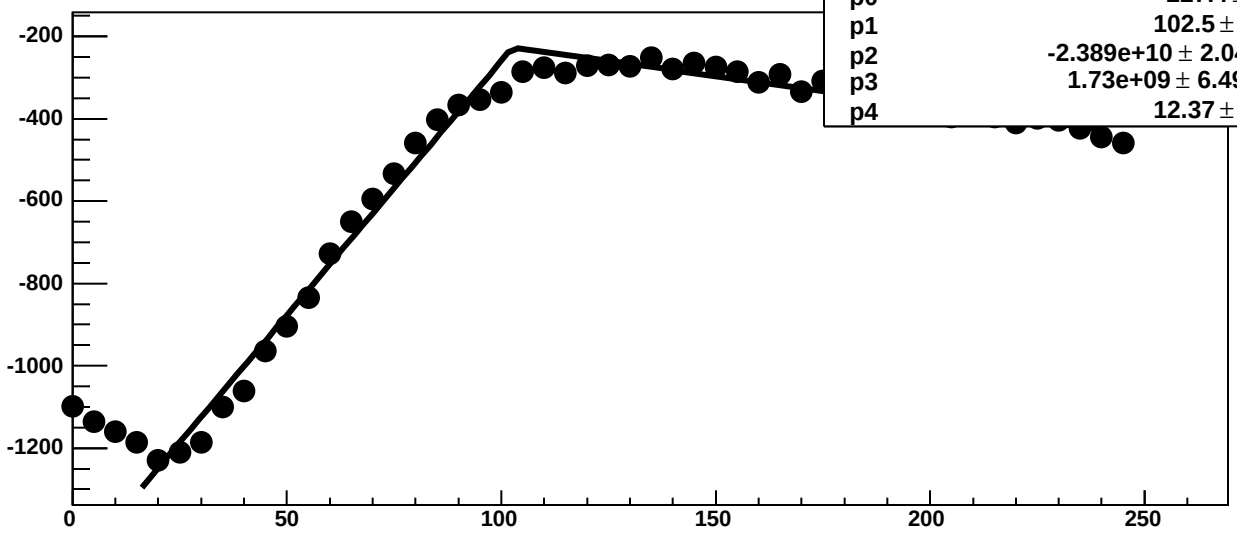
Chip 0, Channel 16, Enable 0, DAC=1600, ADC Noise vs Hold



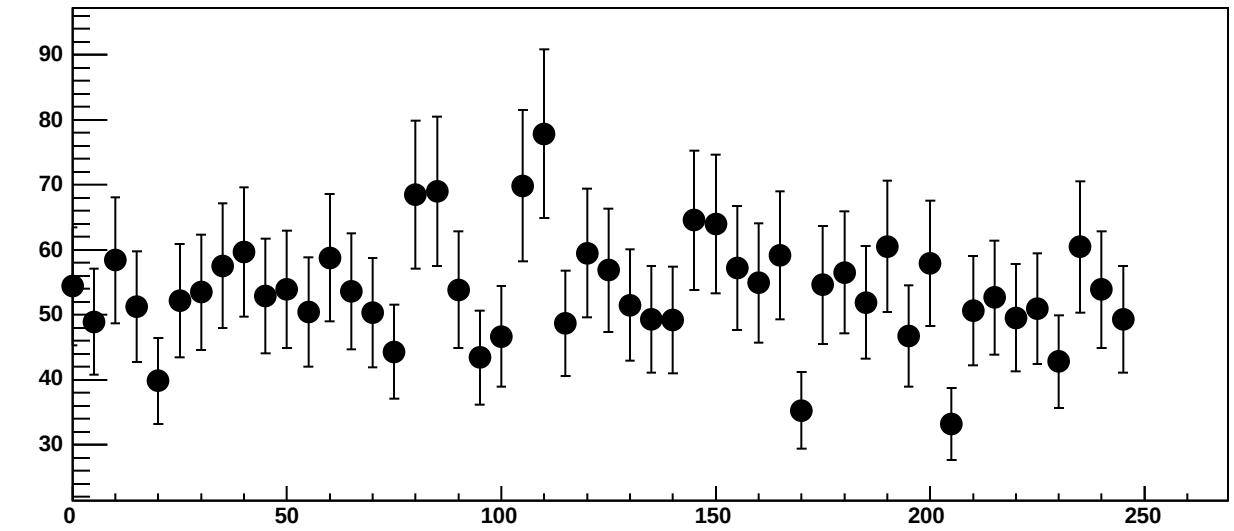
Chip 0, Channel 16, Enable 0, DAC=1600, ADC Residuals vs Hold



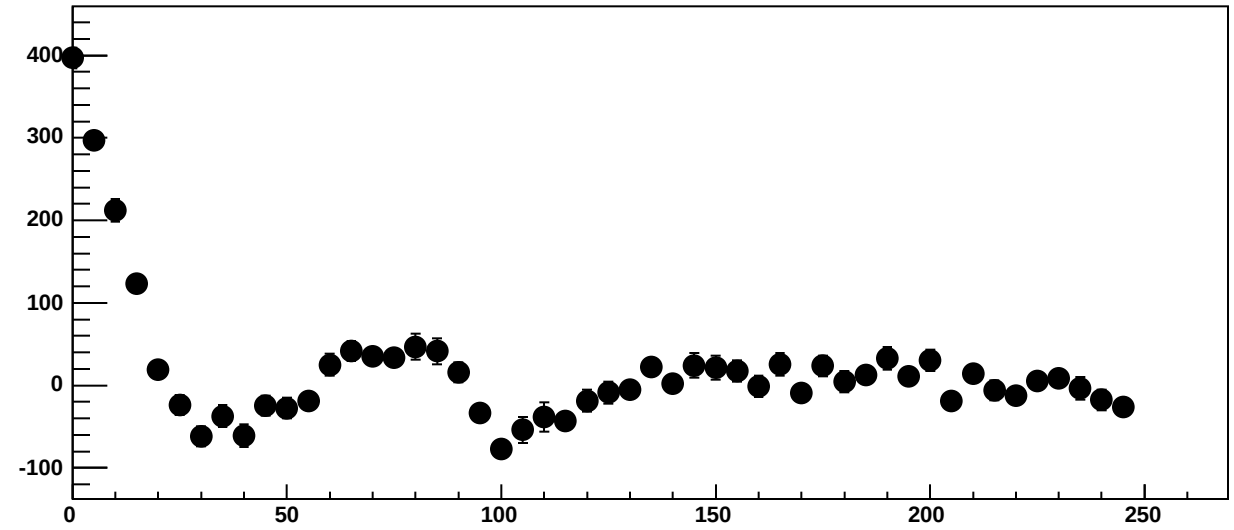
Chip 0, Channel 16, Enable 1, DAC=1600, ADC Mean vs Hold



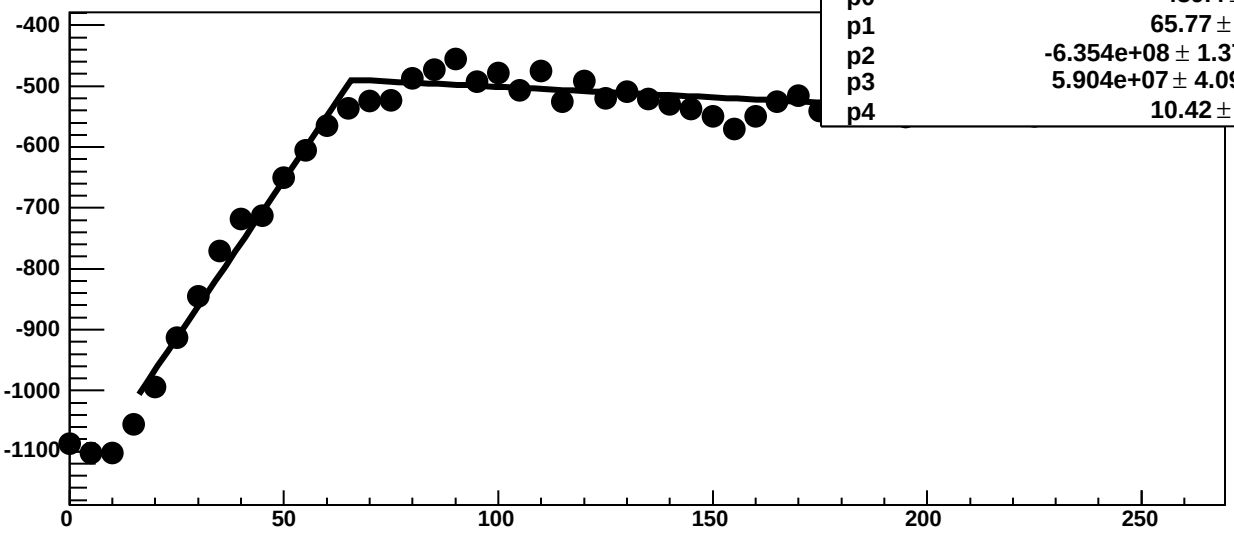
Chip 0, Channel 16, Enable 1, DAC=1600, ADC Noise vs Hold



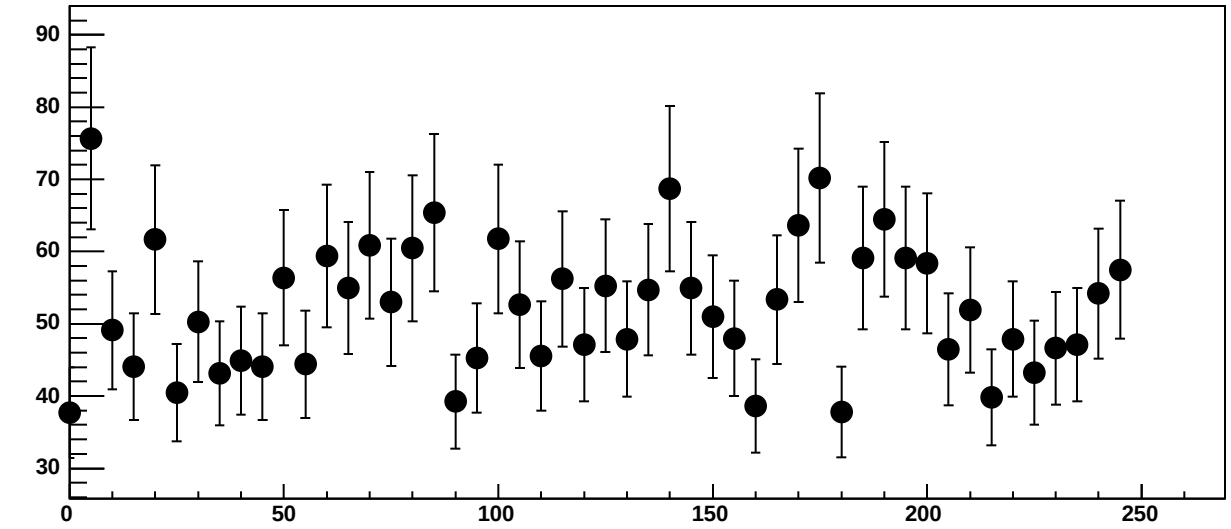
Chip 0, Channel 16, Enable 1, DAC=1600, ADC Residuals vs Hold



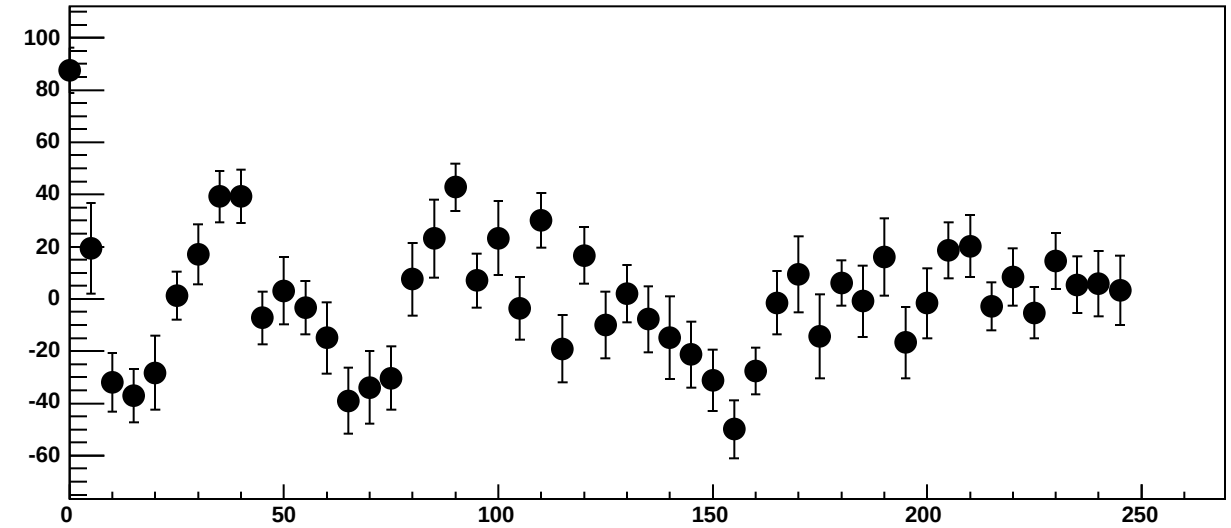
Chip 0, Channel 16, Enable 2, DAC=1600, ADC Mean vs Hold

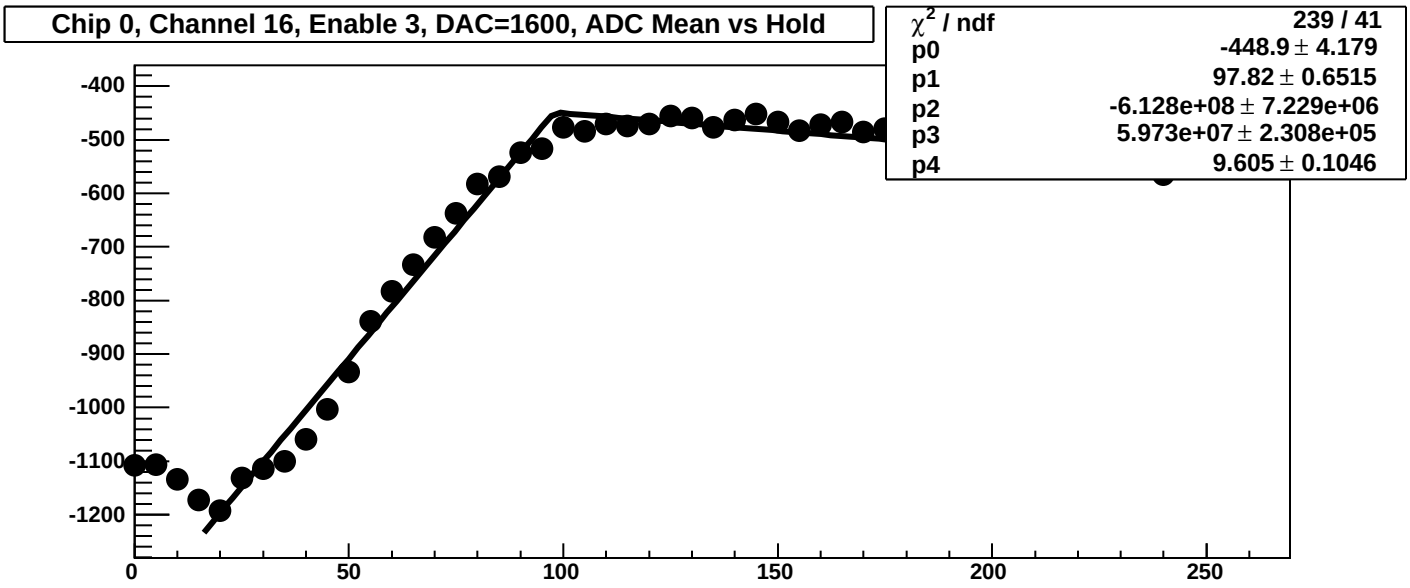


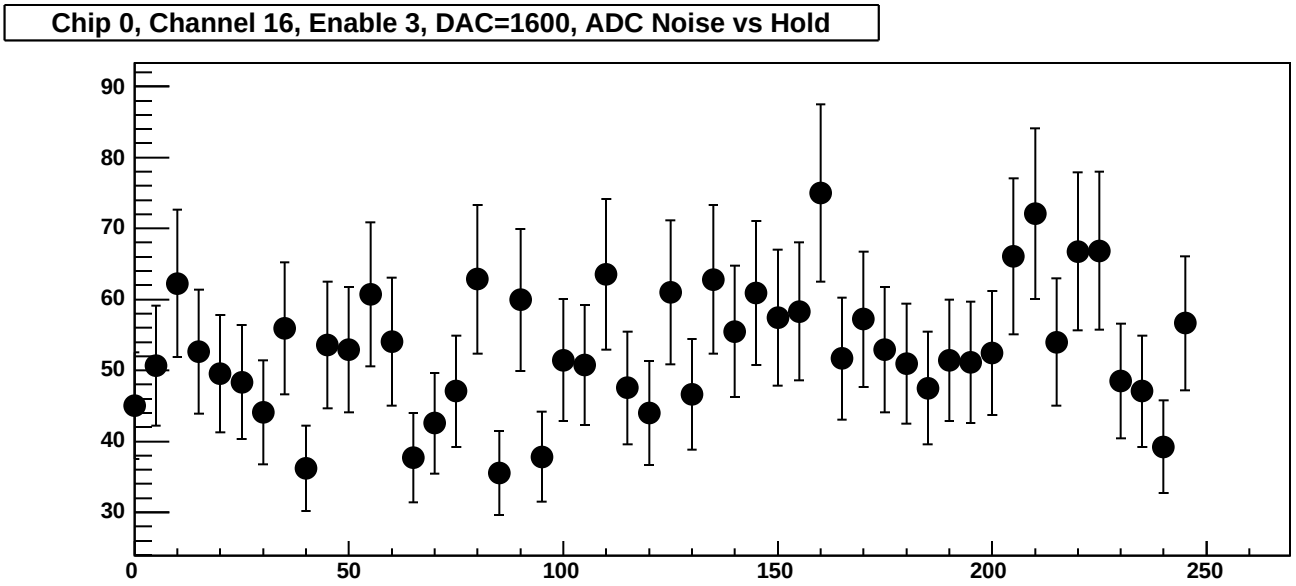
Chip 0, Channel 16, Enable 2, DAC=1600, ADC Noise vs Hold

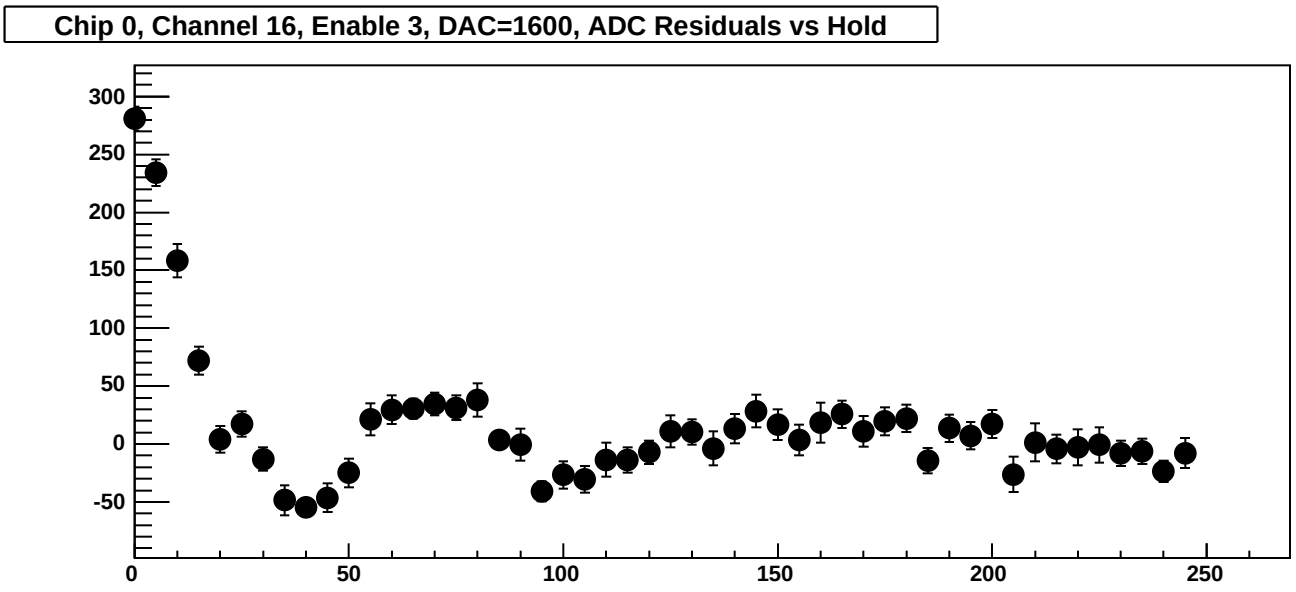


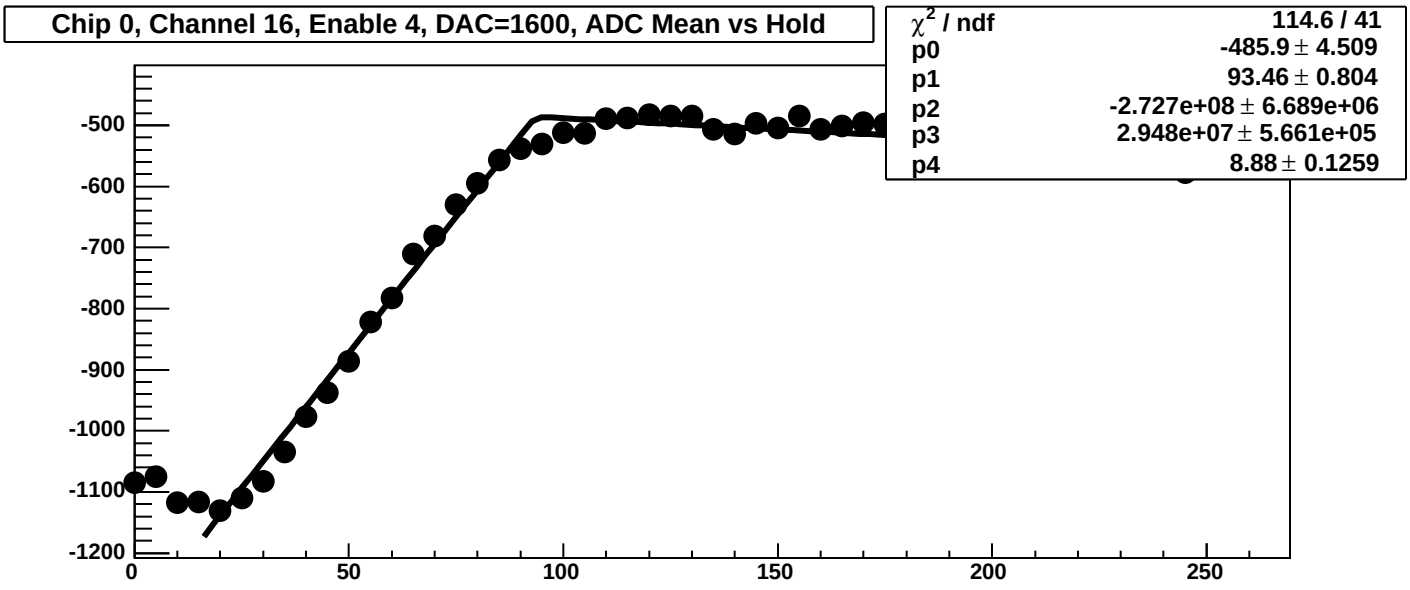
Chip 0, Channel 16, Enable 2, DAC=1600, ADC Residuals vs Hold

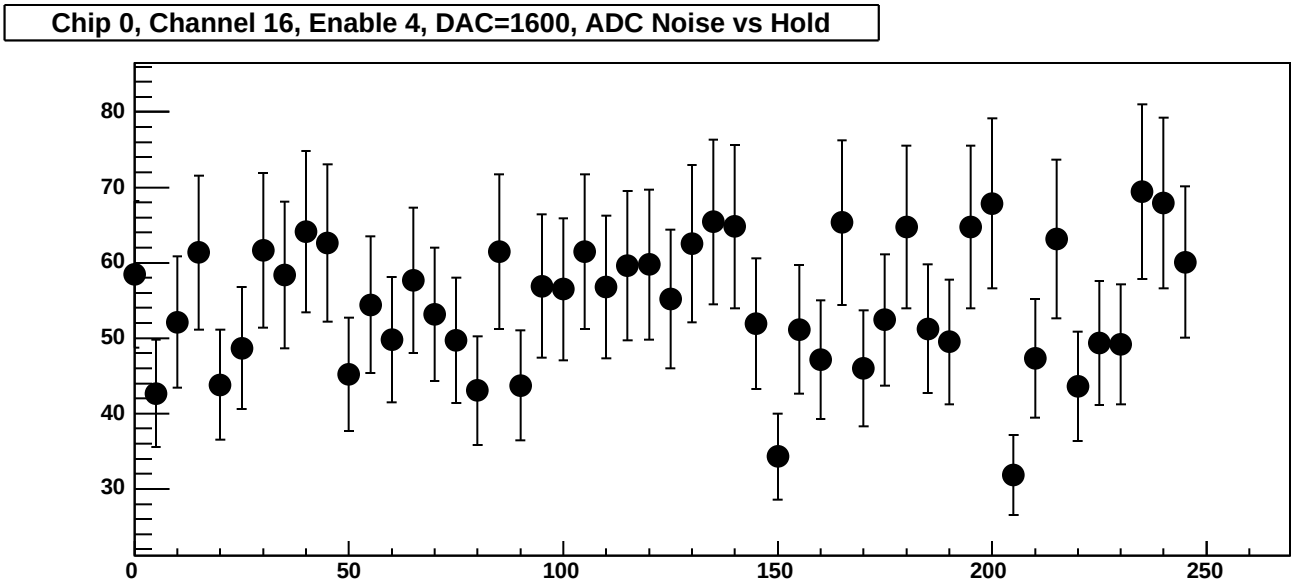


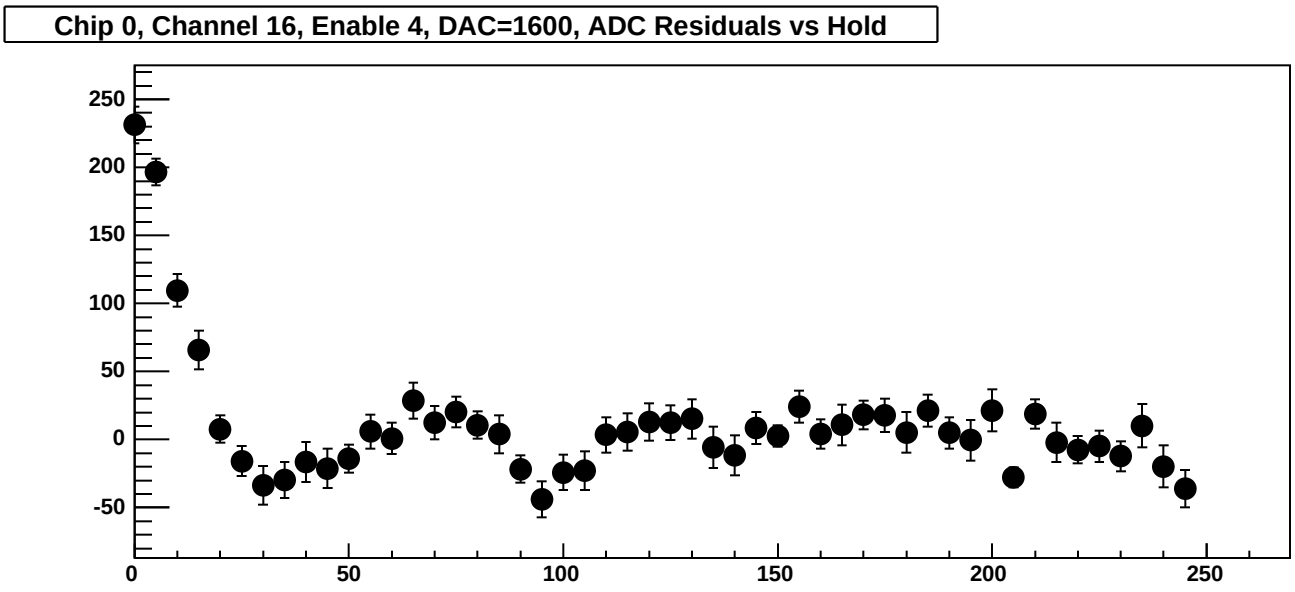

Chip 0, Channel 16, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 239 / 41
p0 -448.9 ± 4.179
p1 97.82 ± 0.6515
p2 -6.128e+08 ± 7.229e+06
p3 5.973e+07 ± 2.308e+05
p4 9.605 ± 0.1046
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
50
100
150
200
250

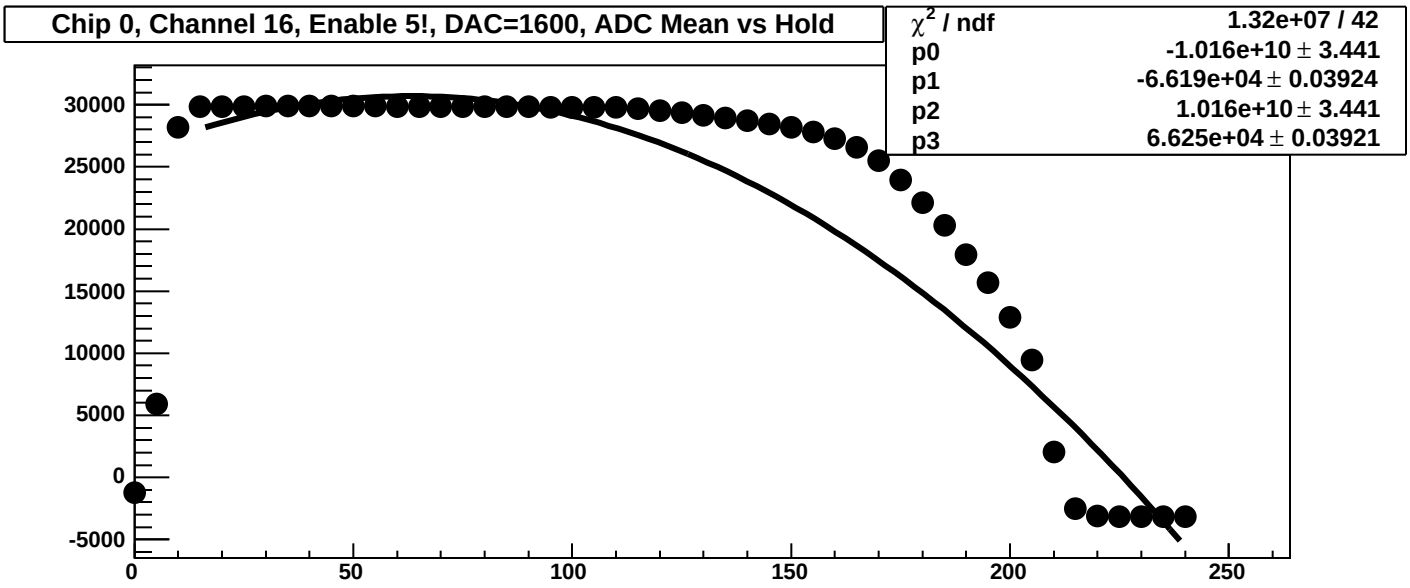

Chip 0, Channel 16, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

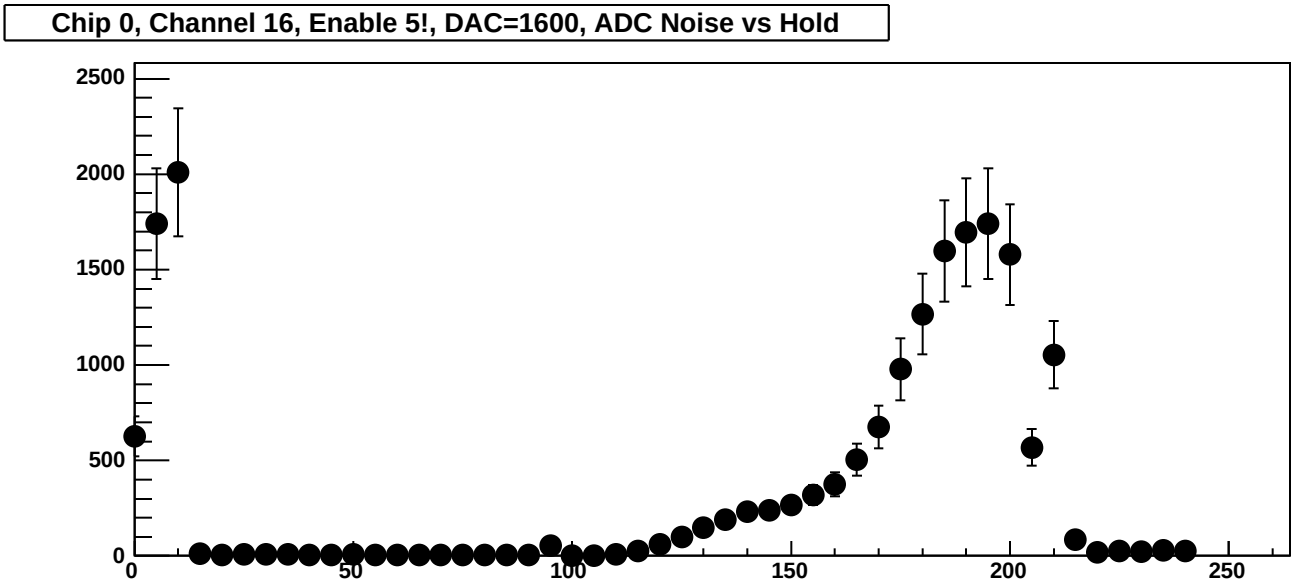

Chip 0, Channel 16, Enable 3, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
0
50
100
150
200
250

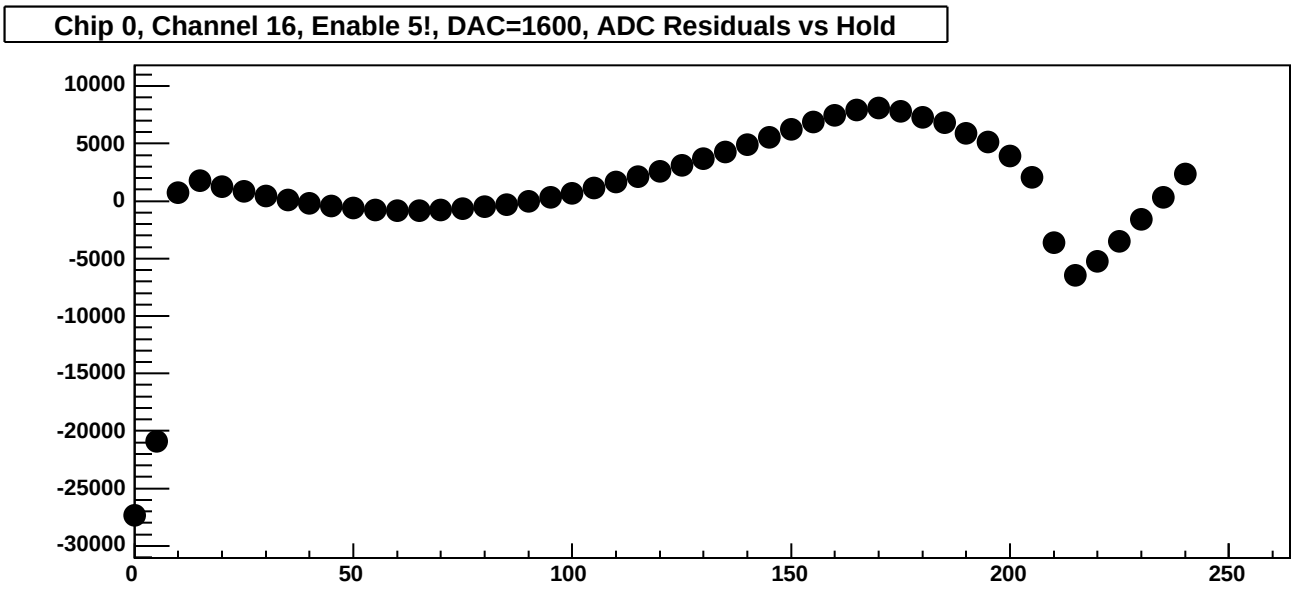

Chip 0, Channel 16, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 114.6 / 41
p0 -485.9 ± 4.509
p1 93.46 ± 0.804
p2 -2.727e+08 ± 6.689e+06
p3 2.948e+07 ± 5.661e+05
p4 8.88 ± 0.1259
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
50
100
150
200
250

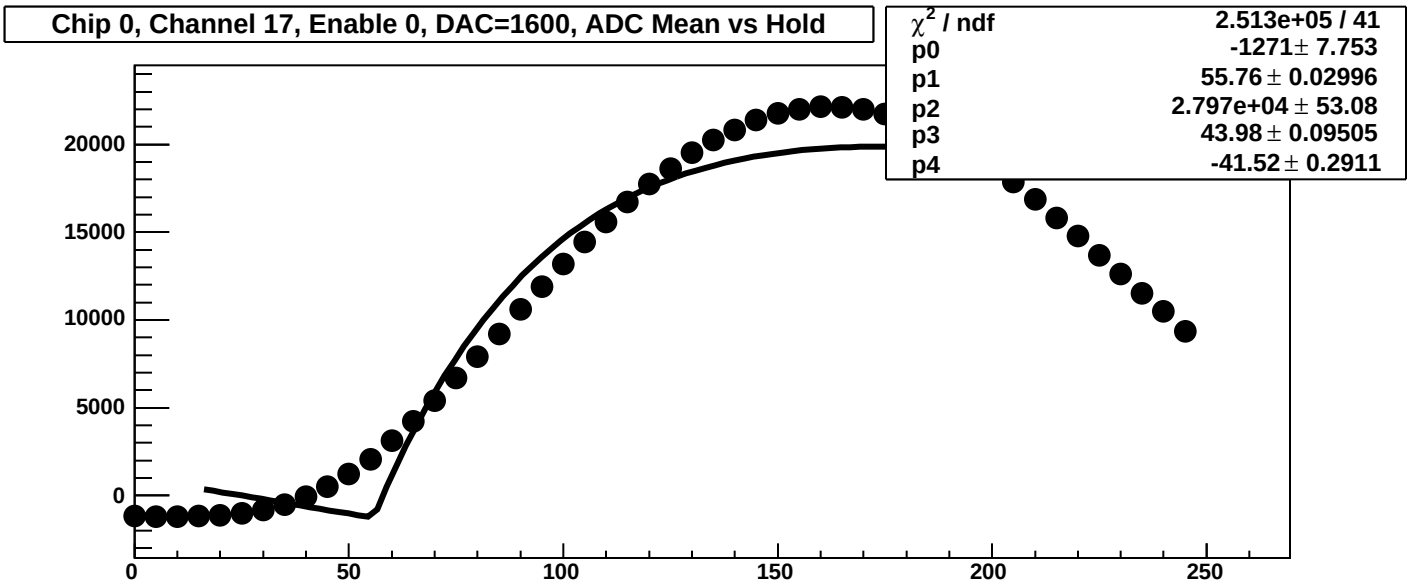

Chip 0, Channel 16, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

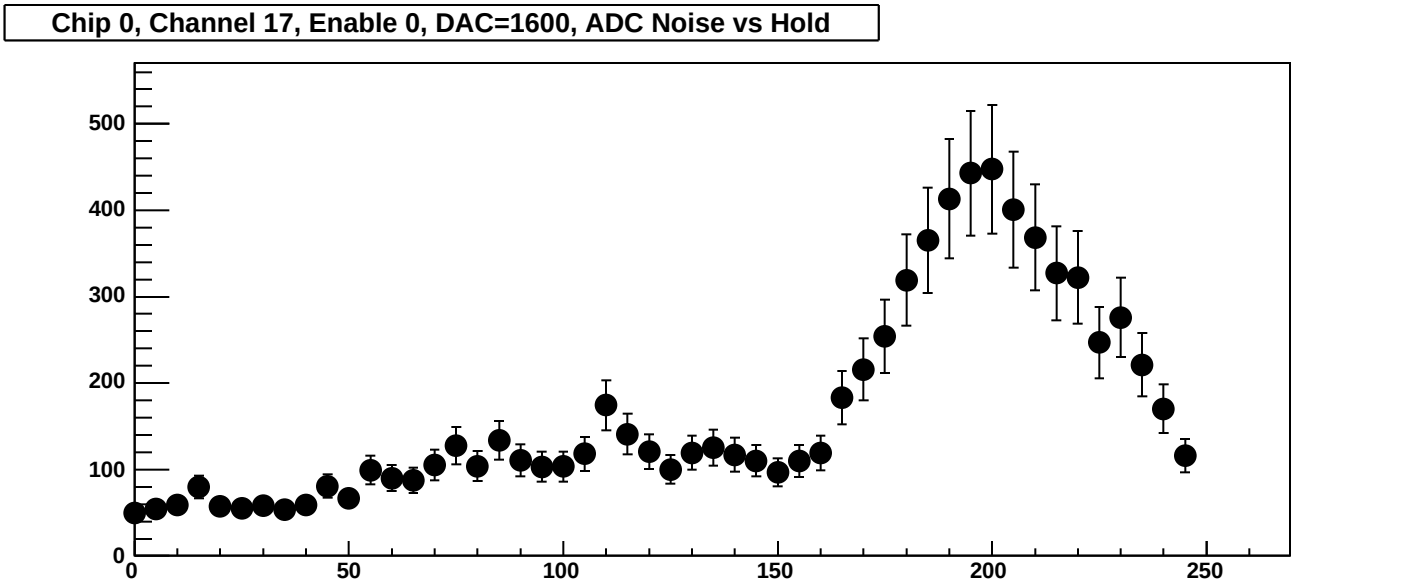

Chip 0, Channel 16, Enable 4, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
0
50
100
150
200
250

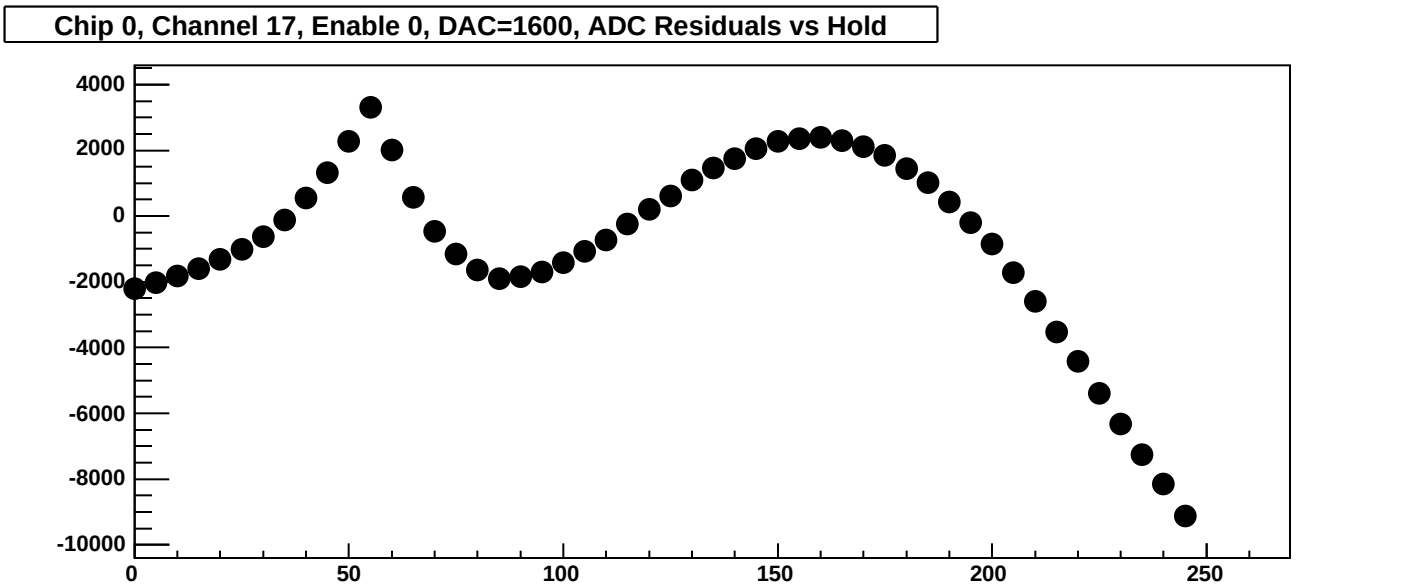

Chip 0, Channel 16, Enable 5!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.32e+07 / 42
p0 -1.016e+10 ± 3.441
p1 -6.619e+04 ± 0.03924
p2 1.016e+10 ± 3.441
p3 6.625e+04 ± 0.03921
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

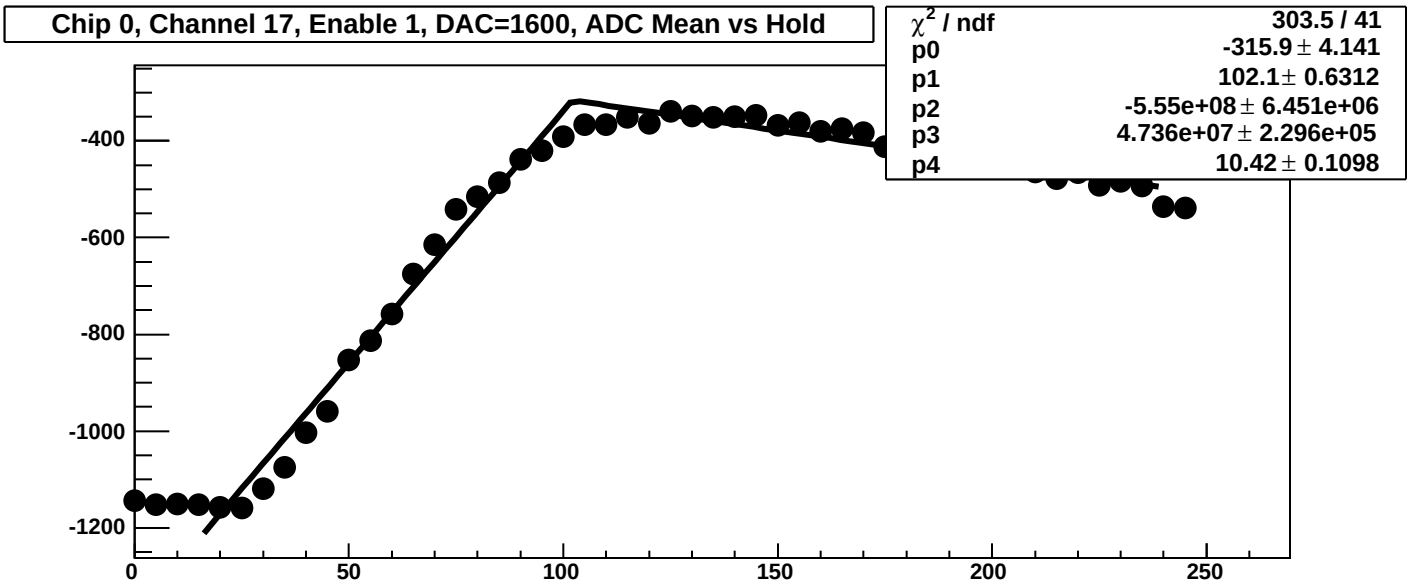

Chip 0, Channel 16, Enable 5!, DAC=1600, ADC Noise vs Hold
2500
2000
1500
1000
500
0
0
50
100
150
200
250

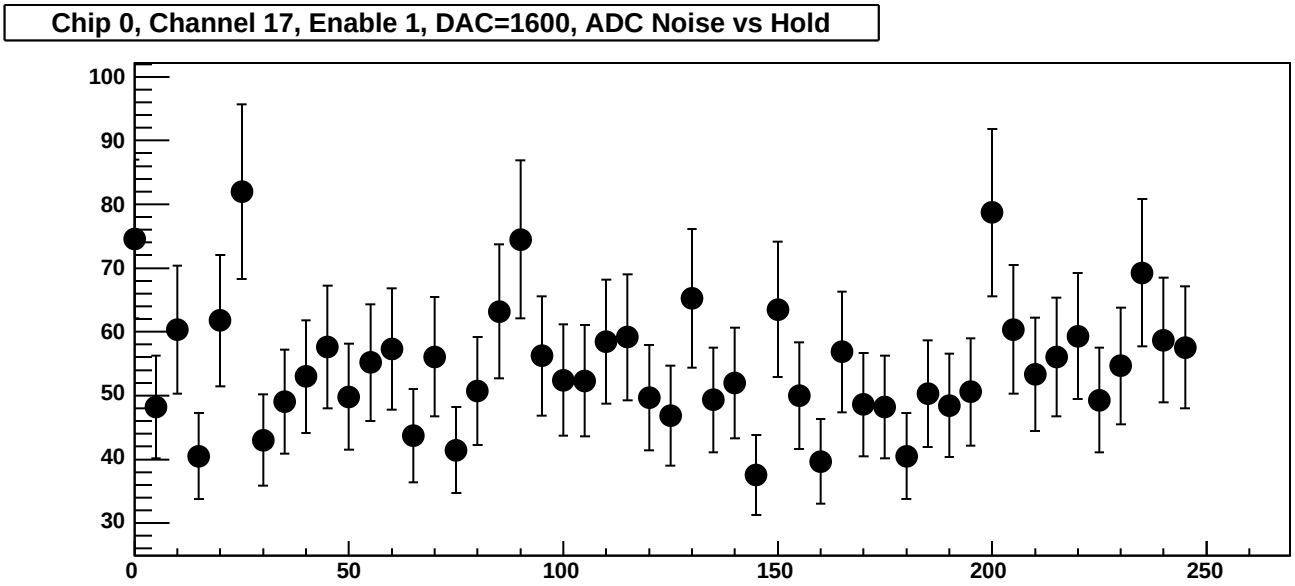

Chip 0, Channel 16, Enable 5!, DAC=1600, ADC Residuals vs Hold
10000
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
0
50
100
150
200
250

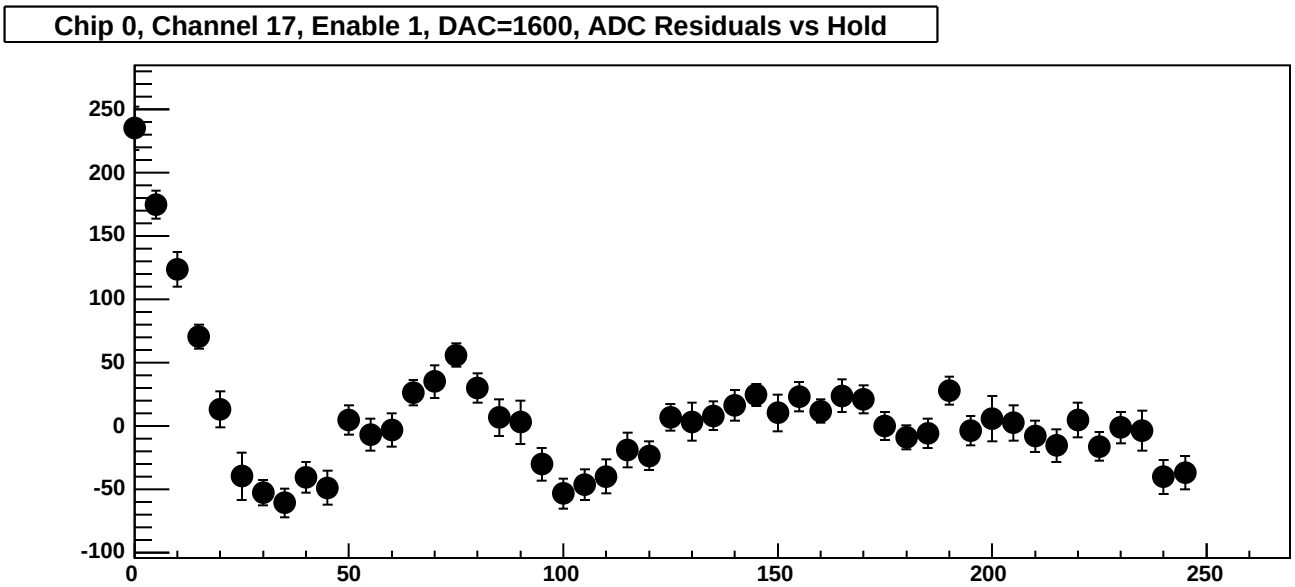

Chip 0, Channel 17, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.513e+05 / 41
p0 -1271 ± 7.753
p1 55.76 ± 0.02996
p2 2.797e+04 ± 53.08
p3 43.98 ± 0.09505
p4 -41.52 ± 0.2911
20000
15000
10000
5000
0
0
50
100
150
200
250

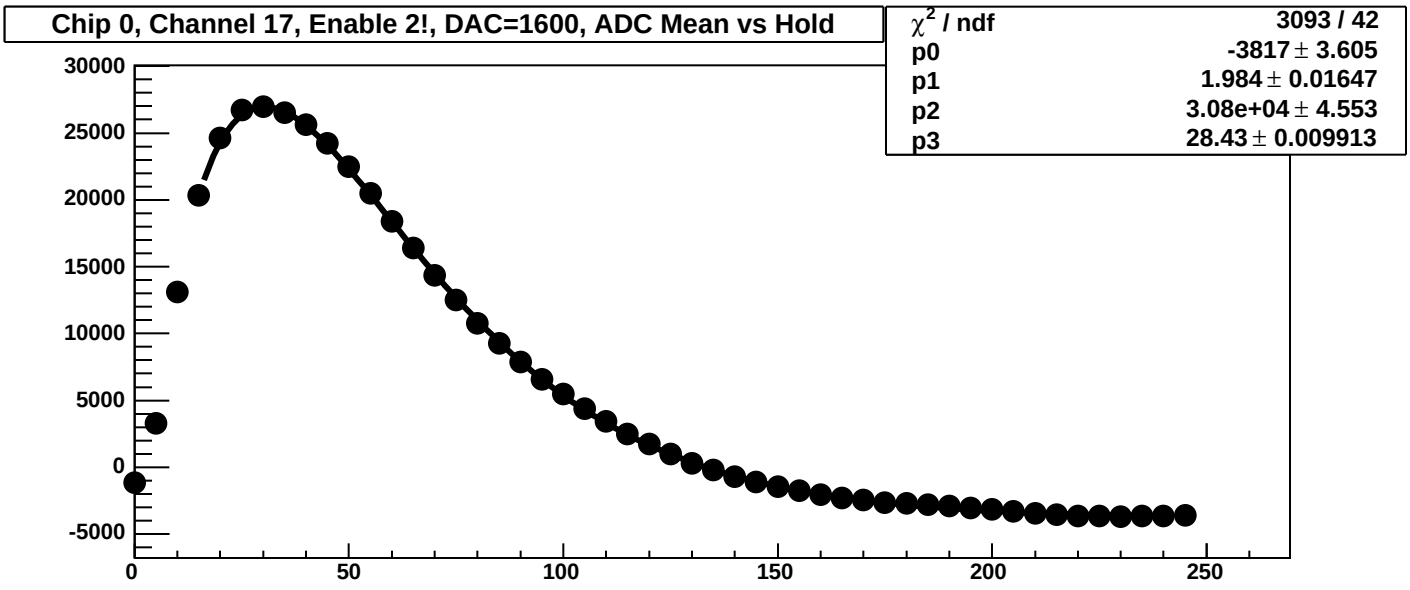

Chip 0, Channel 17, Enable 0, DAC=1600, ADC Noise vs Hold
500
400
300
200
100
0
0
50
100
150
200
250

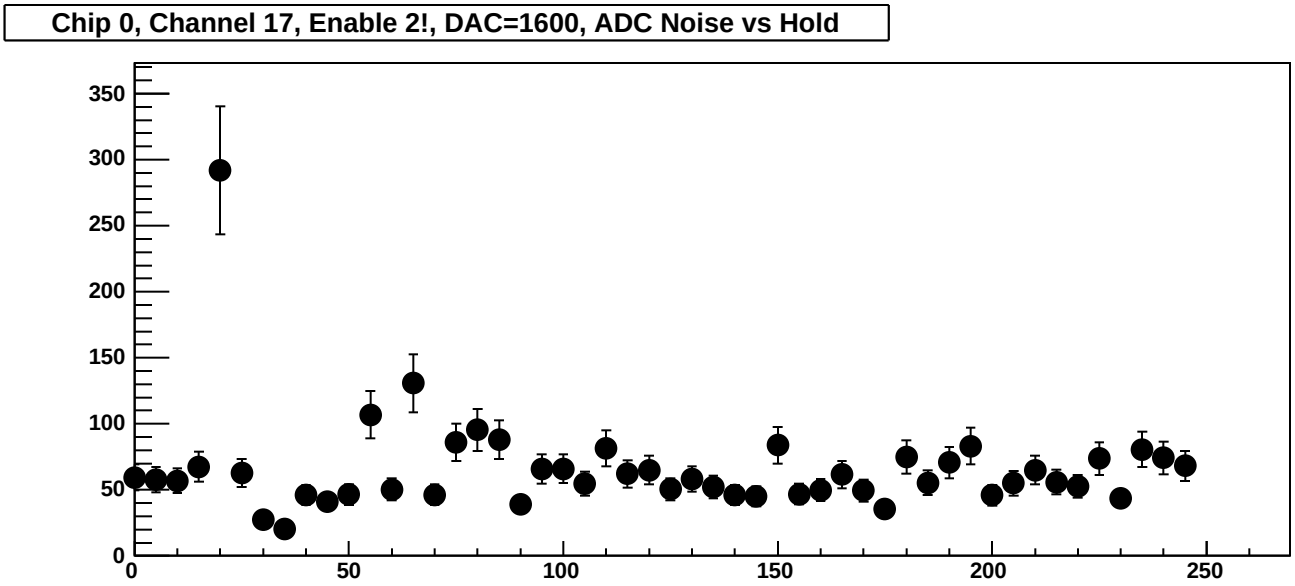

Chip 0, Channel 17, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

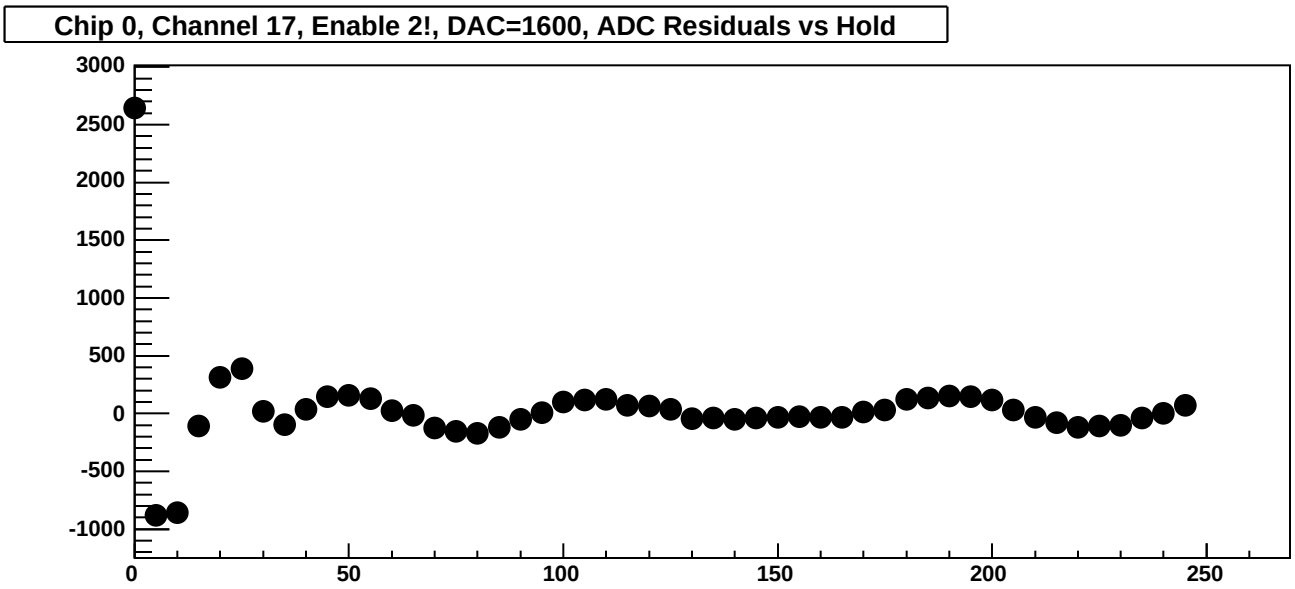

Chip 0, Channel 17, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 303.5 / 41
p0 -315.9 ± 4.141
p1 102.1 ± 0.6312
p2 -5.55e+08 ± 6.451e+06
p3 4.736e+07 ± 2.296e+05
p4 10.42 ± 0.1098
-400
-600
-800
-1000
-1200
0
50
100
150
200
250

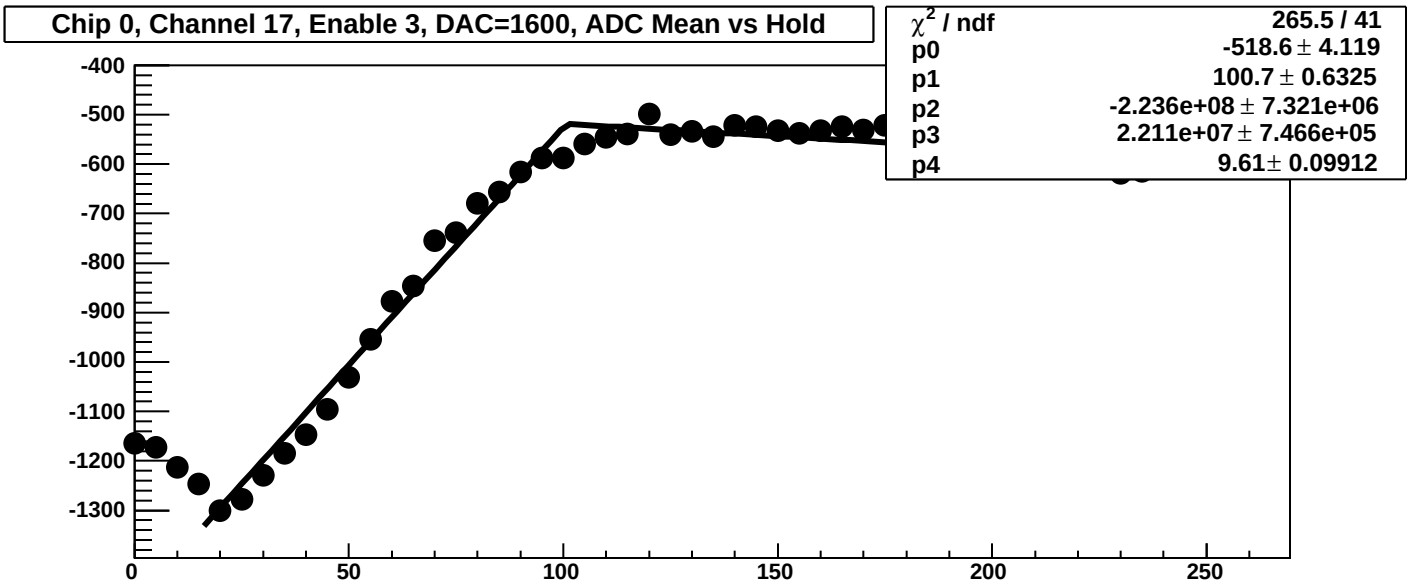

Chip 0, Channel 17, Enable 1, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250

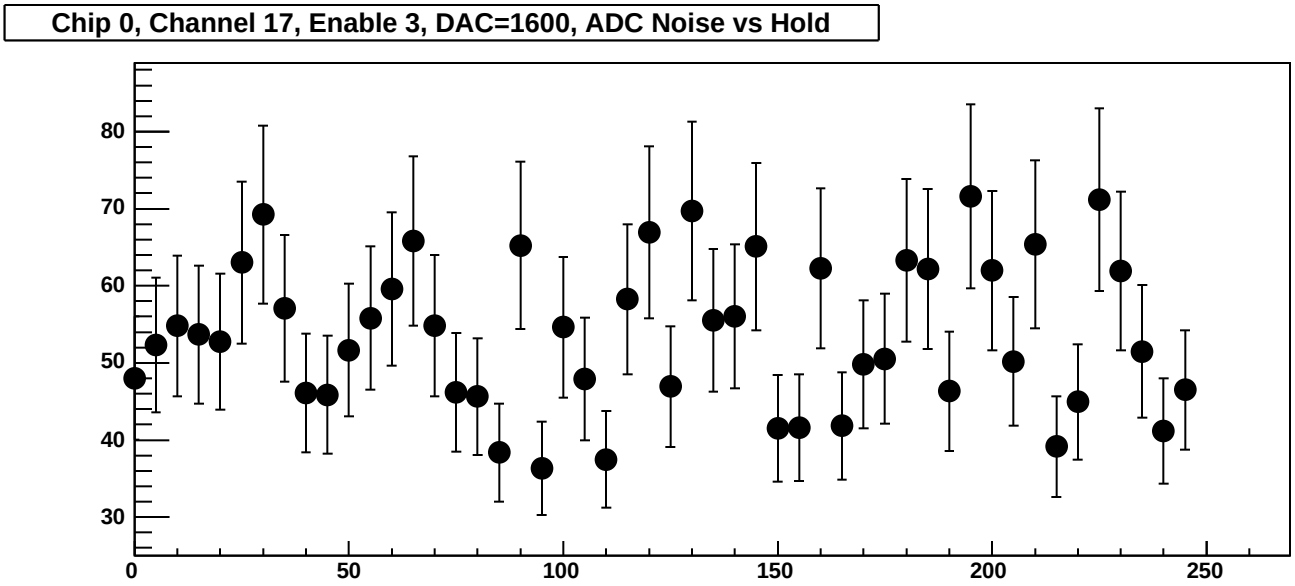

Chip 0, Channel 17, Enable 1, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

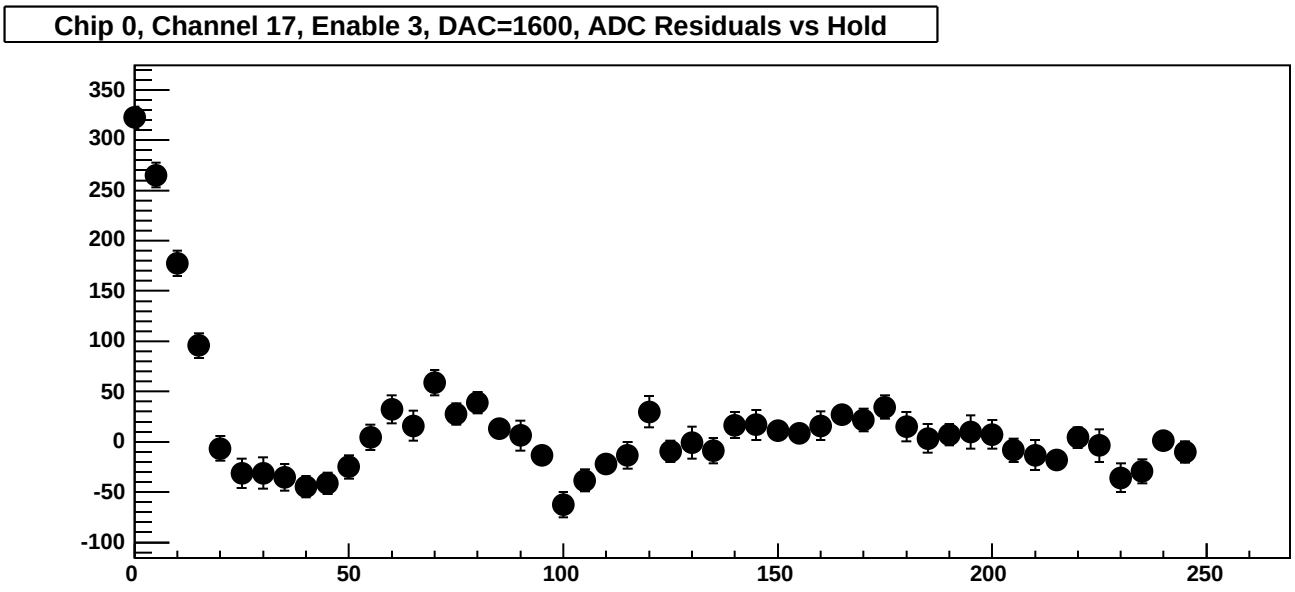

Chip 0, Channel 17, Enable 2!, DAC=1600, ADC Mean vs Hold
χ² / ndf 3093 / 42
p0 -3817 ± 3.605
p1 1.984 ± 0.01647
p2 3.08e+04 ± 4.553
p3 28.43 ± 0.009913
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 0, Channel 17, Enable 2!, DAC=1600, ADC Noise vs Hold
350
300
250
200
150
100
50
0
0
50
100
150
200
250

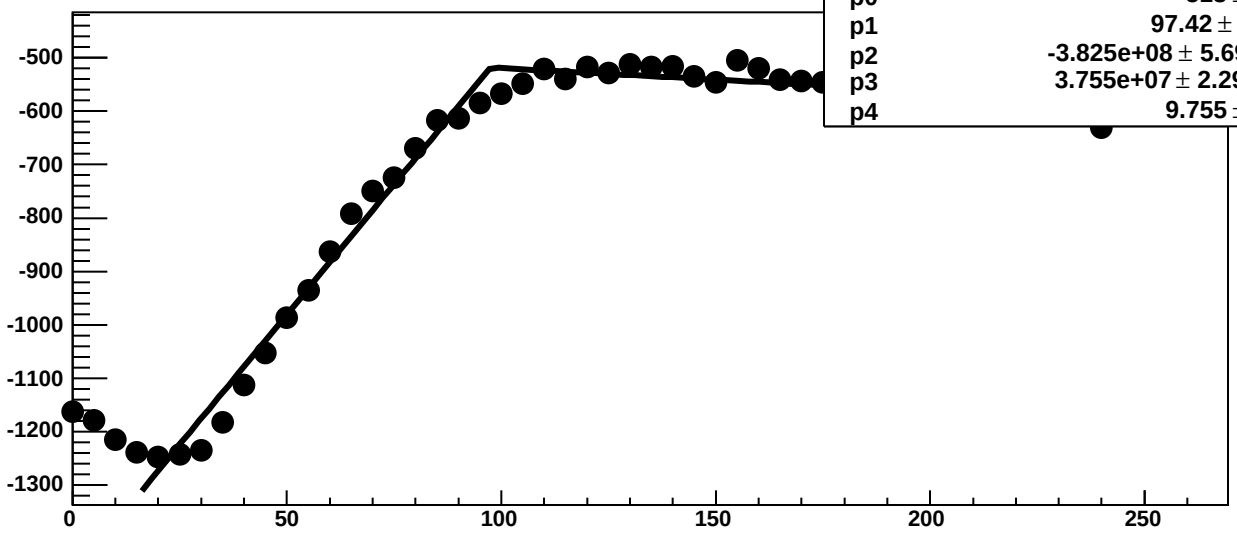

Chip 0, Channel 17, Enable 2!, DAC=1600, ADC Residuals vs Hold
3000
2500
2000
1500
1000
500
0
-500
-1000
0
50
100
150
200
250


Chip 0, Channel 17, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 265.5 / 41
p0 -518.6 ± 4.119
p1 100.7 ± 0.6325
p2 -2.236e+08 ± 7.321e+06
p3 2.211e+07 ± 7.466e+05
p4 9.61 ± 0.09912
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
50
100
150
200
250

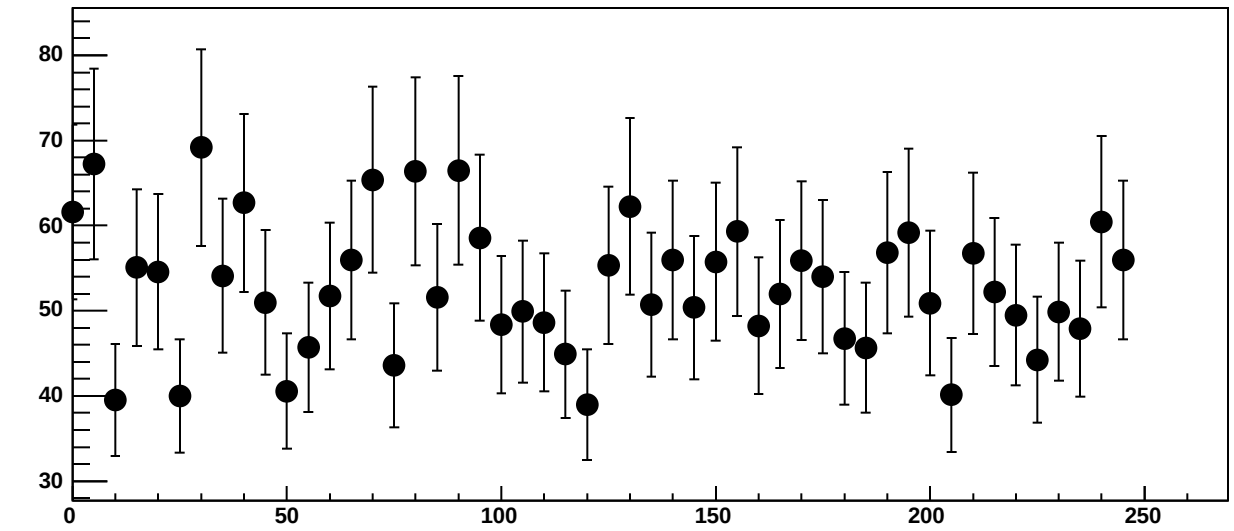

Chip 0, Channel 17, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 0, Channel 17, Enable 3, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

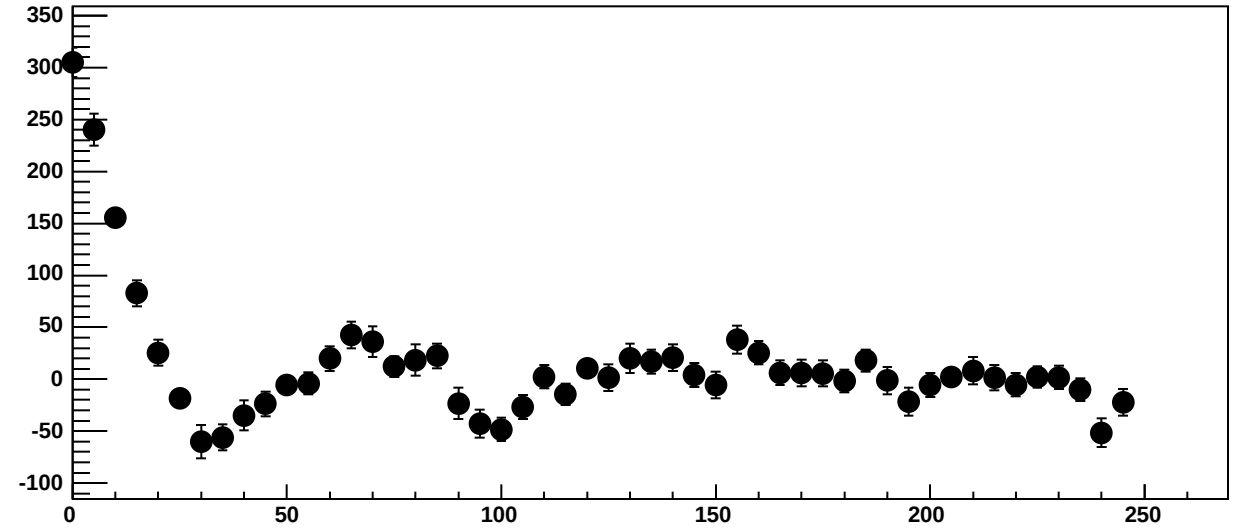
Chip 0, Channel 17, Enable 4, DAC=1600, ADC Mean vs Hold



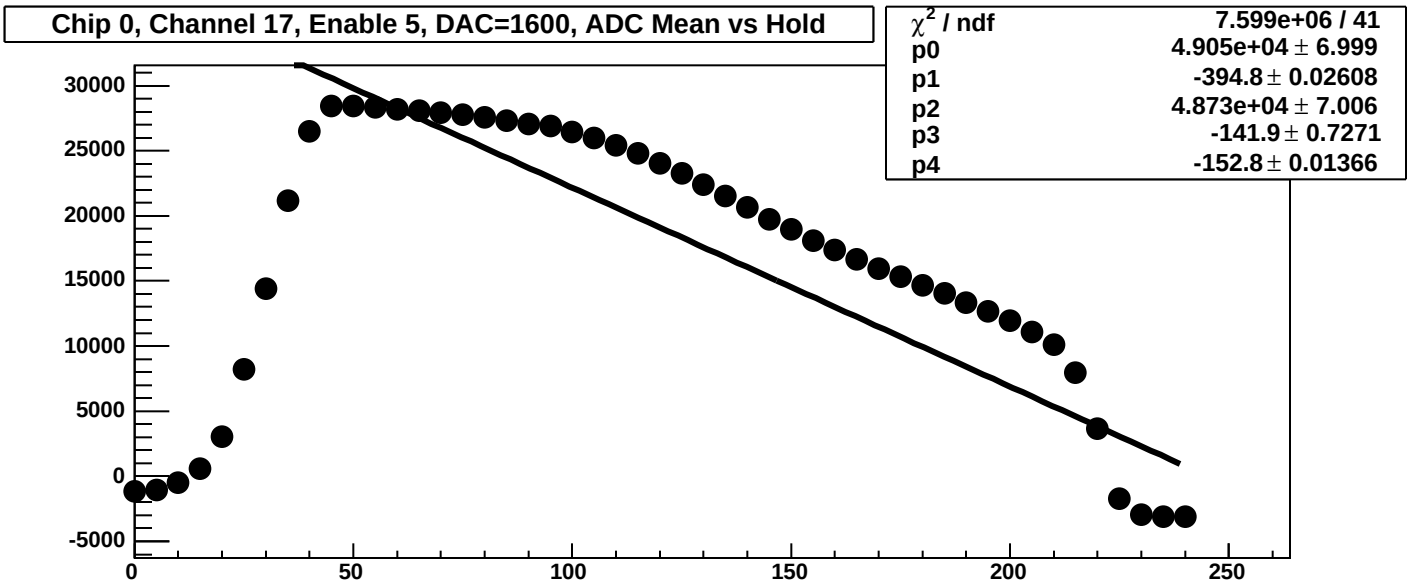
Chip 0, Channel 17, Enable 4, DAC=1600, ADC Noise vs Hold



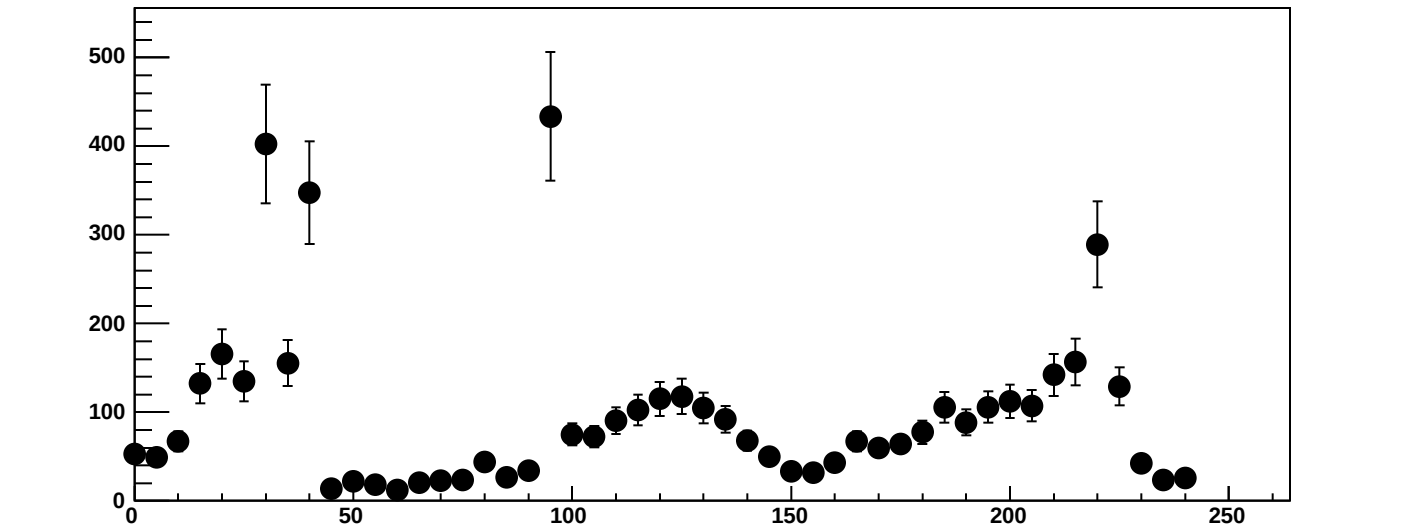
Chip 0, Channel 17, Enable 4, DAC=1600, ADC Residuals vs Hold



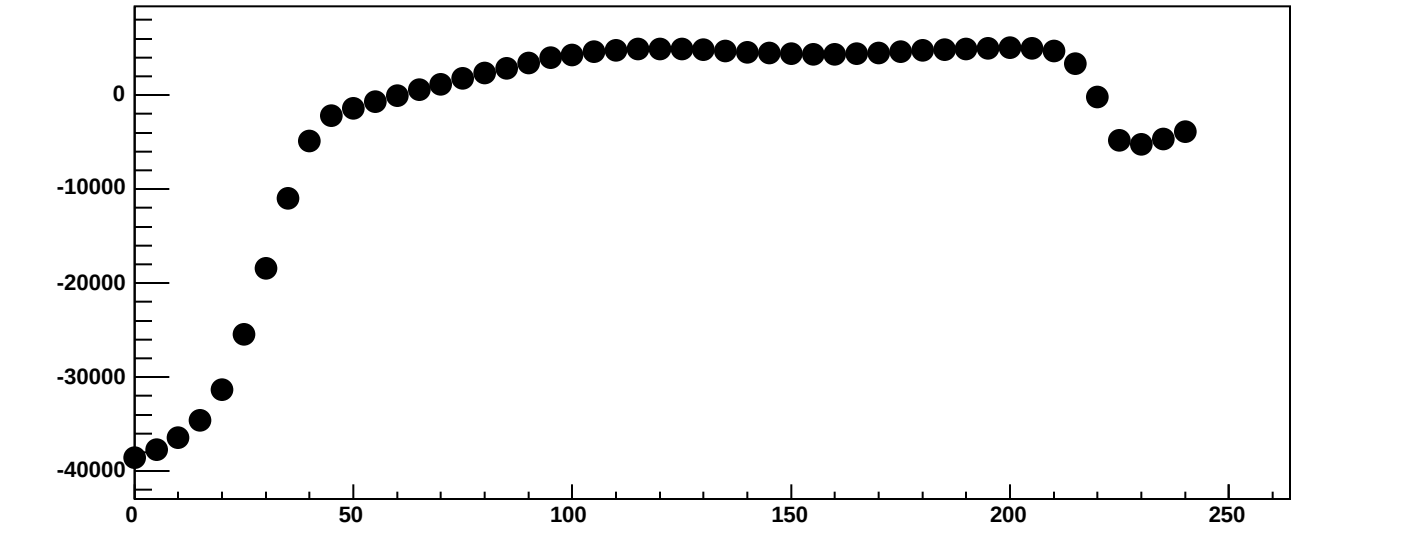
Chip 0, Channel 17, Enable 5, DAC=1600, ADC Mean vs Hold

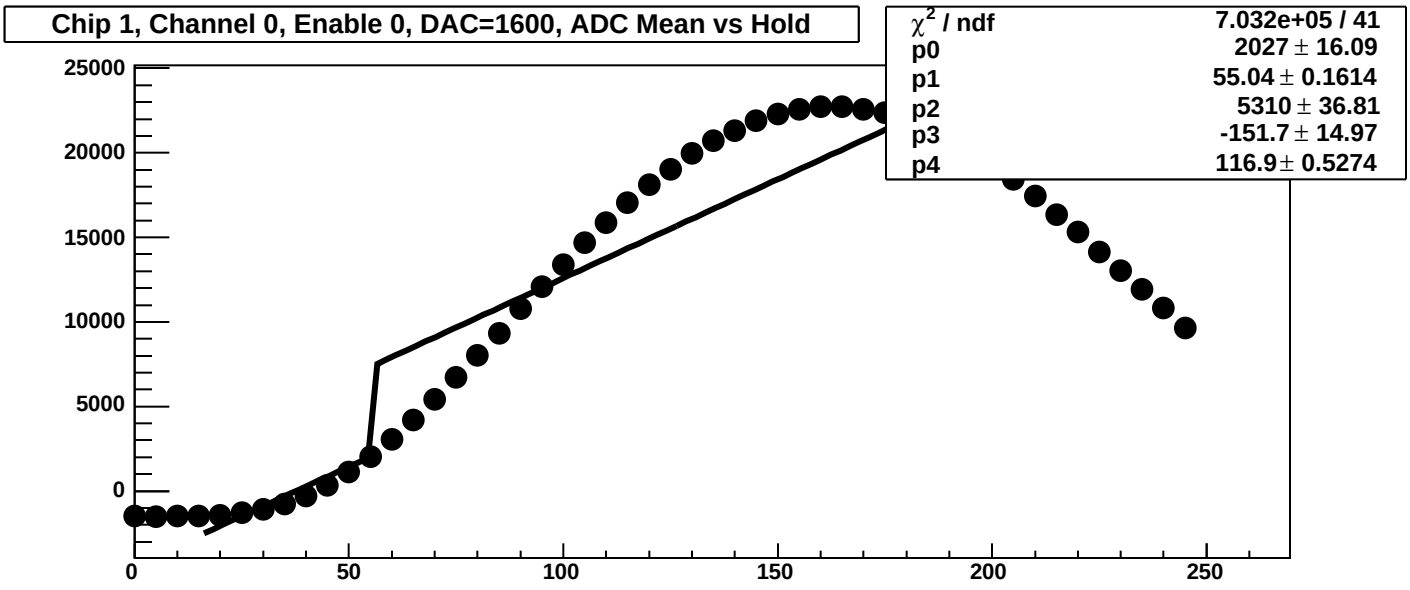


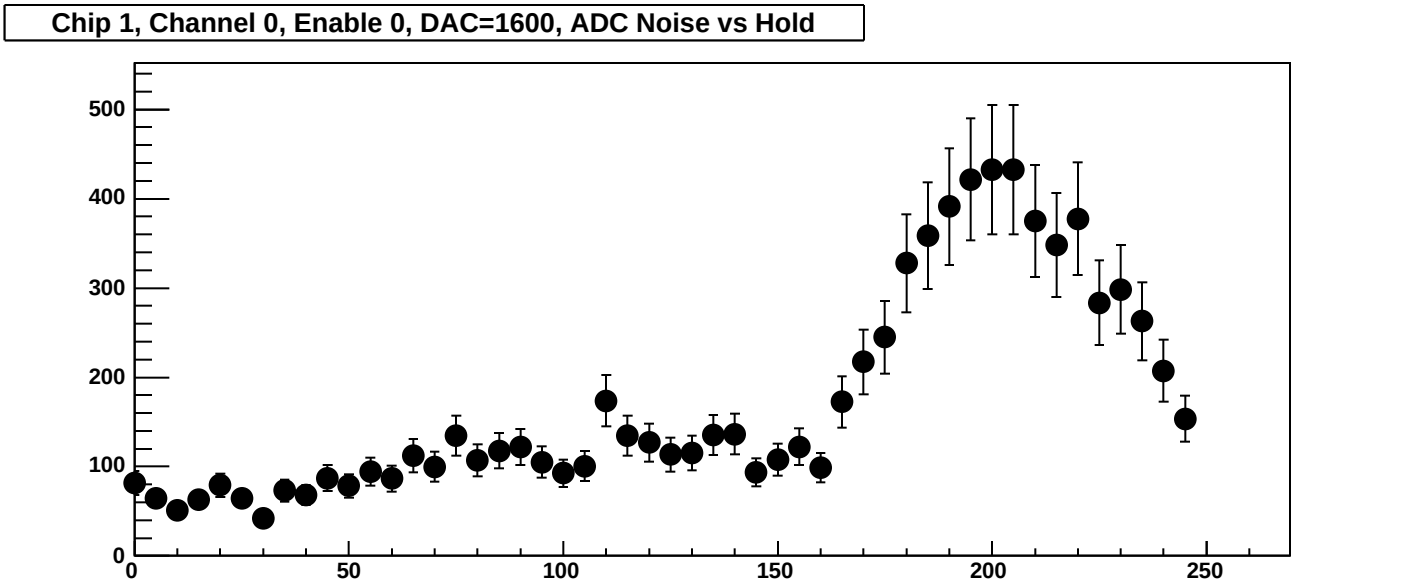
Chip 0, Channel 17, Enable 5, DAC=1600, ADC Noise vs Hold

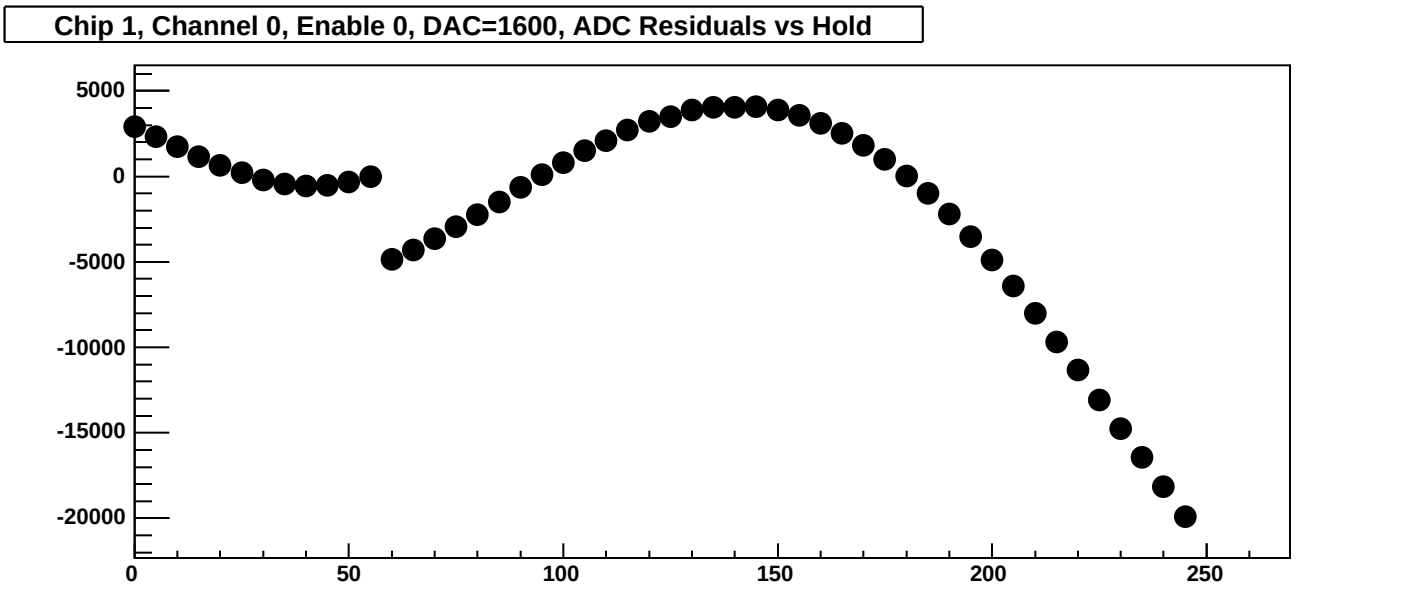


Chip 0, Channel 17, Enable 5, DAC=1600, ADC Residuals vs Hold

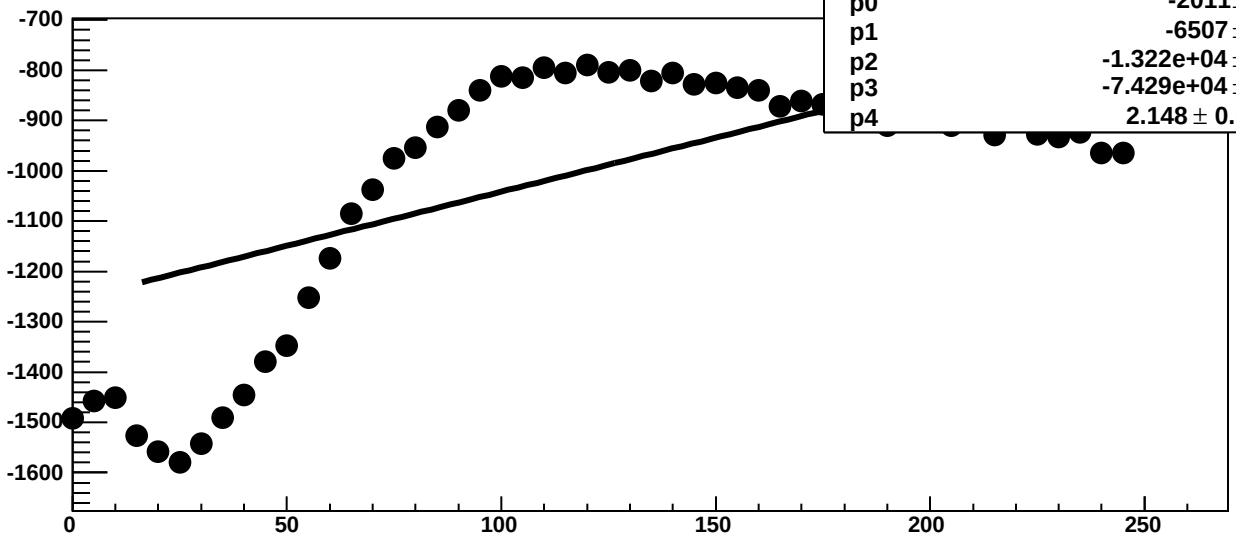



Chip 1, Channel 0, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.032e+05 / 41
p0 2027 ± 16.09
p1 55.04 ± 0.1614
p2 5310 ± 36.81
p3 -151.7 ± 14.97
p4 116.9 ± 0.5274
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

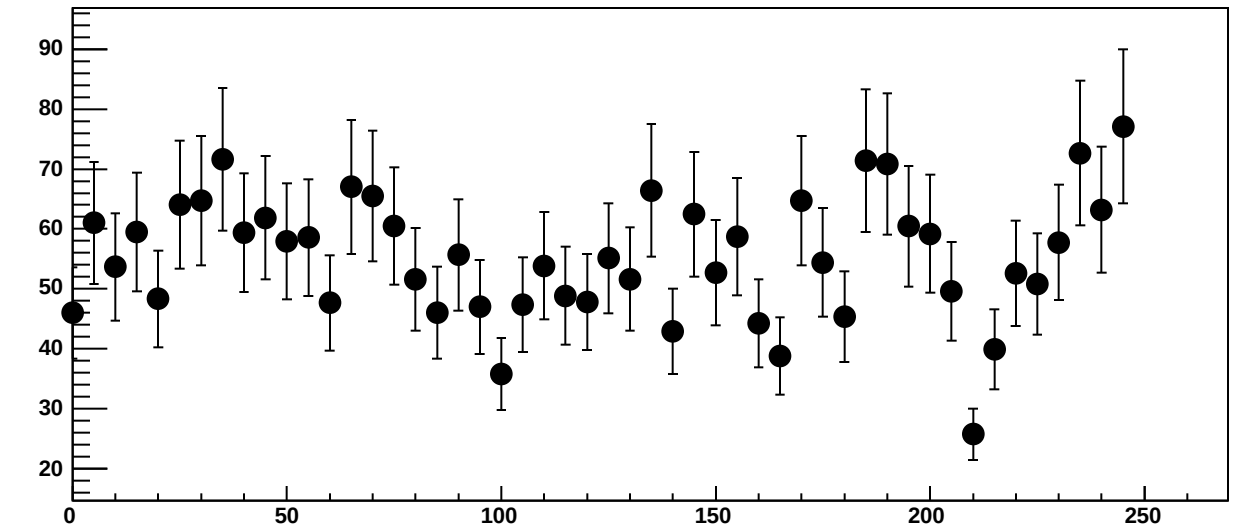

Chip 1, Channel 0, Enable 0, DAC=1600, ADC Noise vs Hold
500
400
300
200
100
0
0
50
100
150
200
250


Chip 1, Channel 0, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

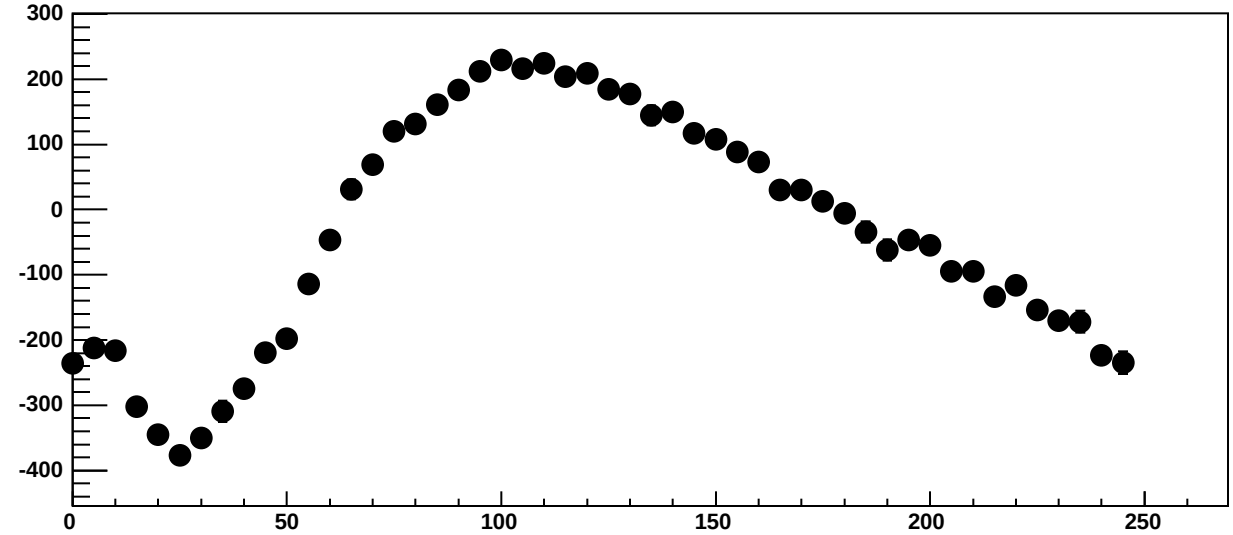
Chip 1, Channel 0, Enable 1, DAC=1600, ADC Mean vs Hold



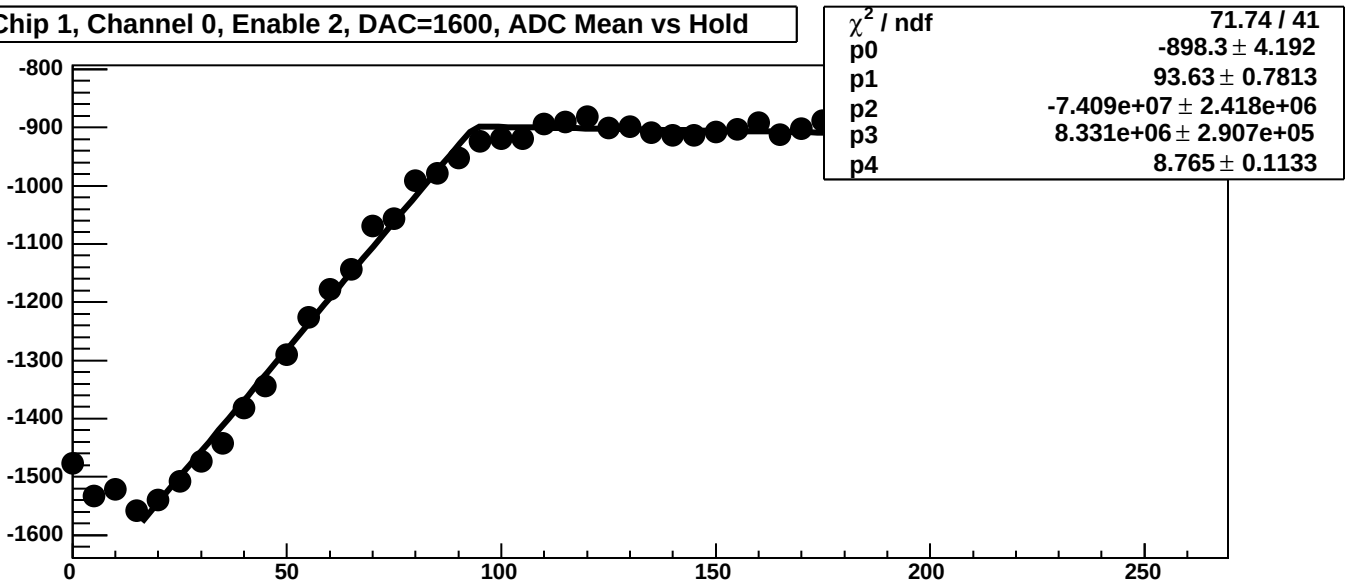
Chip 1, Channel 0, Enable 1, DAC=1600, ADC Noise vs Hold



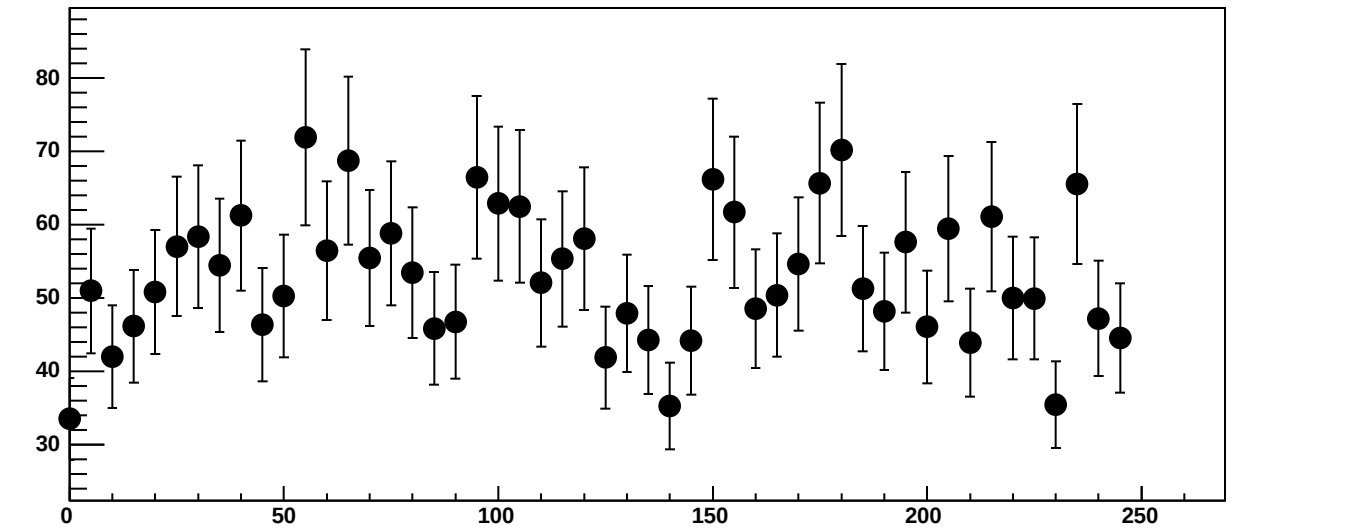
Chip 1, Channel 0, Enable 1, DAC=1600, ADC Residuals vs Hold



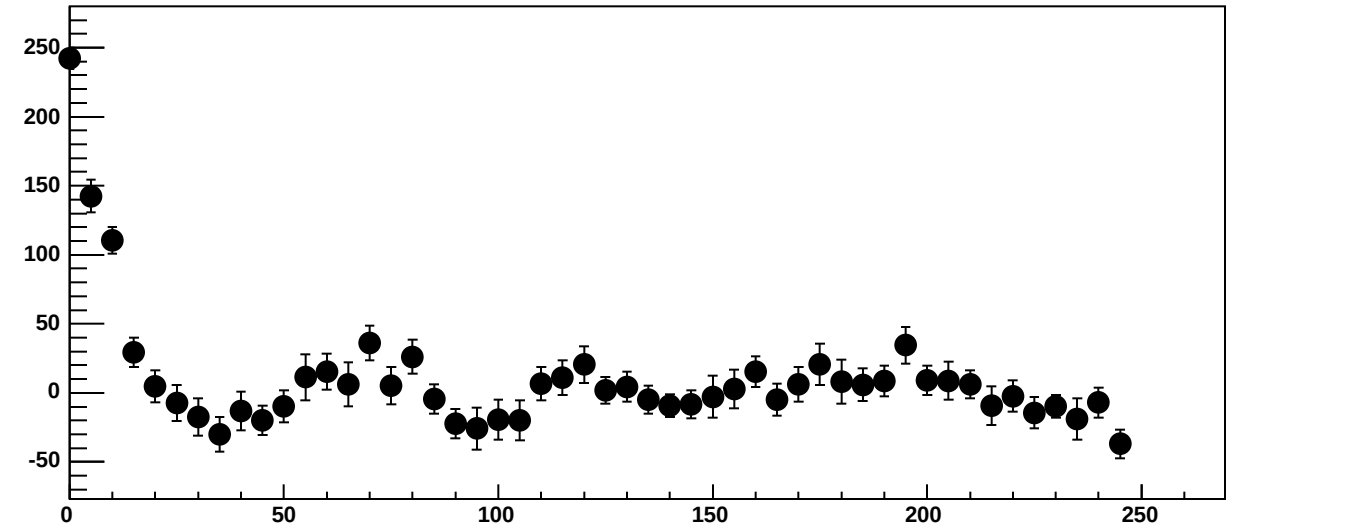
Chip 1, Channel 0, Enable 2, DAC=1600, ADC Mean vs Hold

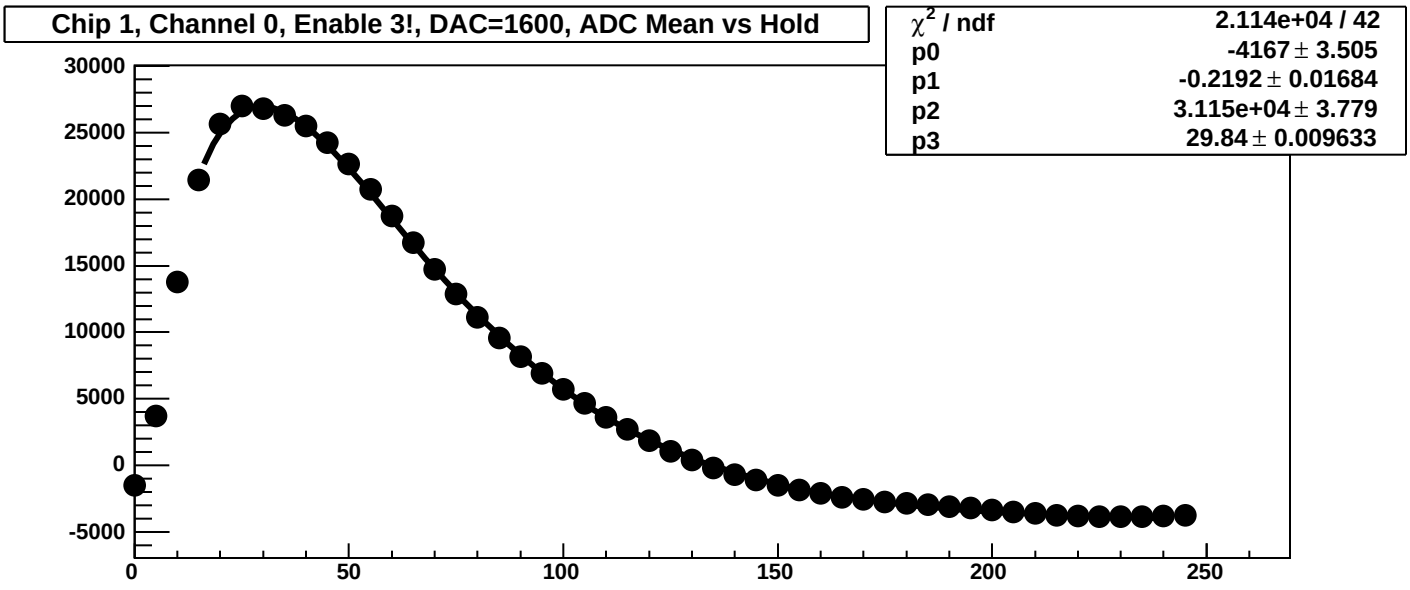


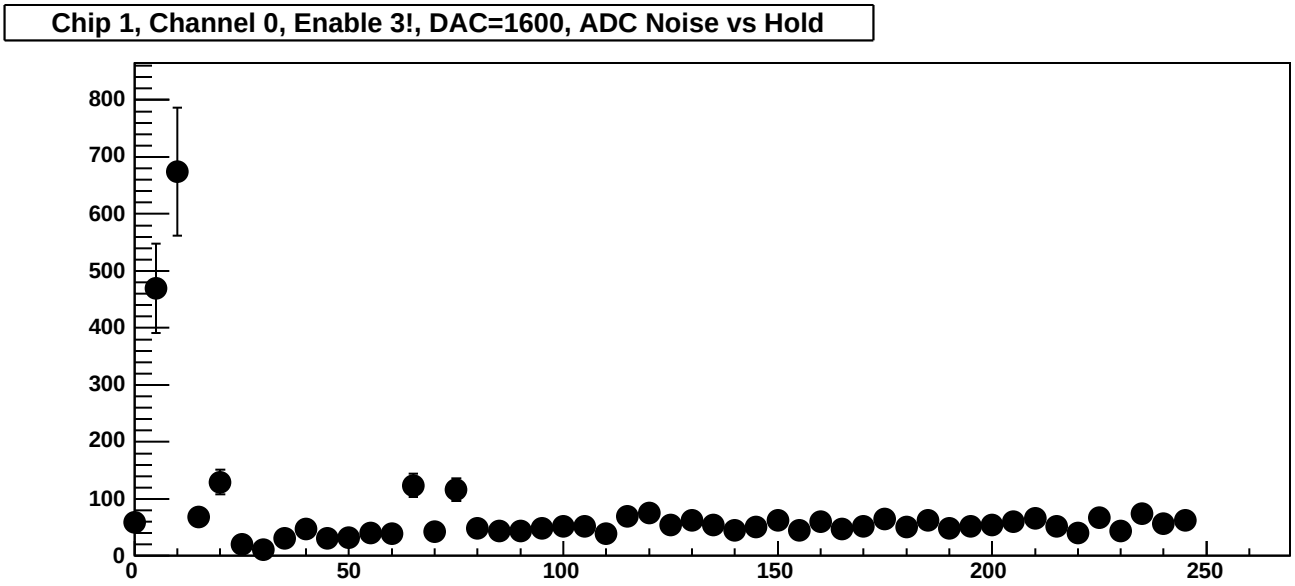
Chip 1, Channel 0, Enable 2, DAC=1600, ADC Noise vs Hold

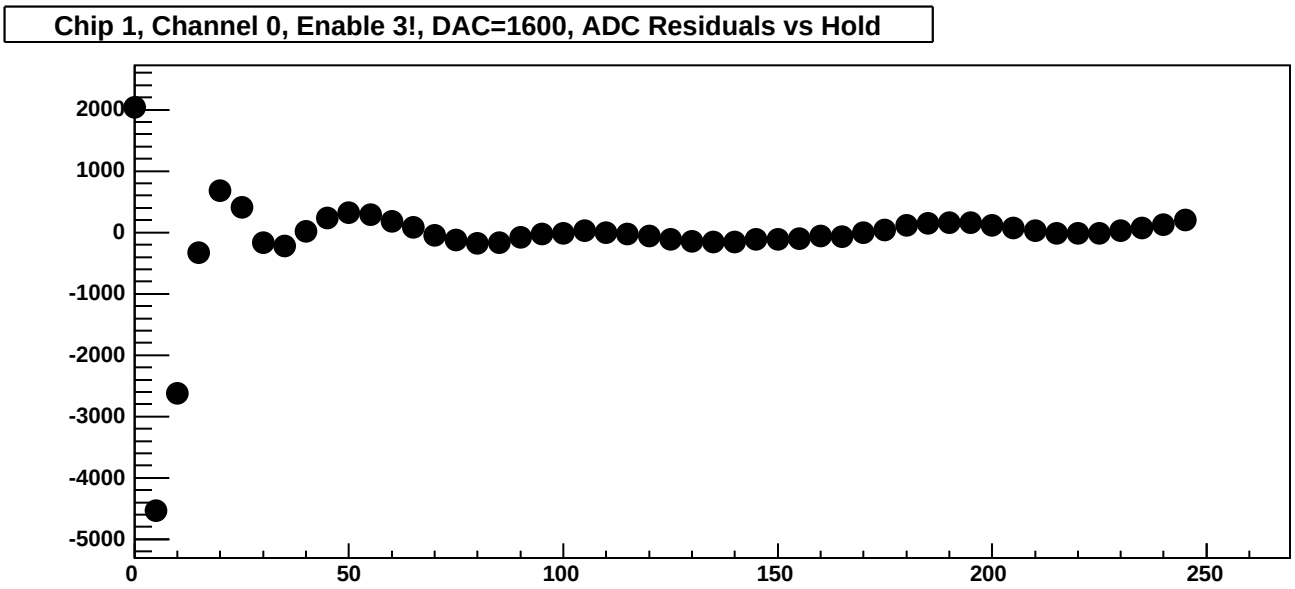


Chip 1, Channel 0, Enable 2, DAC=1600, ADC Residuals vs Hold

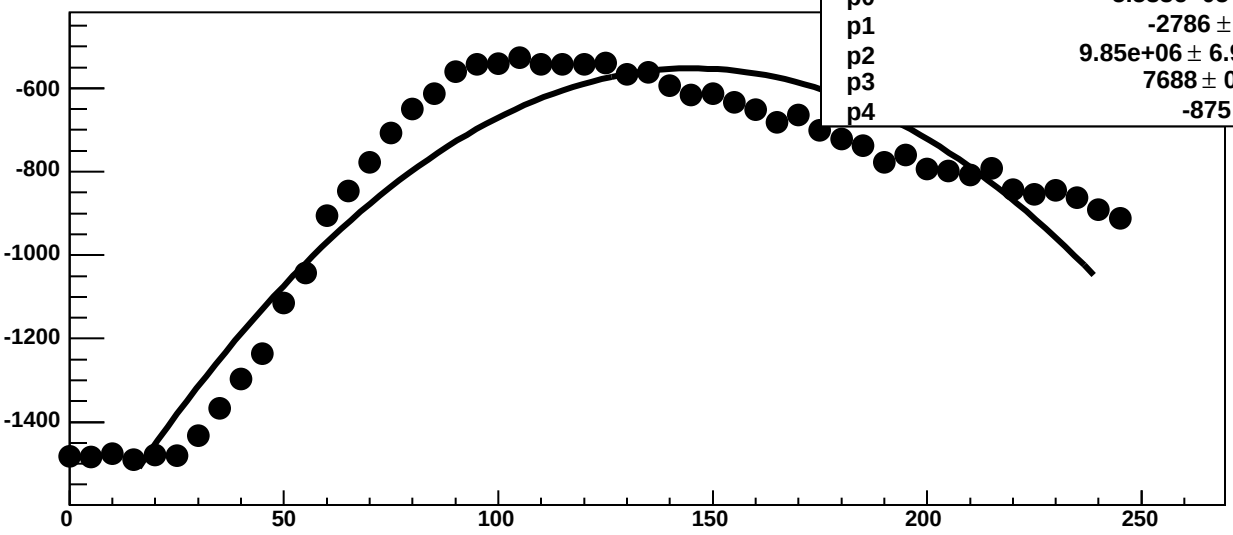



Chip 1, Channel 0, Enable 3!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.114e+04 / 42
p0 -4167 ± 3.505
p1 -0.2192 ± 0.01684
p2 3.115e+04 ± 3.779
p3 29.84 ± 0.009633
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

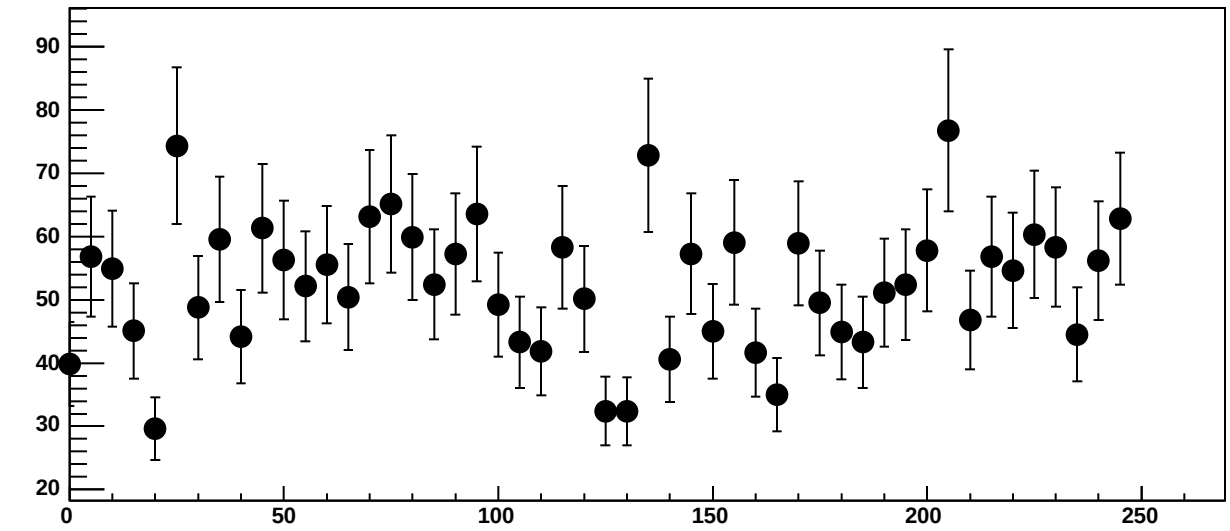

Chip 1, Channel 0, Enable 3!, DAC=1600, ADC Noise vs Hold
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 1, Channel 0, Enable 3!, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

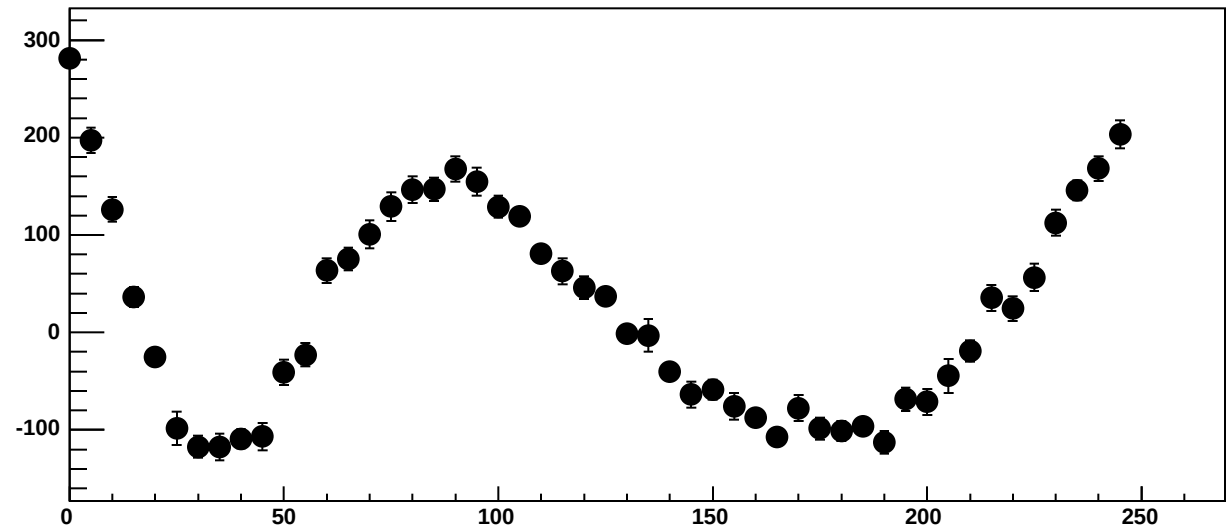
Chip 1, Channel 0, Enable 4, DAC=1600, ADC Mean vs Hold

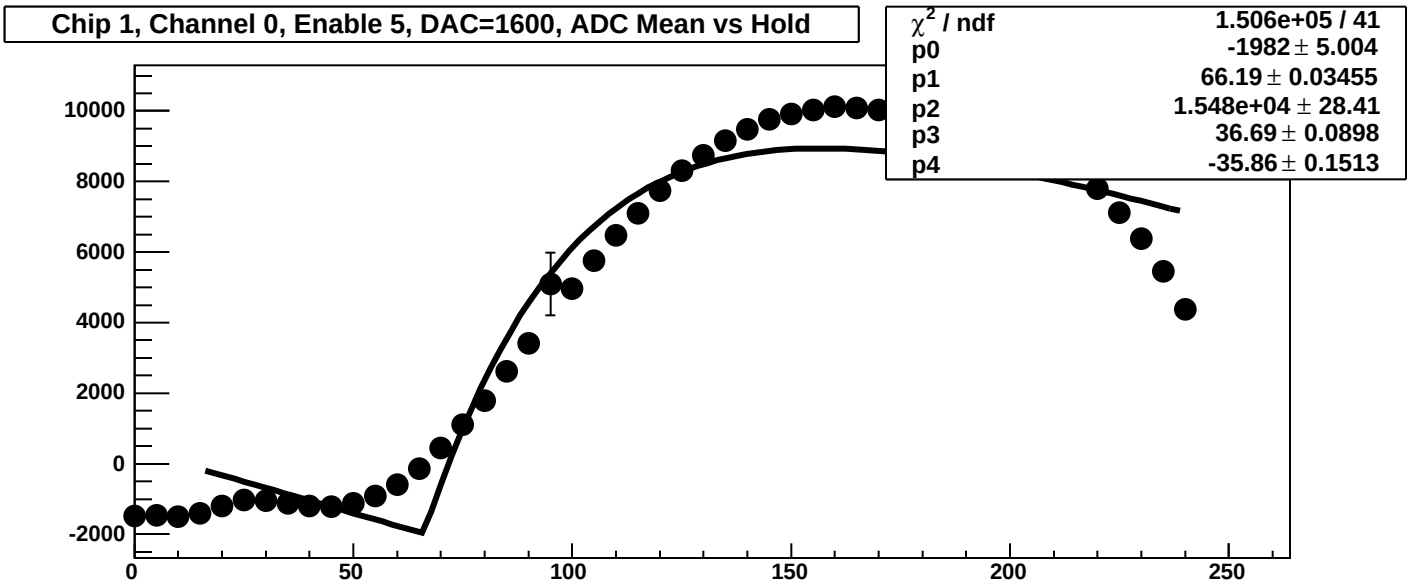


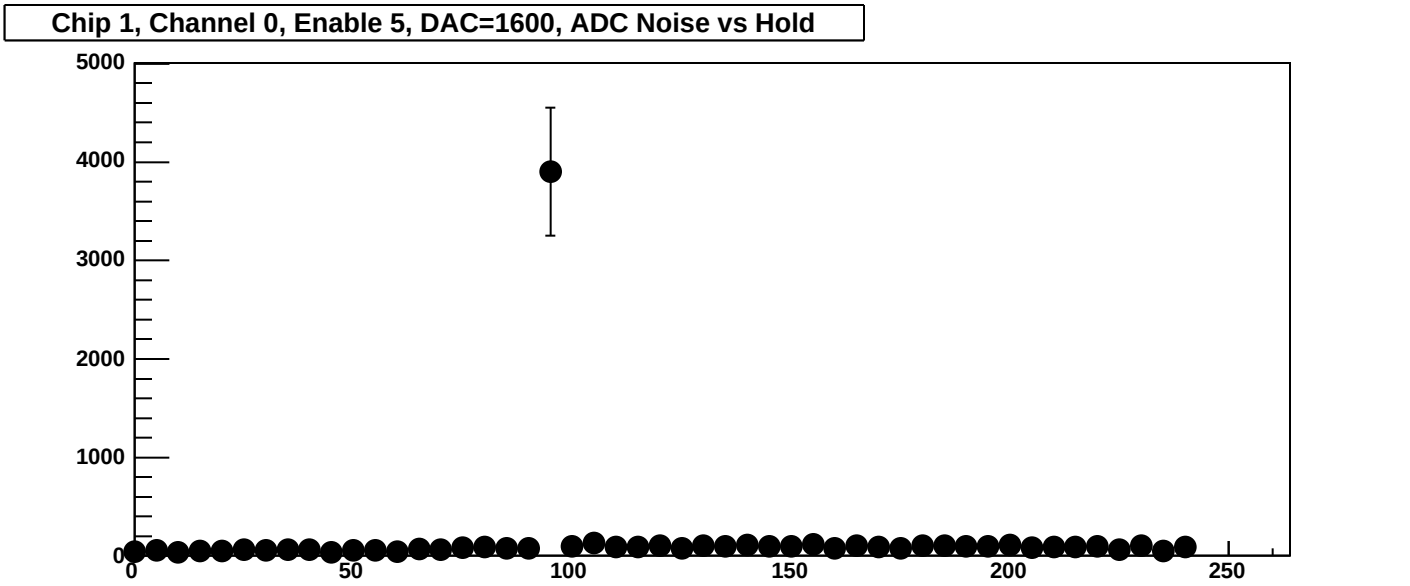
Chip 1, Channel 0, Enable 4, DAC=1600, ADC Noise vs Hold

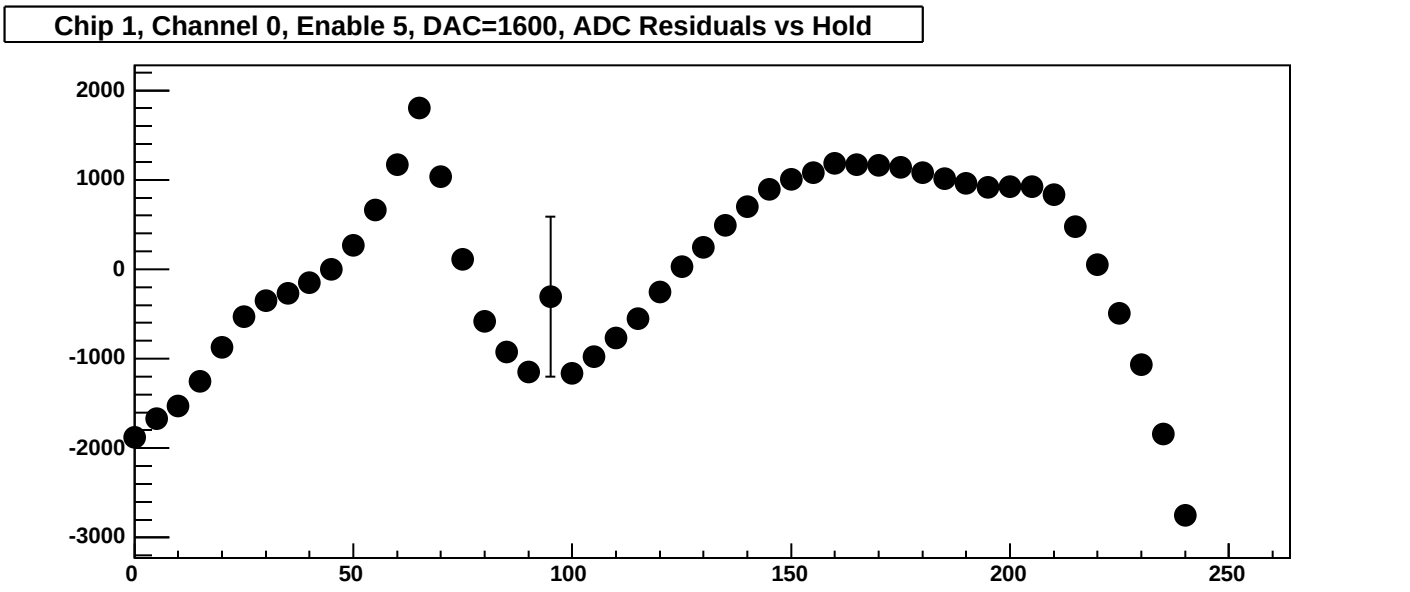


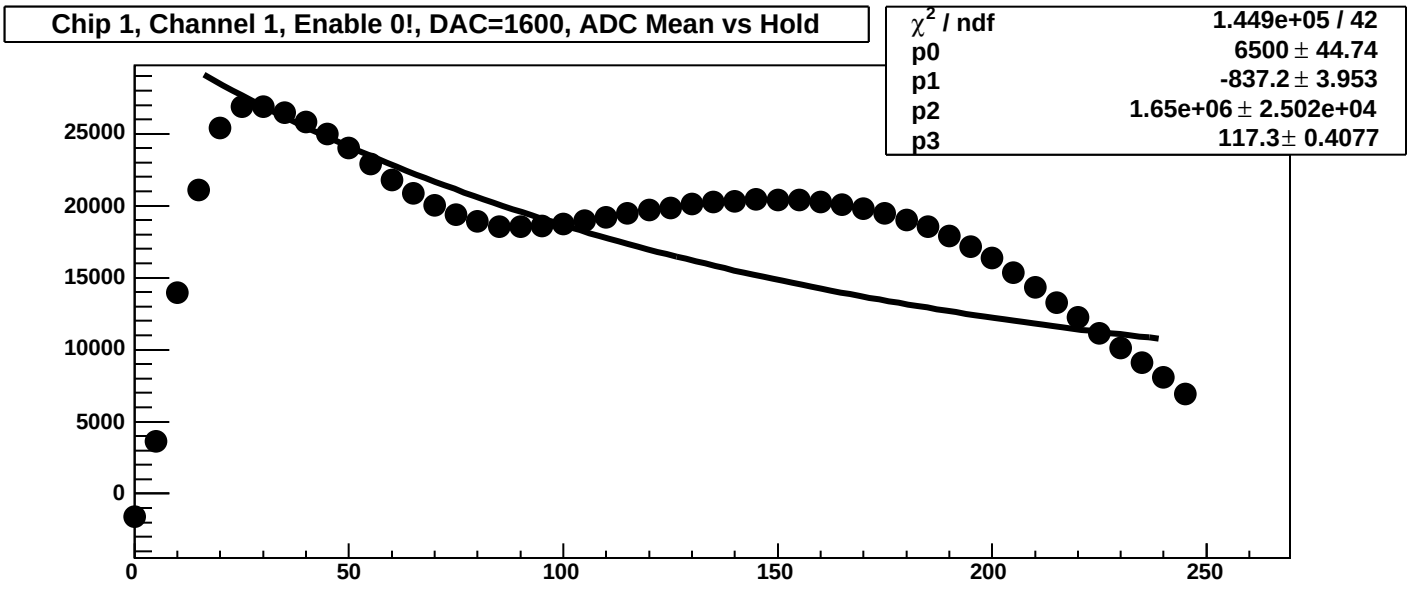
Chip 1, Channel 0, Enable 4, DAC=1600, ADC Residuals vs Hold

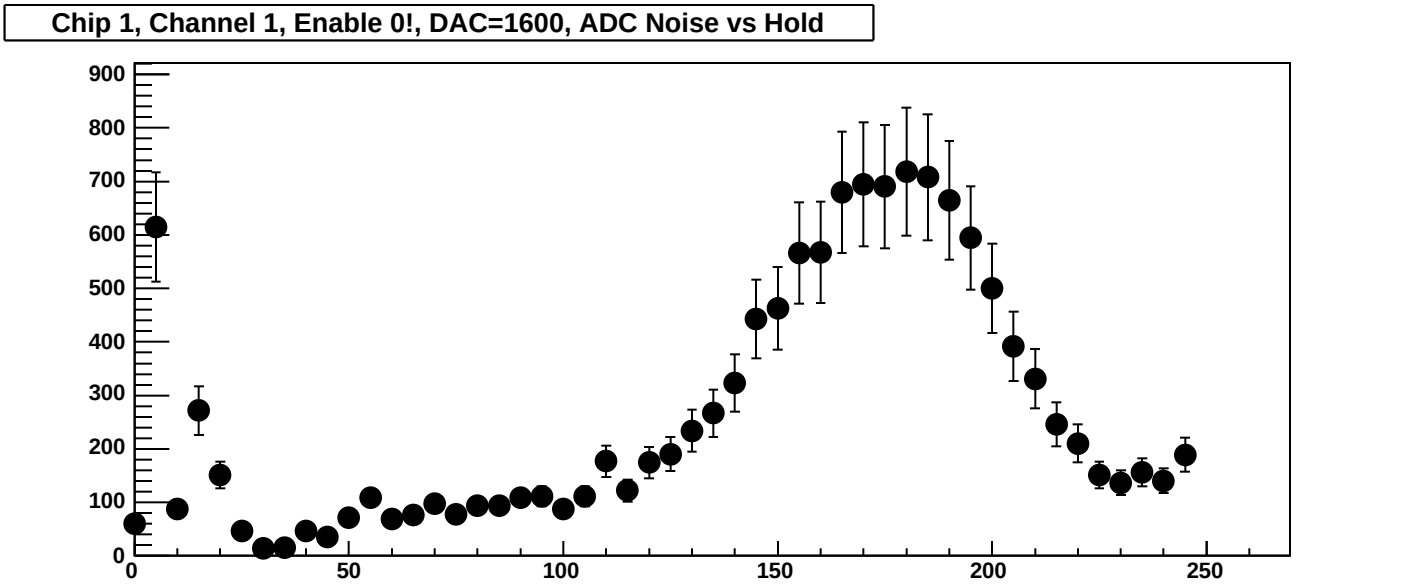


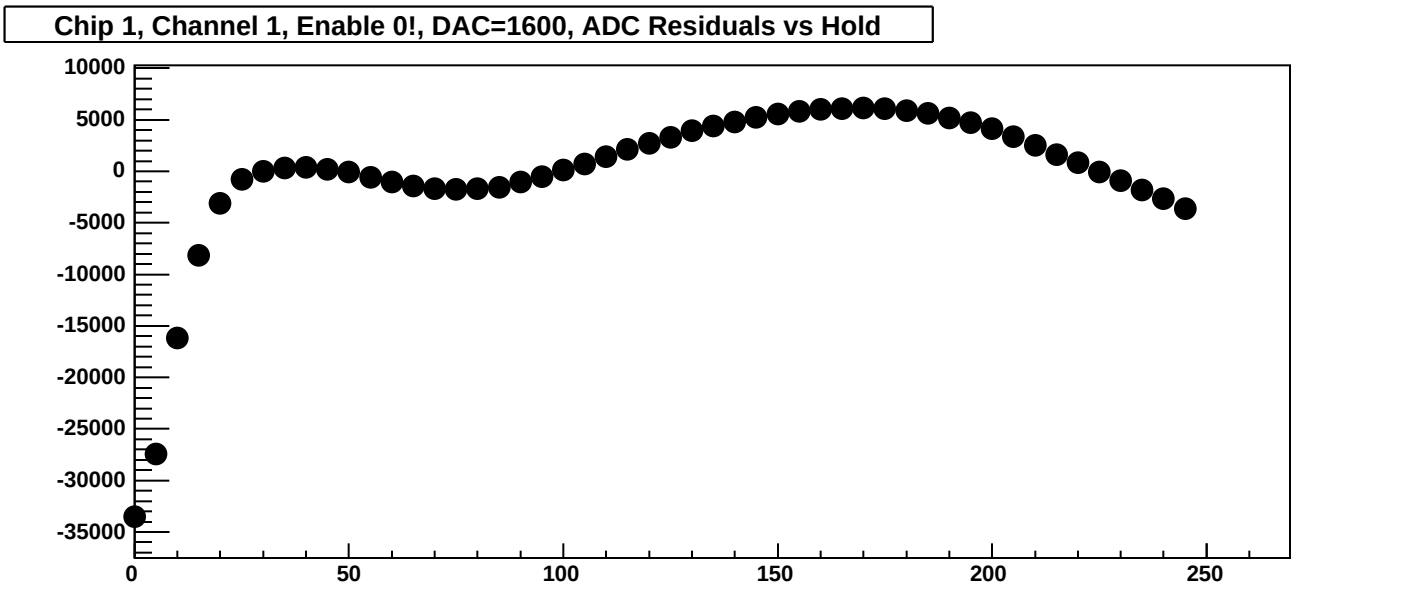

Chip 1, Channel 0, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.506e+05 / 41
p0 -1982 ± 5.004
p1 66.19 ± 0.03455
p2 1.548e+04 ± 28.41
p3 36.69 ± 0.0898
p4 -35.86 ± 0.1513
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250


Chip 1, Channel 0, Enable 5, DAC=1600, ADC Noise vs Hold
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

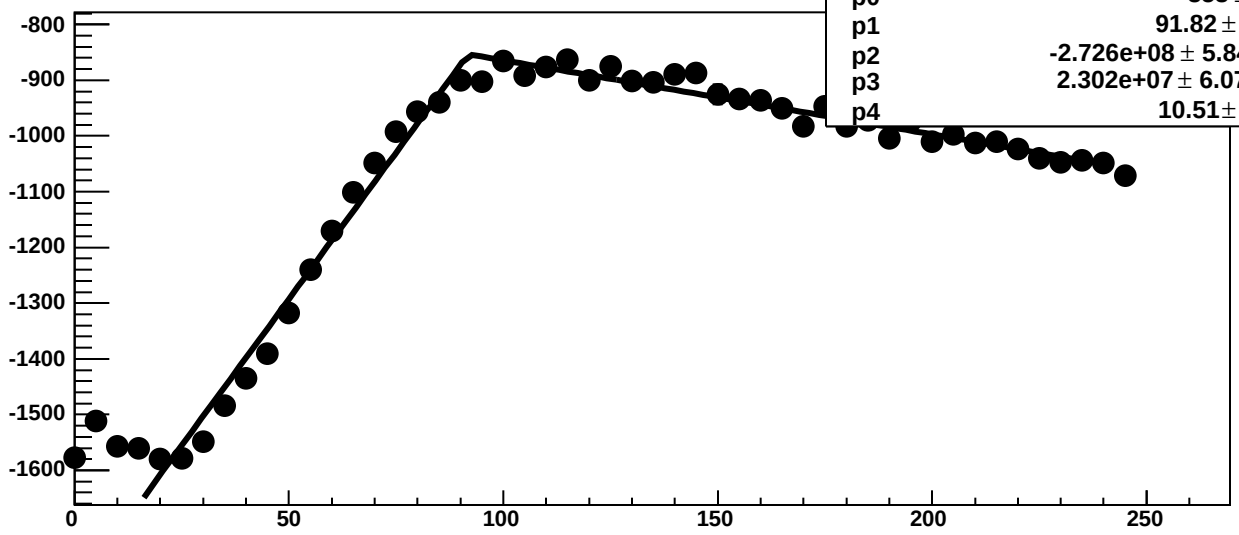

Chip 1, Channel 0, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250


Chip 1, Channel 1, Enable 0!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.449e+05 / 42
p0 6500 ± 44.74
p1 -837.2 ± 3.953
p2 1.65e+06 ± 2.502e+04
p3 117.3 ± 0.4077
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

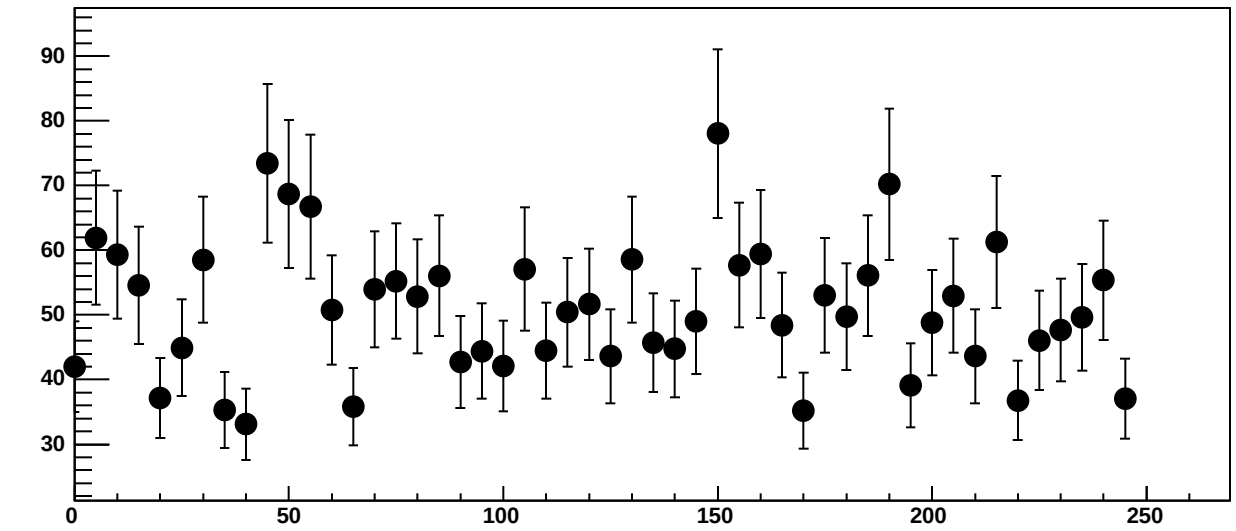

Chip 1, Channel 1, Enable 0!, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 1, Channel 1, Enable 0!, DAC=1600, ADC Residuals vs Hold
10000
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
-35000
0
50
100
150
200
250

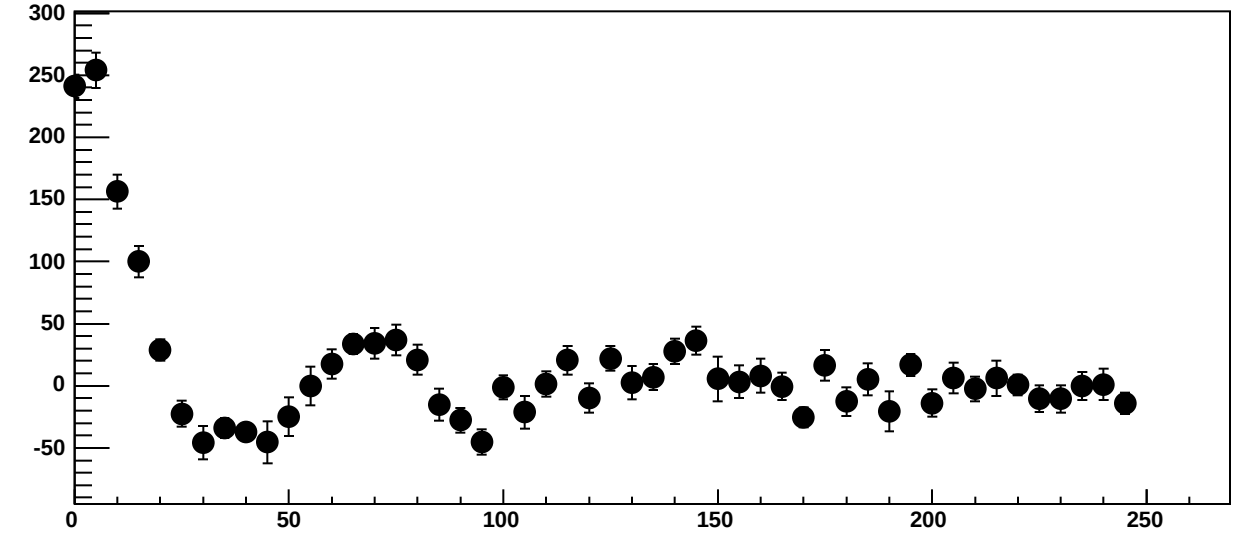
Chip 1, Channel 1, Enable 1, DAC=1600, ADC Mean vs Hold



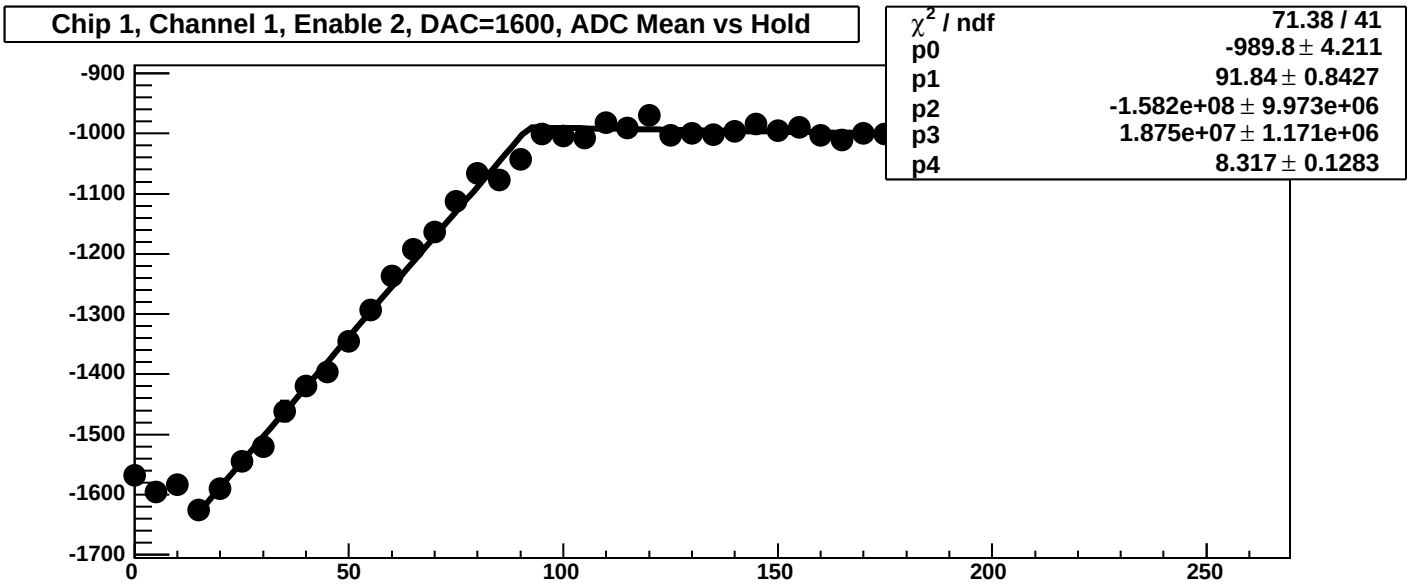
Chip 1, Channel 1, Enable 1, DAC=1600, ADC Noise vs Hold



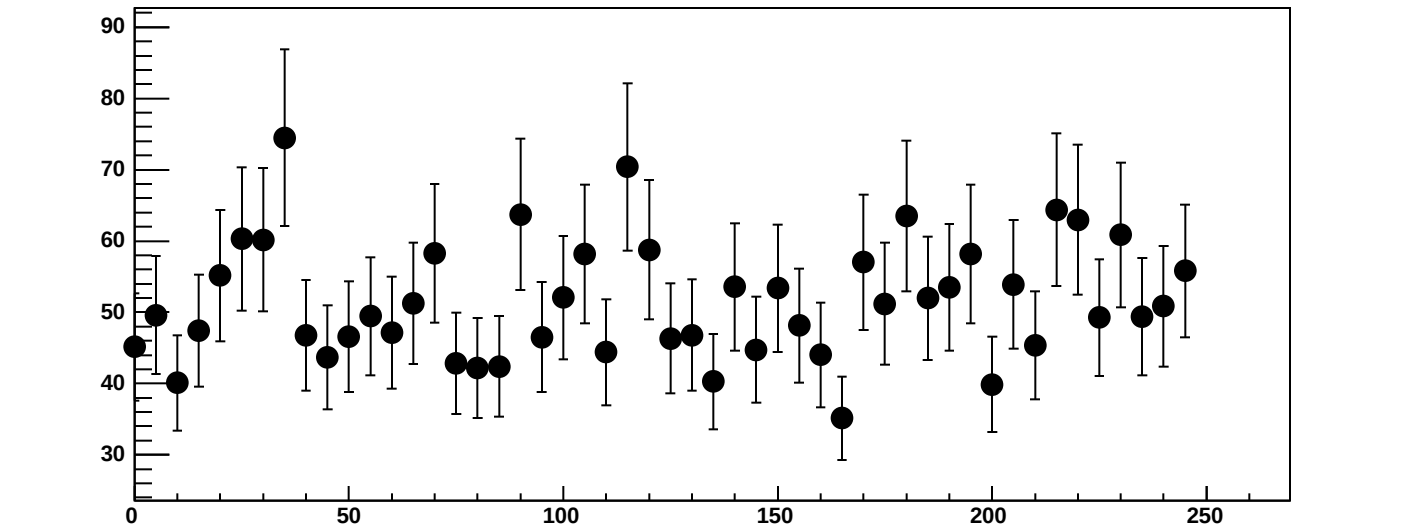
Chip 1, Channel 1, Enable 1, DAC=1600, ADC Residuals vs Hold



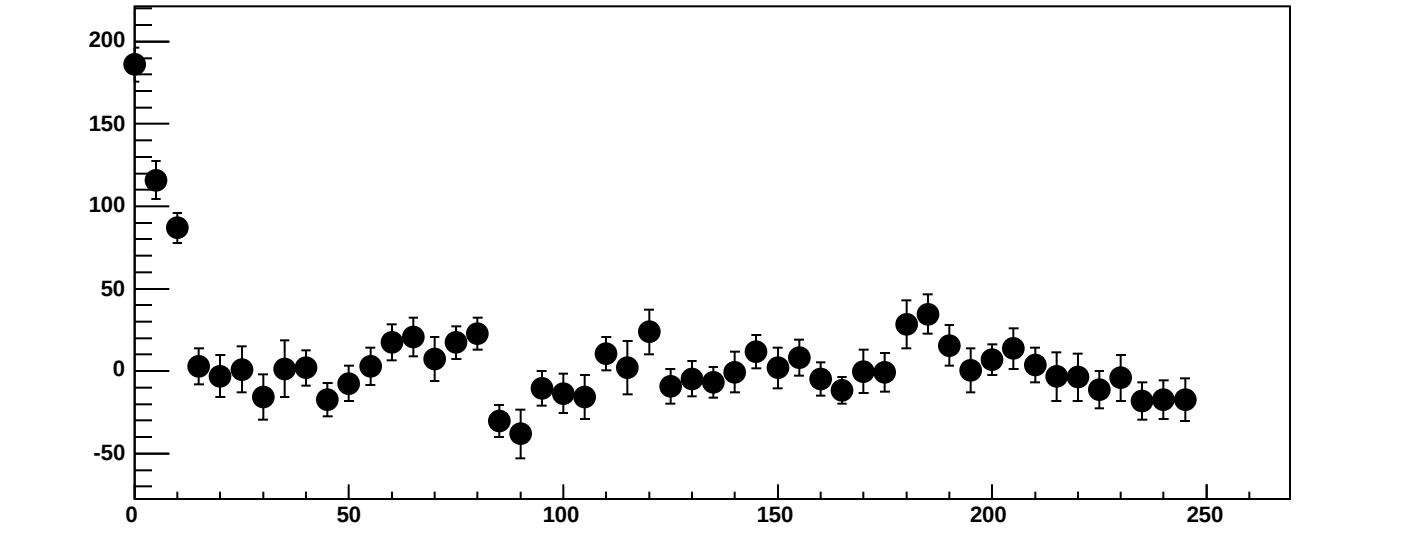
Chip 1, Channel 1, Enable 2, DAC=1600, ADC Mean vs Hold



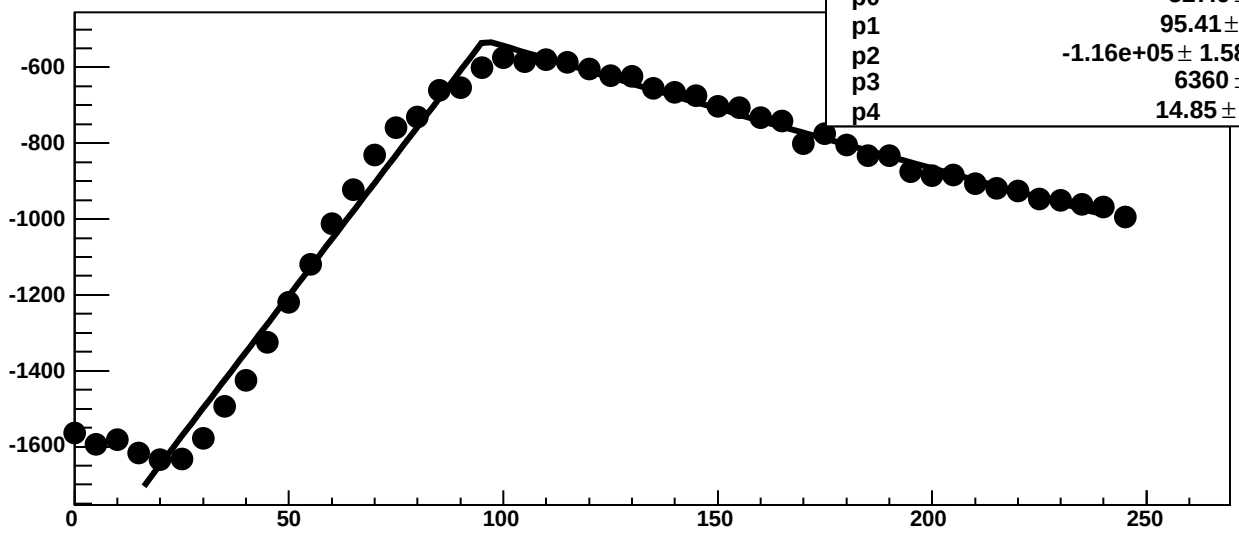
Chip 1, Channel 1, Enable 2, DAC=1600, ADC Noise vs Hold



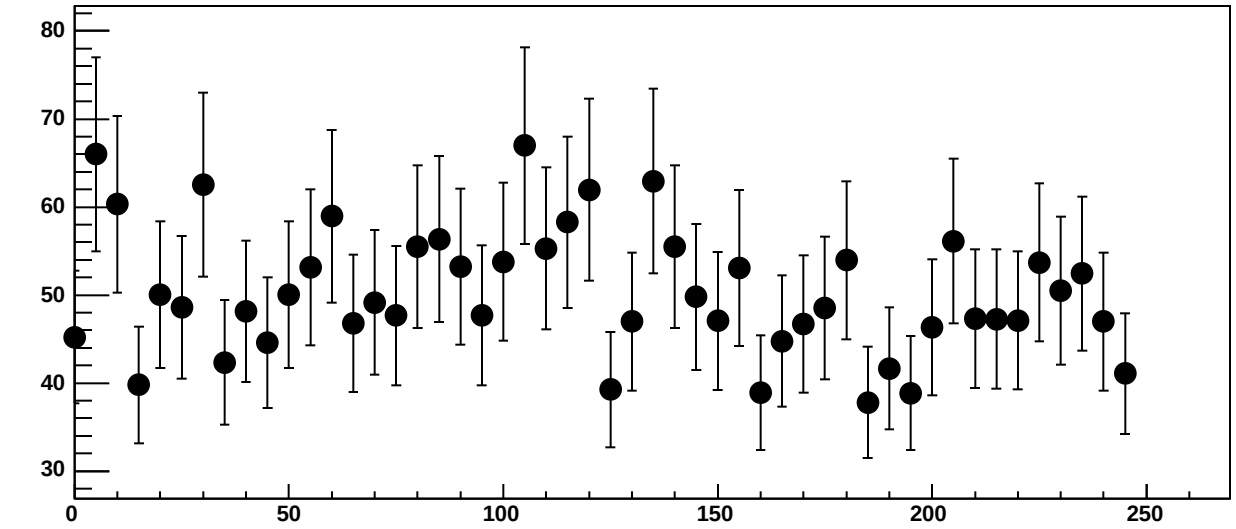
Chip 1, Channel 1, Enable 2, DAC=1600, ADC Residuals vs Hold



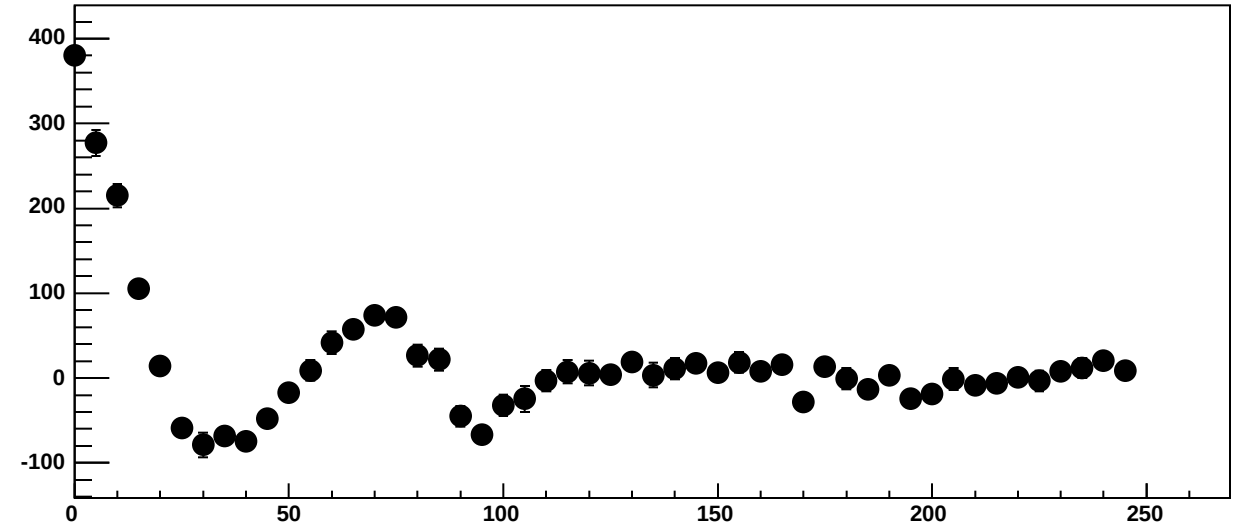
Chip 1, Channel 1, Enable 3, DAC=1600, ADC Mean vs Hold



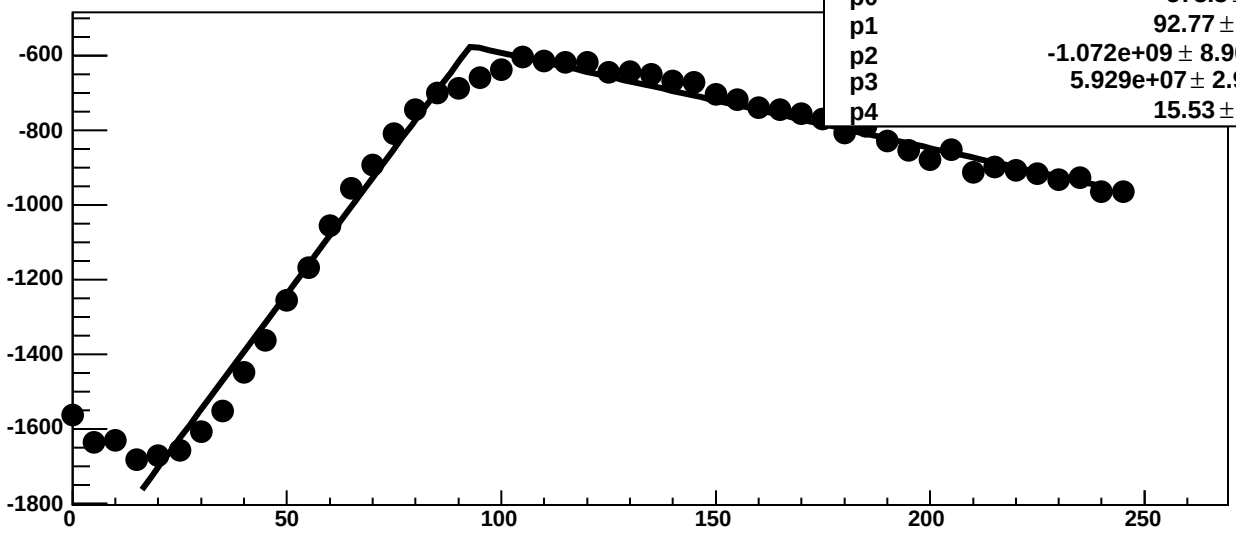
Chip 1, Channel 1, Enable 3, DAC=1600, ADC Noise vs Hold



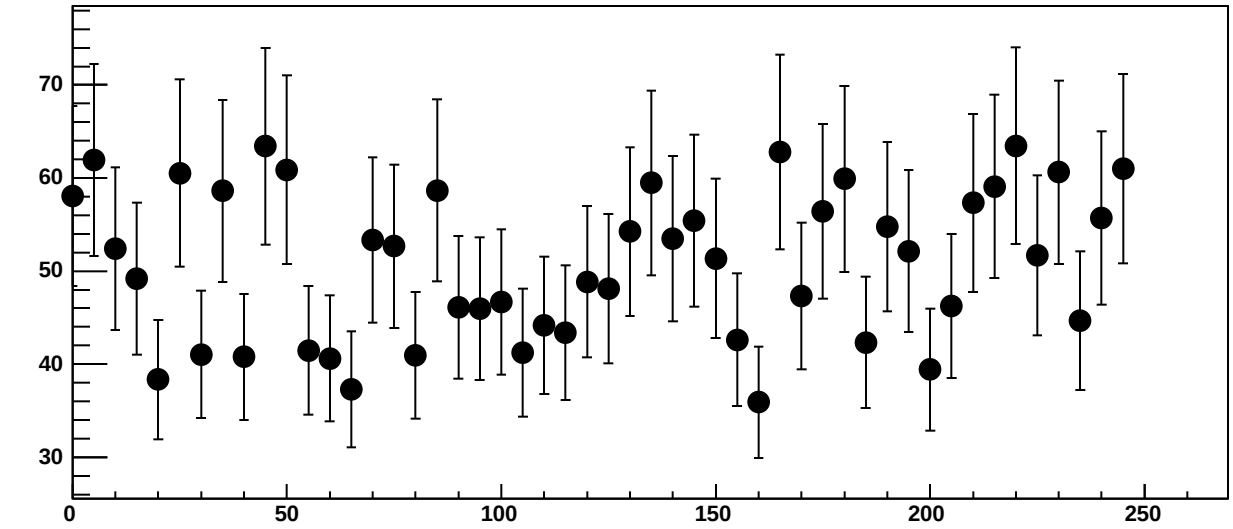
Chip 1, Channel 1, Enable 3, DAC=1600, ADC Residuals vs Hold



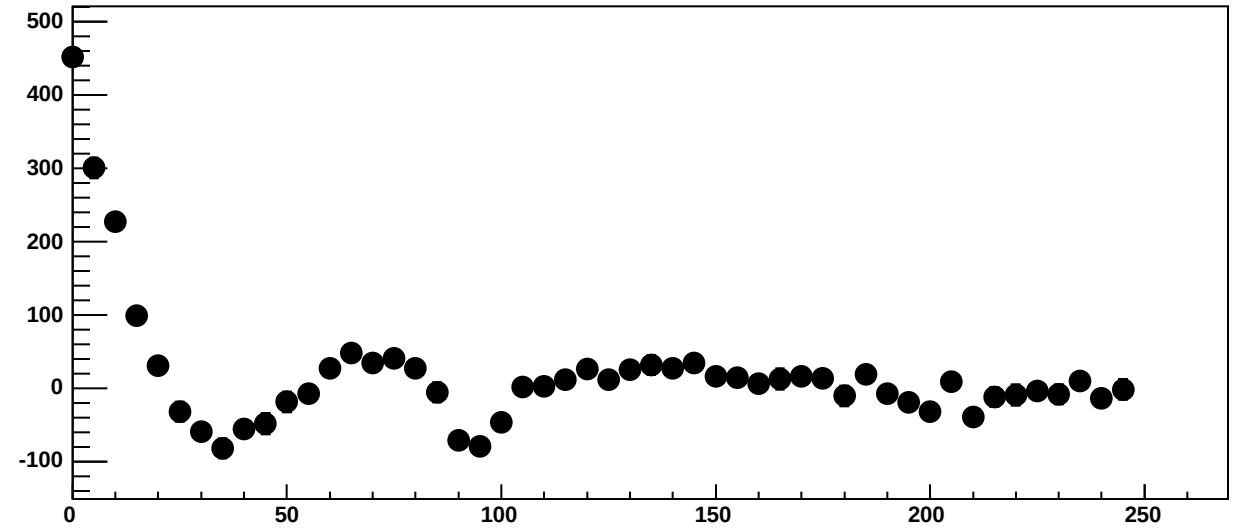
Chip 1, Channel 1, Enable 4, DAC=1600, ADC Mean vs Hold



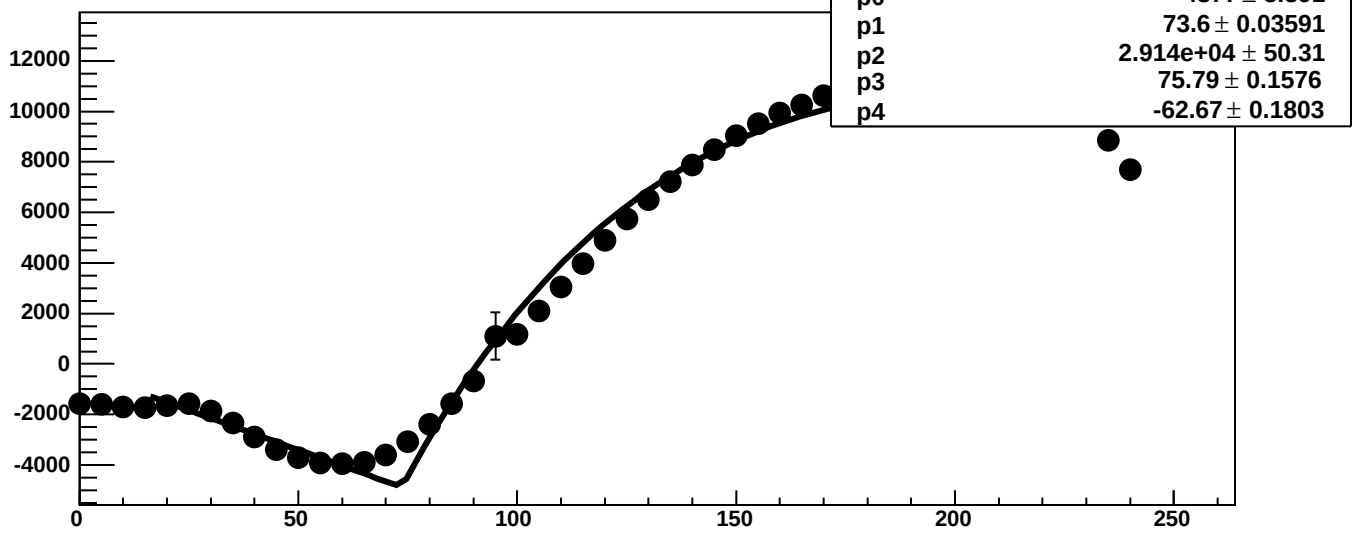
Chip 1, Channel 1, Enable 4, DAC=1600, ADC Noise vs Hold



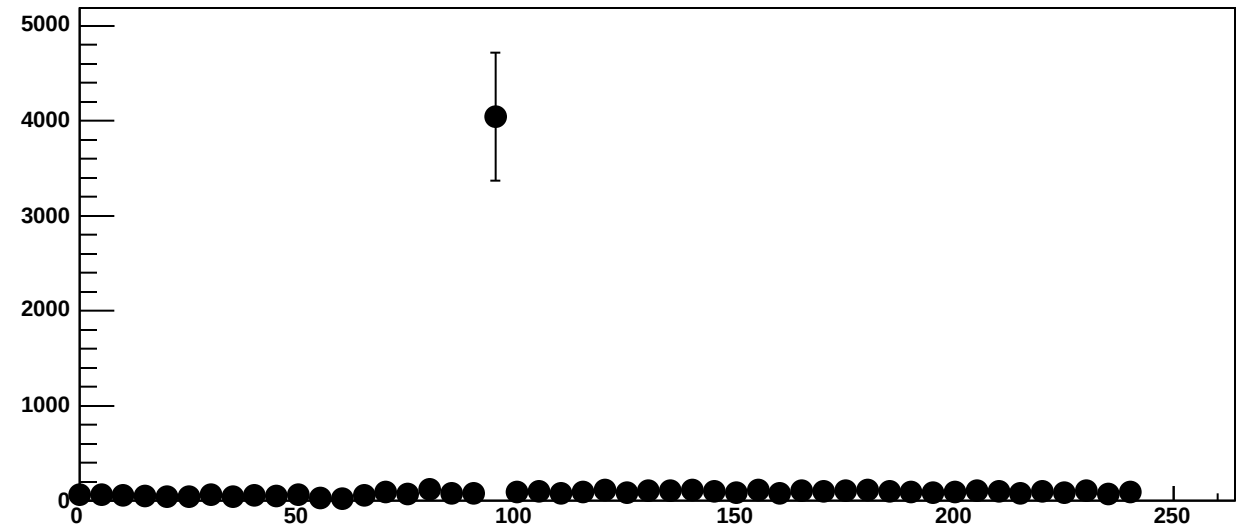
Chip 1, Channel 1, Enable 4, DAC=1600, ADC Residuals vs Hold



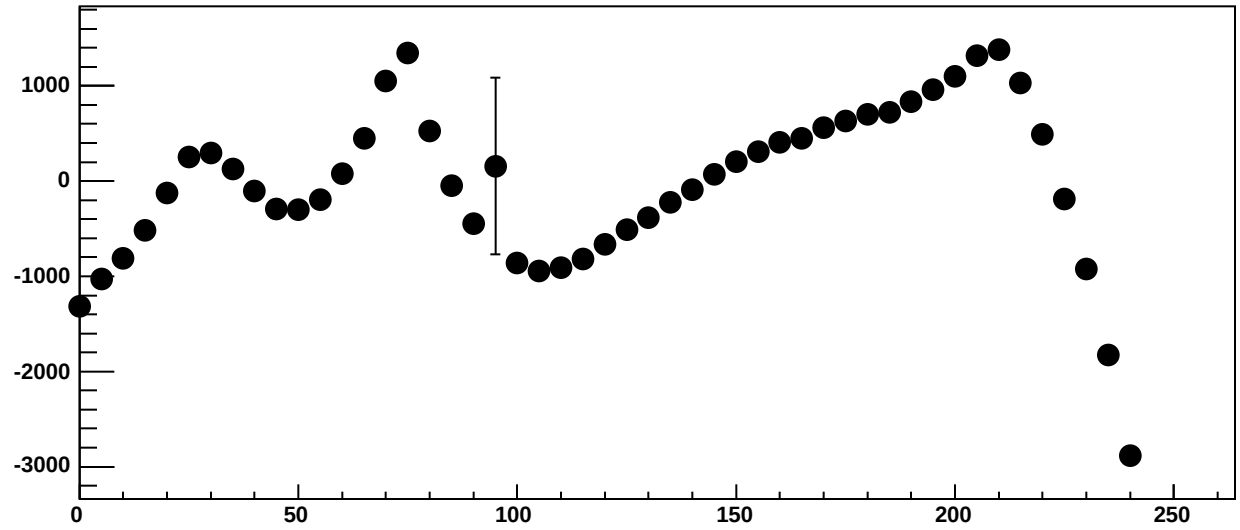
Chip 1, Channel 1, Enable 5, DAC=1600, ADC Mean vs Hold



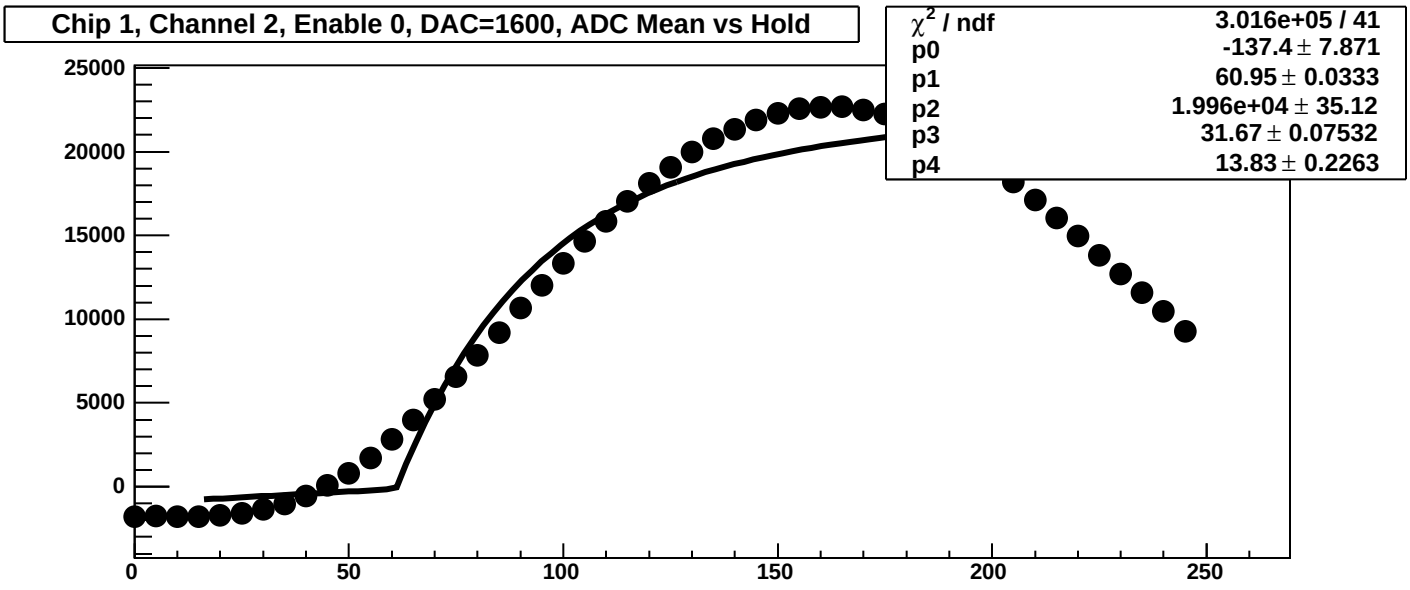
Chip 1, Channel 1, Enable 5, DAC=1600, ADC Noise vs Hold



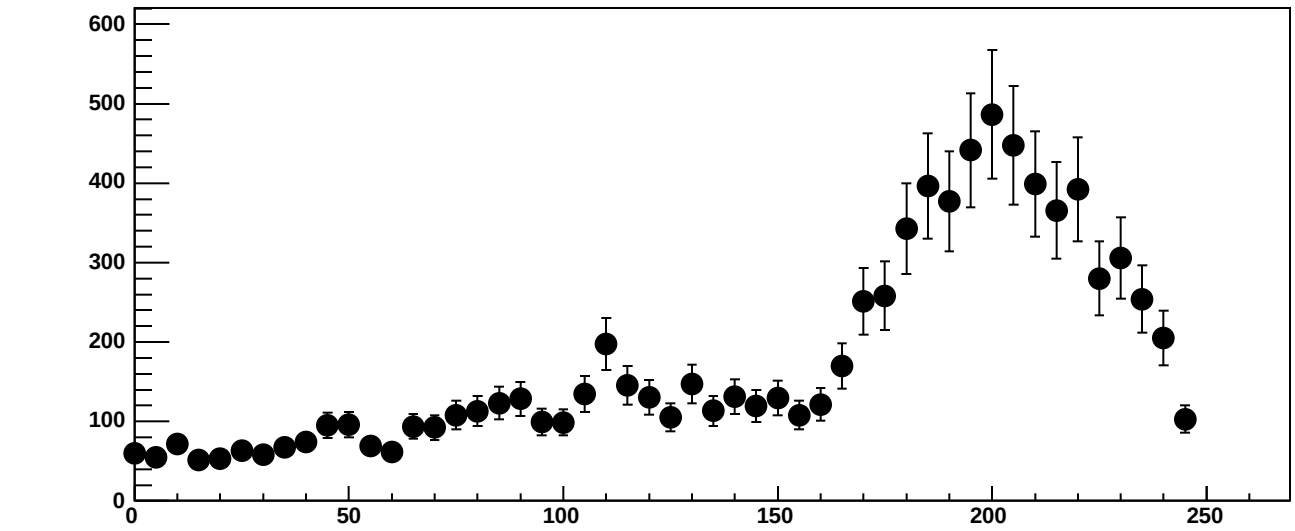
Chip 1, Channel 1, Enable 5, DAC=1600, ADC Residuals vs Hold



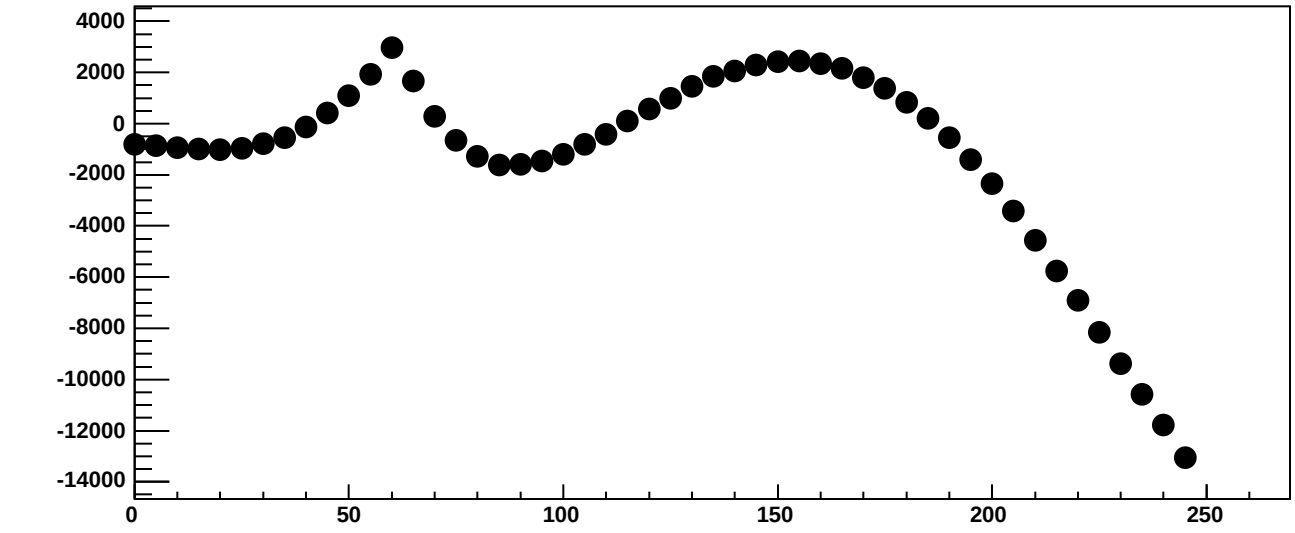
Chip 1, Channel 2, Enable 0, DAC=1600, ADC Mean vs Hold

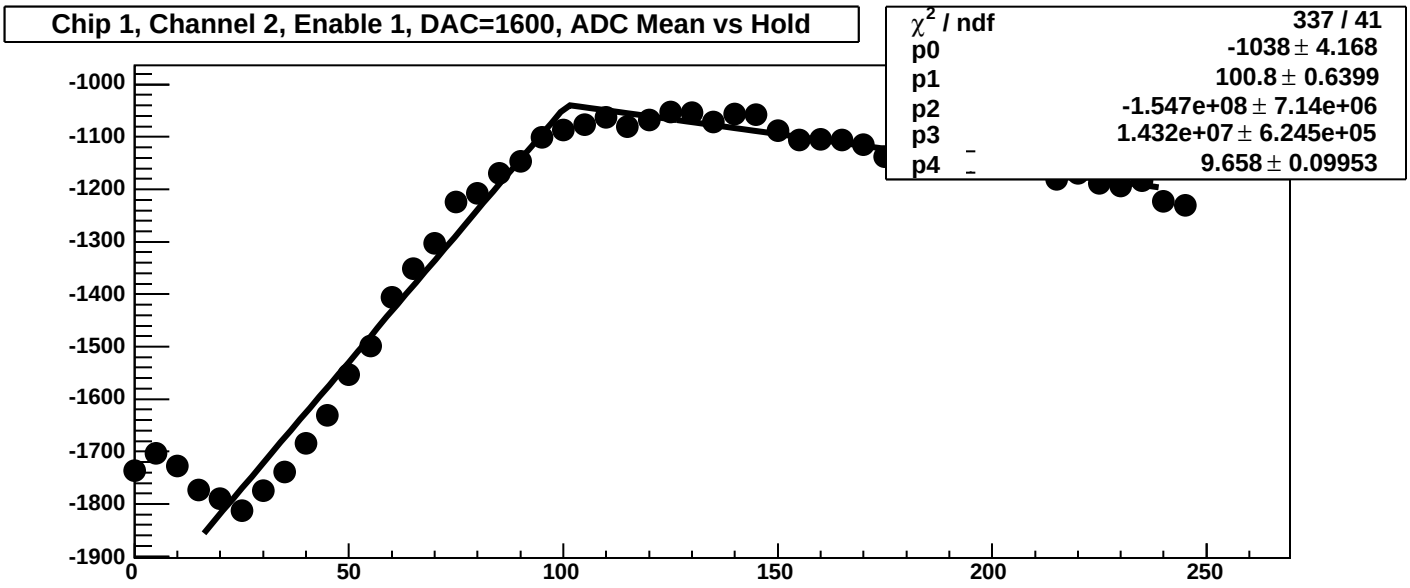


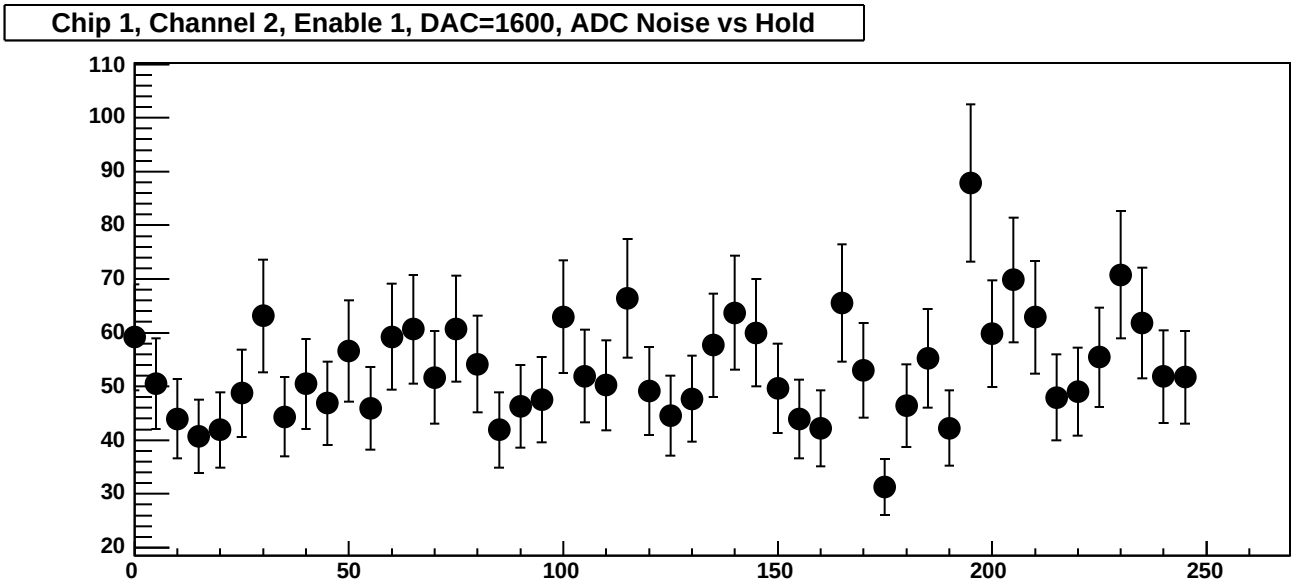
Chip 1, Channel 2, Enable 0, DAC=1600, ADC Noise vs Hold

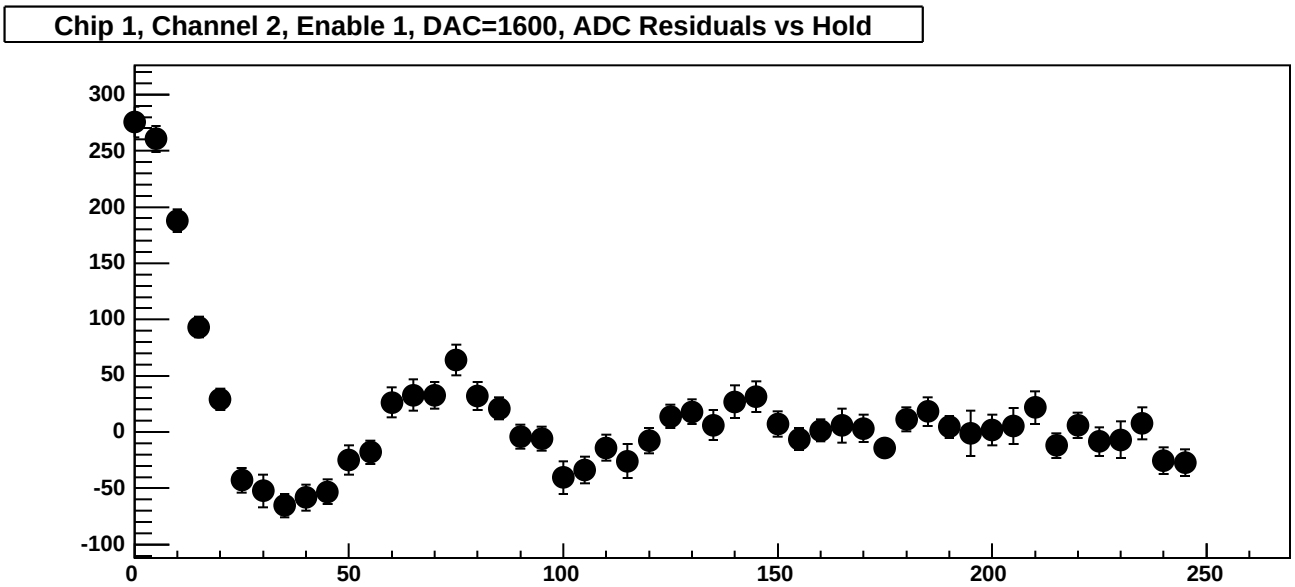


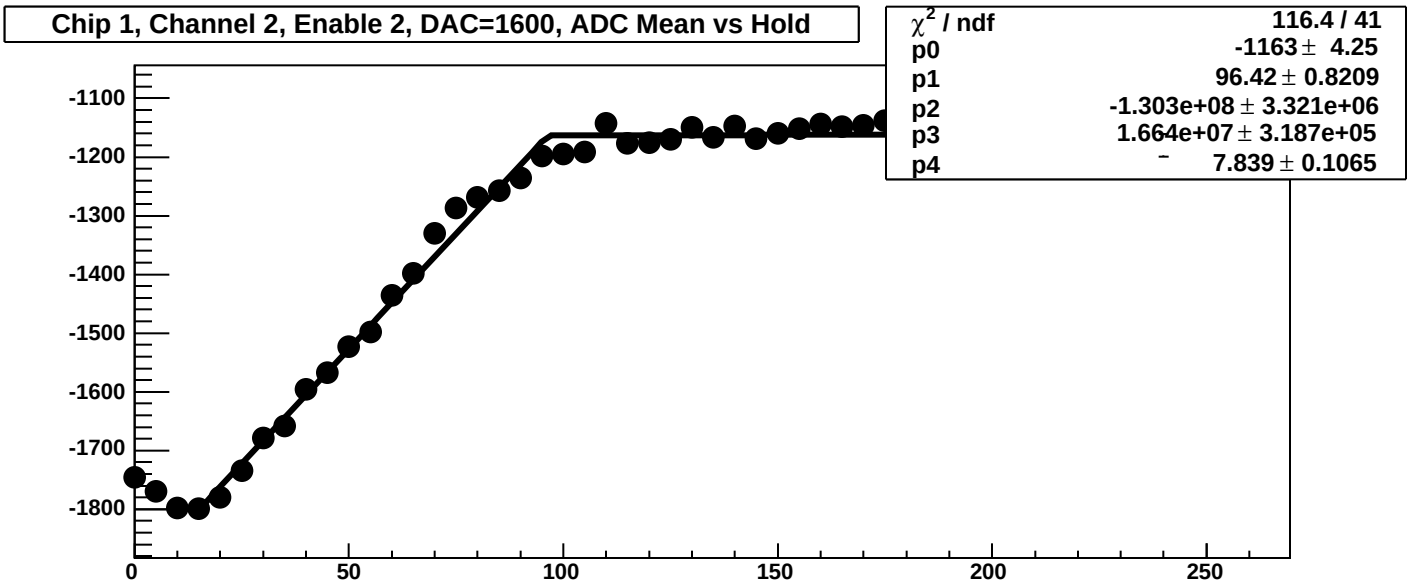
Chip 1, Channel 2, Enable 0, DAC=1600, ADC Residuals vs Hold

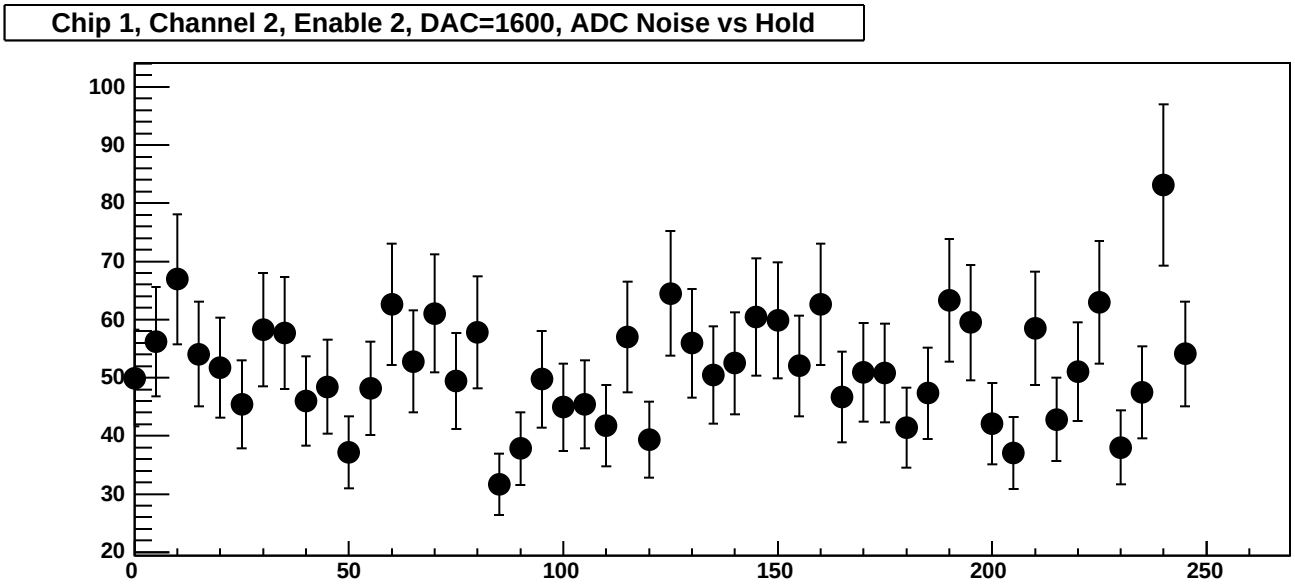


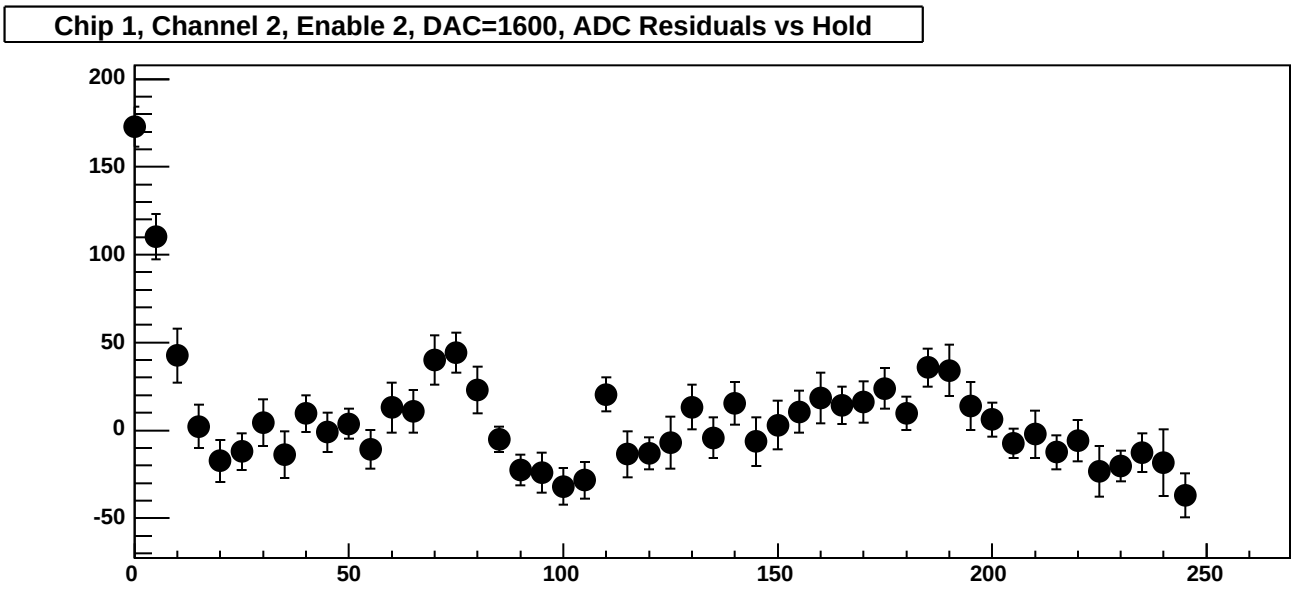

Chip 1, Channel 2, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 337 / 41
p0 -1038 ± 4.168
p1 100.8 ± 0.6399
p2 -1.547e+08 ± 7.14e+06
p3 1.432e+07 ± 6.245e+05
p4 9.658 ± 0.09953
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
-1800
-1900
0
50
100
150
200
250

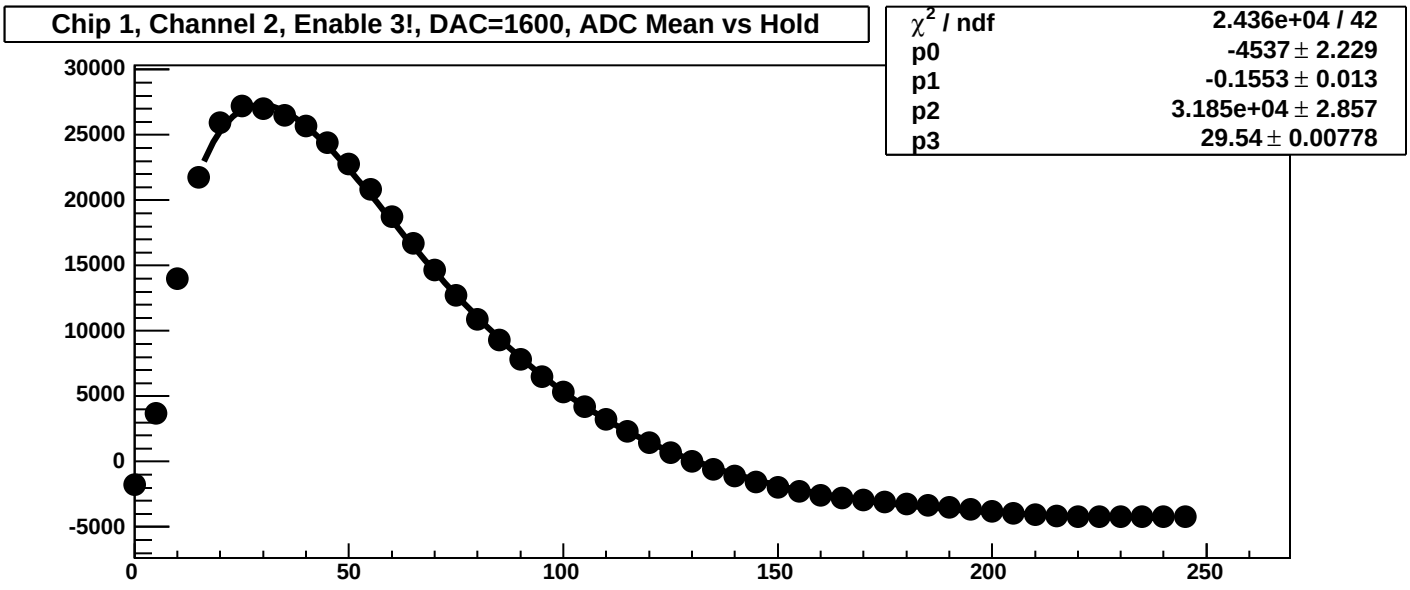

Chip 1, Channel 2, Enable 1, DAC=1600, ADC Noise vs Hold
110
100
90
80
70
60
50
40
30
20
0
50
100
150
200
250

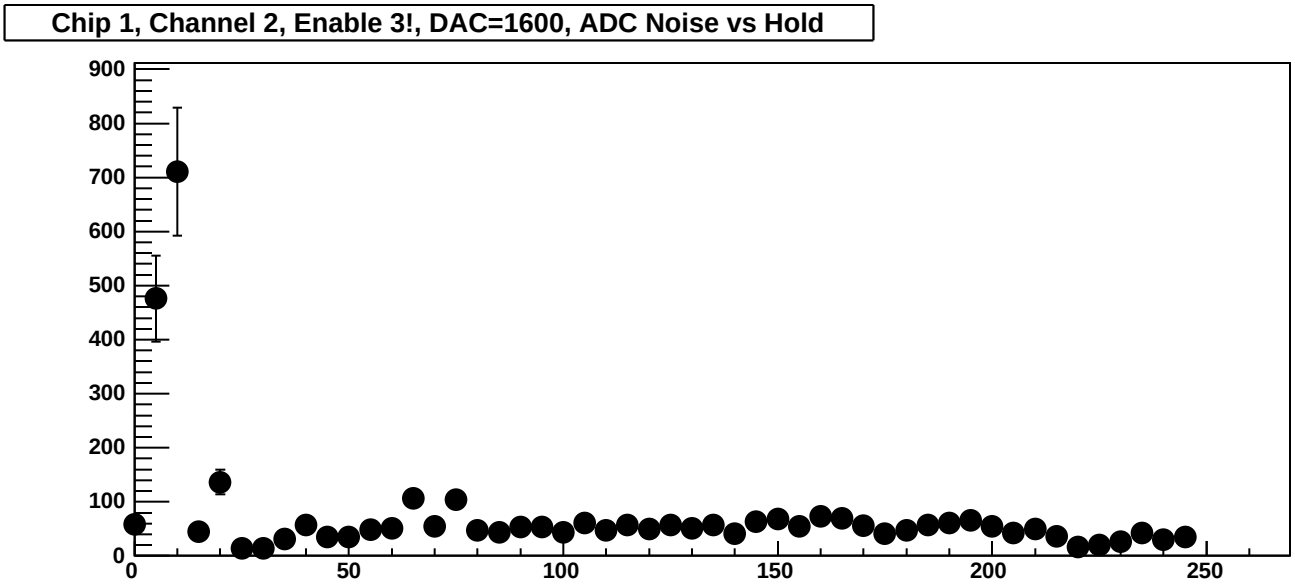

Chip 1, Channel 2, Enable 1, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

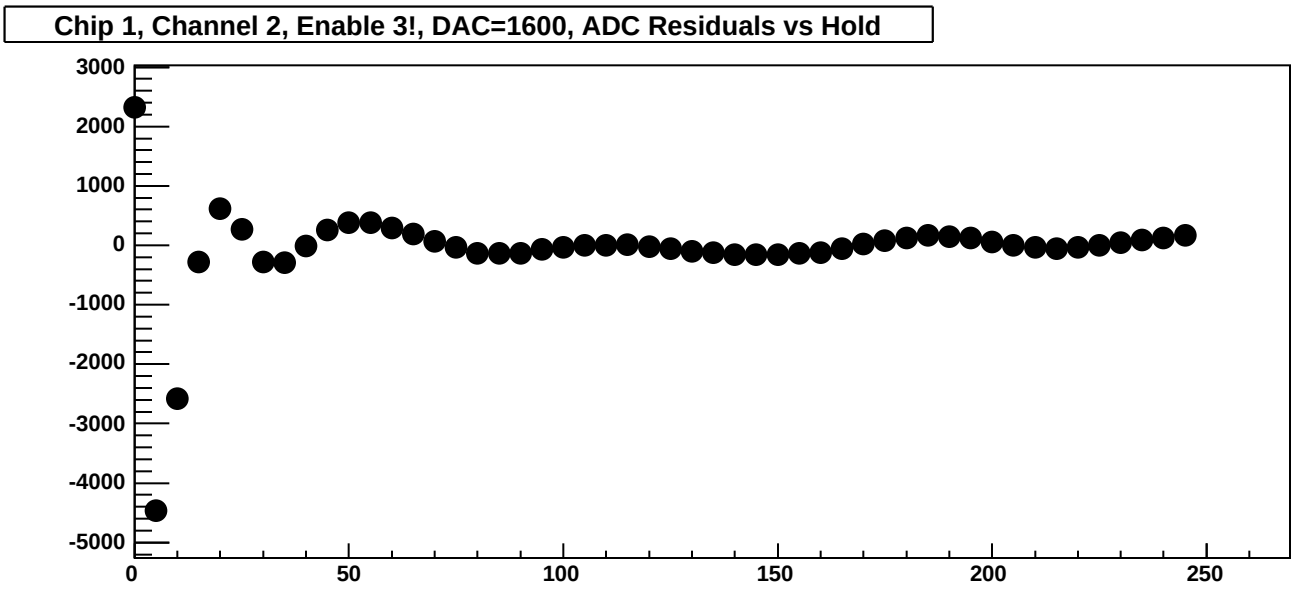

Chip 1, Channel 2, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 116.4 / 41
p0 -1163 ± 4.25
p1 96.42 ± 0.8209
p2 -1.303e+08 ± 3.321e+06
p3 1.664e+07 ± 3.187e+05
p4 7.839 ± 0.1065
-1100
-1200
-1300
-1400
-1500
-1600
-1700
-1800
0
50
100
150
200
250


Chip 1, Channel 2, Enable 2, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
20
0
50
100
150
200
250

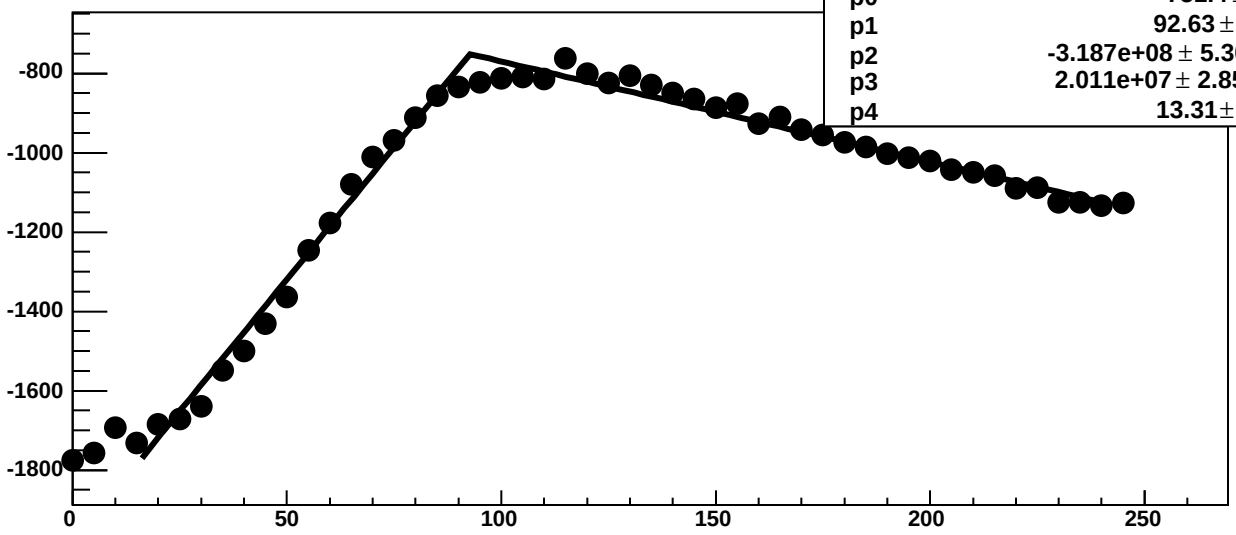

Chip 1, Channel 2, Enable 2, DAC=1600, ADC Residuals vs Hold
200
150
100
50
0
-50
0
50
100
150
200
250


Chip 1, Channel 2, Enable 3!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.436e+04 / 42
p0 -4537 ± 2.229
p1 -0.1553 ± 0.013
p2 3.185e+04 ± 2.857
p3 29.54 ± 0.00778
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

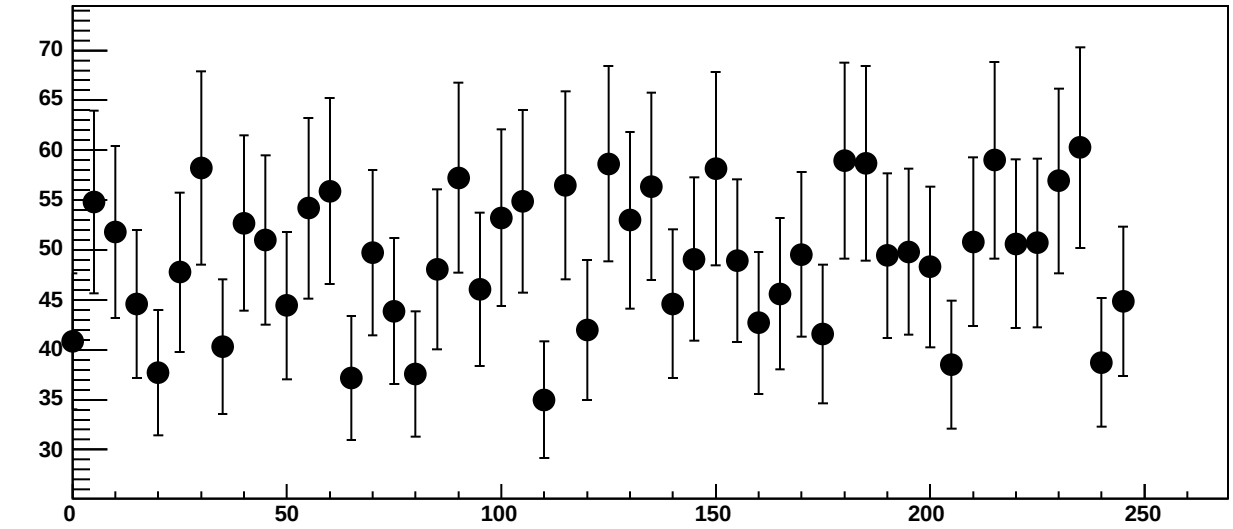

Chip 1, Channel 2, Enable 3!, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 1, Channel 2, Enable 3!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

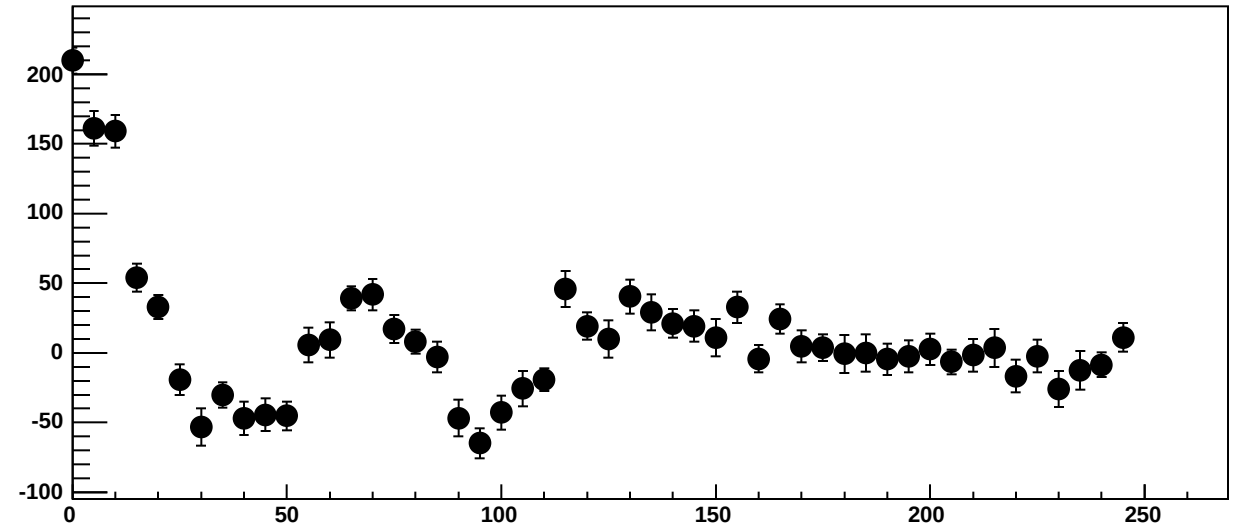
Chip 1, Channel 2, Enable 4, DAC=1600, ADC Mean vs Hold

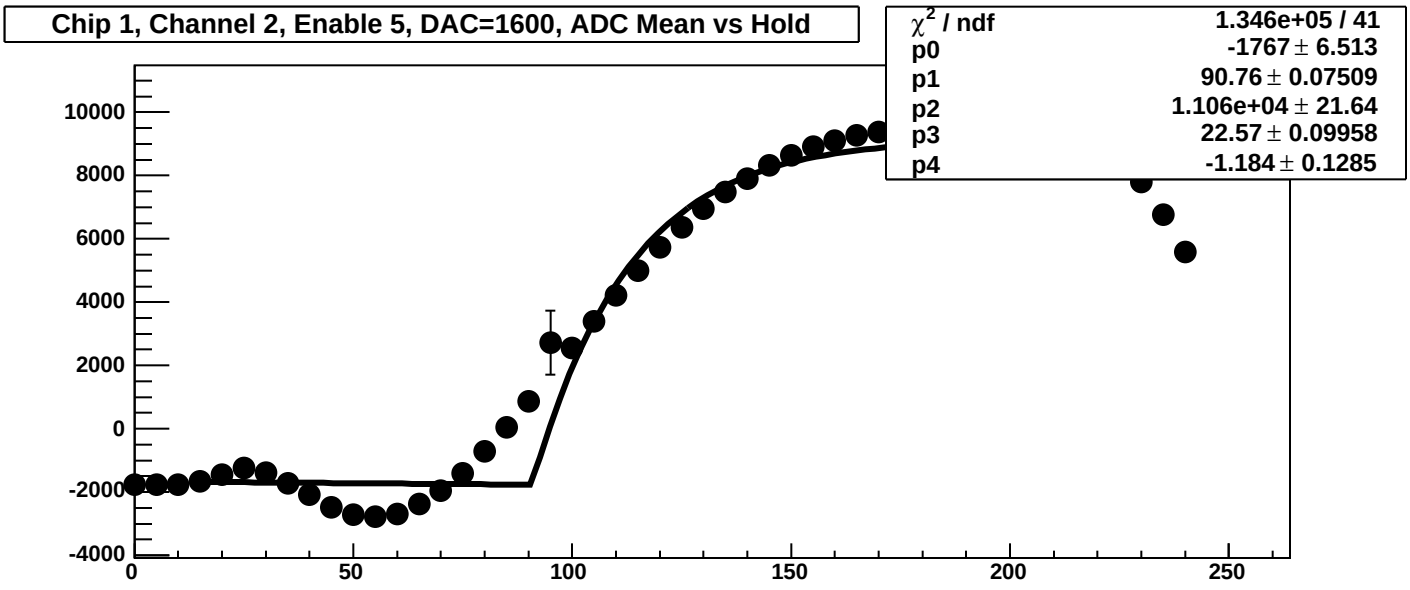


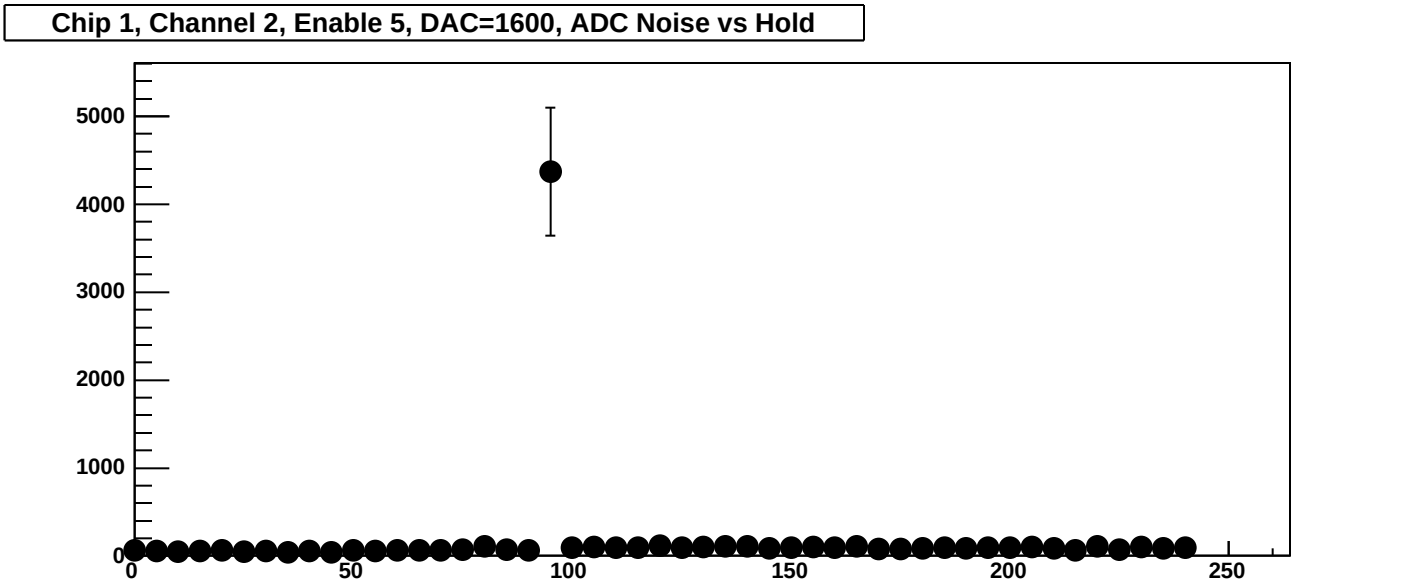
Chip 1, Channel 2, Enable 4, DAC=1600, ADC Noise vs Hold

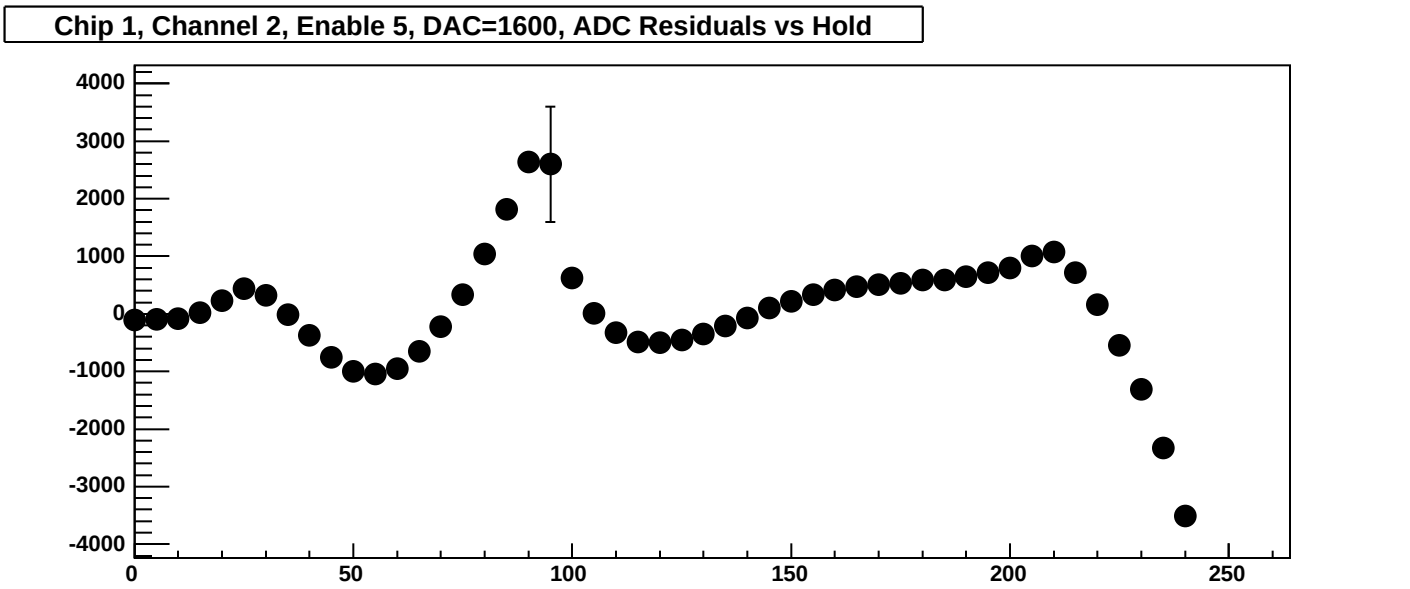


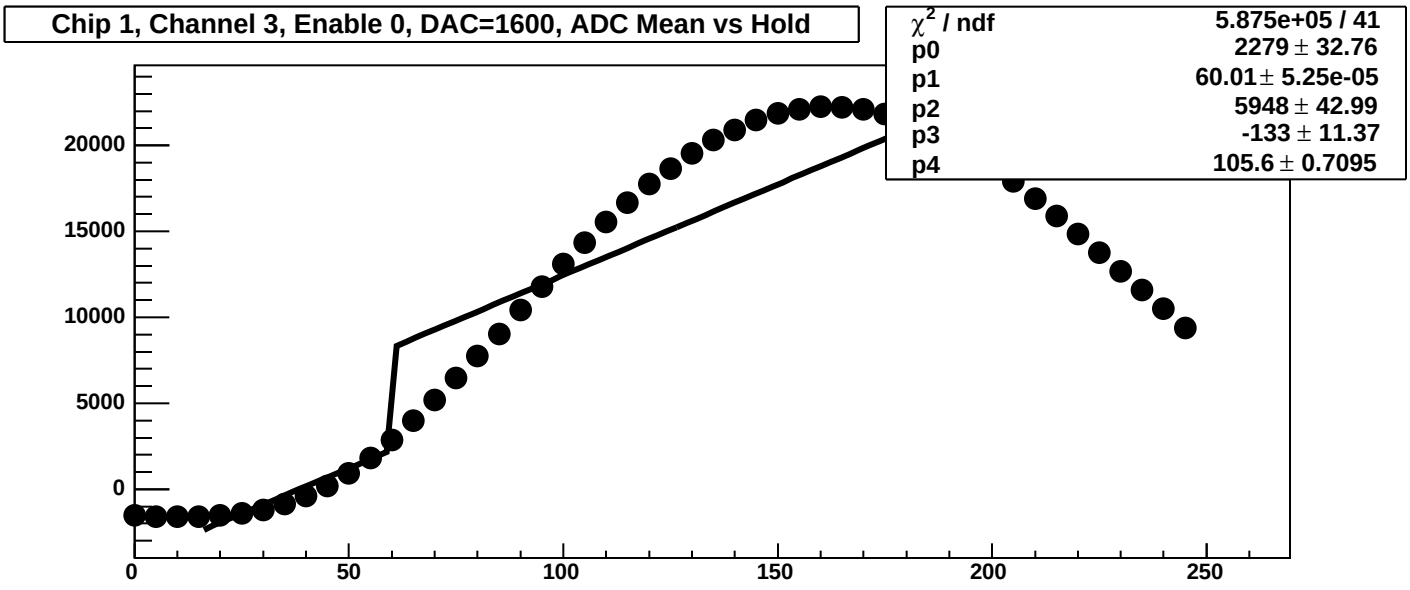
Chip 1, Channel 2, Enable 4, DAC=1600, ADC Residuals vs Hold

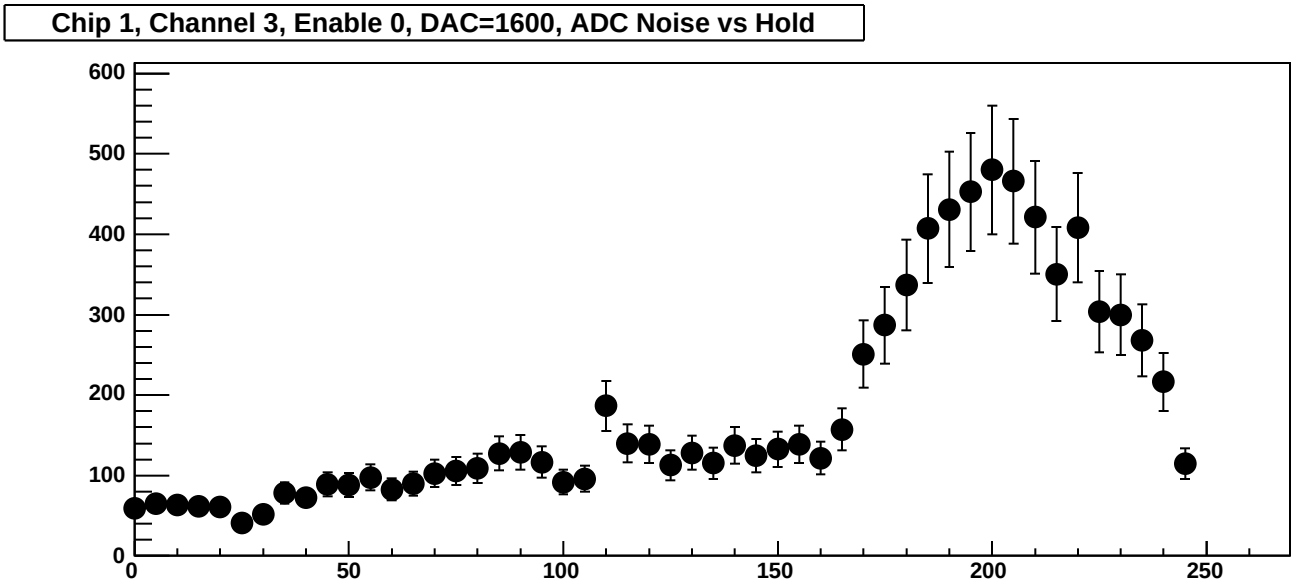


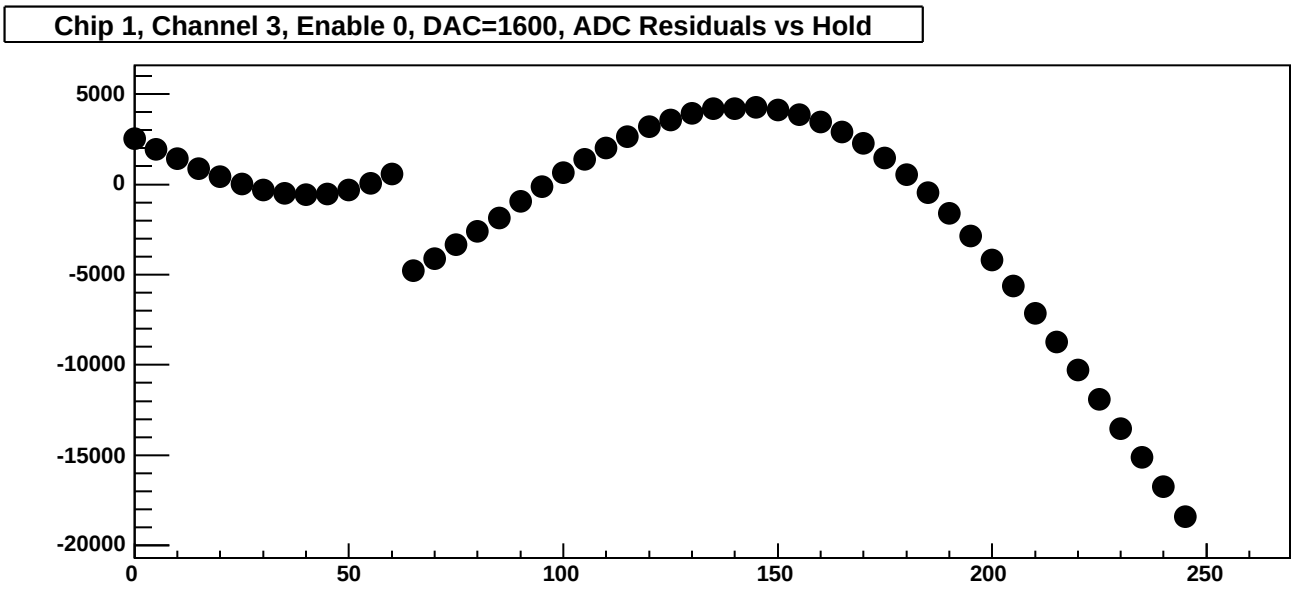

Chip 1, Channel 2, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.346e+05 / 41
p0 -1767 ± 6.513
p1 90.76 ± 0.07509
p2 1.106e+04 ± 21.64
p3 22.57 ± 0.09958
p4 -1.184 ± 0.1285
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250


Chip 1, Channel 2, Enable 5, DAC=1600, ADC Noise vs Hold
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

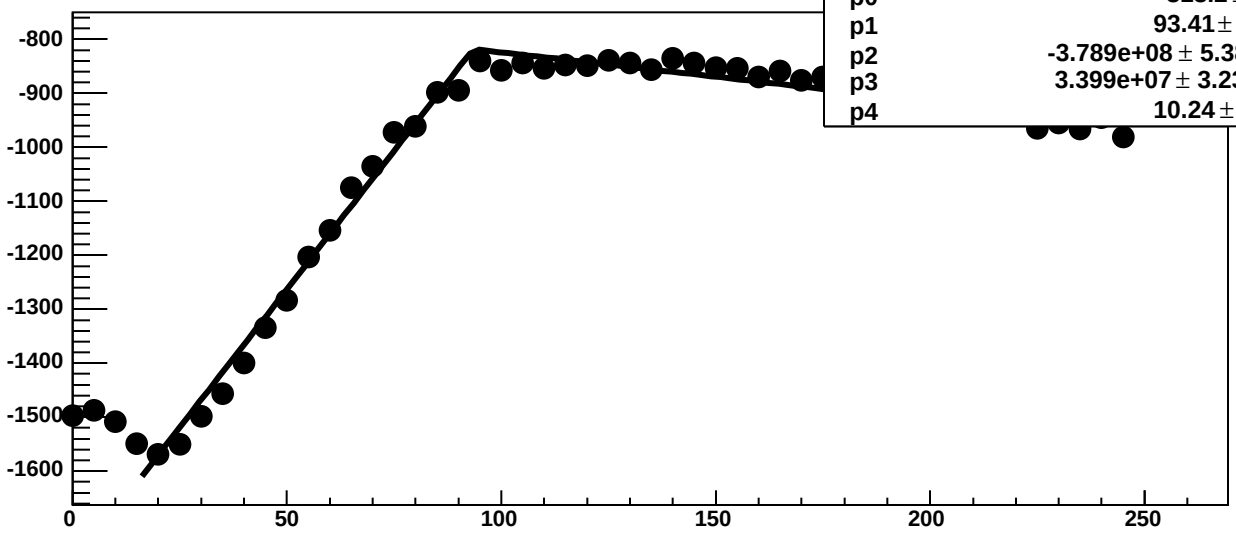

Chip 1, Channel 2, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
3000
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250


Chip 1, Channel 3, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.875e+05 / 41
p0 2279 ± 32.76
p1 60.01 ± 5.25e-05
p2 5948 ± 42.99
p3 -133 ± 11.37
p4 105.6 ± 0.7095
20000
15000
10000
5000
0
0
50
100
150
200
250

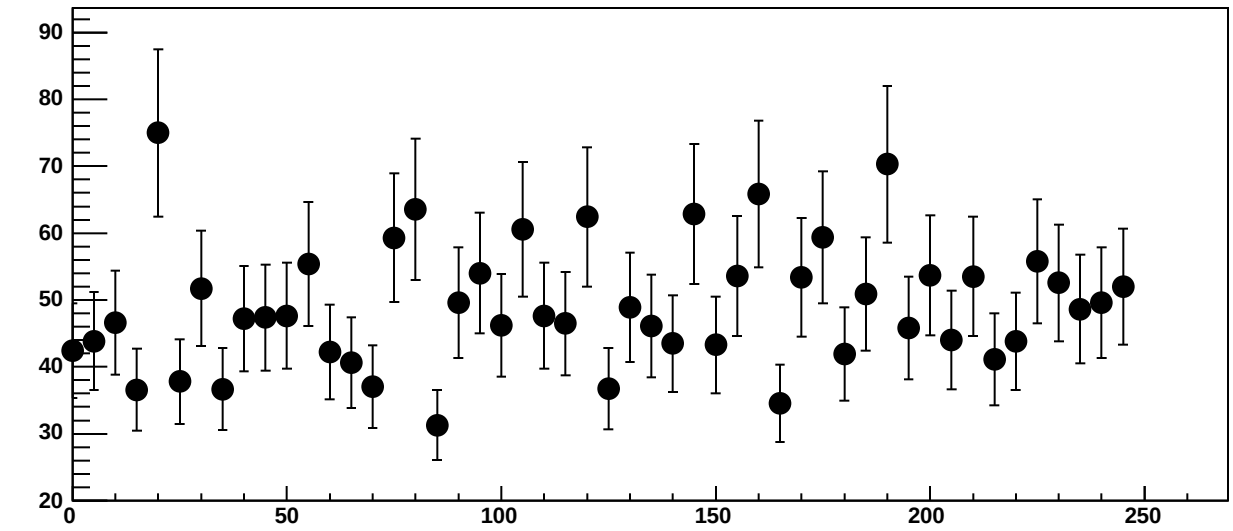

Chip 1, Channel 3, Enable 0, DAC=1600, ADC Noise vs Hold
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 1, Channel 3, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

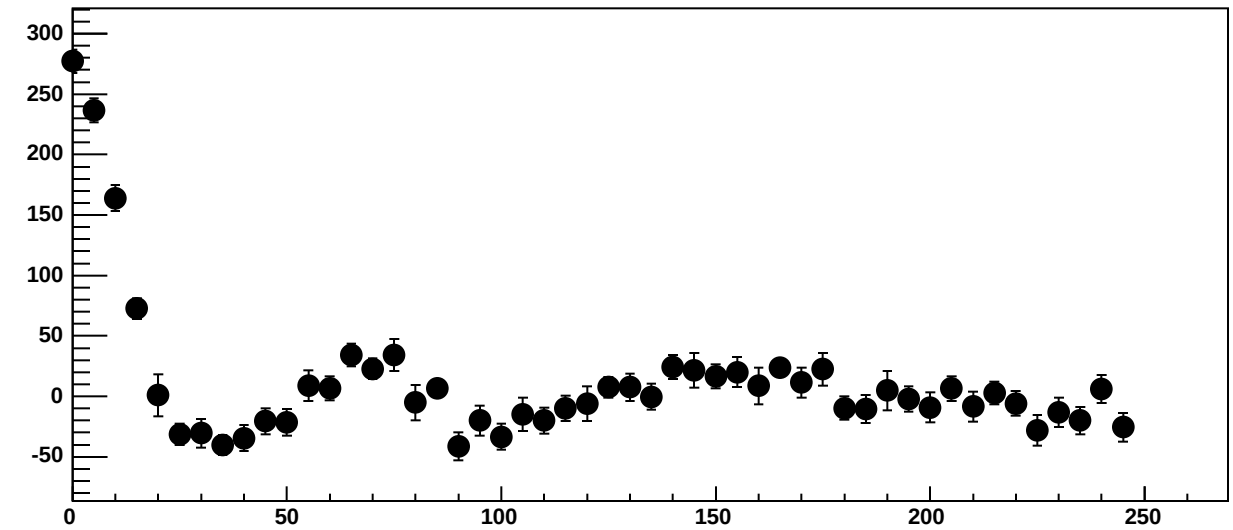
Chip 1, Channel 3, Enable 1, DAC=1600, ADC Mean vs Hold



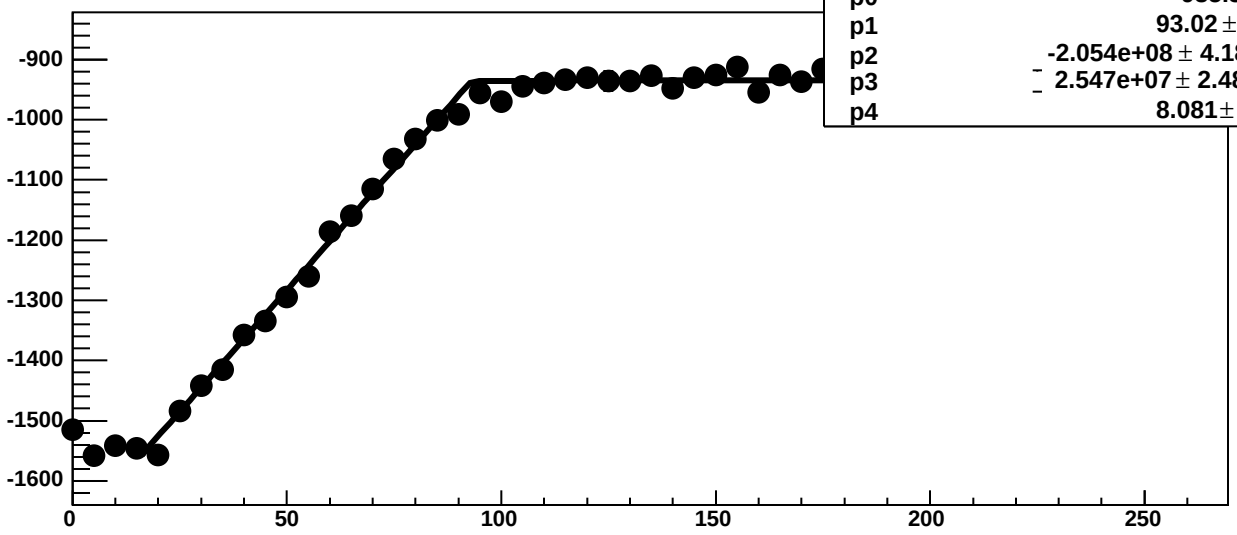
Chip 1, Channel 3, Enable 1, DAC=1600, ADC Noise vs Hold



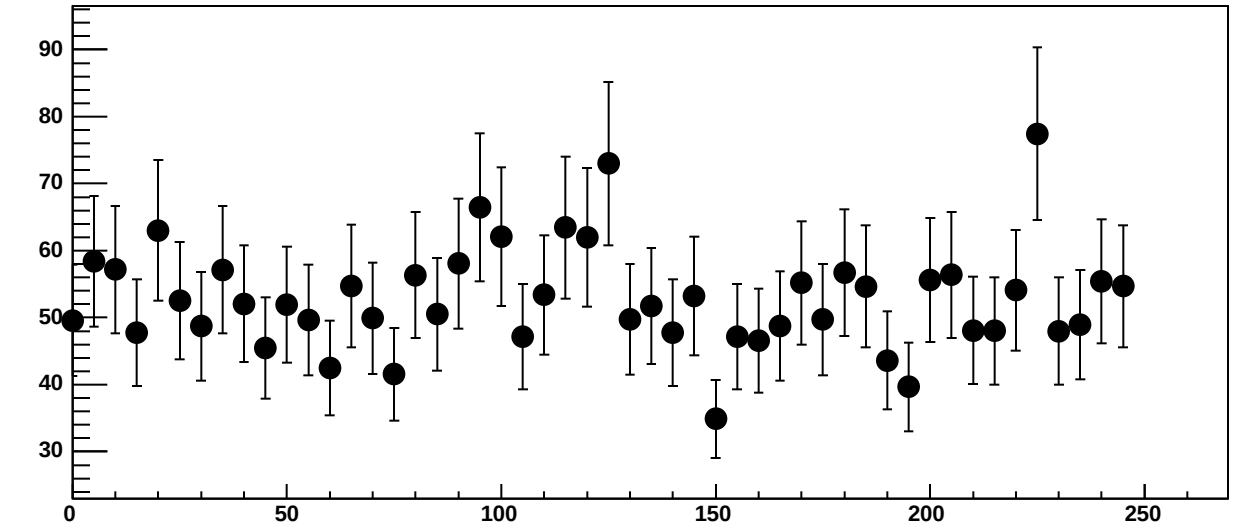
Chip 1, Channel 3, Enable 1, DAC=1600, ADC Residuals vs Hold



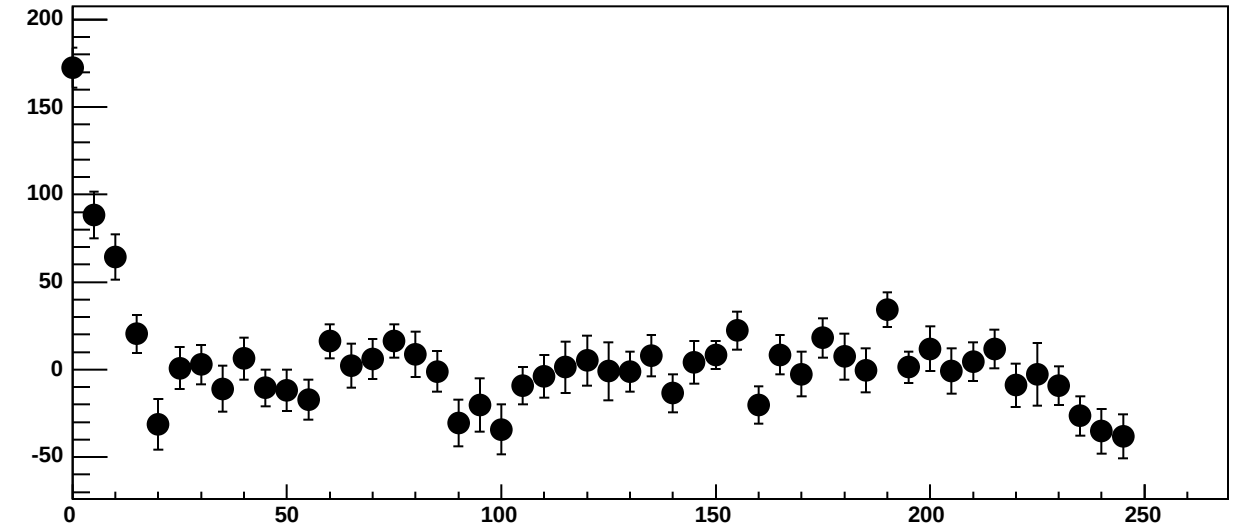
Chip 1, Channel 3, Enable 2, DAC=1600, ADC Mean vs Hold



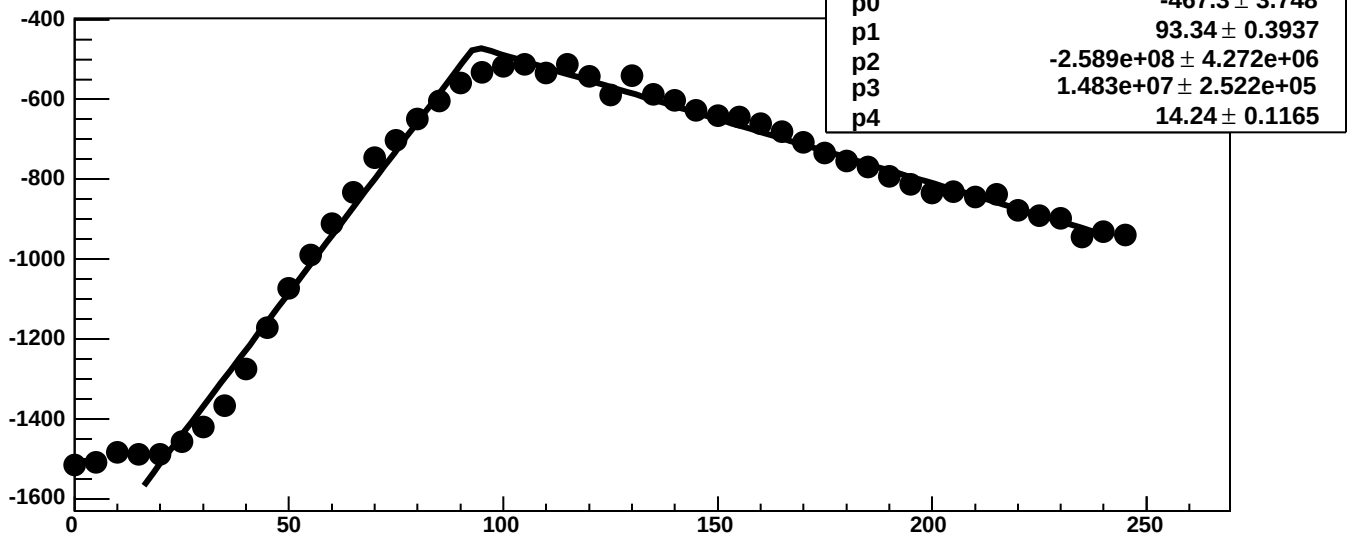
Chip 1, Channel 3, Enable 2, DAC=1600, ADC Noise vs Hold



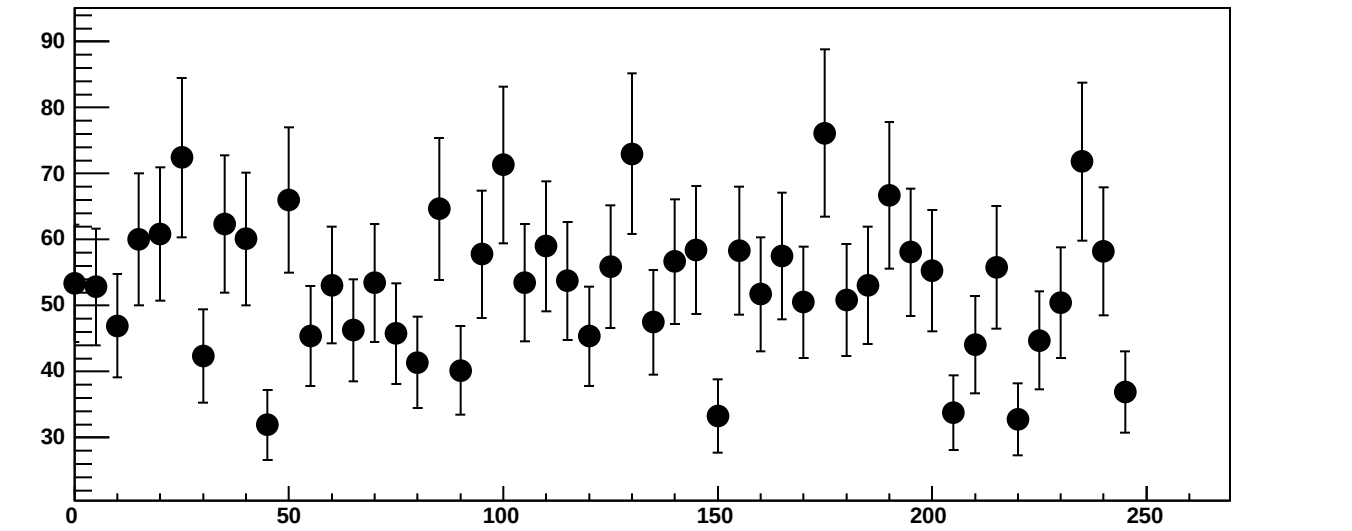
Chip 1, Channel 3, Enable 2, DAC=1600, ADC Residuals vs Hold



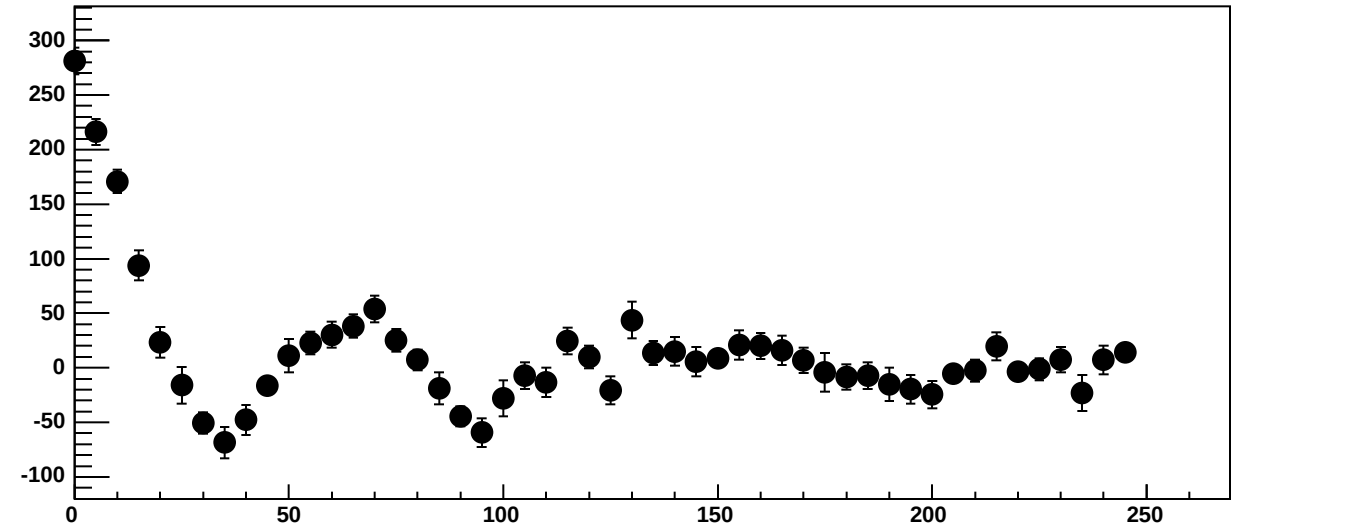
Chip 1, Channel 3, Enable 3, DAC=1600, ADC Mean vs Hold



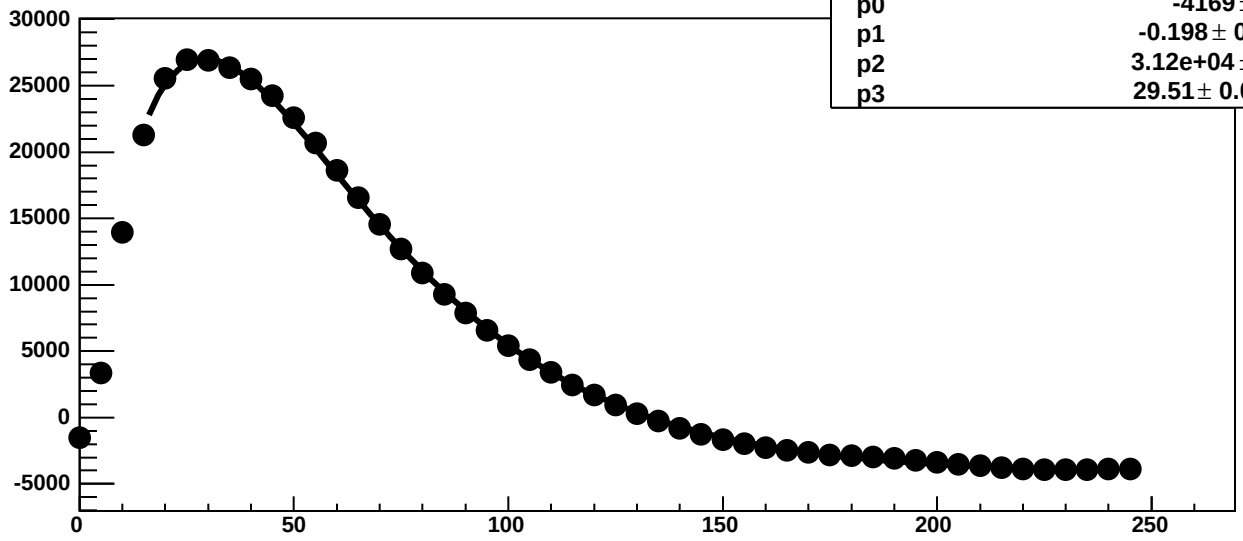
Chip 1, Channel 3, Enable 3, DAC=1600, ADC Noise vs Hold



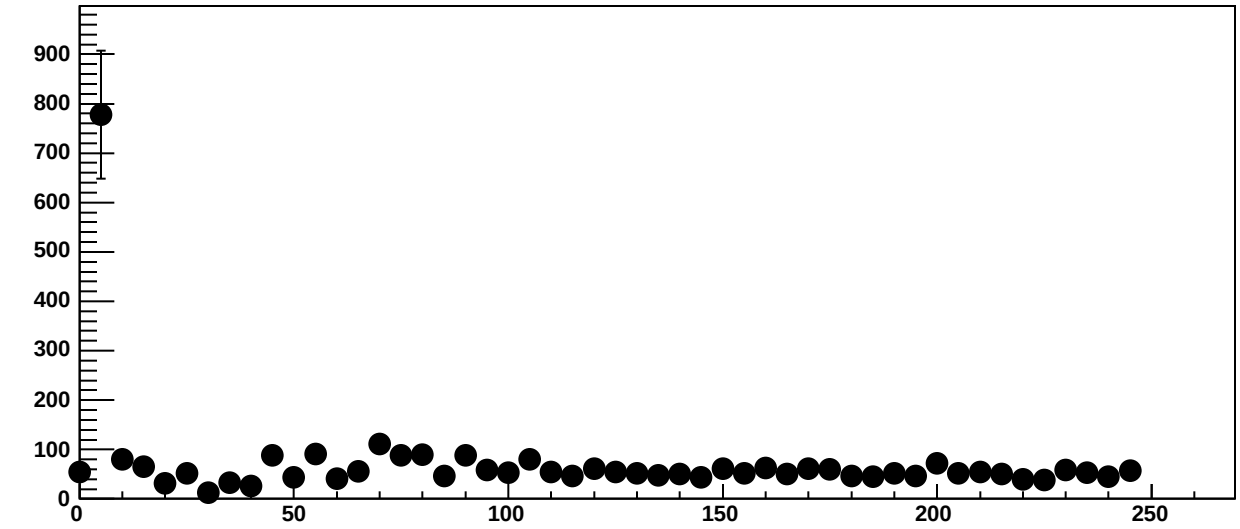
Chip 1, Channel 3, Enable 3, DAC=1600, ADC Residuals vs Hold



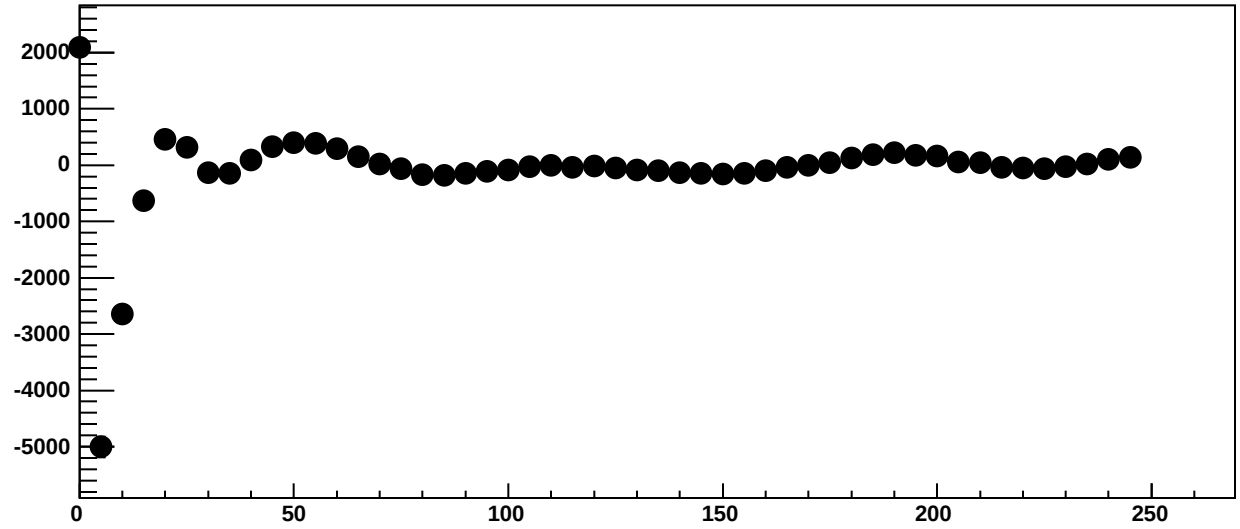
Chip 1, Channel 3, Enable 4!, DAC=1600, ADC Mean vs Hold



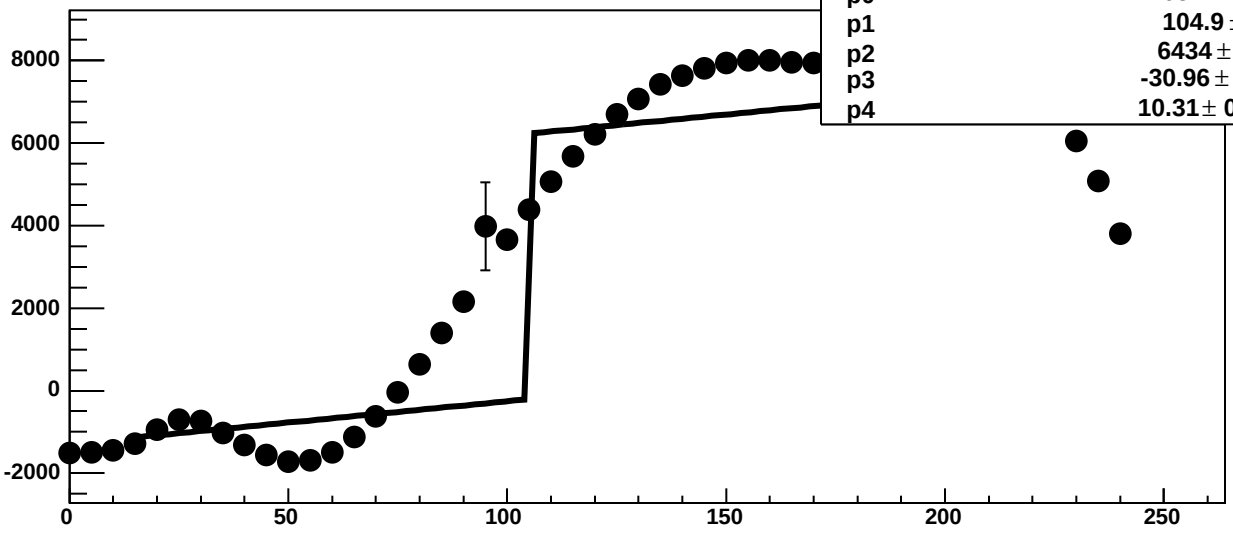
Chip 1, Channel 3, Enable 4!, DAC=1600, ADC Noise vs Hold



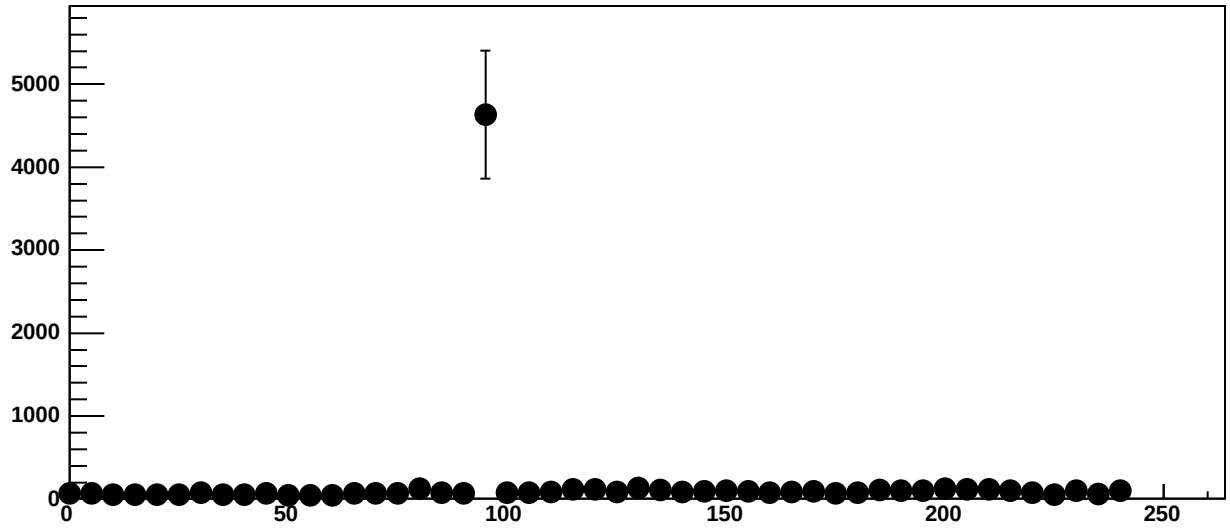
Chip 1, Channel 3, Enable 4!, DAC=1600, ADC Residuals vs Hold



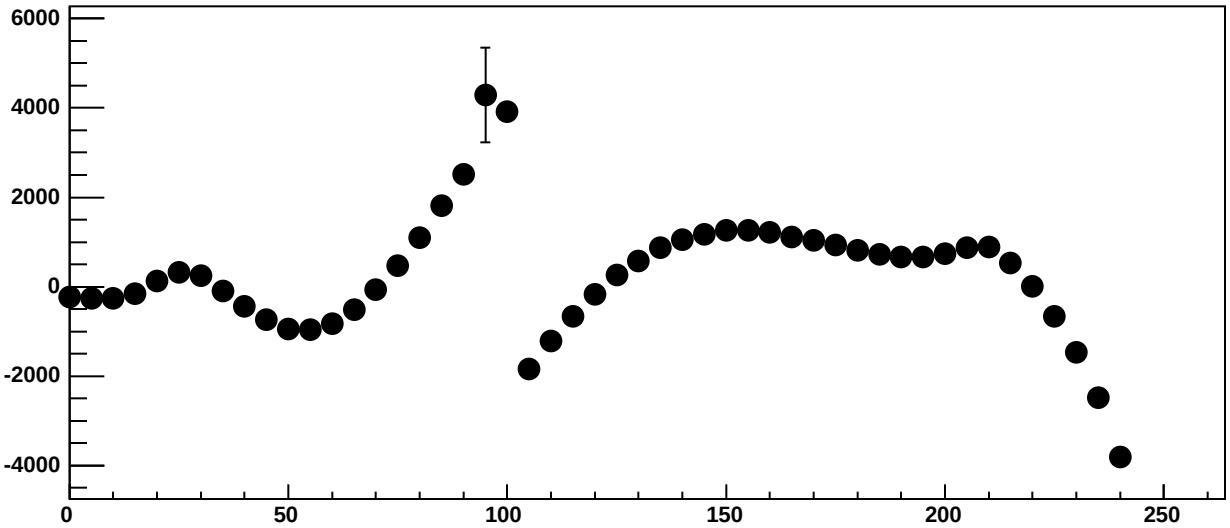
Chip 1, Channel 3, Enable 5, DAC=1600, ADC Mean vs Hold

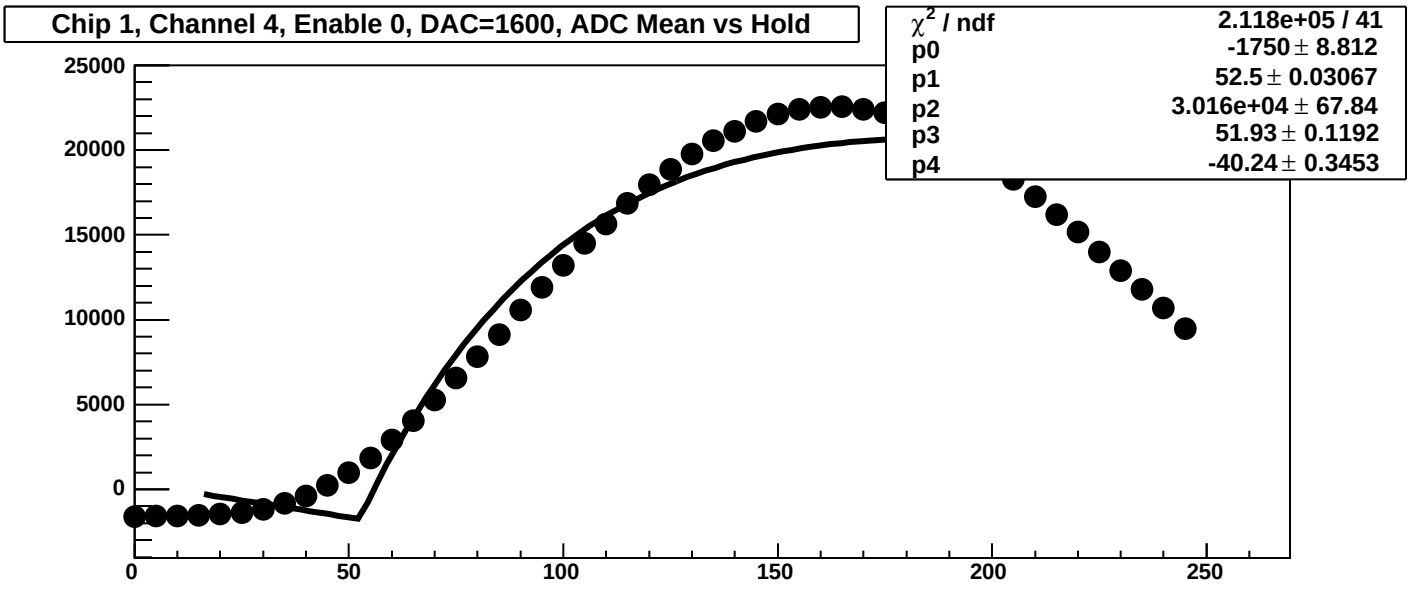


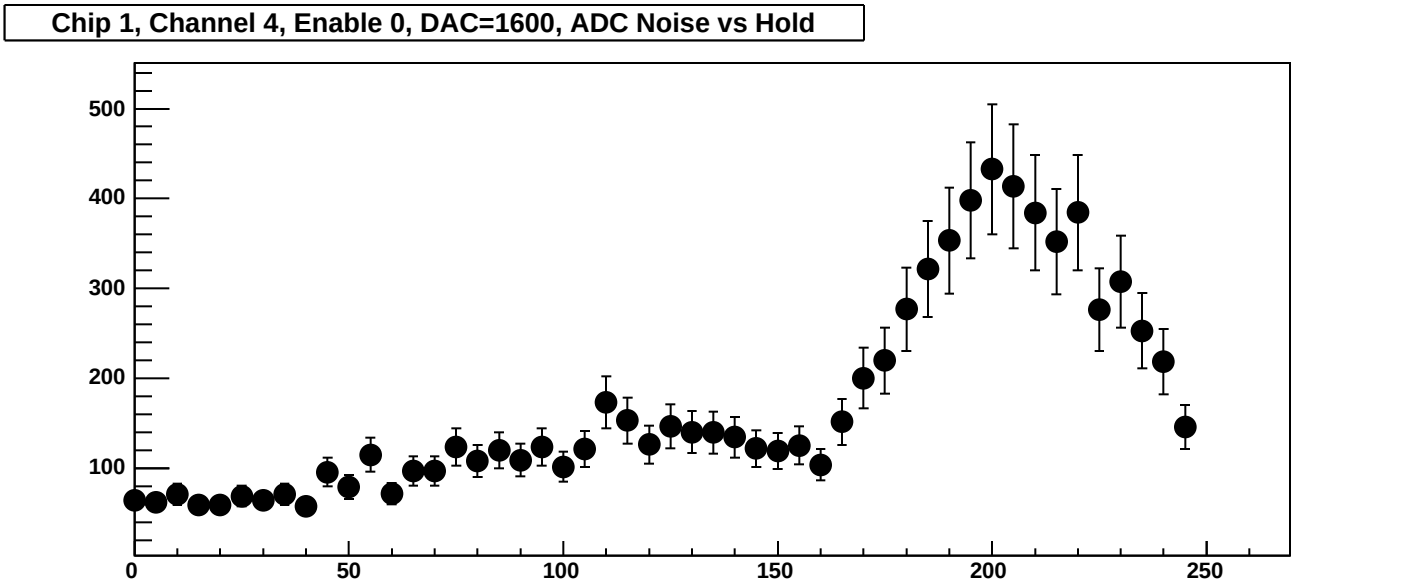
Chip 1, Channel 3, Enable 5, DAC=1600, ADC Noise vs Hold

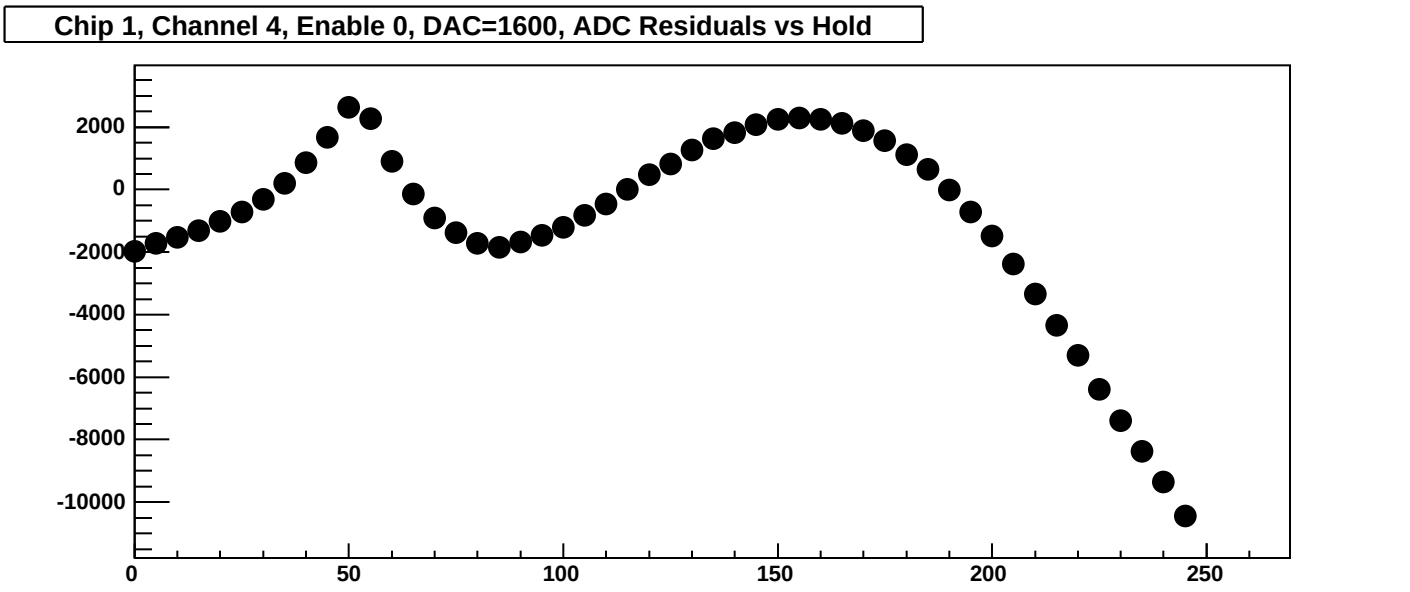


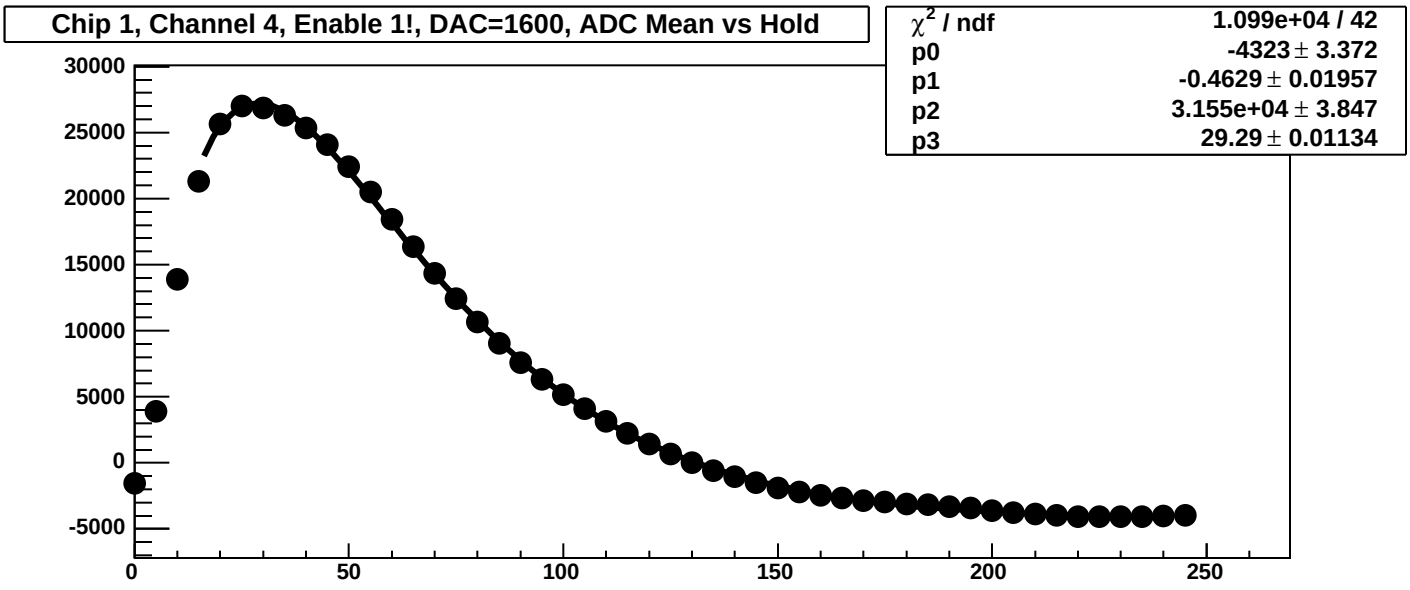
Chip 1, Channel 3, Enable 5, DAC=1600, ADC Residuals vs Hold

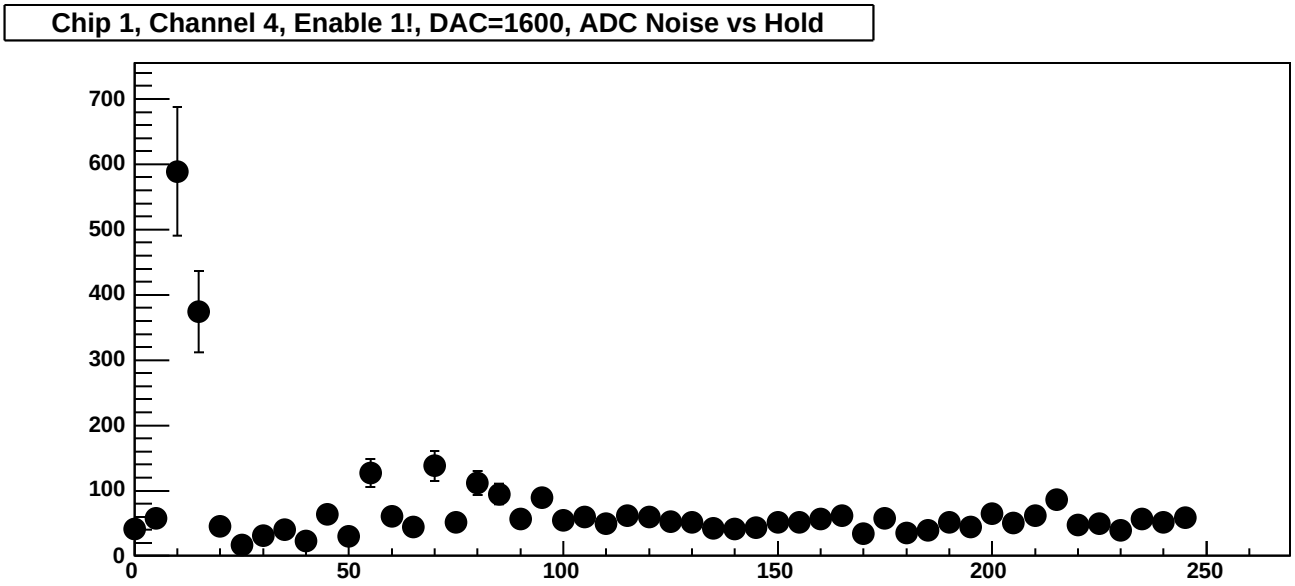


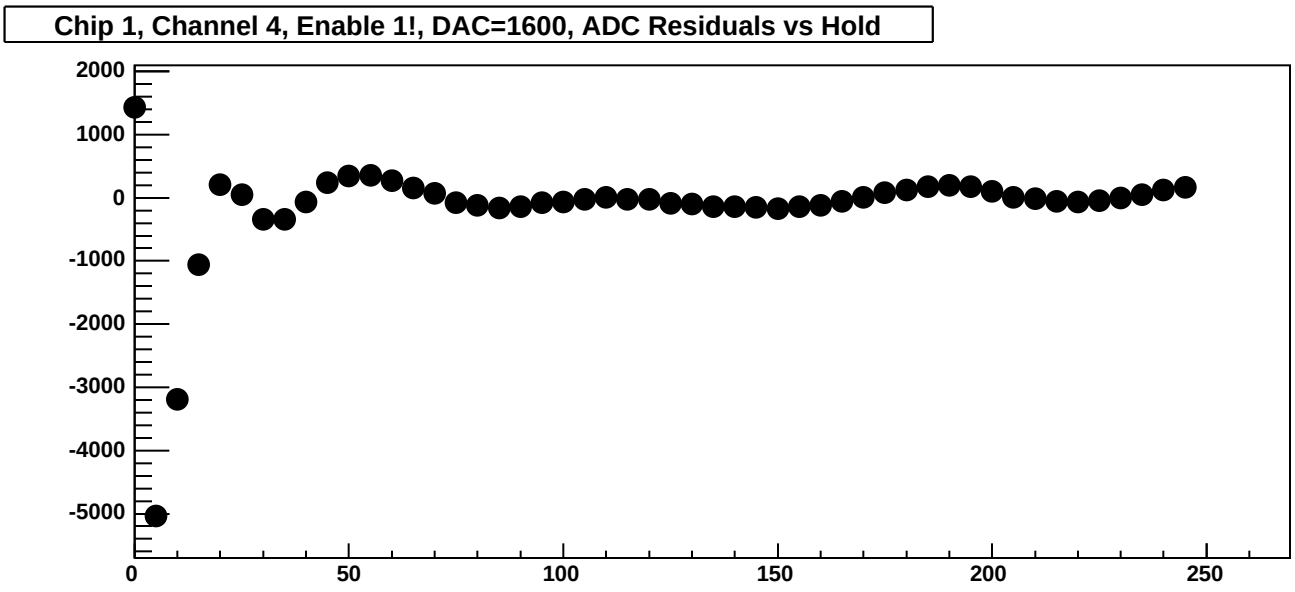

Chip 1, Channel 4, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.118e+05 / 41
p0 -1750 ± 8.812
p1 52.5 ± 0.03067
p2 3.016e+04 ± 67.84
p3 51.93 ± 0.1192
p4 -40.24 ± 0.3453
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

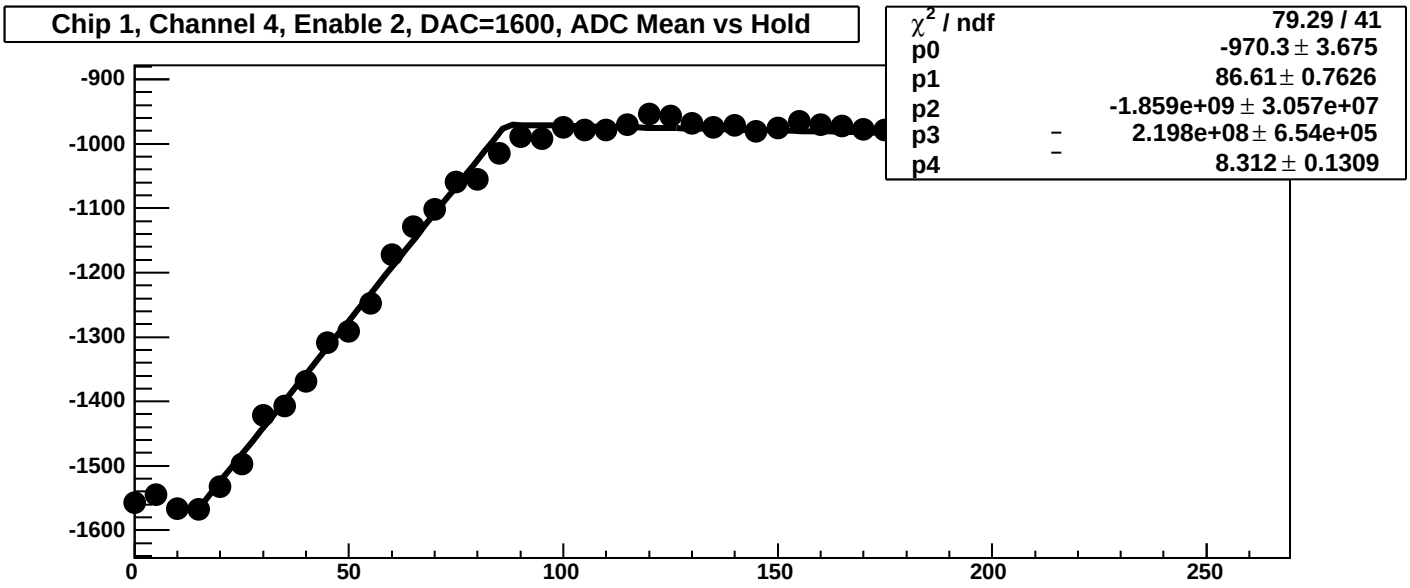

Chip 1, Channel 4, Enable 0, DAC=1600, ADC Noise vs Hold
500
400
300
200
100
0
50
100
150
200
250

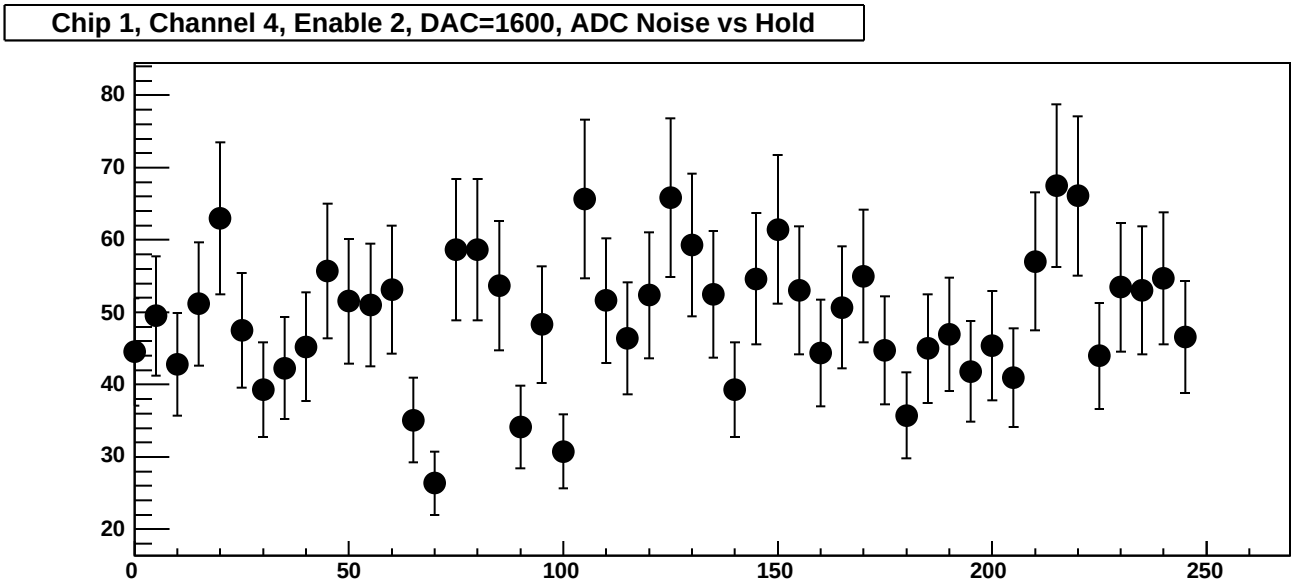

Chip 1, Channel 4, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

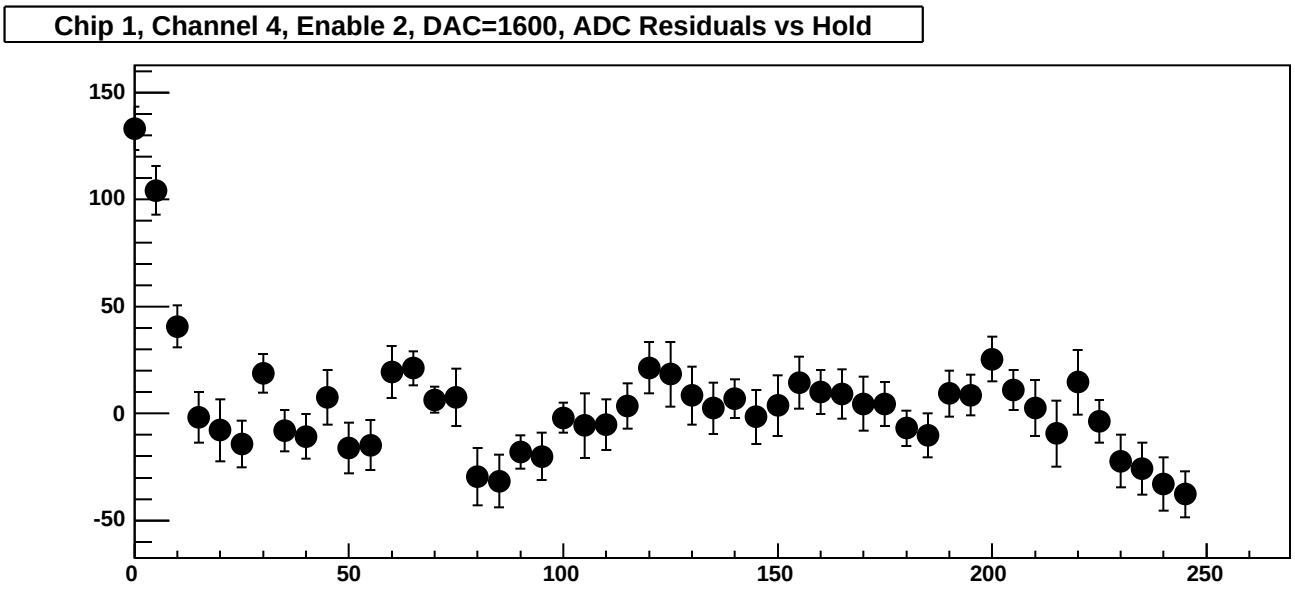

Chip 1, Channel 4, Enable 1!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.099e+04 / 42
p0 -4323 ± 3.372
p1 -0.4629 ± 0.01957
p2 3.155e+04 ± 3.847
p3 29.29 ± 0.01134
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 1, Channel 4, Enable 1!, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250

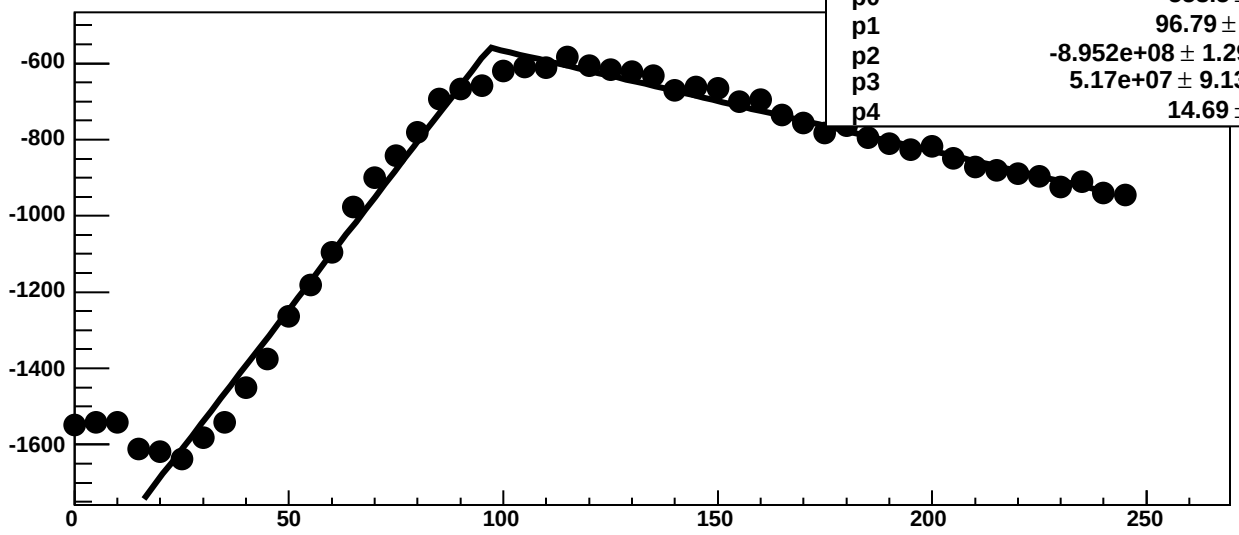

Chip 1, Channel 4, Enable 1!, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250


Chip 1, Channel 4, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 79.29 / 41
p0 -970.3 ± 3.675
p1 86.61 ± 0.7626
p2 -1.859e+09 ± 3.057e+07
p3 2.198e+08 ± 6.54e+05
p4 8.312 ± 0.1309
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
50
100
150
200
250

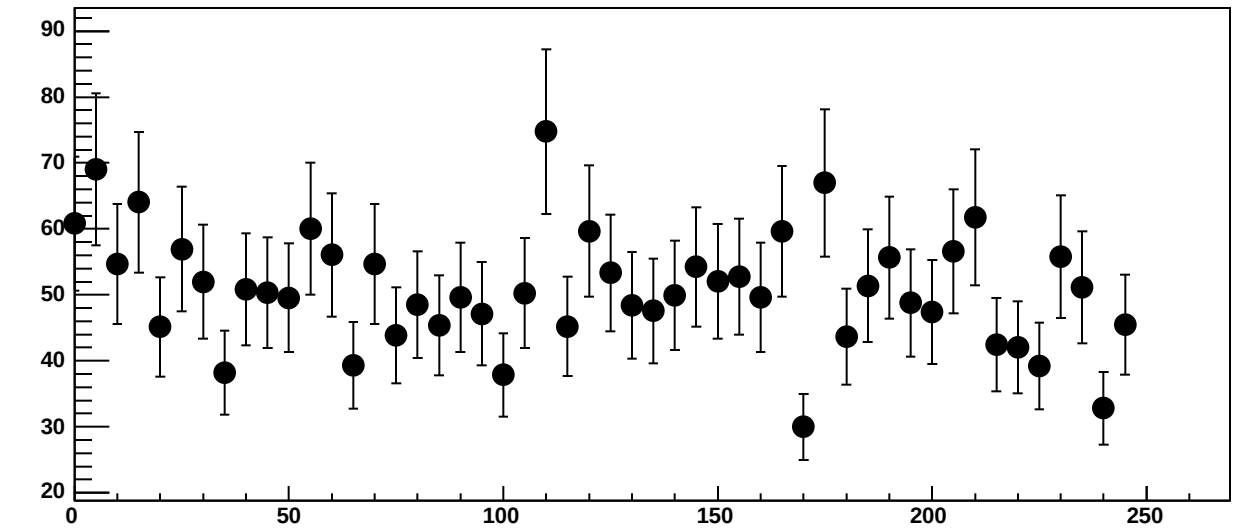

Chip 1, Channel 4, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 1, Channel 4, Enable 2, DAC=1600, ADC Residuals vs Hold
150
100
50
0
-50
0
50
100
150
200
250

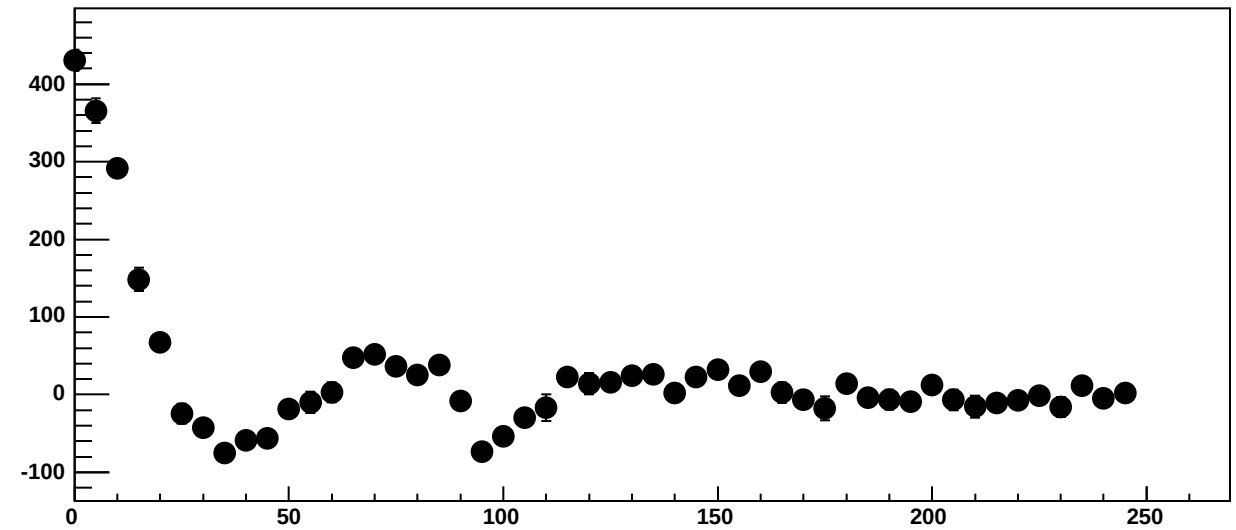
Chip 1, Channel 4, Enable 3, DAC=1600, ADC Mean vs Hold



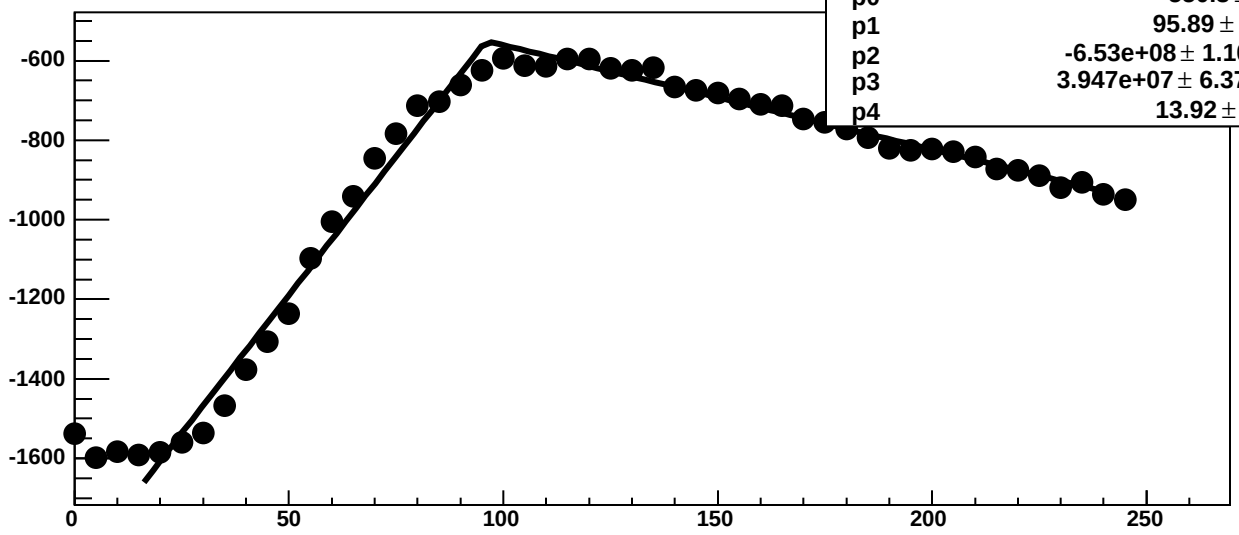
Chip 1, Channel 4, Enable 3, DAC=1600, ADC Noise vs Hold



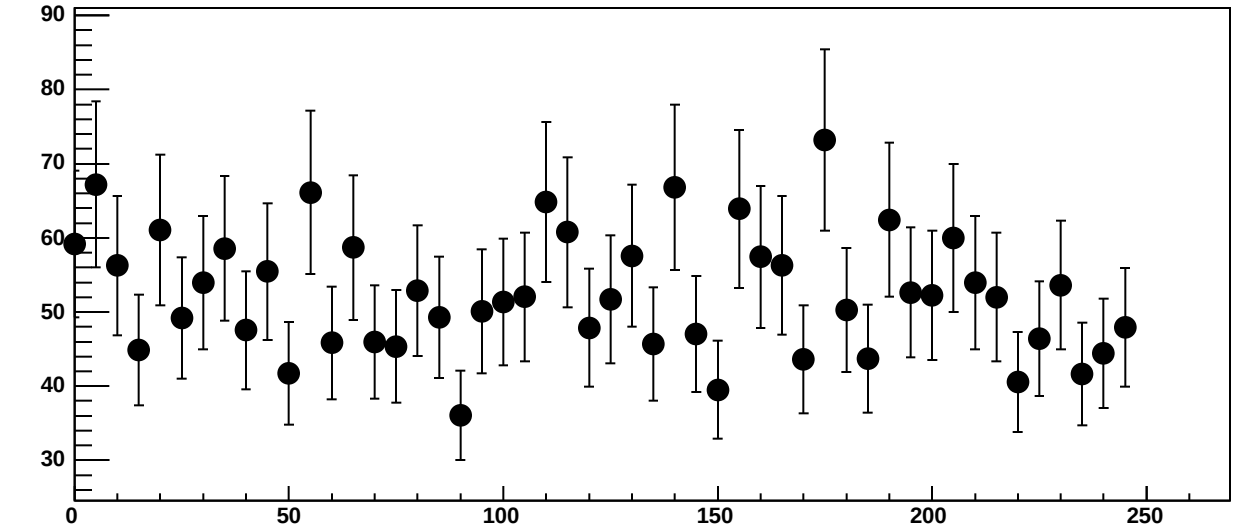
Chip 1, Channel 4, Enable 3, DAC=1600, ADC Residuals vs Hold



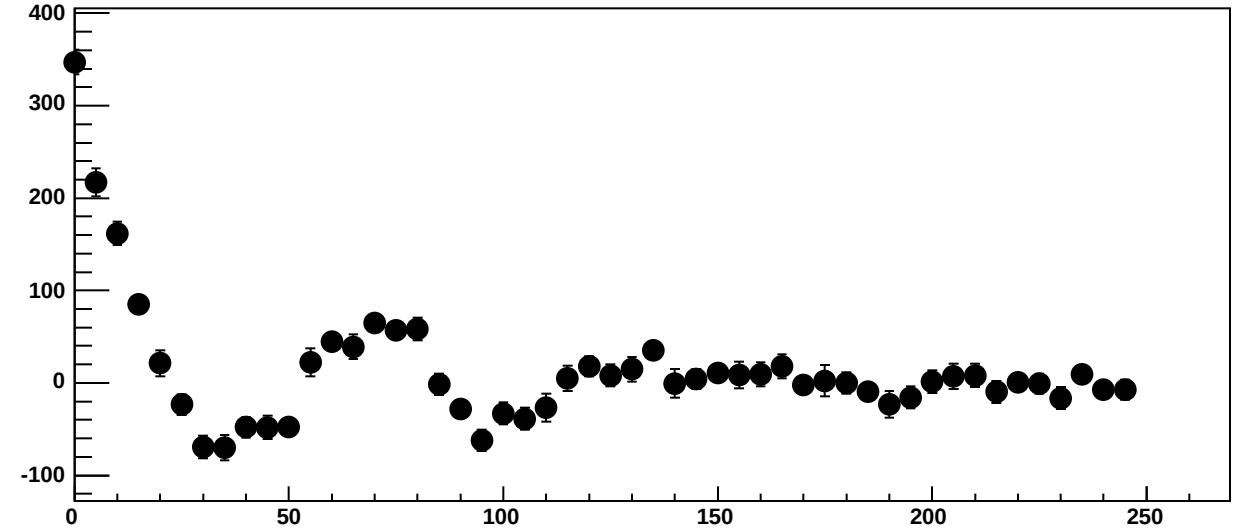
Chip 1, Channel 4, Enable 4, DAC=1600, ADC Mean vs Hold

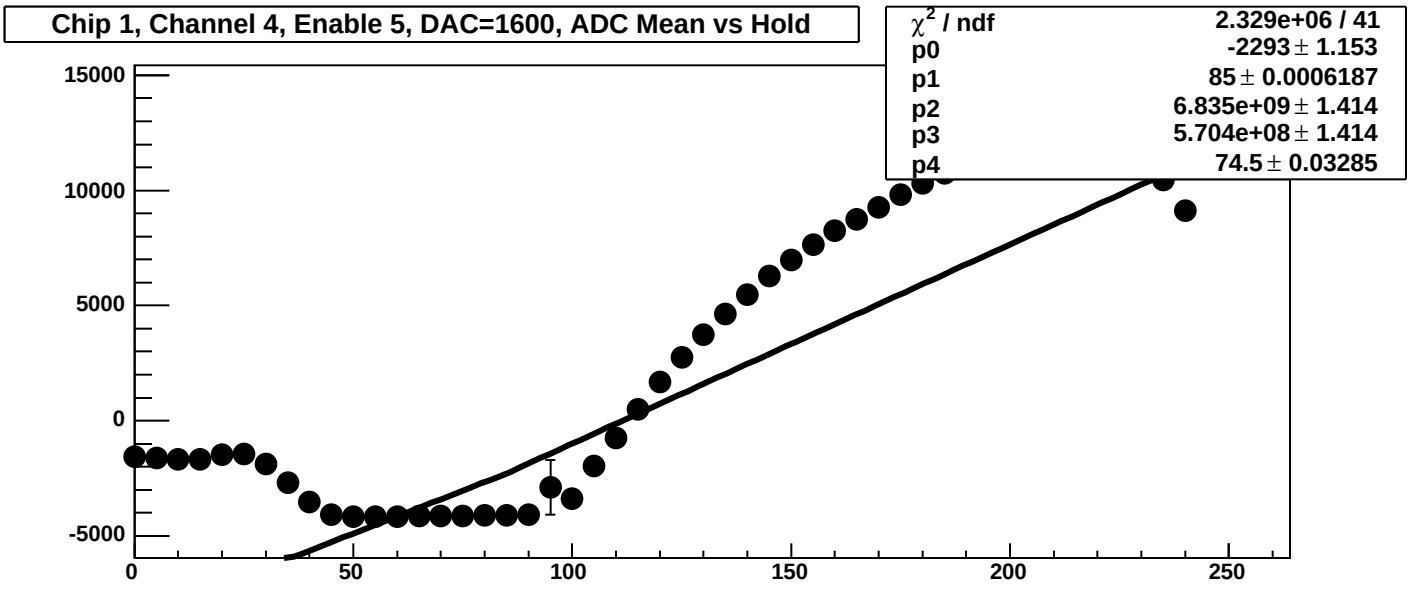


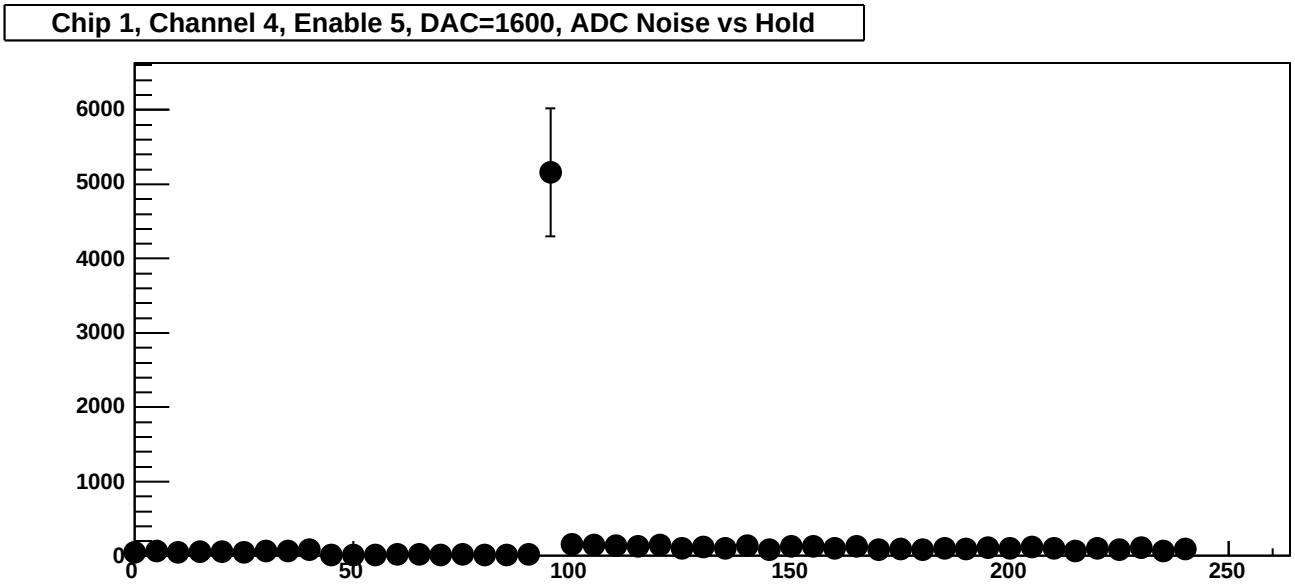
Chip 1, Channel 4, Enable 4, DAC=1600, ADC Noise vs Hold

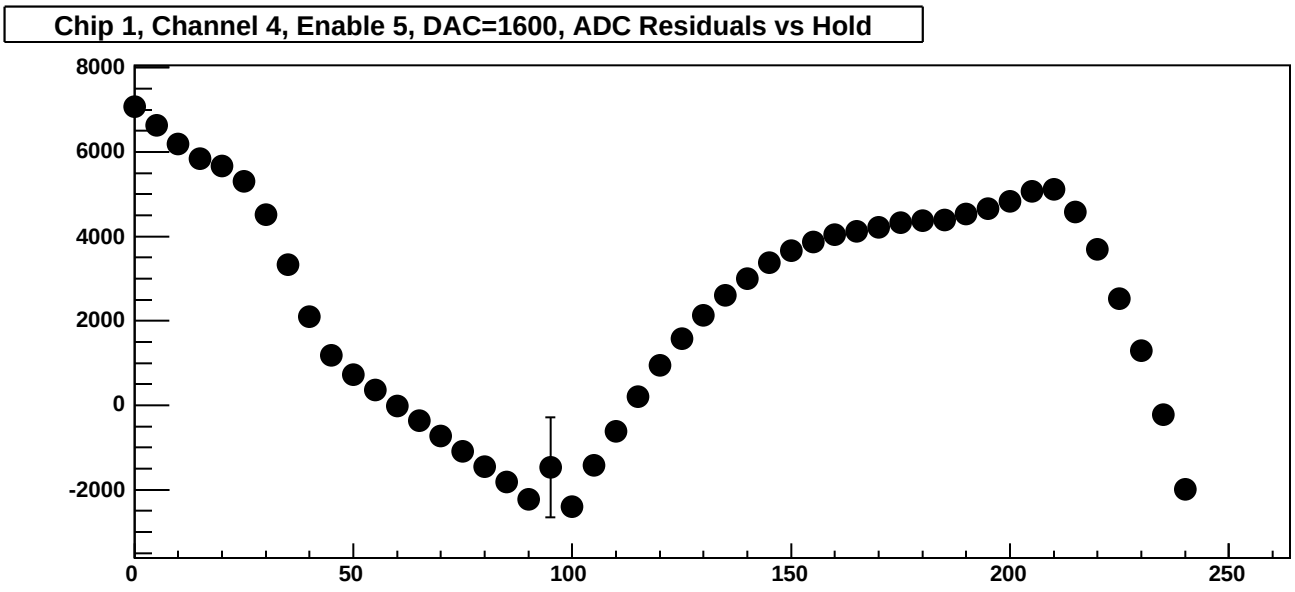


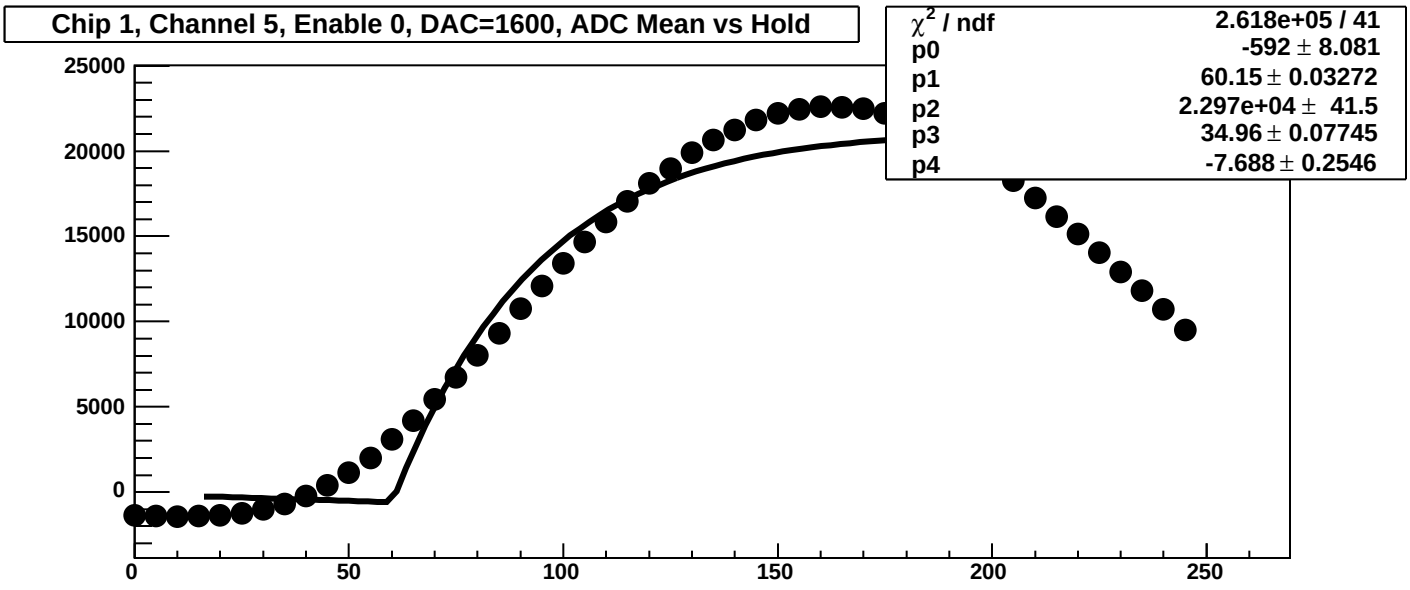
Chip 1, Channel 4, Enable 4, DAC=1600, ADC Residuals vs Hold

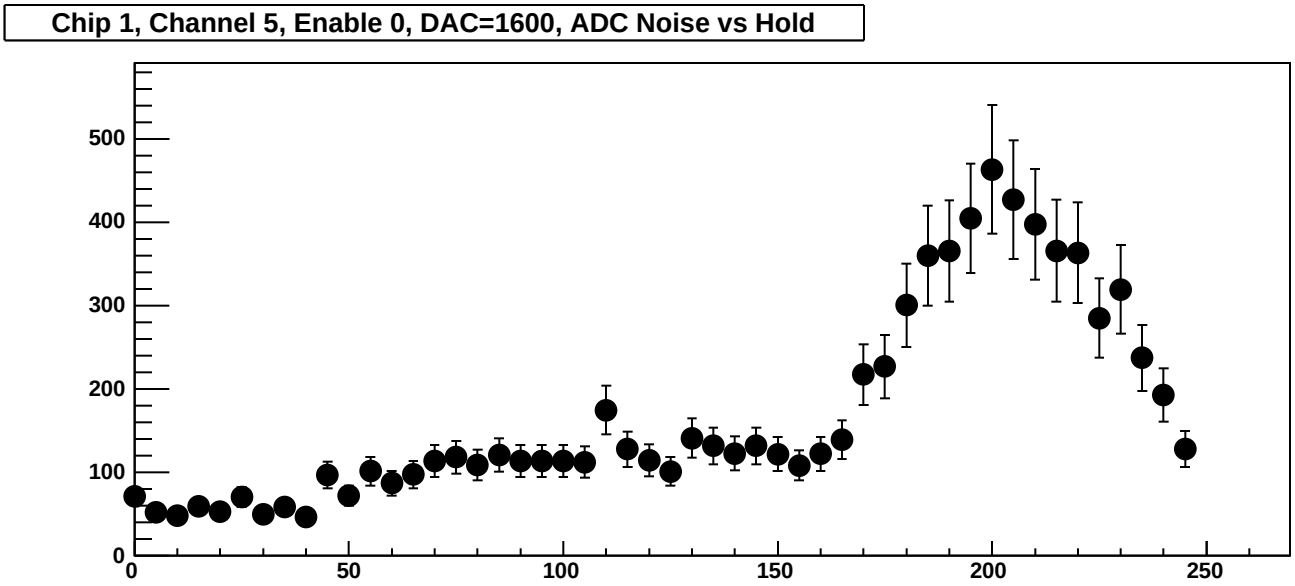


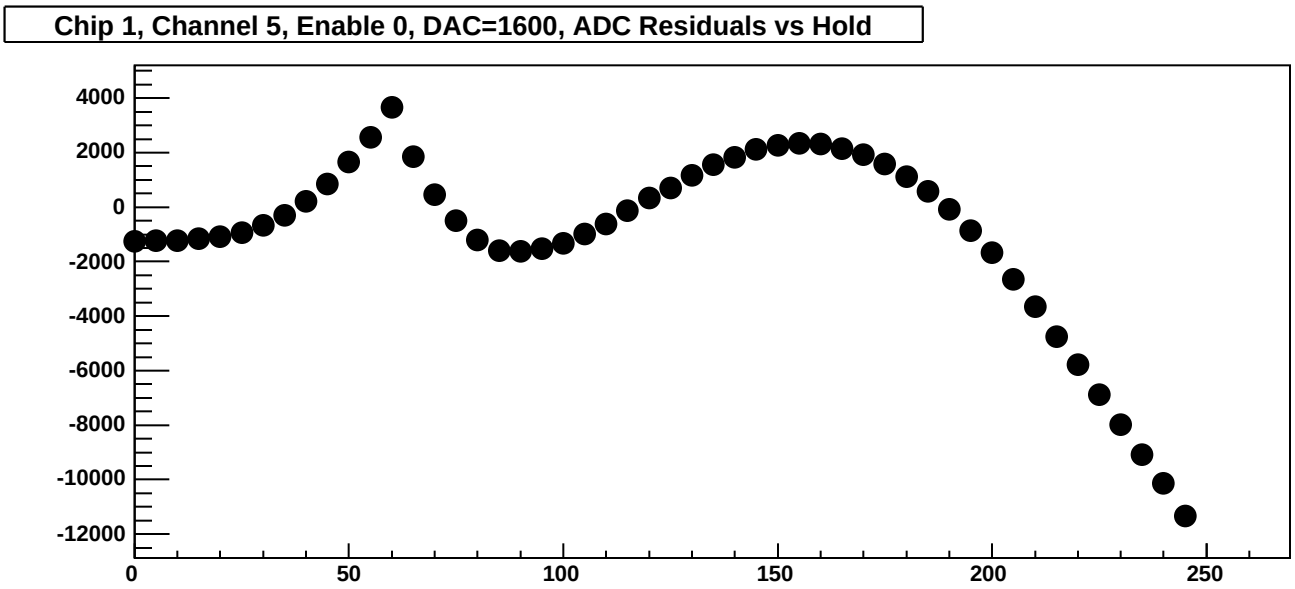

Chip 1, Channel 4, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.329e+06 / 41
p0 -2293 ± 1.153
p1 85 ± 0.0006187
p2 6.835e+09 ± 1.414
p3 5.704e+08 ± 1.414
p4 74.5 ± 0.03285
15000
10000
5000
0
-5000
0
50
100
150
200
250

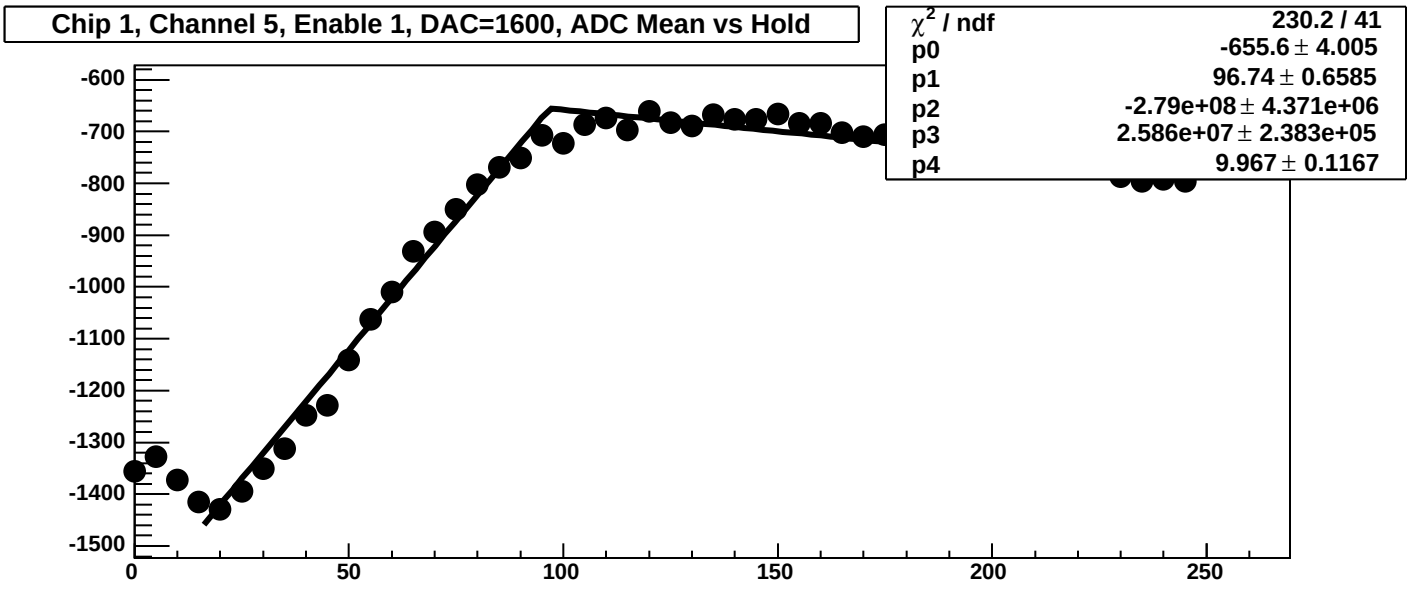

Chip 1, Channel 4, Enable 5, DAC=1600, ADC Noise vs Hold
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

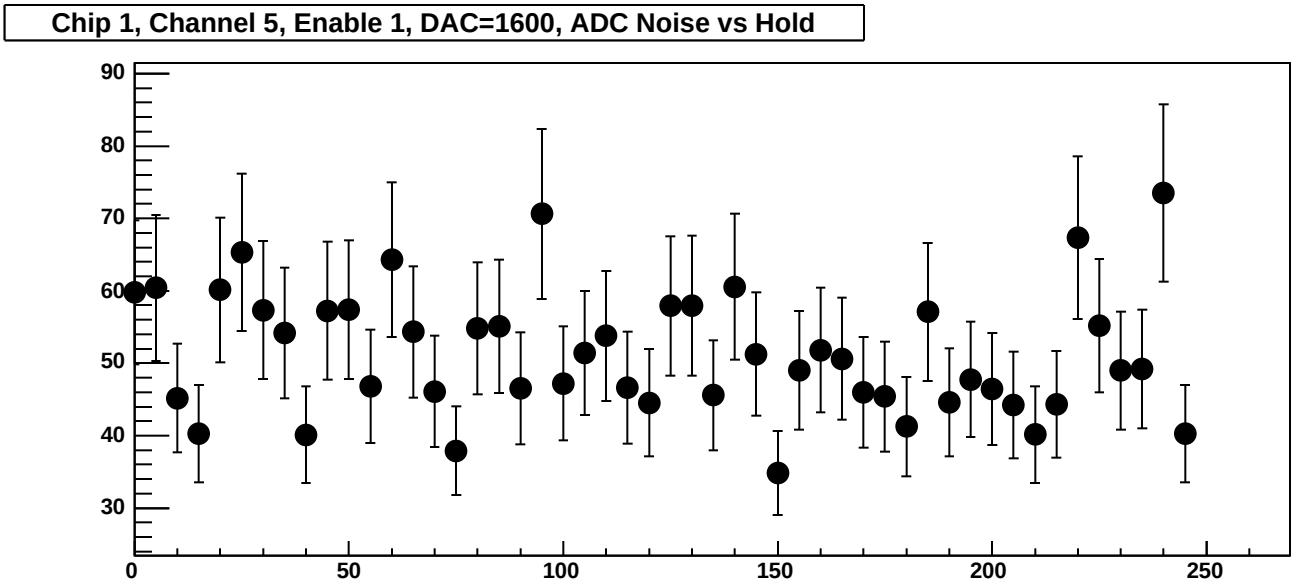

Chip 1, Channel 4, Enable 5, DAC=1600, ADC Residuals vs Hold
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

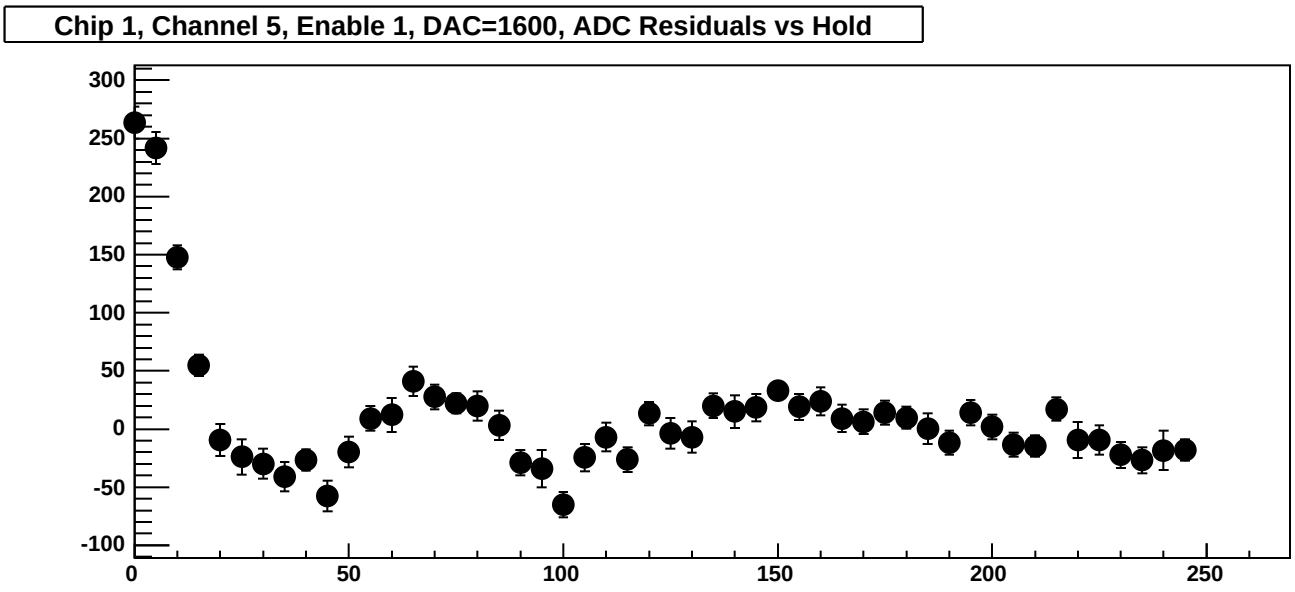

Chip 1, Channel 5, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.618e+05 / 41
p0 -592 ± 8.081
p1 60.15 ± 0.03272
p2 2.297e+04 ± 41.5
p3 34.96 ± 0.07745
p4 -7.688 ± 0.2546
25000
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 1, Channel 5, Enable 0, DAC=1600, ADC Noise vs Hold
500
400
300
200
100
0
0
50
100
150
200
250

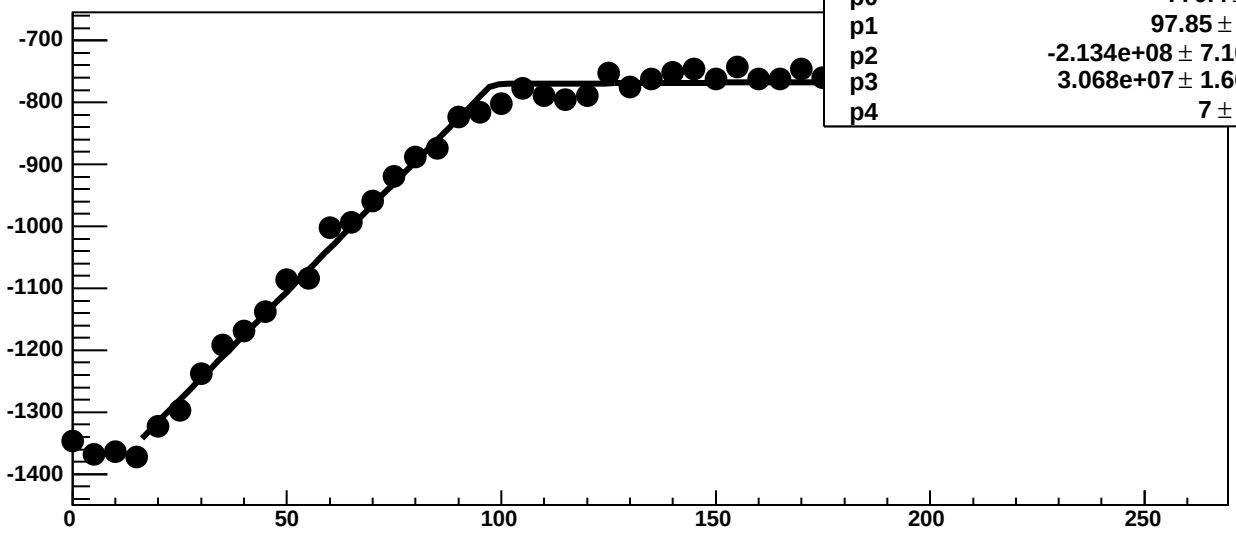

Chip 1, Channel 5, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
0
50
100
150
200
250


Chip 1, Channel 5, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 230.2 / 41
p0 -655.6 ± 4.005
p1 96.74 ± 0.6585
p2 -2.79e+08 ± 4.371e+06
p3 2.586e+07 ± 2.383e+05
p4 9.967 ± 0.1167
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250

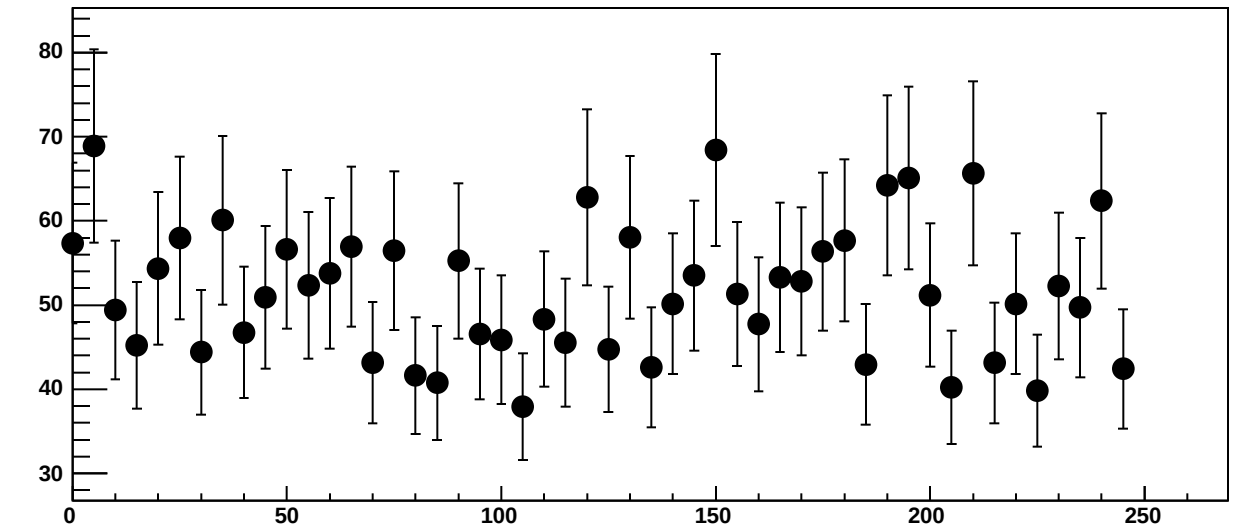

Chip 1, Channel 5, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 1, Channel 5, Enable 1, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

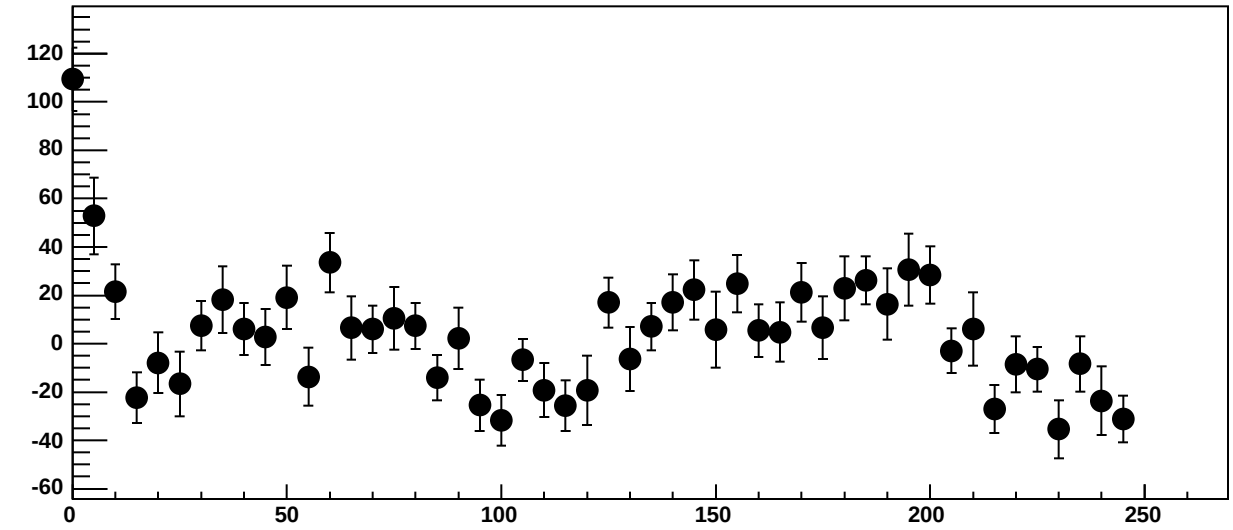
Chip 1, Channel 5, Enable 2, DAC=1600, ADC Mean vs Hold



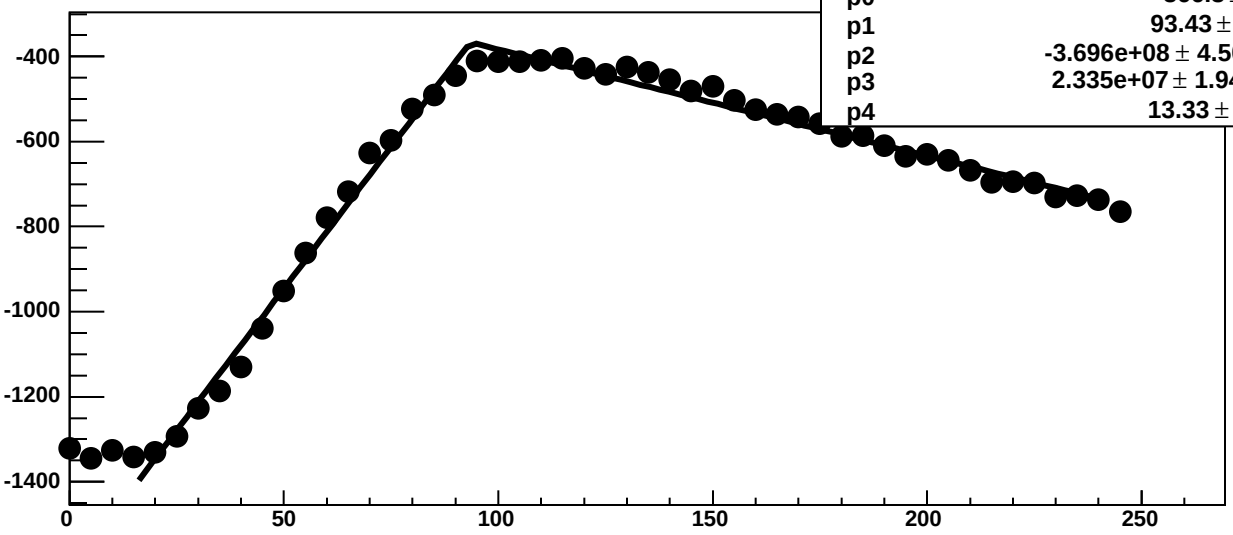
Chip 1, Channel 5, Enable 2, DAC=1600, ADC Noise vs Hold



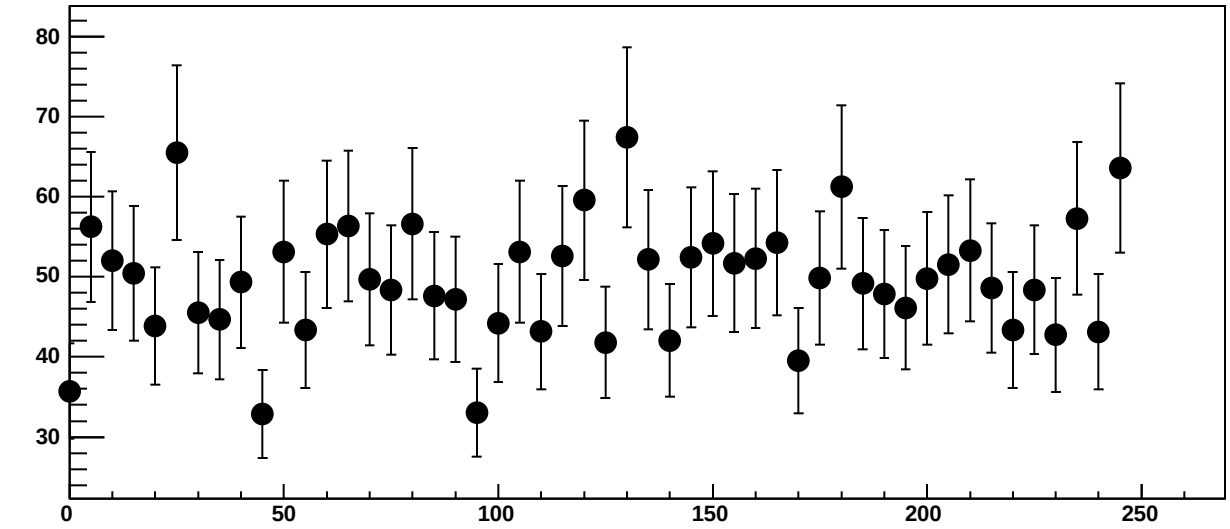
Chip 1, Channel 5, Enable 2, DAC=1600, ADC Residuals vs Hold



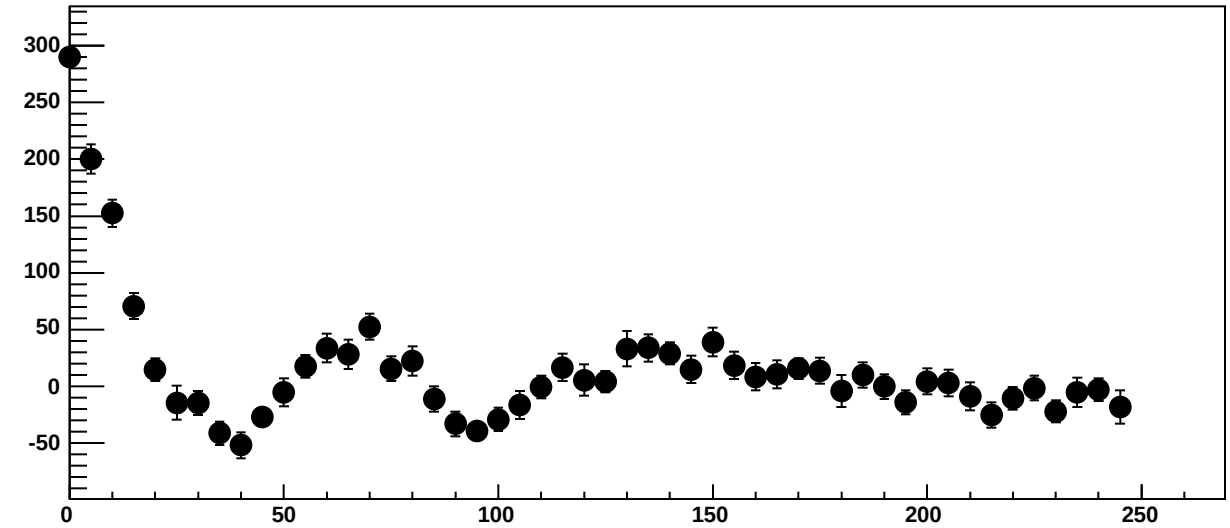
Chip 1, Channel 5, Enable 3, DAC=1600, ADC Mean vs Hold



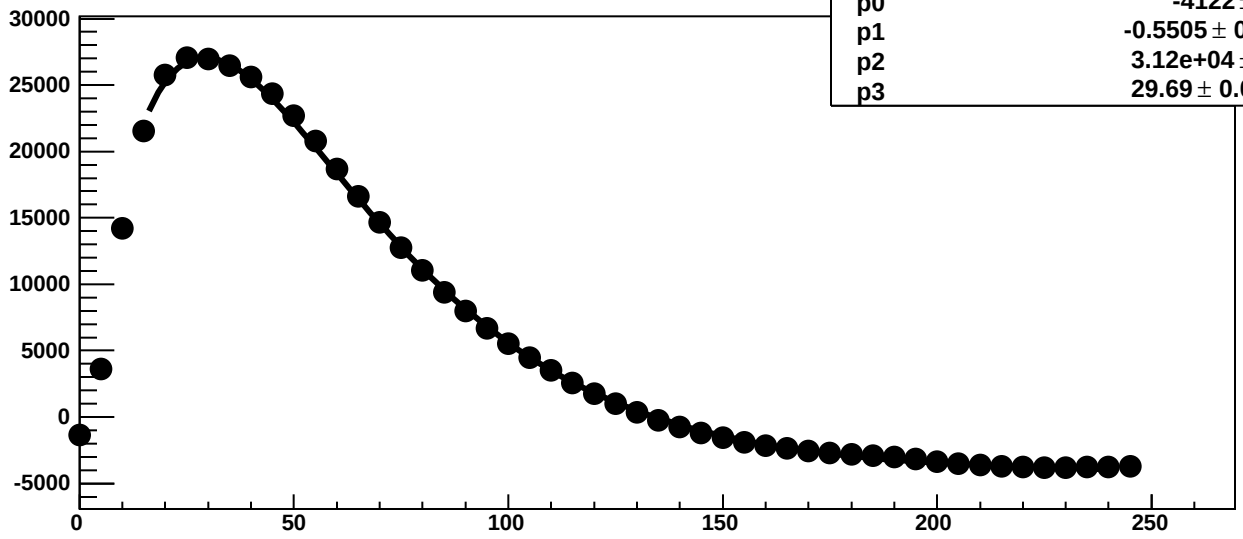
Chip 1, Channel 5, Enable 3, DAC=1600, ADC Noise vs Hold



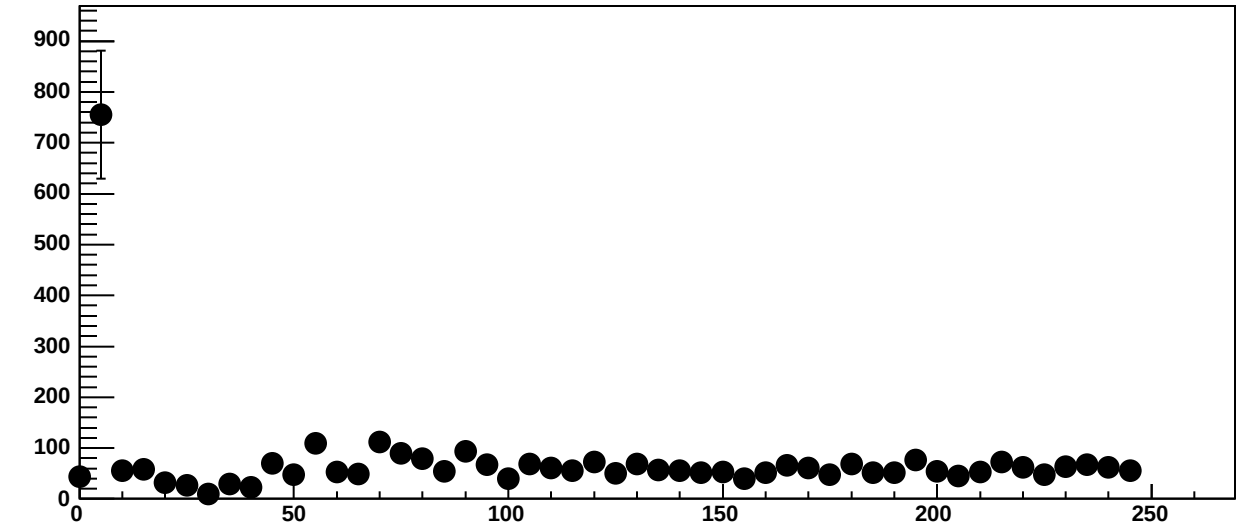
Chip 1, Channel 5, Enable 3, DAC=1600, ADC Residuals vs Hold



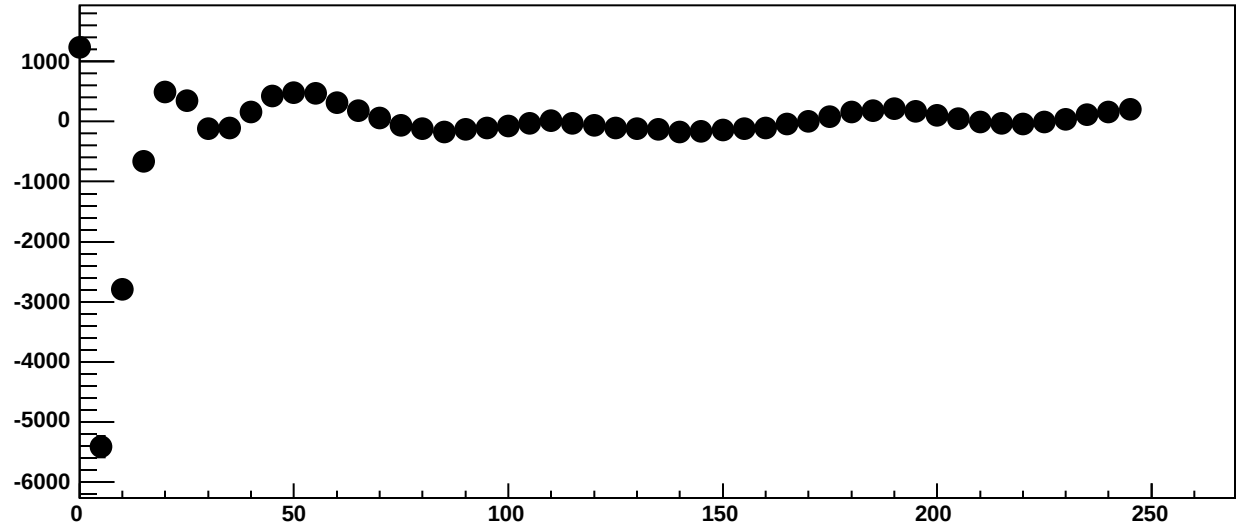
Chip 1, Channel 5, Enable 4!, DAC=1600, ADC Mean vs Hold



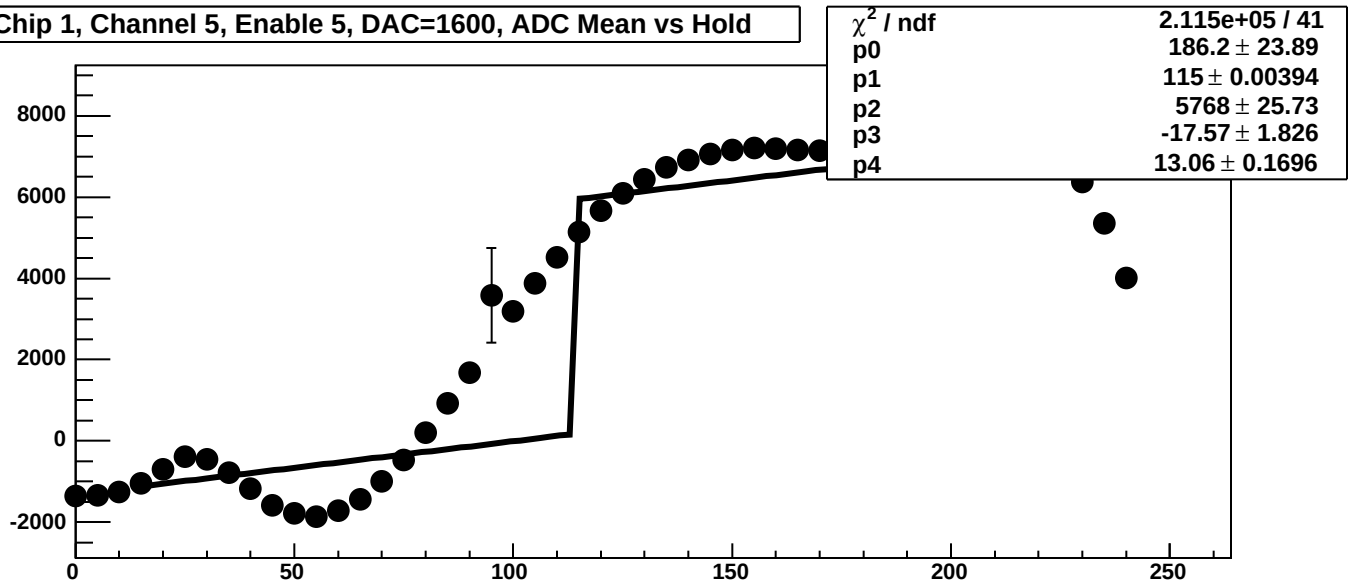
Chip 1, Channel 5, Enable 4!, DAC=1600, ADC Noise vs Hold



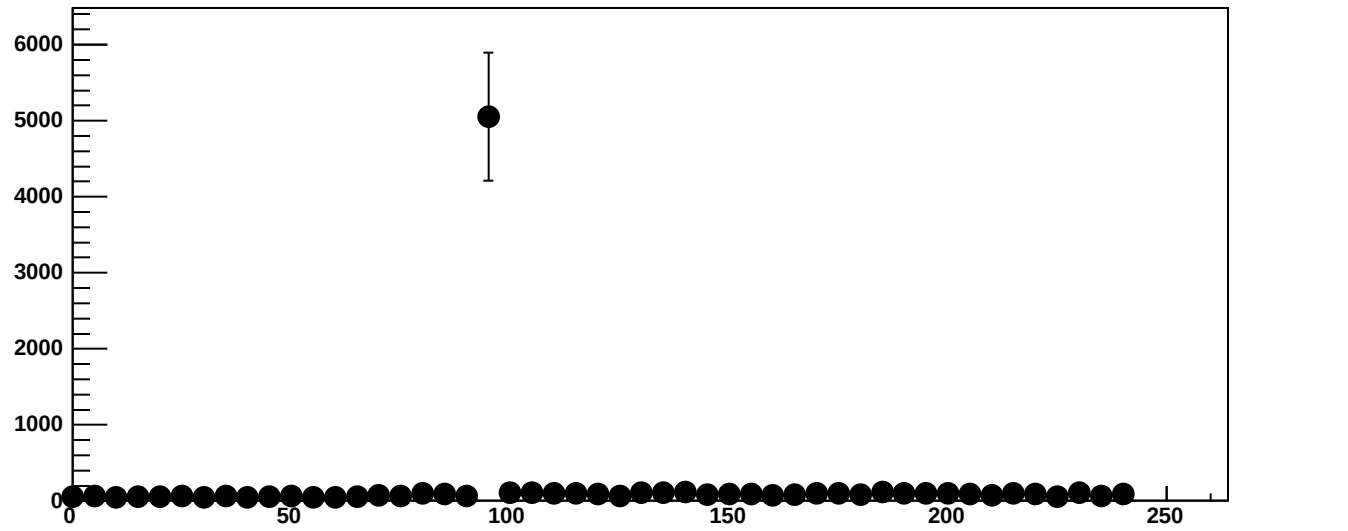
Chip 1, Channel 5, Enable 4!, DAC=1600, ADC Residuals vs Hold



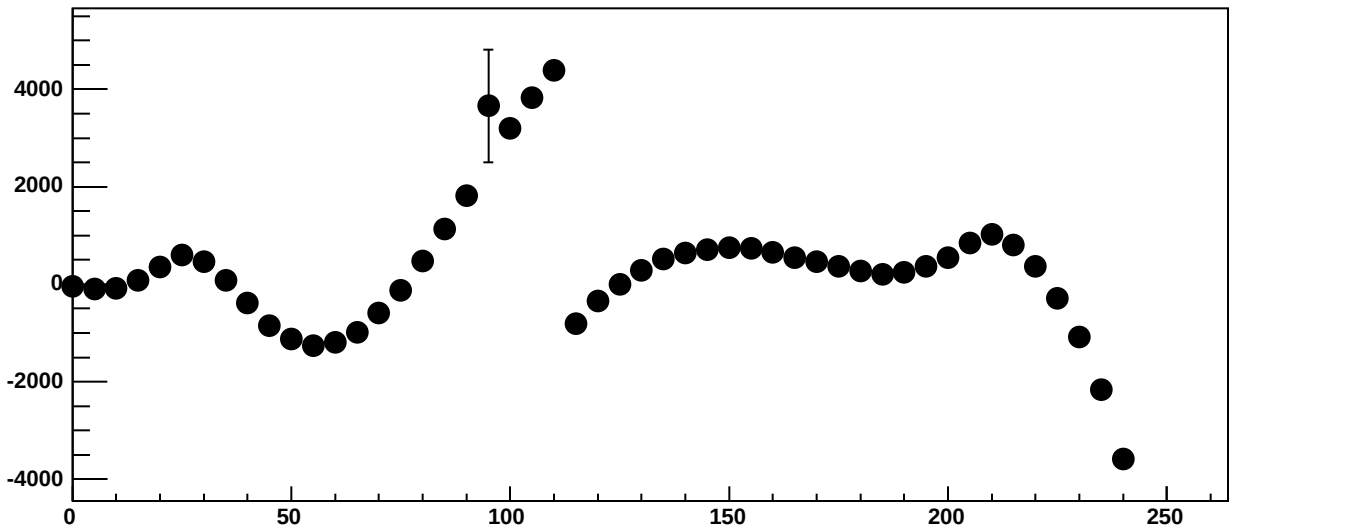
Chip 1, Channel 5, Enable 5, DAC=1600, ADC Mean vs Hold

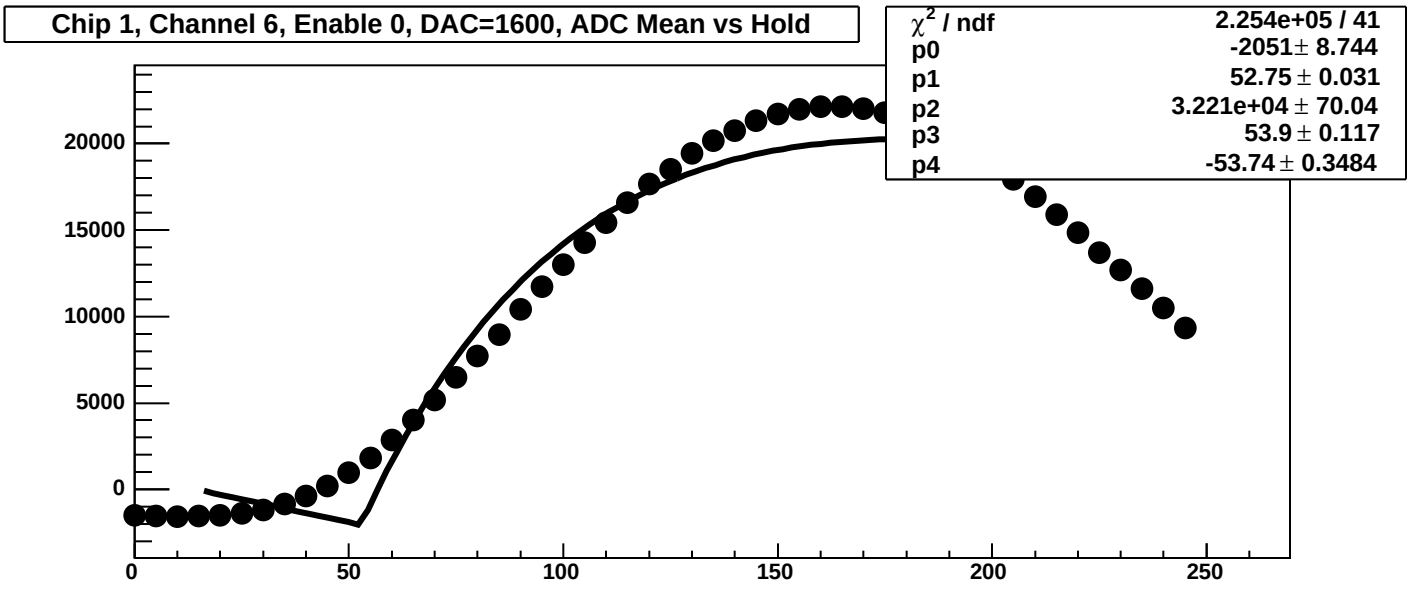


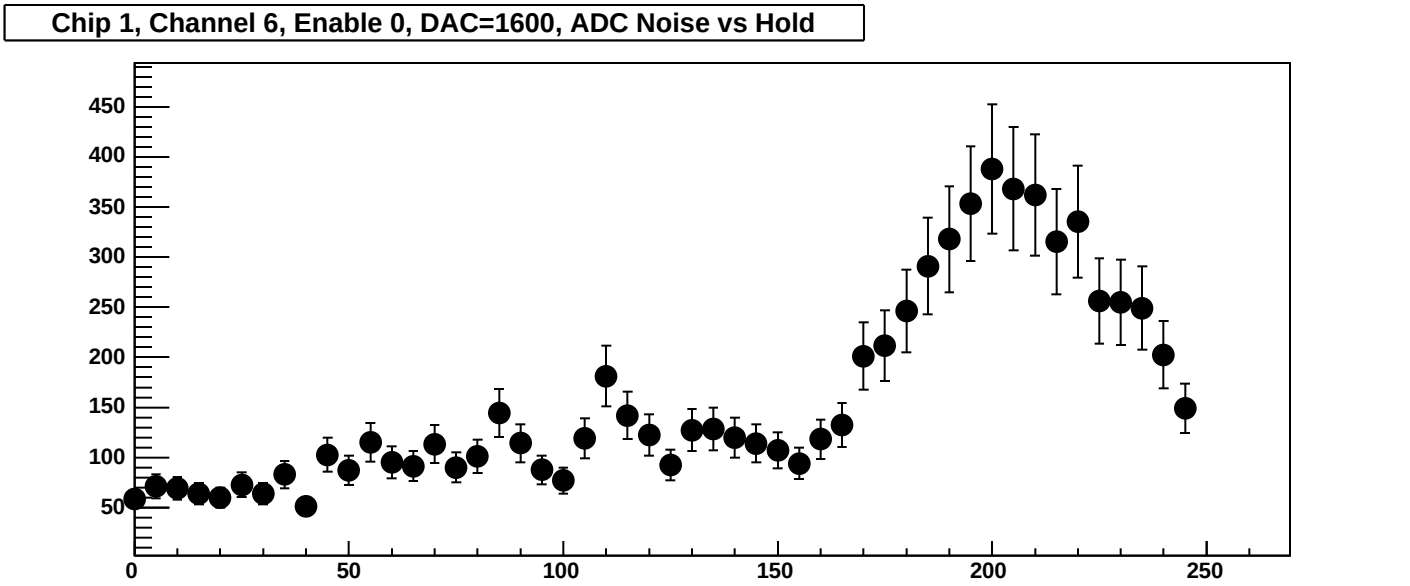
Chip 1, Channel 5, Enable 5, DAC=1600, ADC Noise vs Hold

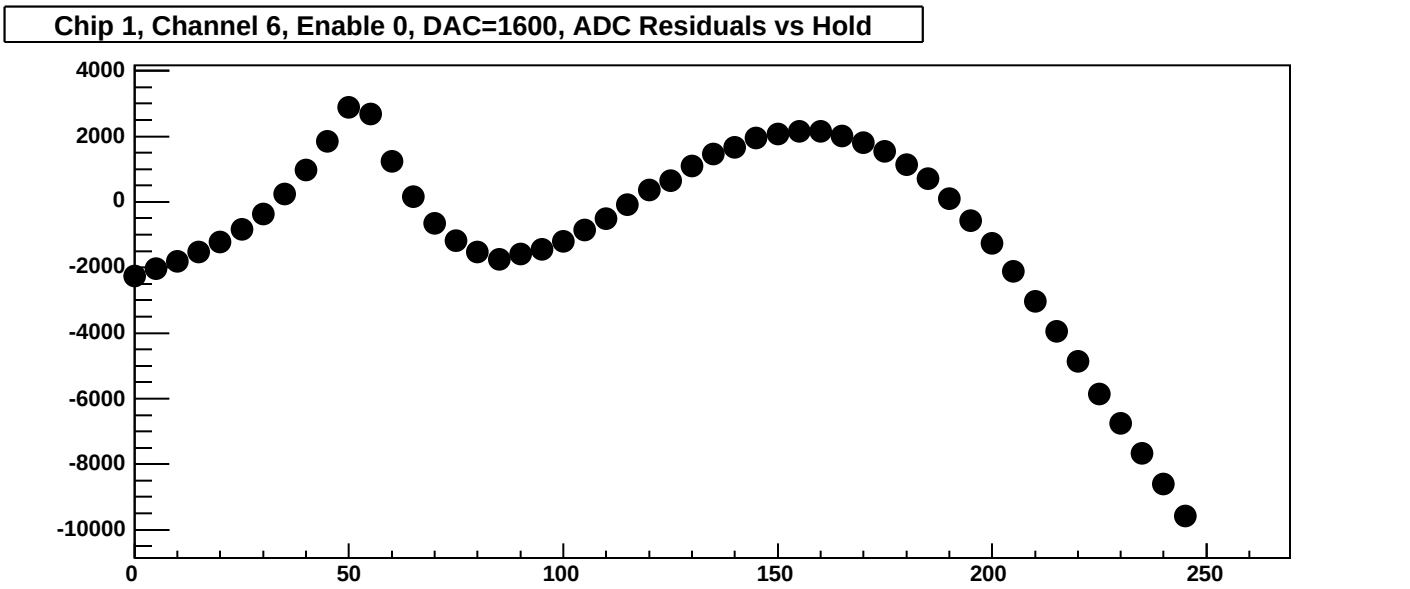


Chip 1, Channel 5, Enable 5, DAC=1600, ADC Residuals vs Hold

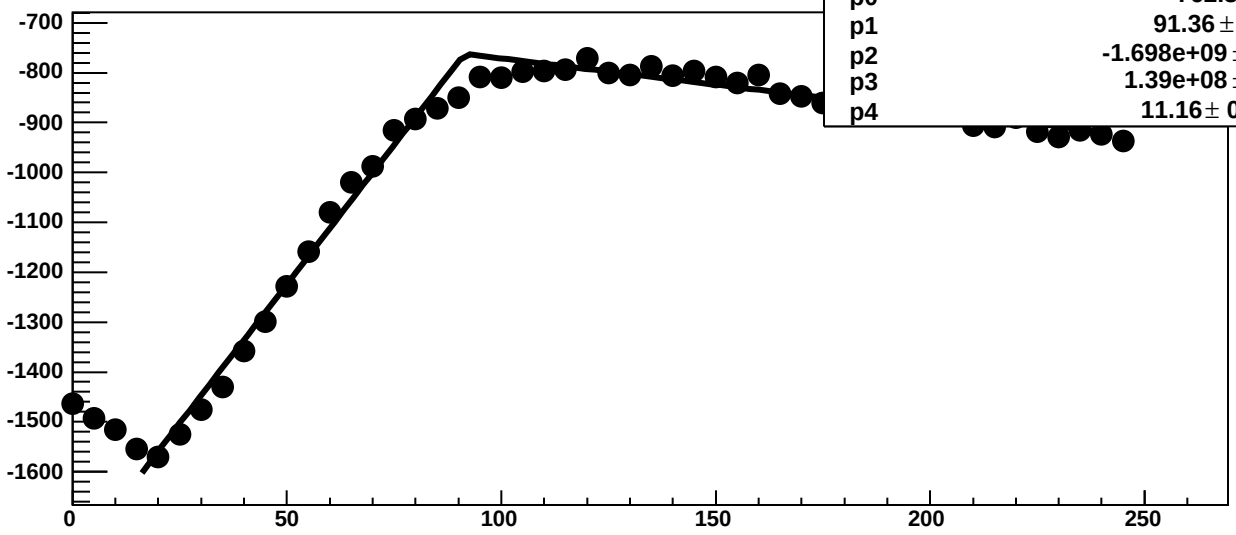



Chip 1, Channel 6, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.254e+05 / 41
p0 -2051 ± 8.744
p1 52.75 ± 0.031
p2 3.221e+04 ± 70.04
p3 53.9 ± 0.117
p4 -53.74 ± 0.3484
20000
15000
10000
5000
0
0
50
100
150
200
250

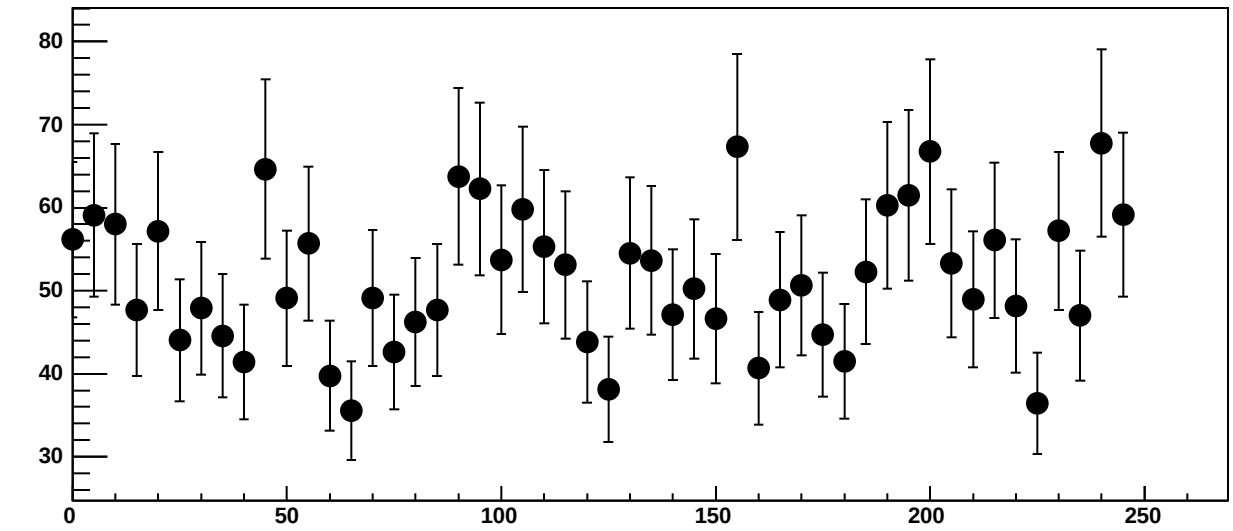

Chip 1, Channel 6, Enable 0, DAC=1600, ADC Noise vs Hold
450
400
350
300
250
200
150
100
50
0
50
100
150
200
250


Chip 1, Channel 6, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

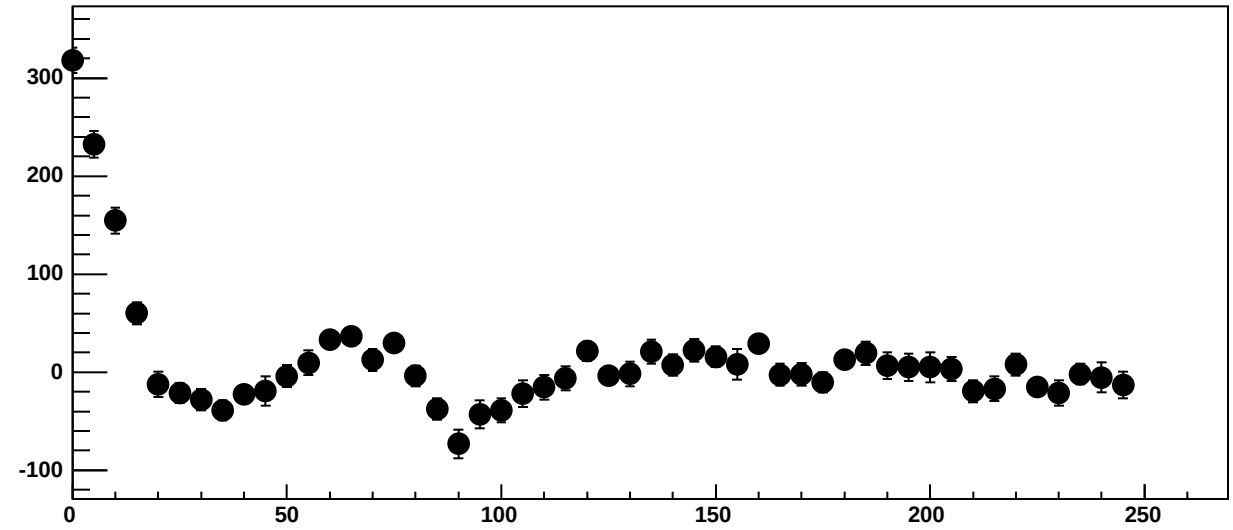
Chip 1, Channel 6, Enable 1, DAC=1600, ADC Mean vs Hold



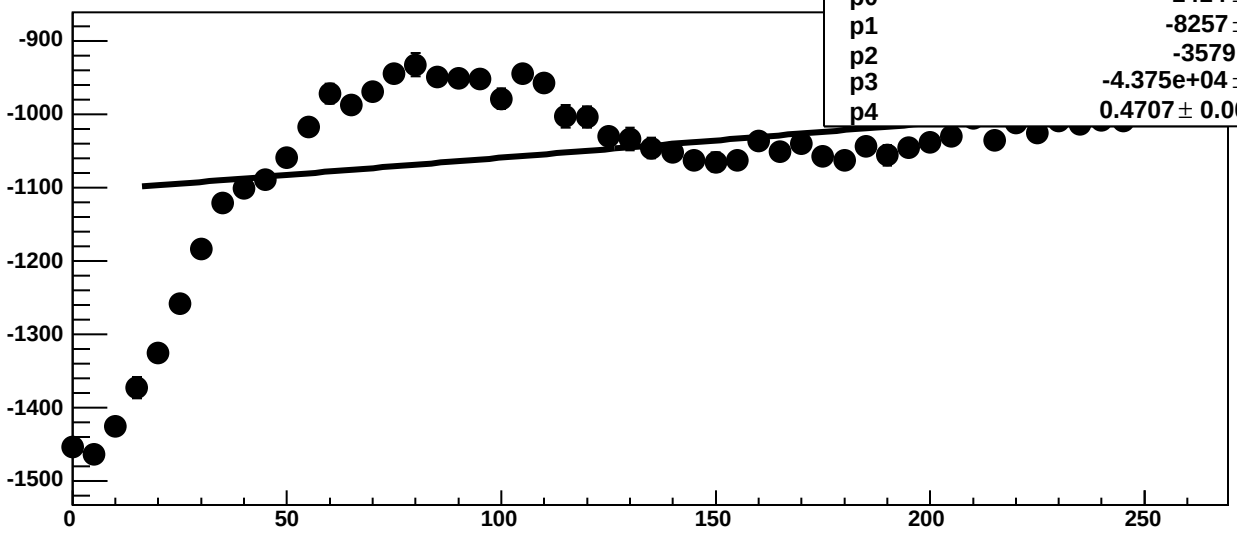
Chip 1, Channel 6, Enable 1, DAC=1600, ADC Noise vs Hold



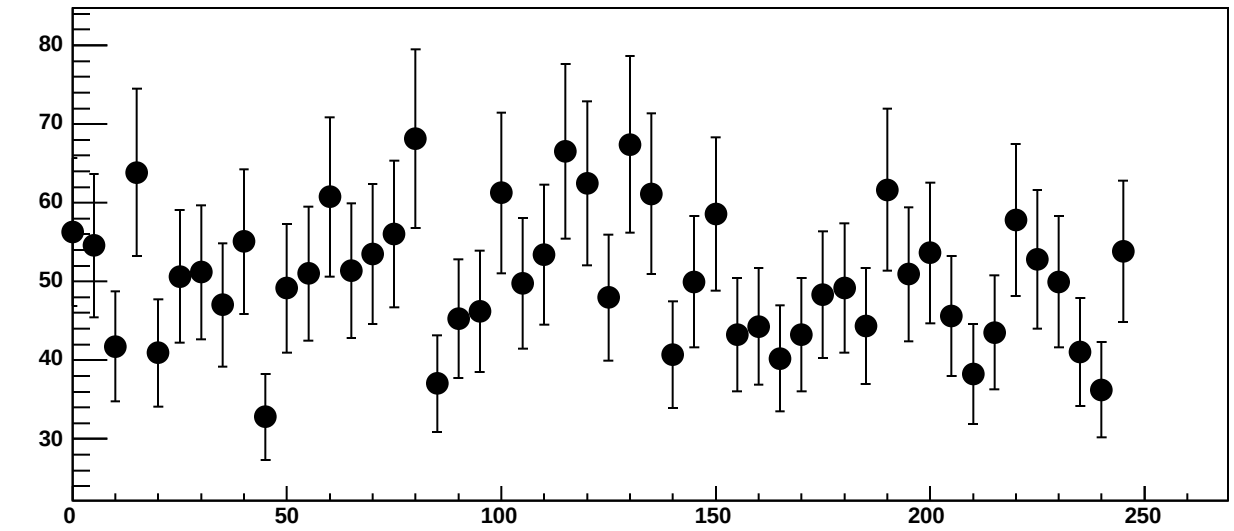
Chip 1, Channel 6, Enable 1, DAC=1600, ADC Residuals vs Hold



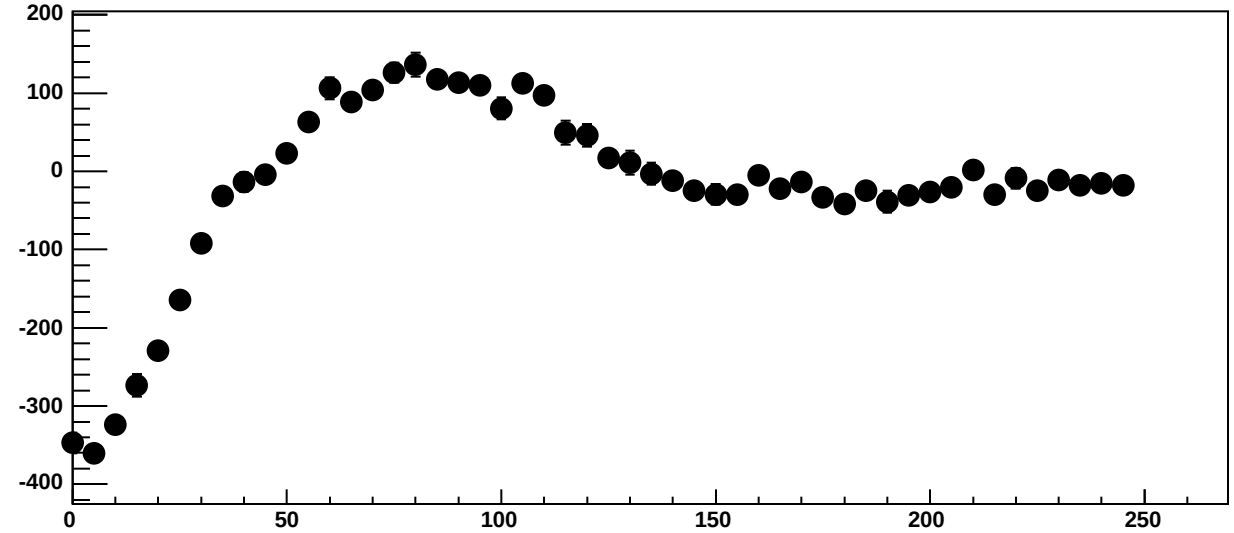
Chip 1, Channel 6, Enable 2, DAC=1600, ADC Mean vs Hold



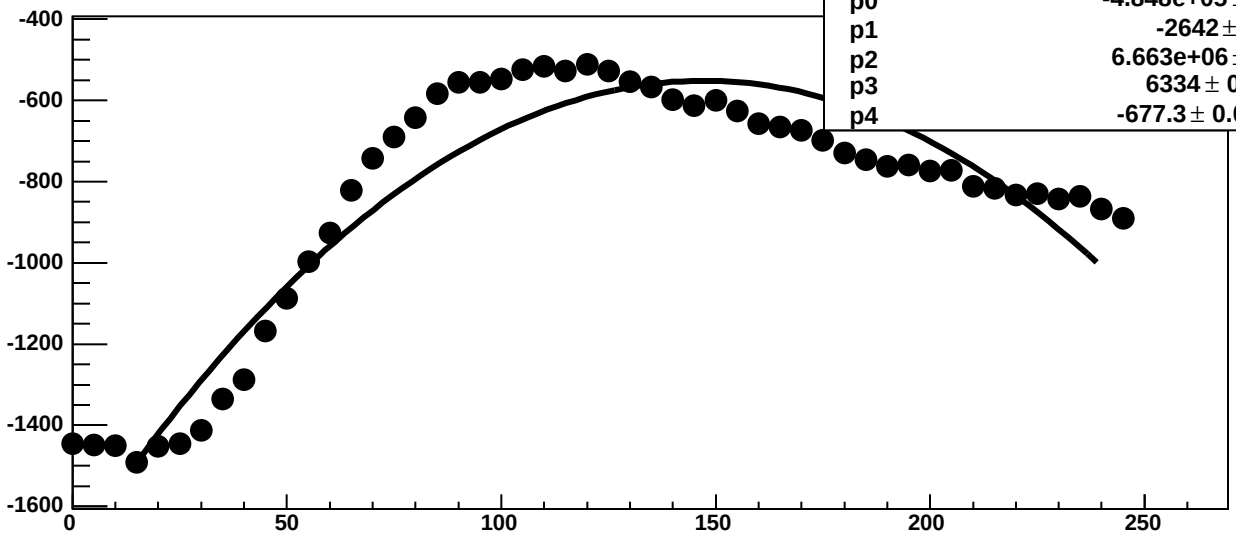
Chip 1, Channel 6, Enable 2, DAC=1600, ADC Noise vs Hold



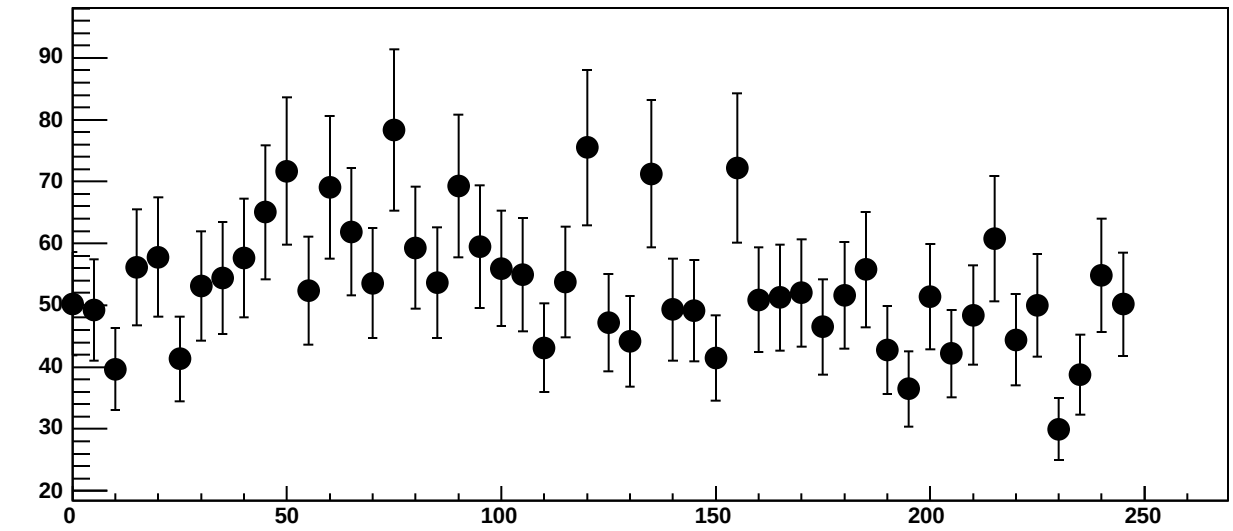
Chip 1, Channel 6, Enable 2, DAC=1600, ADC Residuals vs Hold



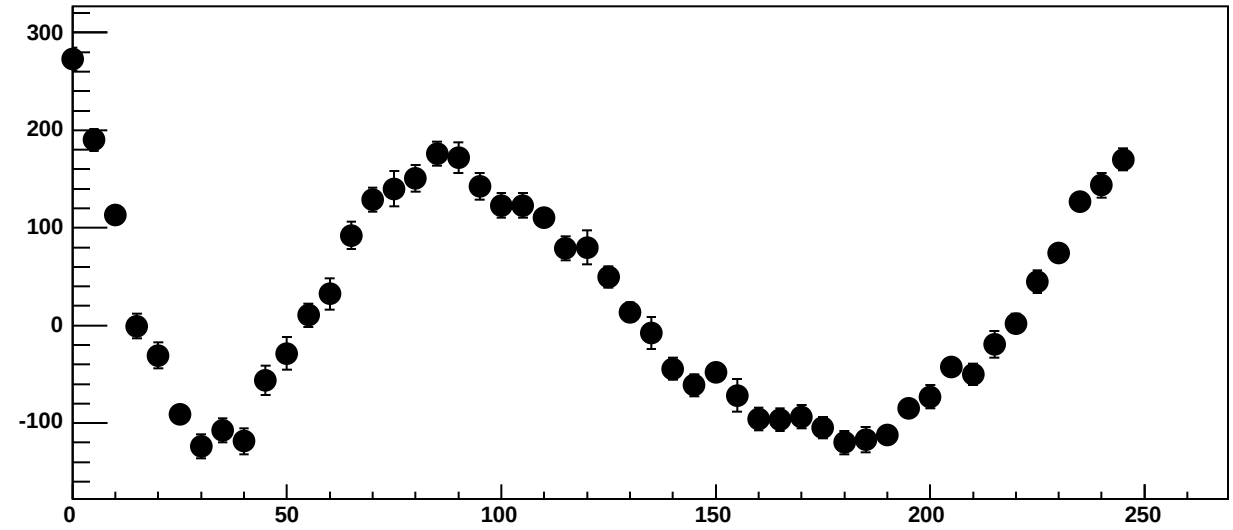
Chip 1, Channel 6, Enable 3, DAC=1600, ADC Mean vs Hold



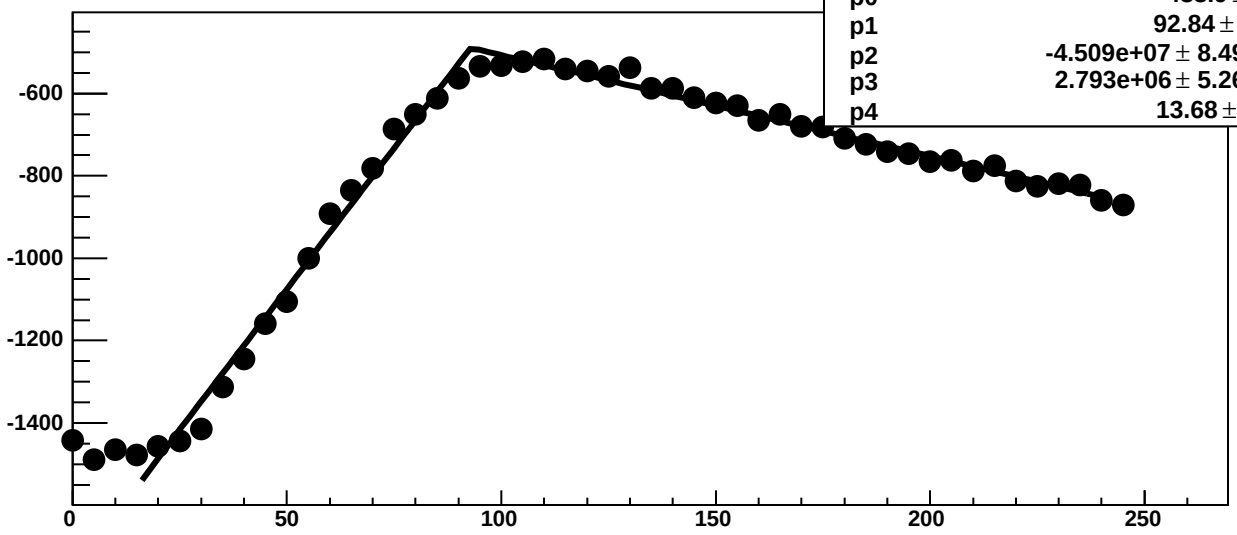
Chip 1, Channel 6, Enable 3, DAC=1600, ADC Noise vs Hold



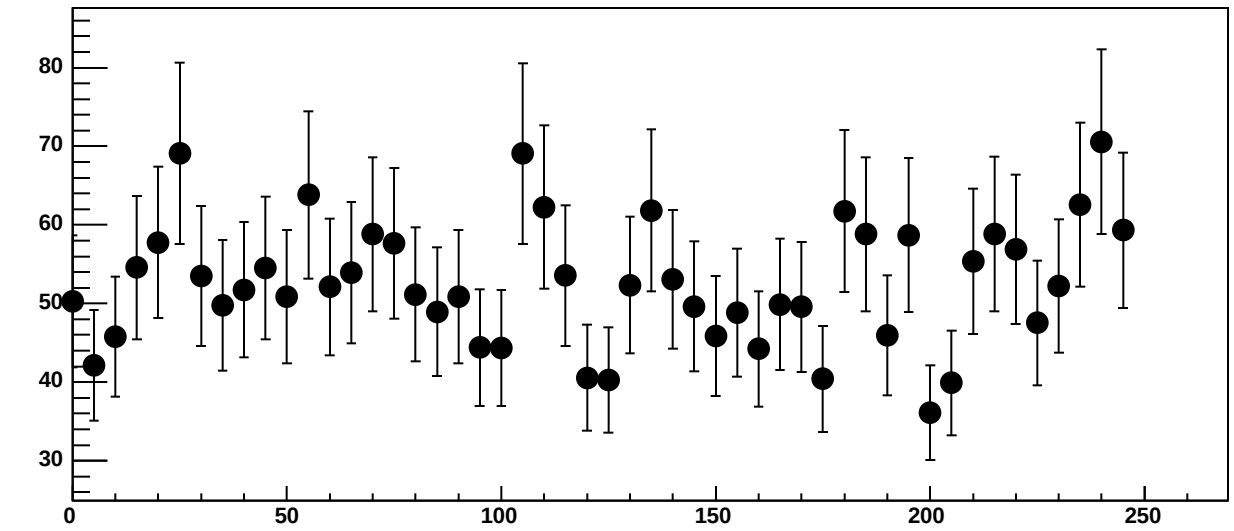
Chip 1, Channel 6, Enable 3, DAC=1600, ADC Residuals vs Hold



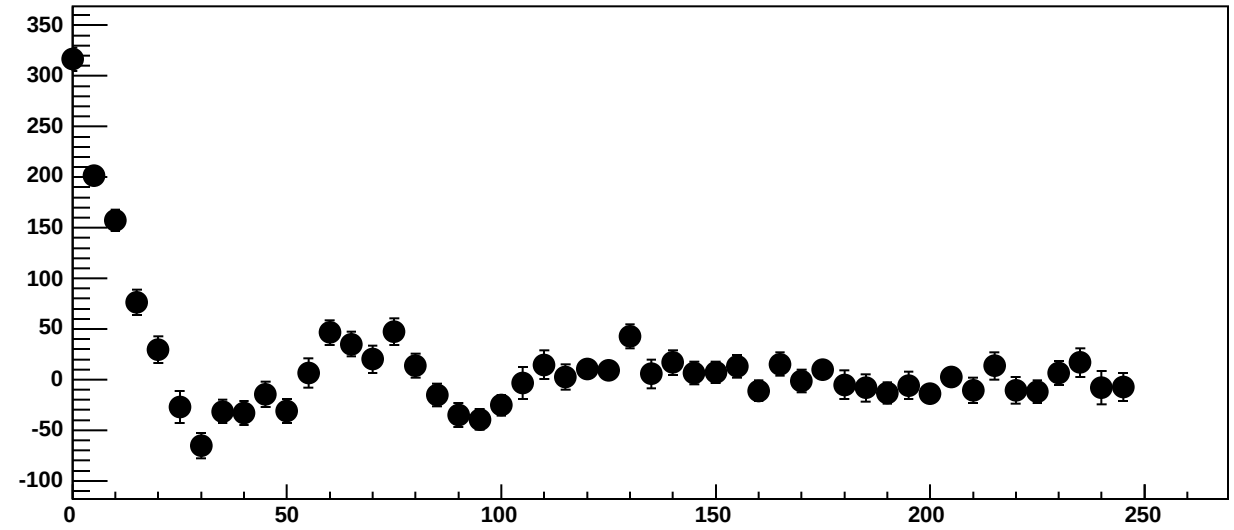
Chip 1, Channel 6, Enable 4, DAC=1600, ADC Mean vs Hold



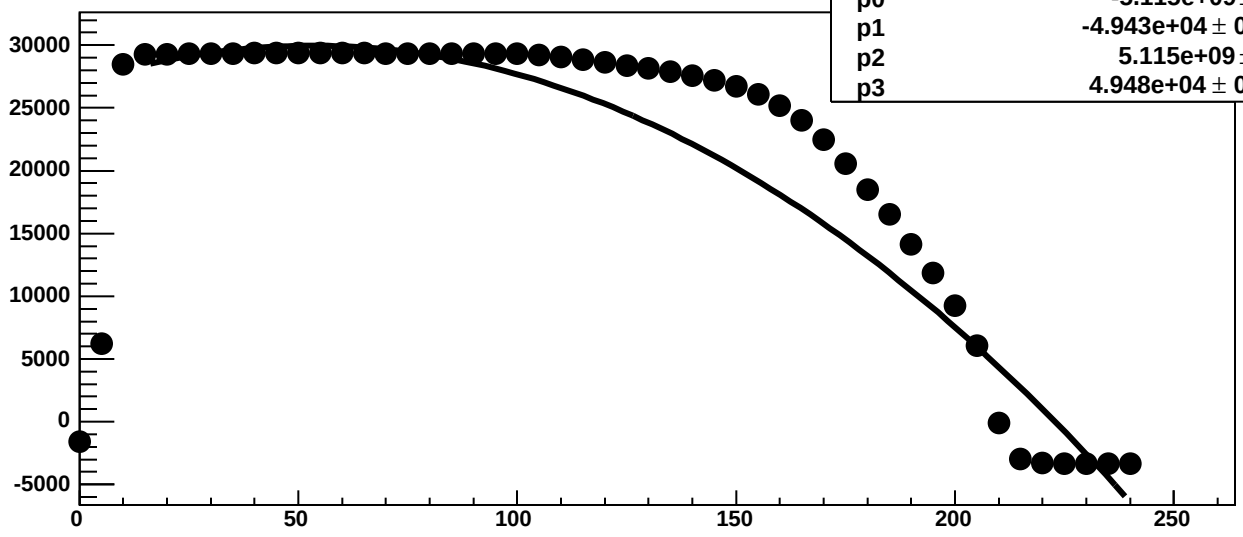
Chip 1, Channel 6, Enable 4, DAC=1600, ADC Noise vs Hold



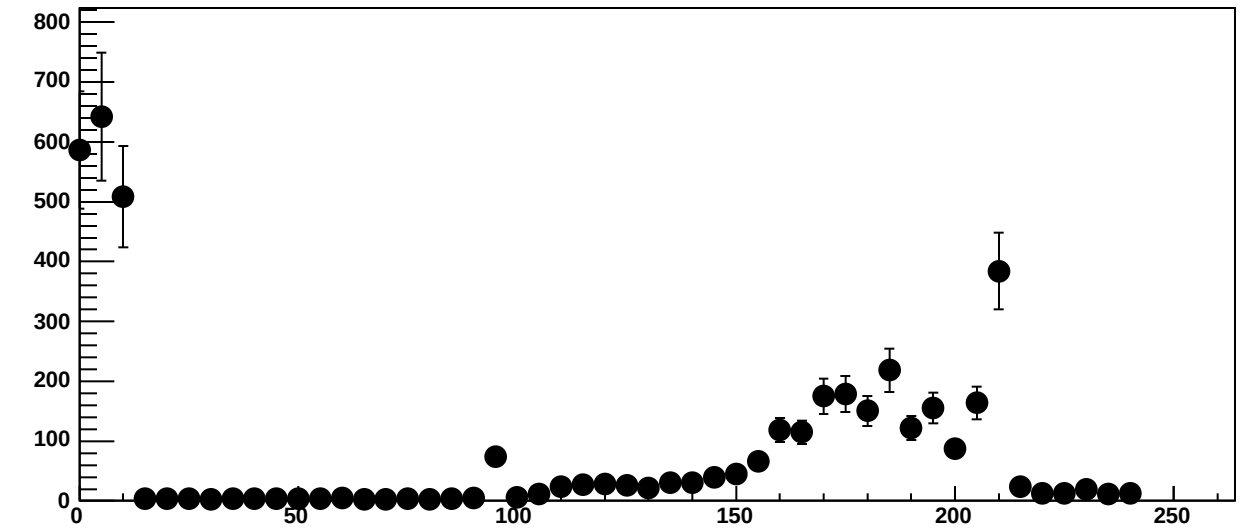
Chip 1, Channel 6, Enable 4, DAC=1600, ADC Residuals vs Hold



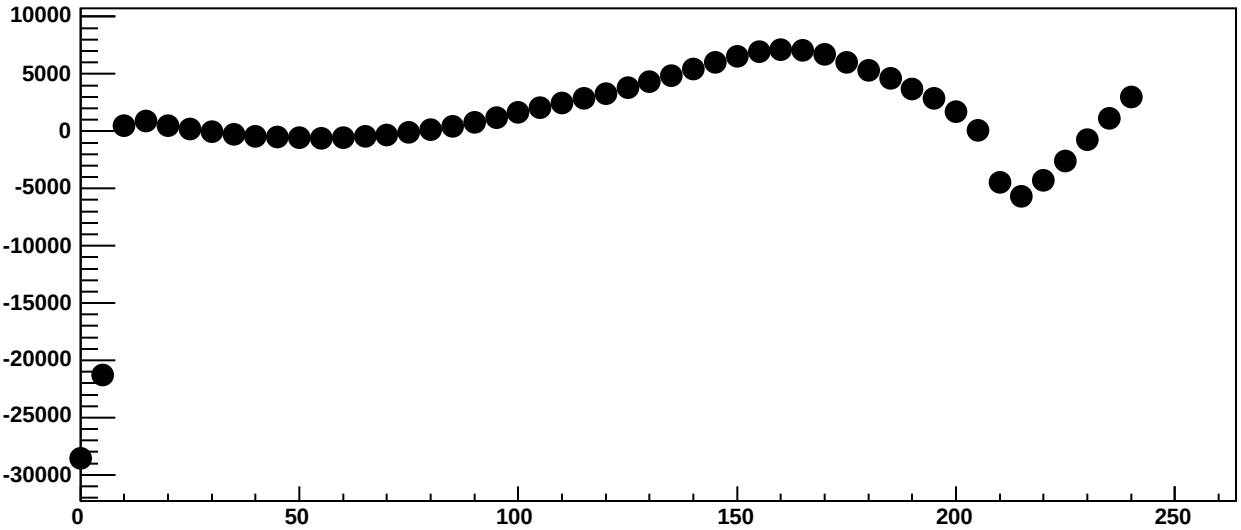
Chip 1, Channel 6, Enable 5!, DAC=1600, ADC Mean vs Hold



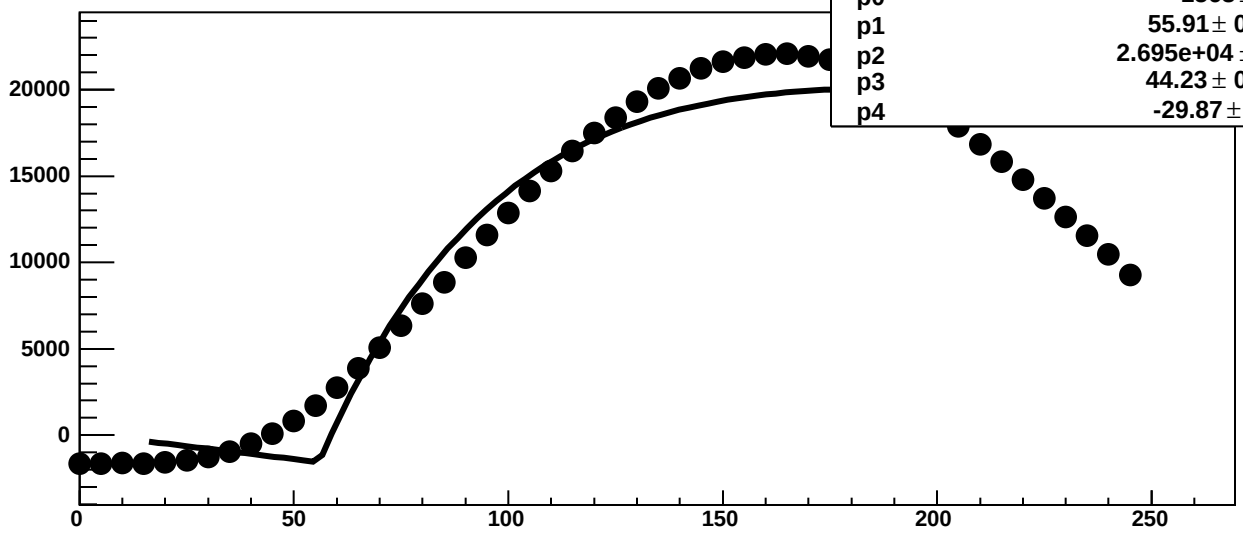
Chip 1, Channel 6, Enable 5!, DAC=1600, ADC Noise vs Hold



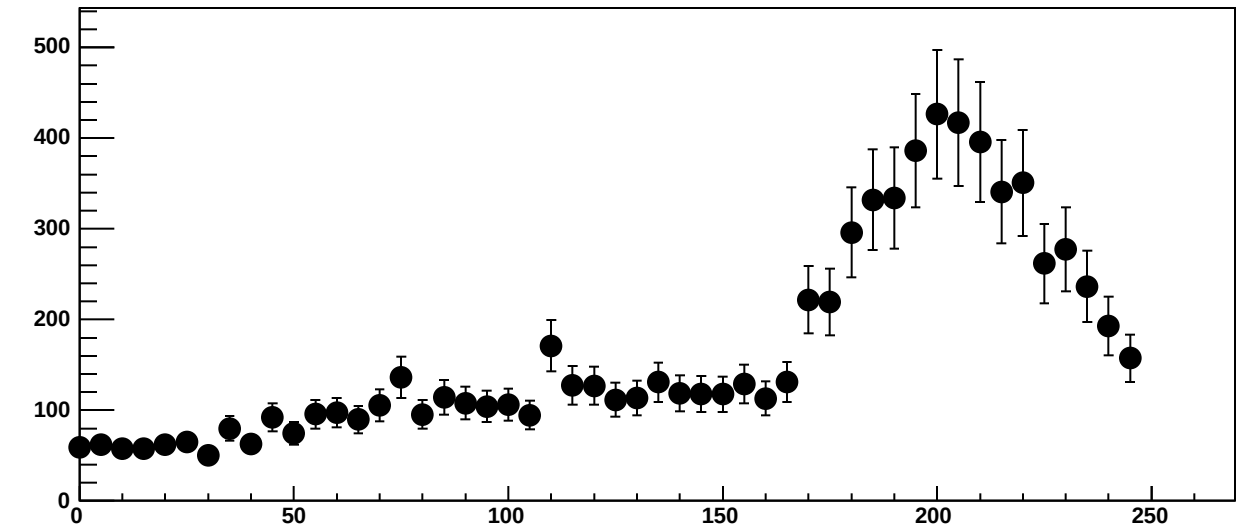
Chip 1, Channel 6, Enable 5!, DAC=1600, ADC Residuals vs Hold



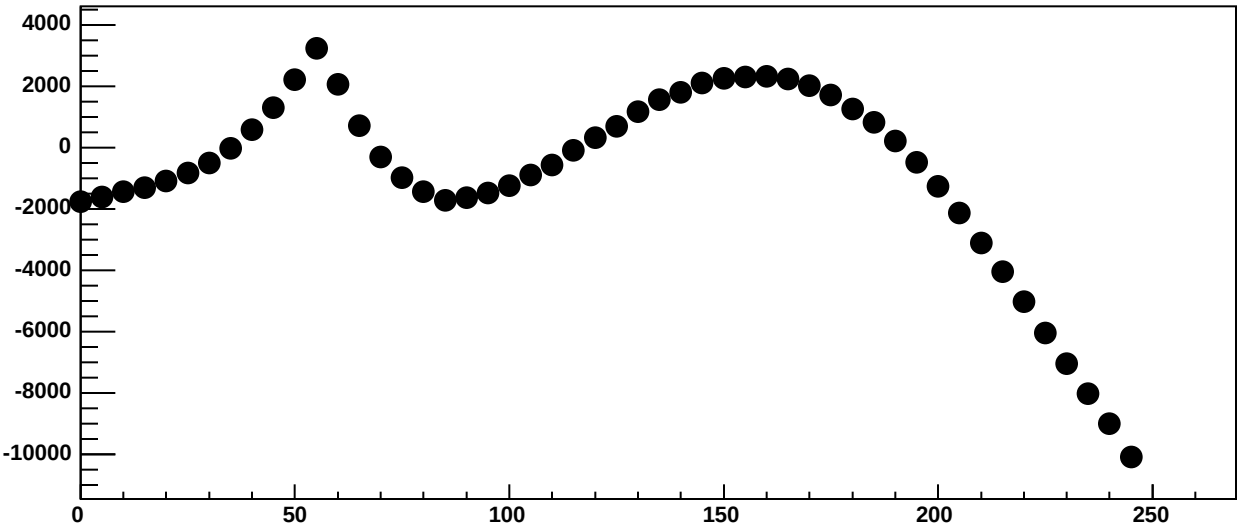
Chip 1, Channel 7, Enable 0, DAC=1600, ADC Mean vs Hold



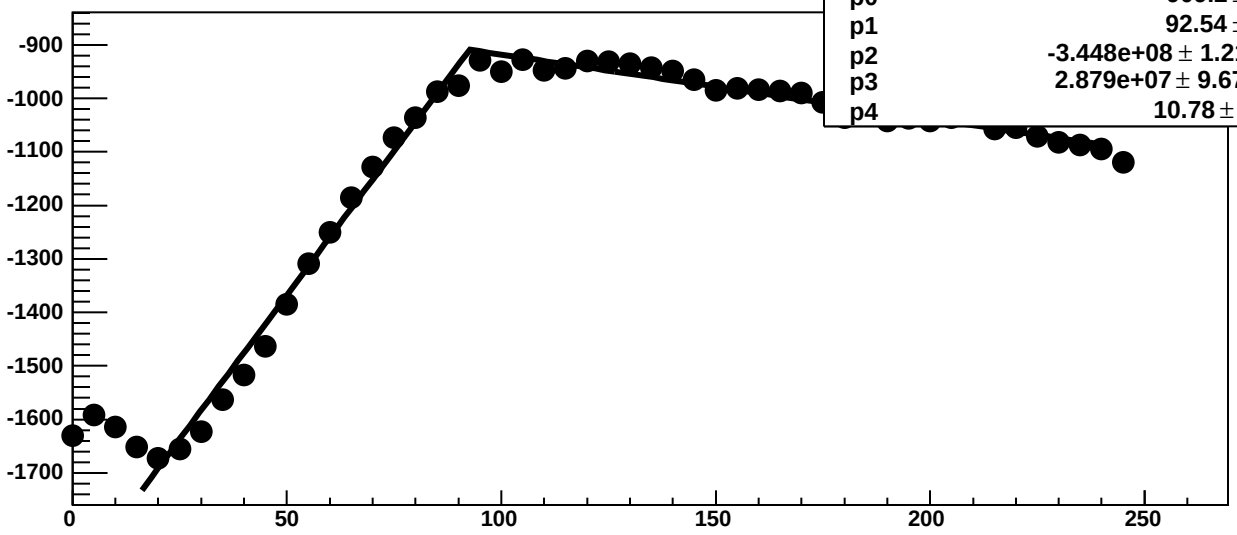
Chip 1, Channel 7, Enable 0, DAC=1600, ADC Noise vs Hold



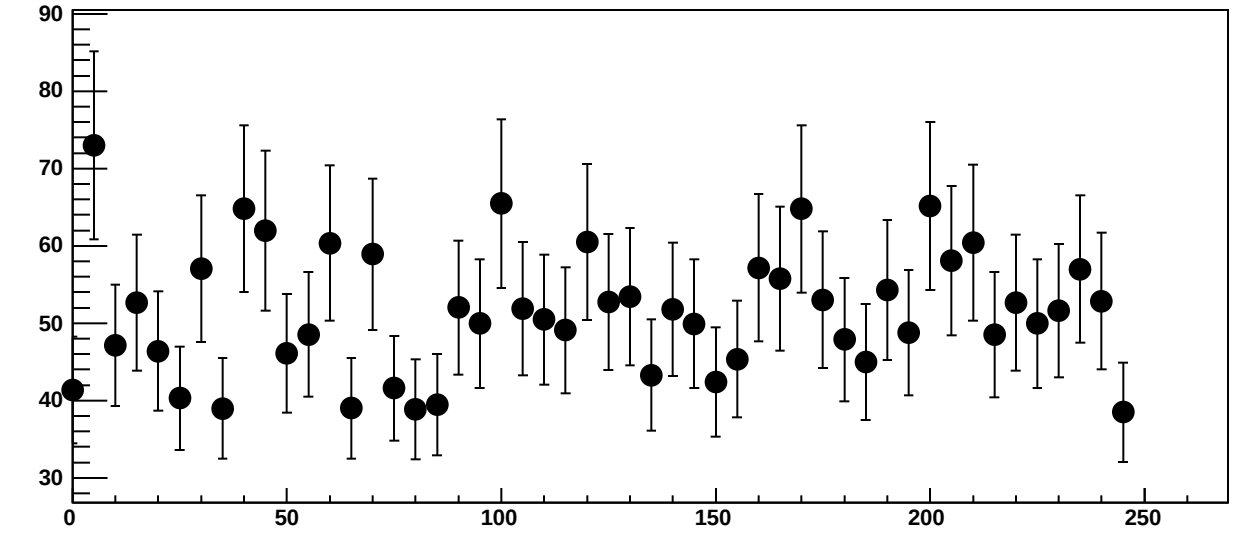
Chip 1, Channel 7, Enable 0, DAC=1600, ADC Residuals vs Hold



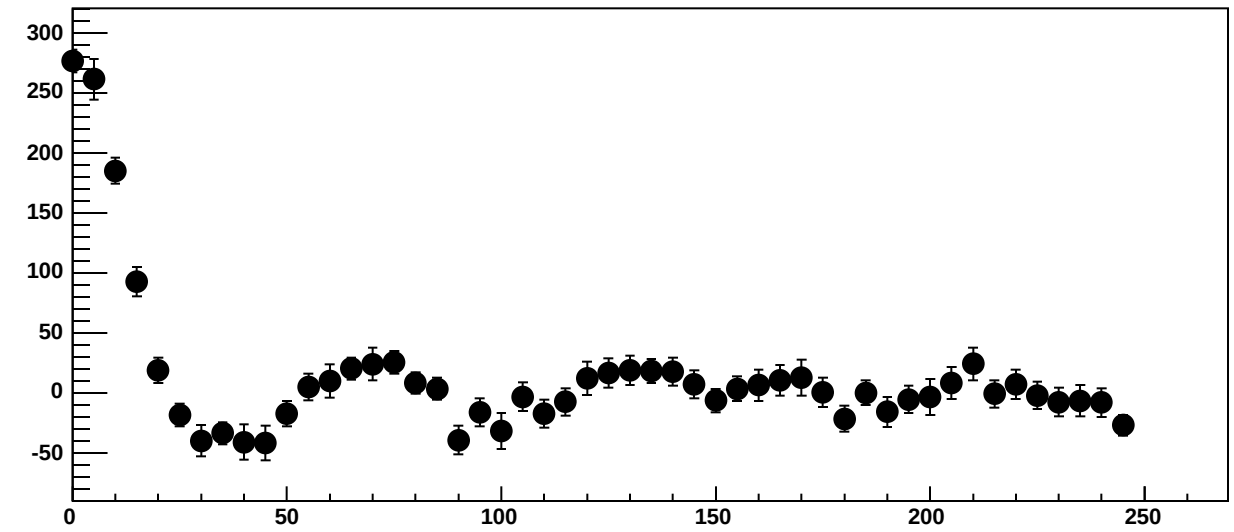
Chip 1, Channel 7, Enable 1, DAC=1600, ADC Mean vs Hold



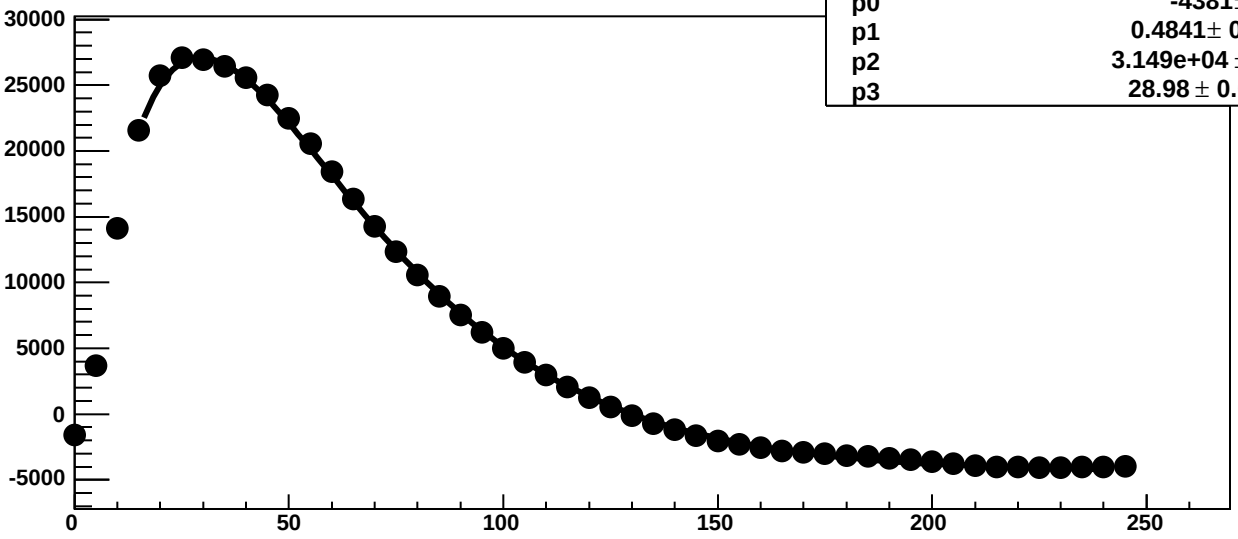
Chip 1, Channel 7, Enable 1, DAC=1600, ADC Noise vs Hold



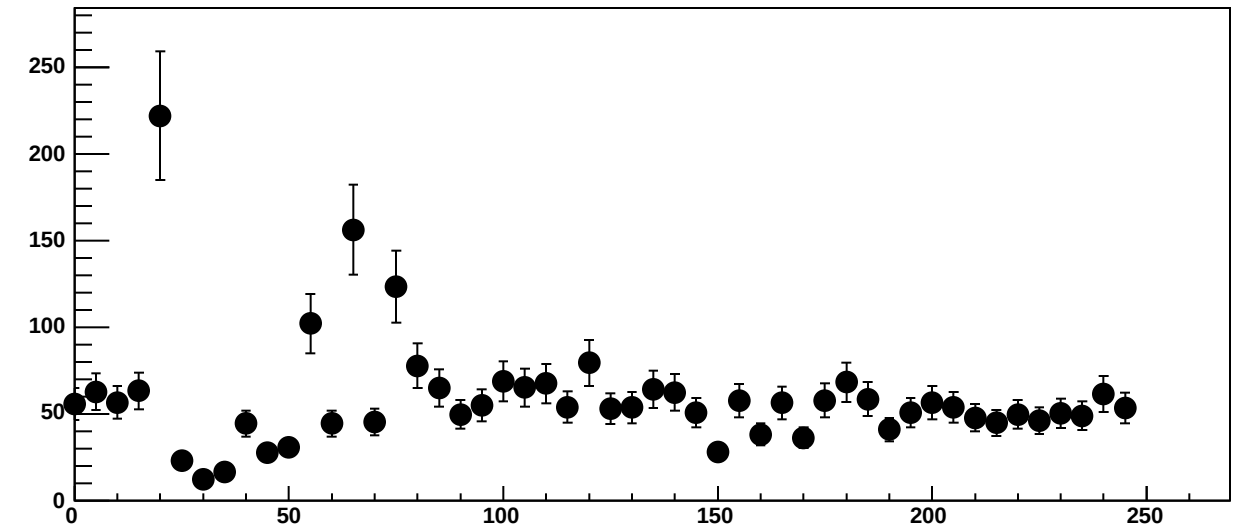
Chip 1, Channel 7, Enable 1, DAC=1600, ADC Residuals vs Hold



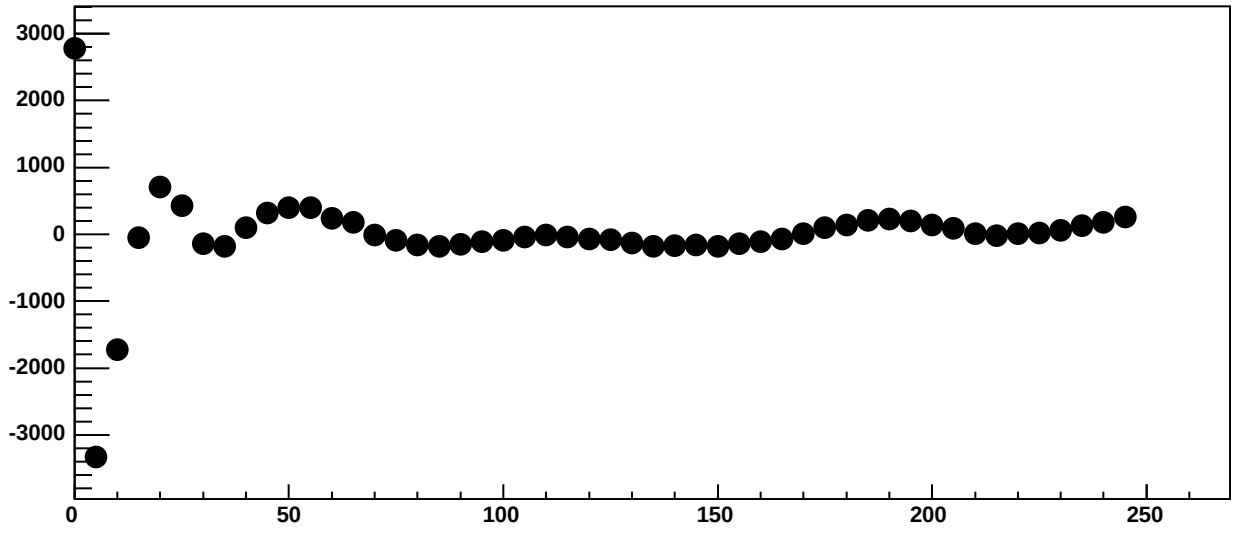
Chip 1, Channel 7, Enable 2!, DAC=1600, ADC Mean vs Hold

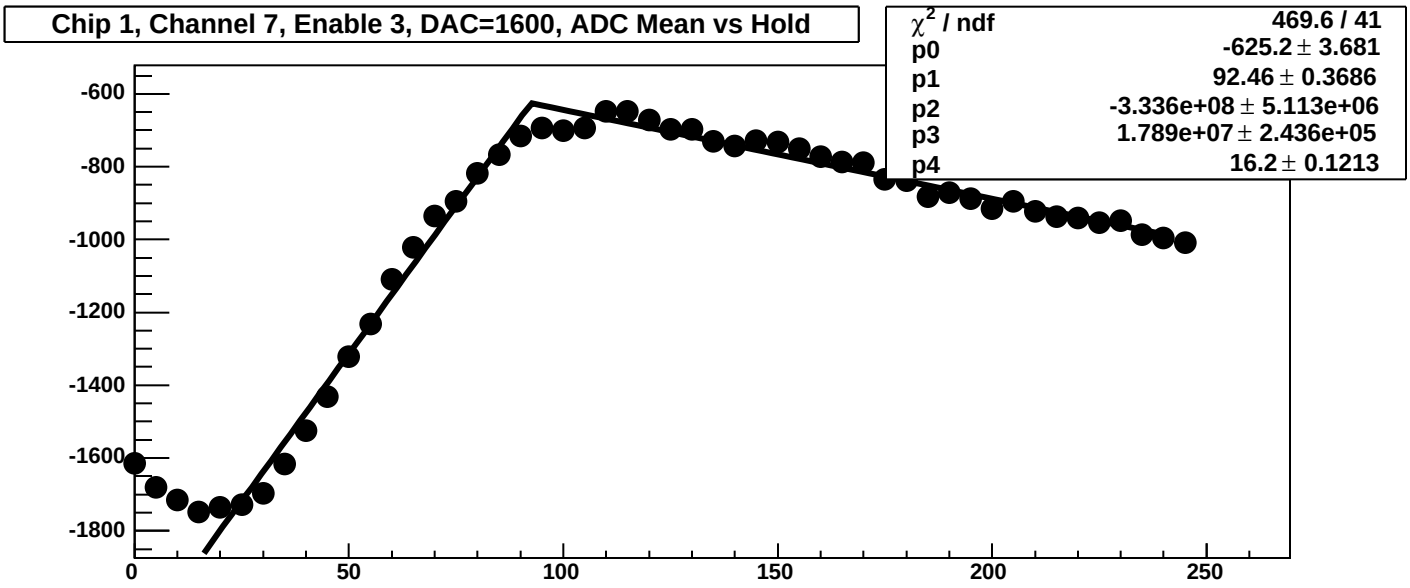


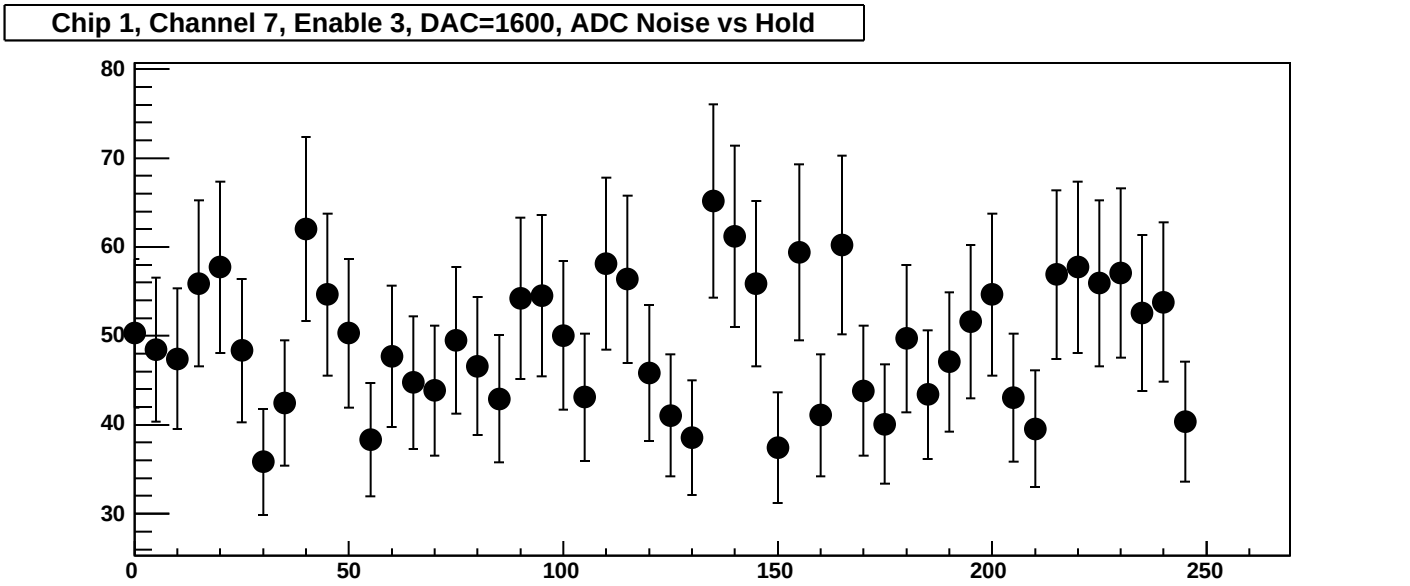
Chip 1, Channel 7, Enable 2!, DAC=1600, ADC Noise vs Hold

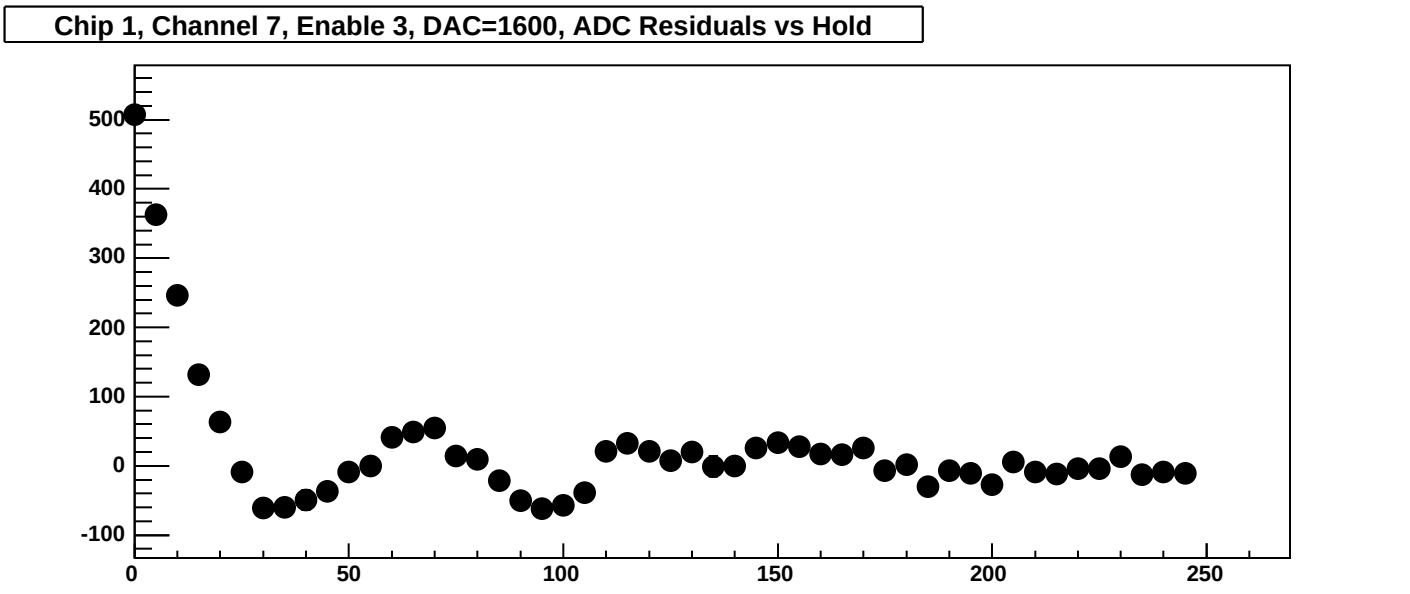


Chip 1, Channel 7, Enable 2!, DAC=1600, ADC Residuals vs Hold

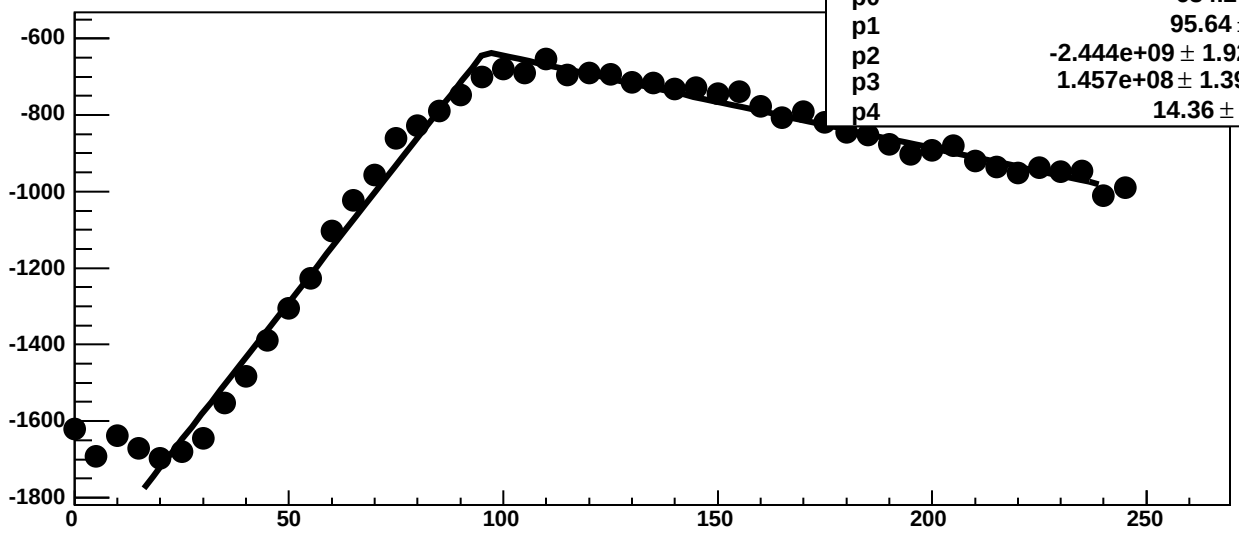



Chip 1, Channel 7, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 469.6 / 41
p0 -625.2 ± 3.681
p1 92.46 ± 0.3686
p2 -3.336e+08 ± 5.113e+06
p3 1.789e+07 ± 2.436e+05
p4 16.2 ± 0.1213
-600
-800
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250

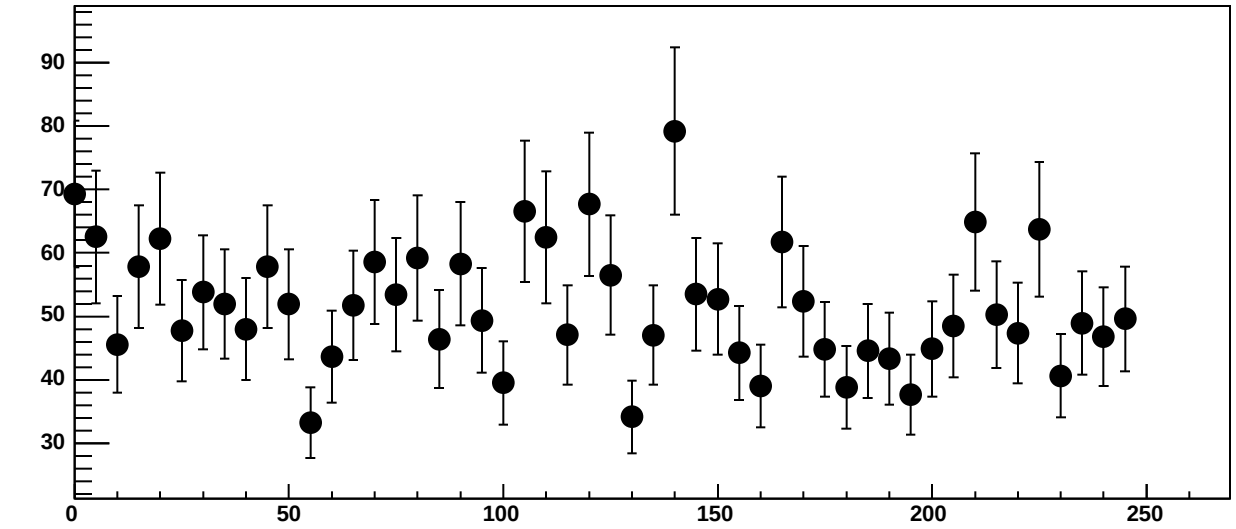

Chip 1, Channel 7, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 1, Channel 7, Enable 3, DAC=1600, ADC Residuals vs Hold
500
400
300
200
100
0
-100
0
50
100
150
200
250

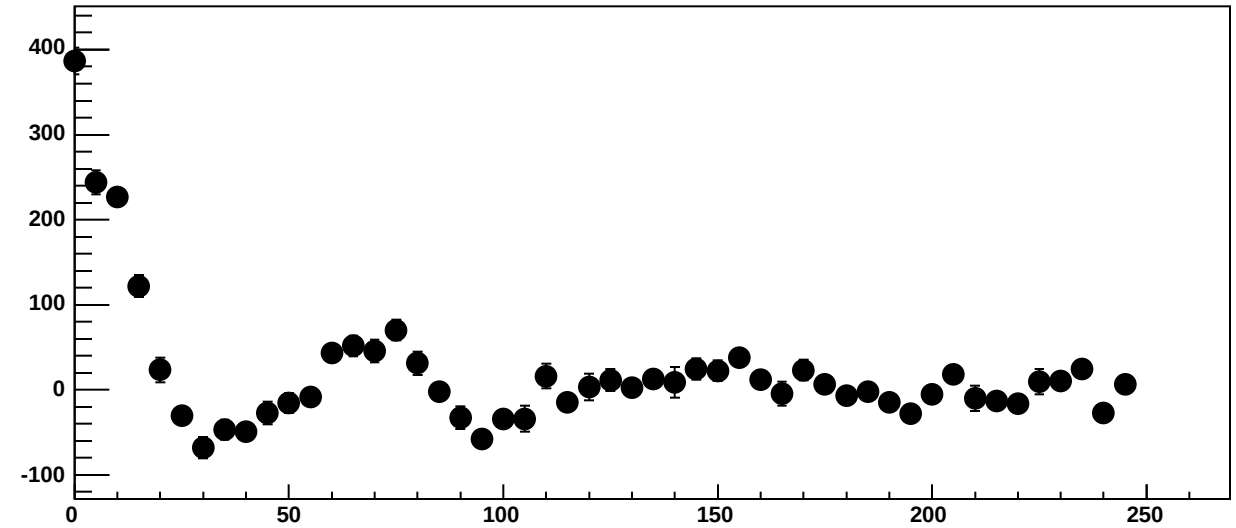
Chip 1, Channel 7, Enable 4, DAC=1600, ADC Mean vs Hold

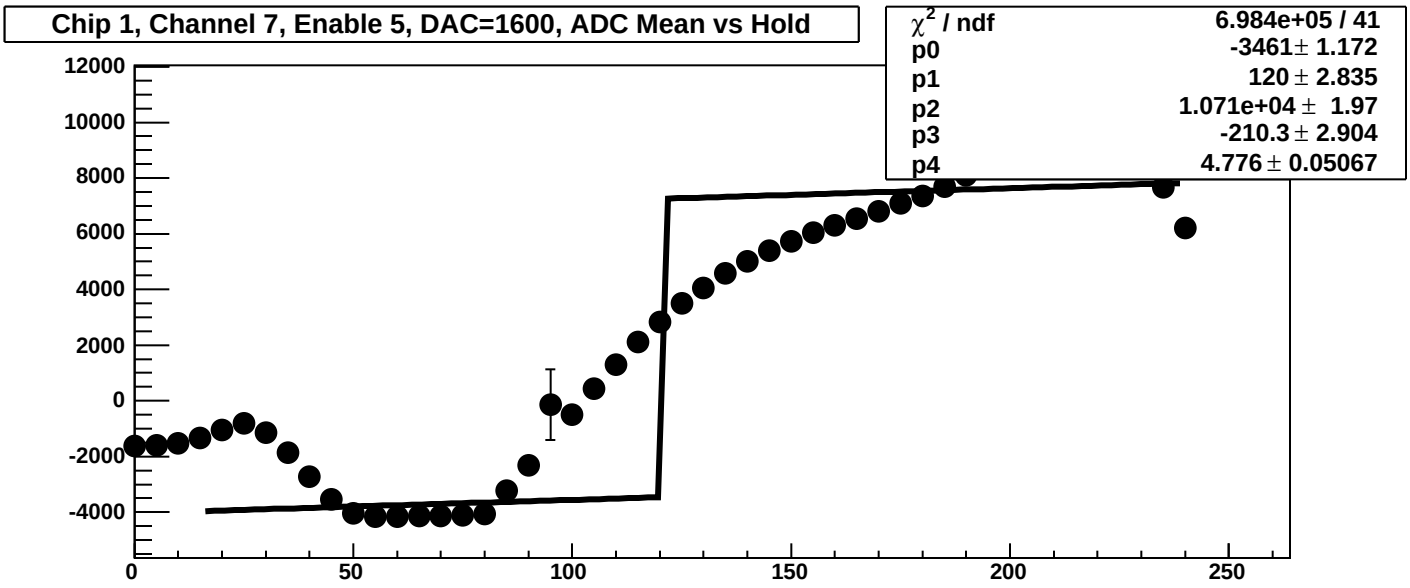


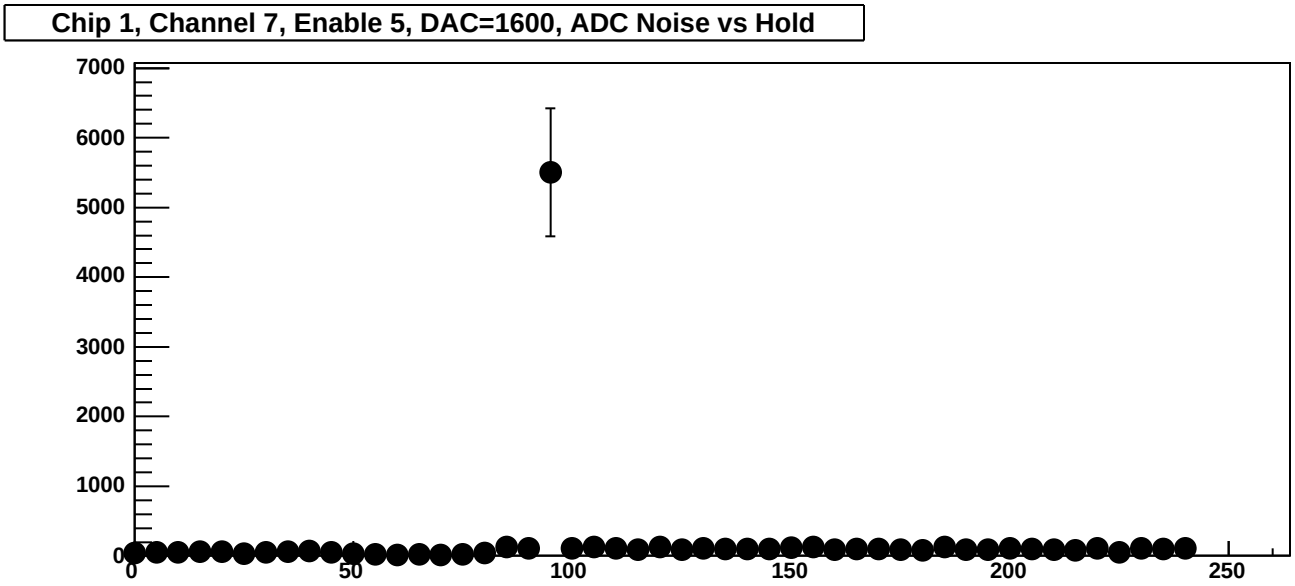
Chip 1, Channel 7, Enable 4, DAC=1600, ADC Noise vs Hold

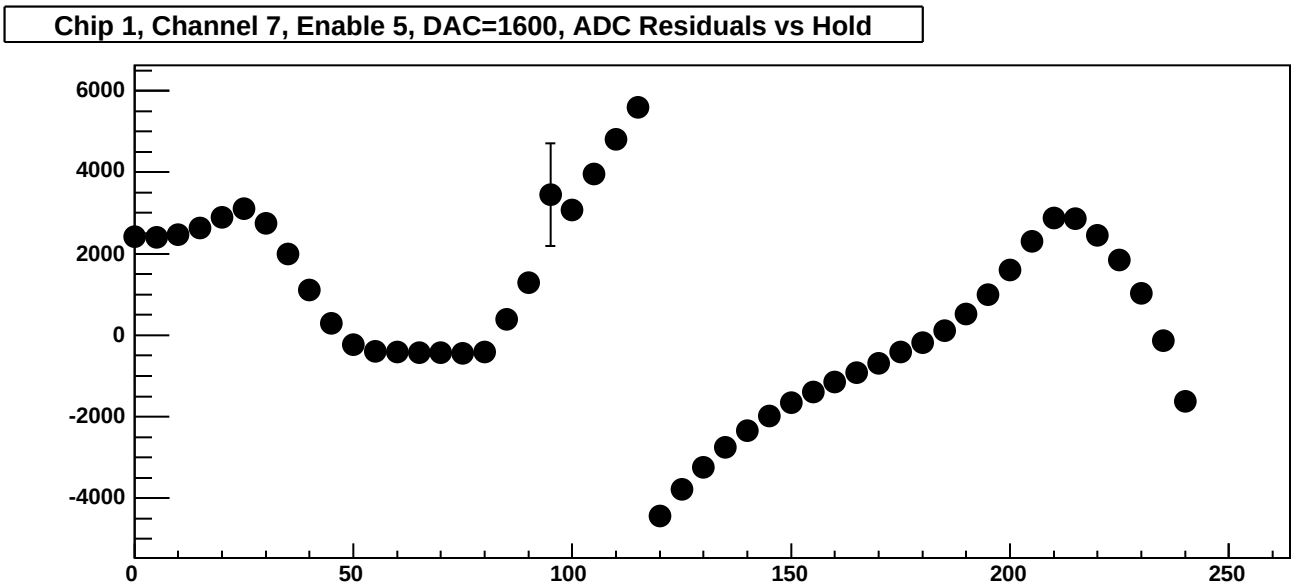


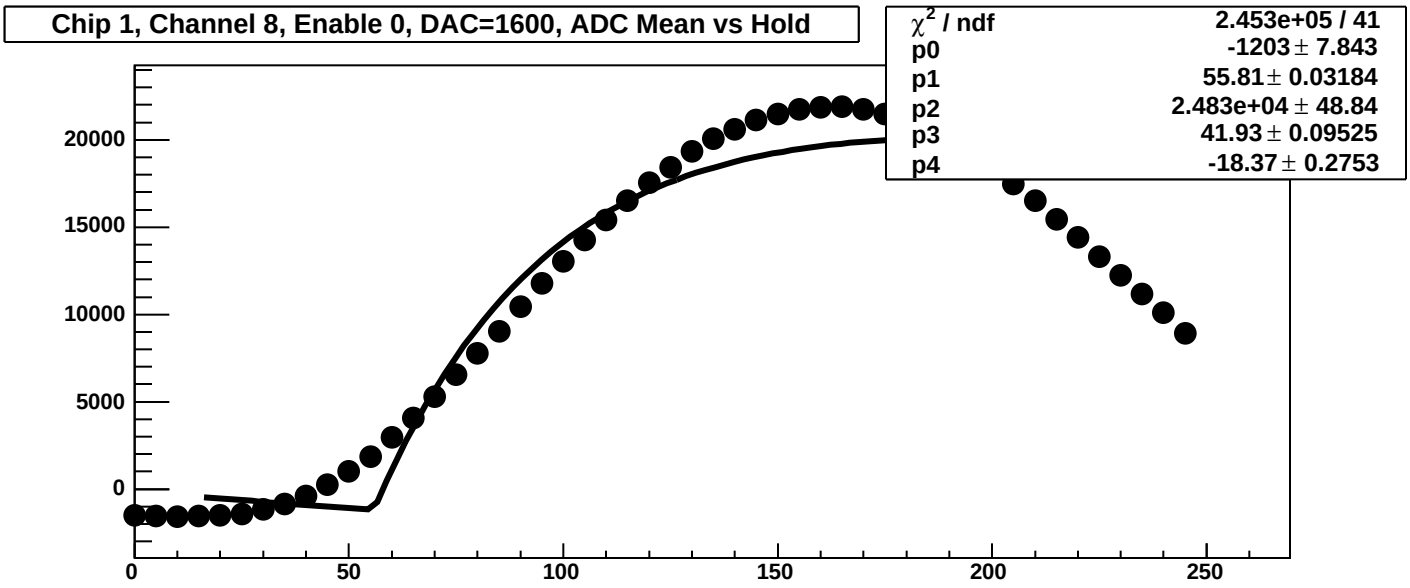
Chip 1, Channel 7, Enable 4, DAC=1600, ADC Residuals vs Hold

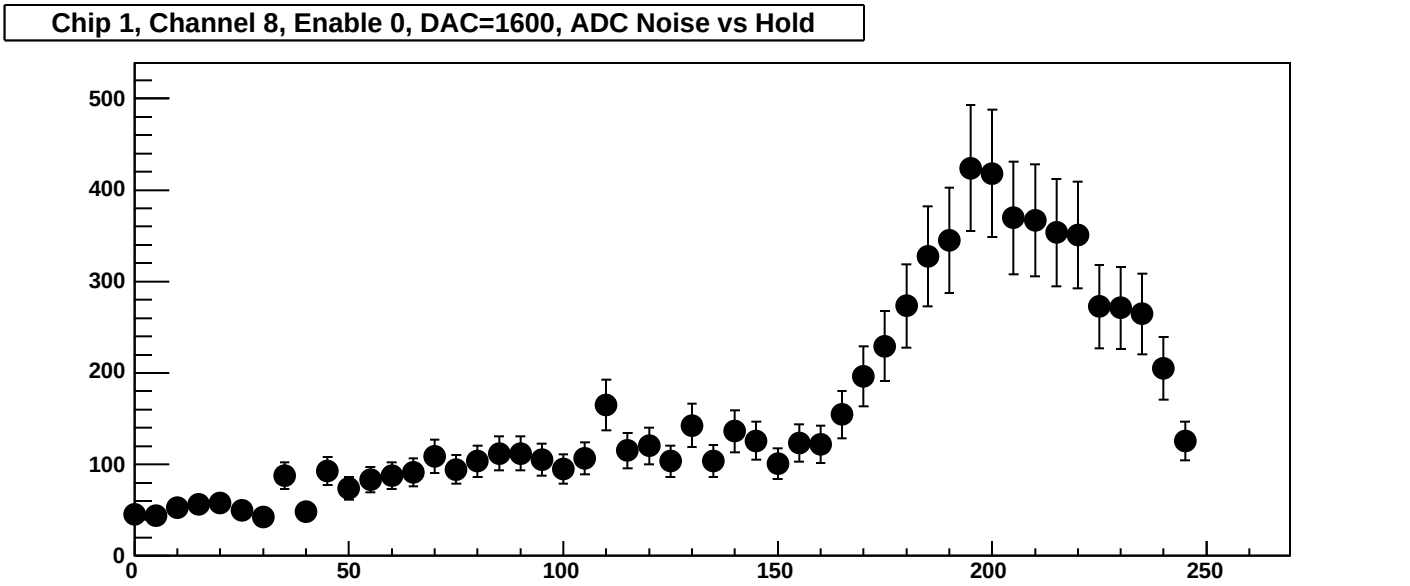


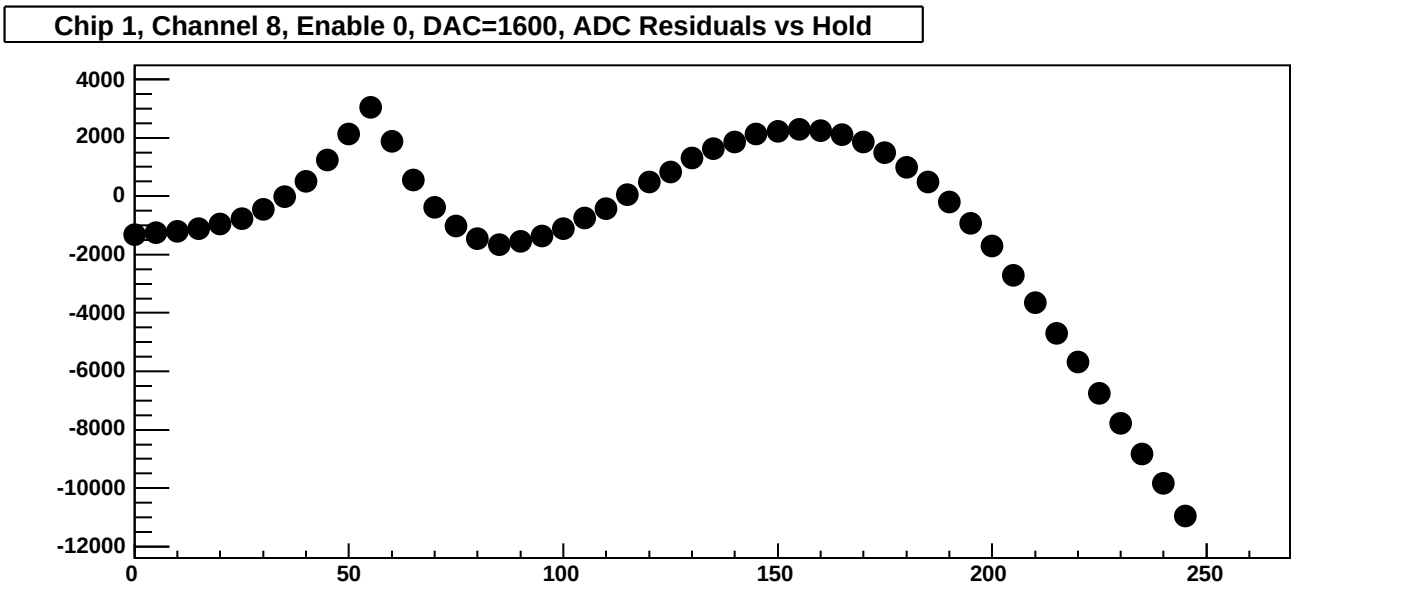

Chip 1, Channel 7, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 6.984e+05 / 41
p0 -3461 ± 1.172
p1 120 ± 2.835
p2 1.071e+04 ± 1.97
p3 -210.3 ± 2.904
p4 4.776 ± 0.05067
12000
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250


Chip 1, Channel 7, Enable 5, DAC=1600, ADC Noise vs Hold
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

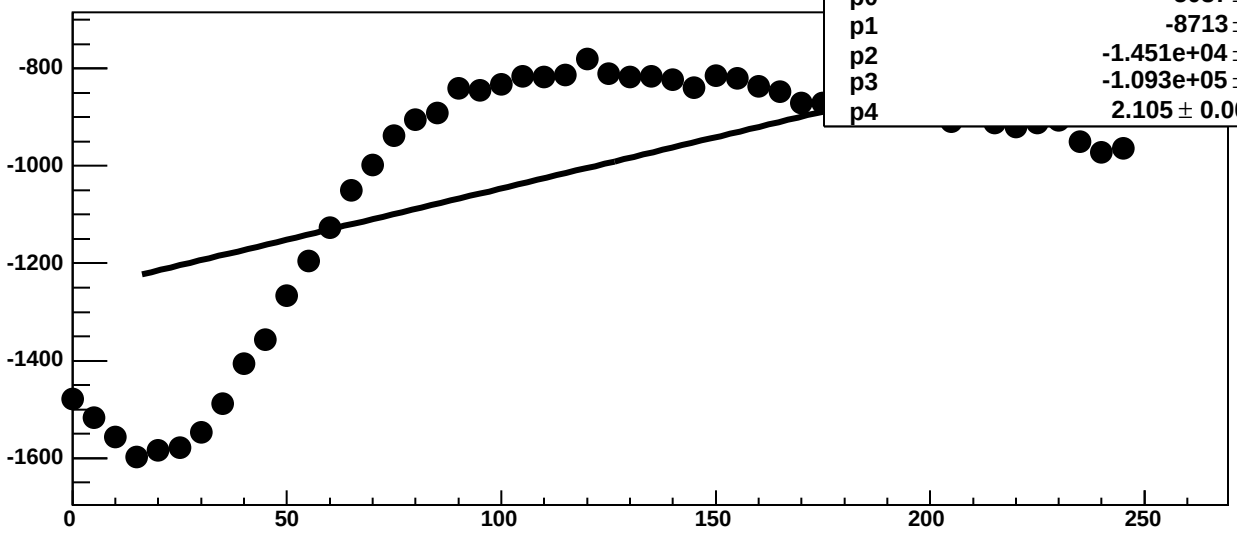

Chip 1, Channel 7, Enable 5, DAC=1600, ADC Residuals vs Hold
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250


Chip 1, Channel 8, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.453e+05 / 41
p0 -1203 ± 7.843
p1 55.81 ± 0.03184
p2 2.483e+04 ± 48.84
p3 41.93 ± 0.09525
p4 -18.37 ± 0.2753
20000
15000
10000
5000
0
0
50
100
150
200
250

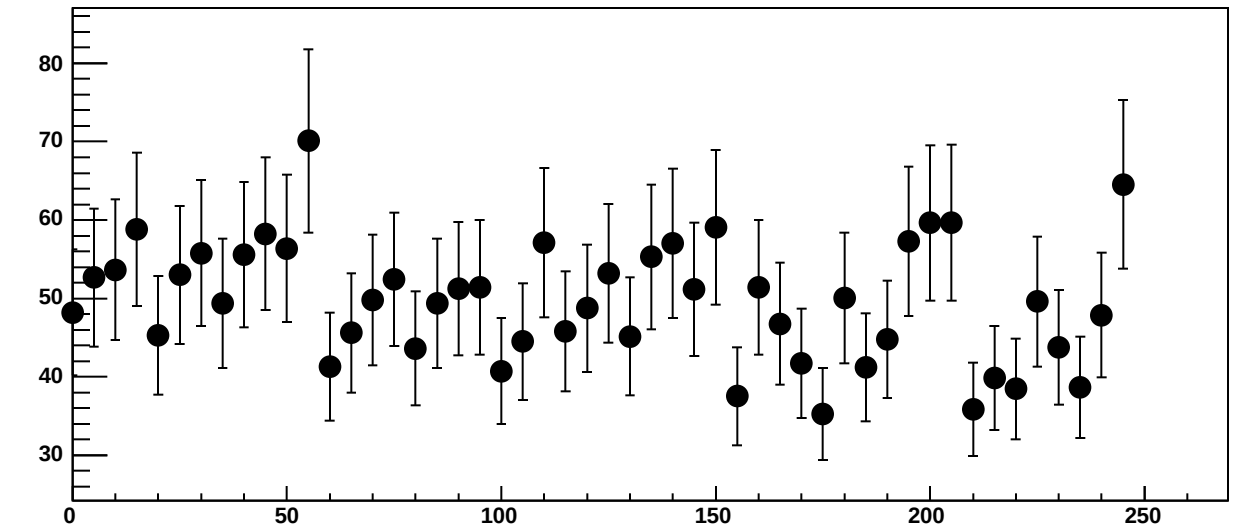

Chip 1, Channel 8, Enable 0, DAC=1600, ADC Noise vs Hold
500
400
300
200
100
0
0
50
100
150
200
250


Chip 1, Channel 8, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
0
50
100
150
200
250

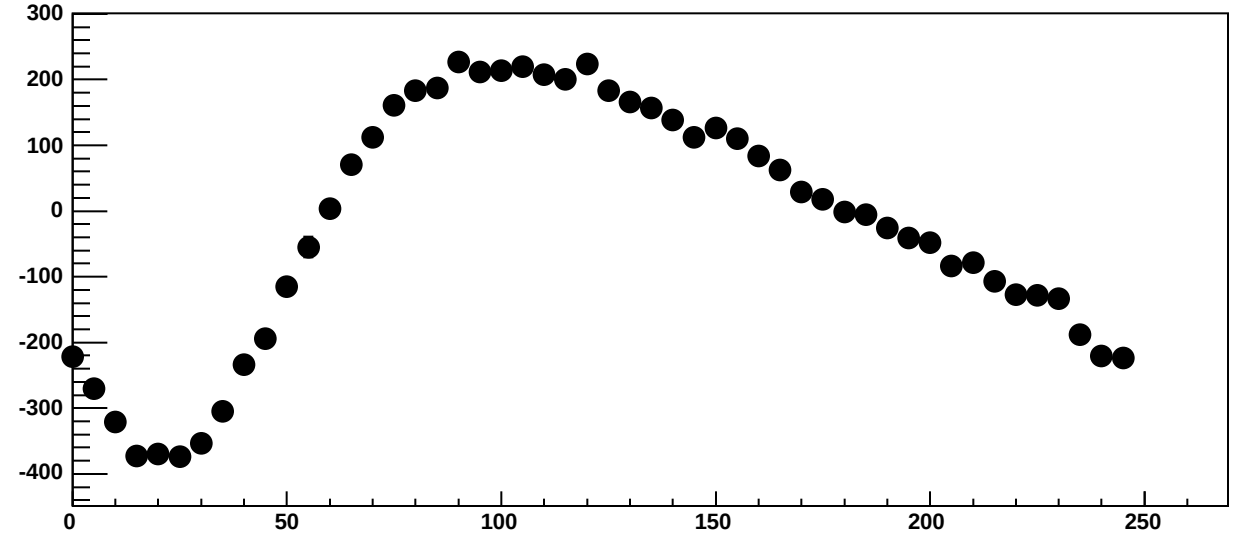
Chip 1, Channel 8, Enable 1, DAC=1600, ADC Mean vs Hold



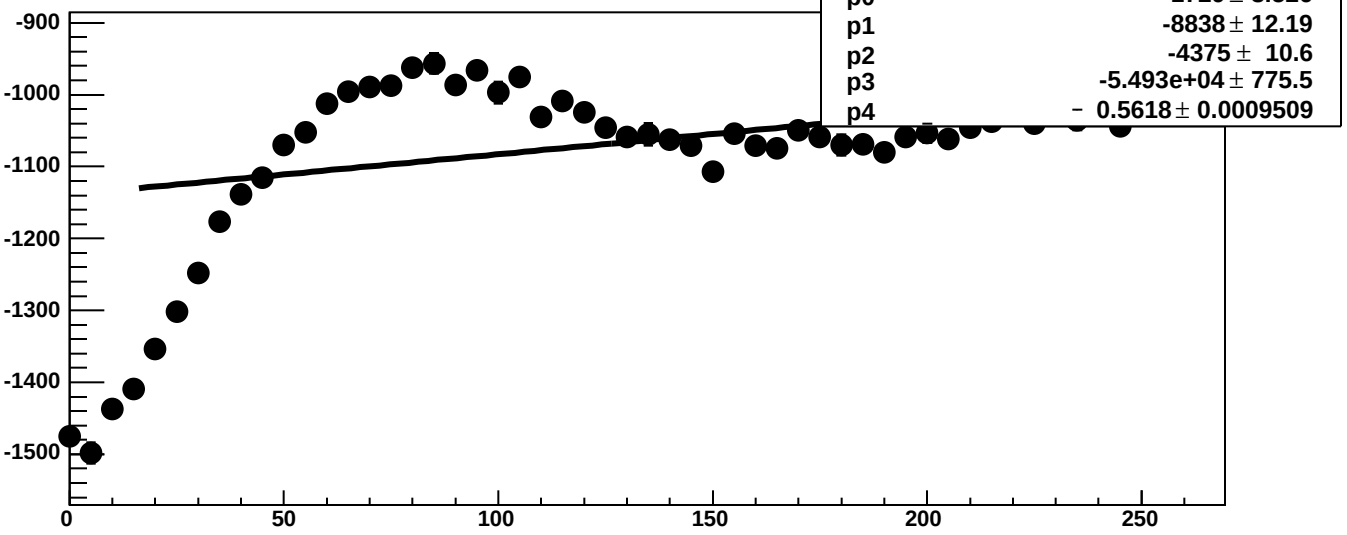
Chip 1, Channel 8, Enable 1, DAC=1600, ADC Noise vs Hold



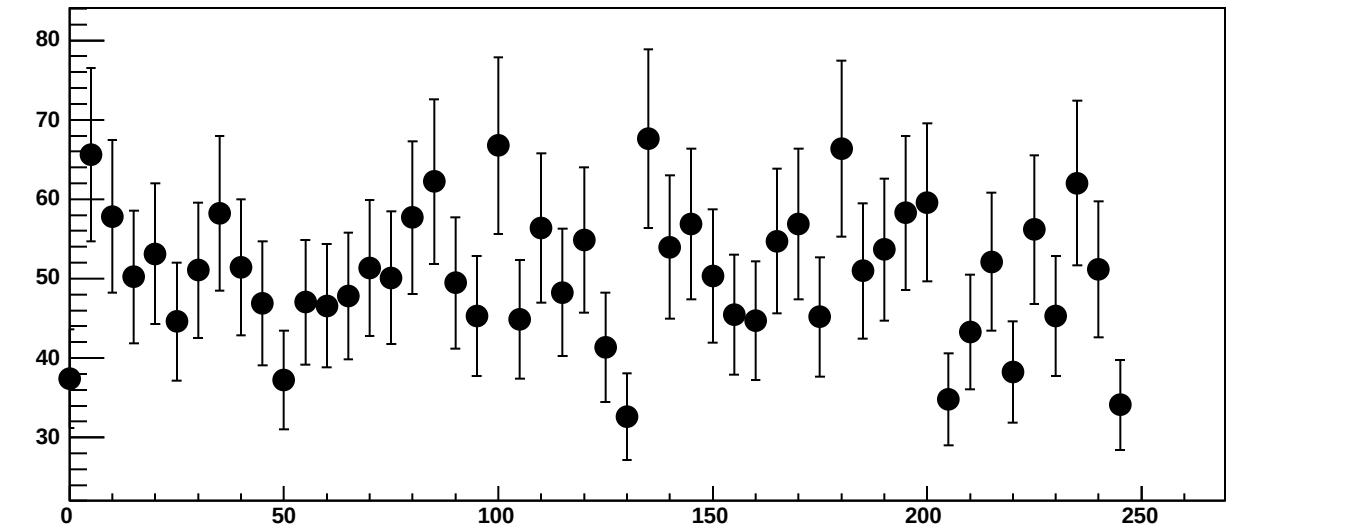
Chip 1, Channel 8, Enable 1, DAC=1600, ADC Residuals vs Hold



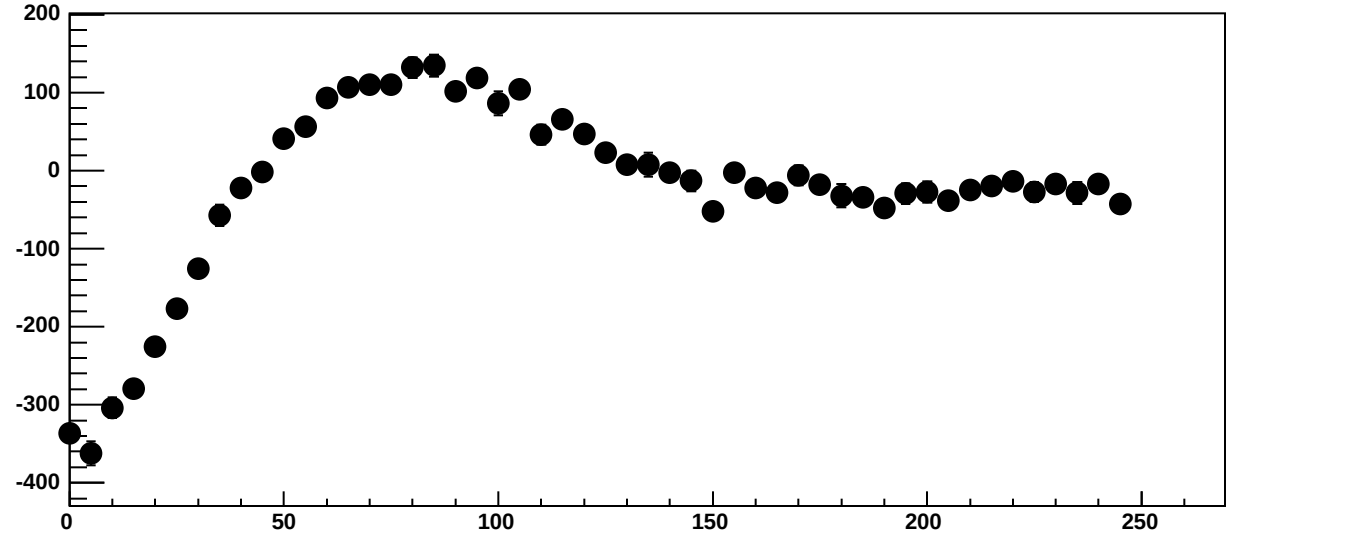
Chip 1, Channel 8, Enable 2, DAC=1600, ADC Mean vs Hold



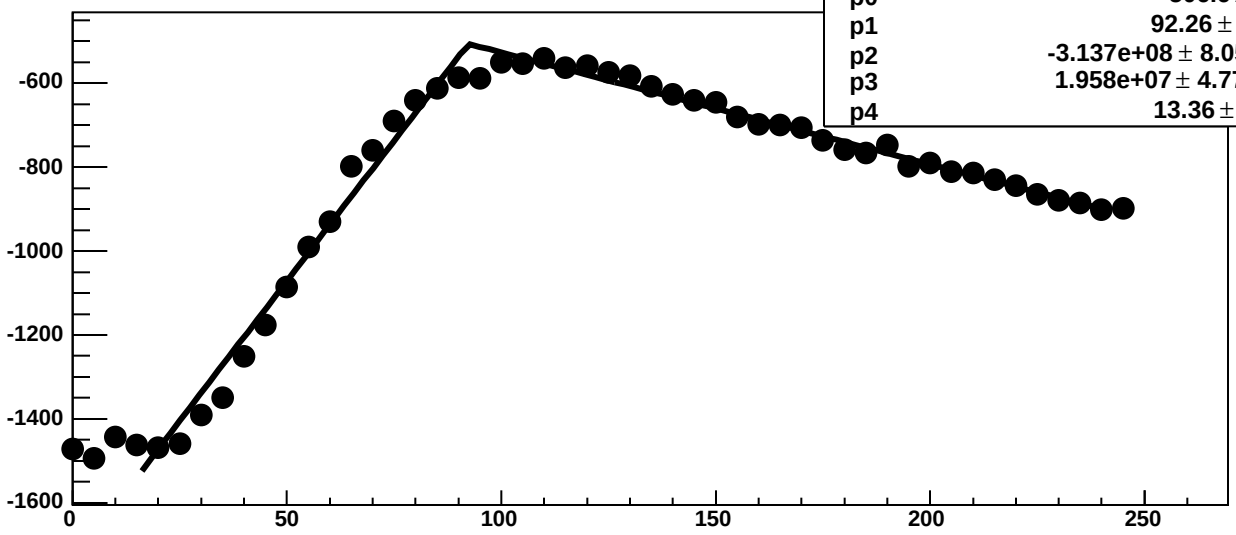
Chip 1, Channel 8, Enable 2, DAC=1600, ADC Noise vs Hold



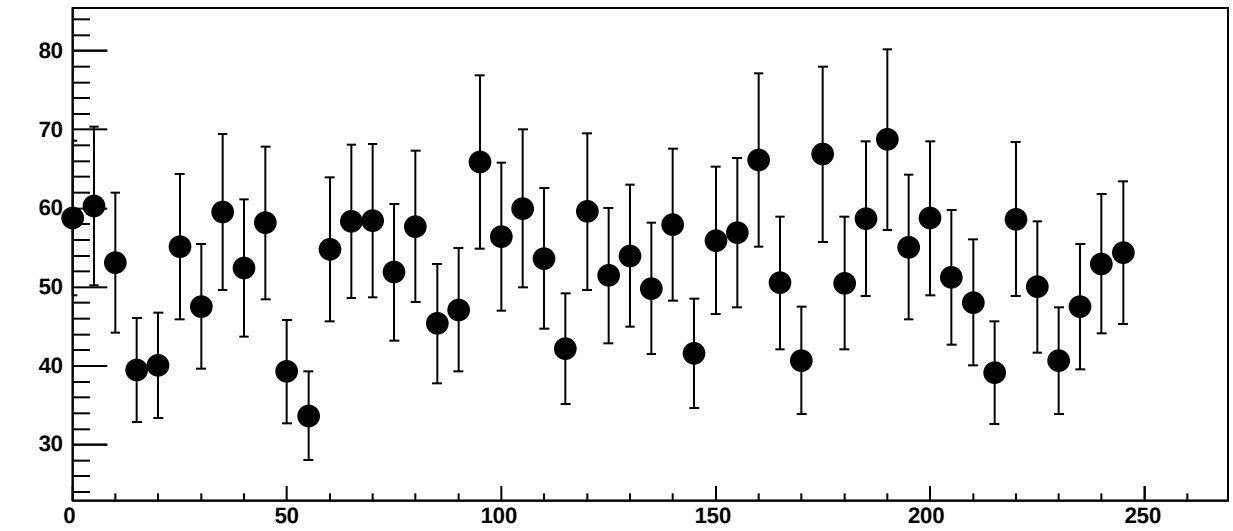
Chip 1, Channel 8, Enable 2, DAC=1600, ADC Residuals vs Hold



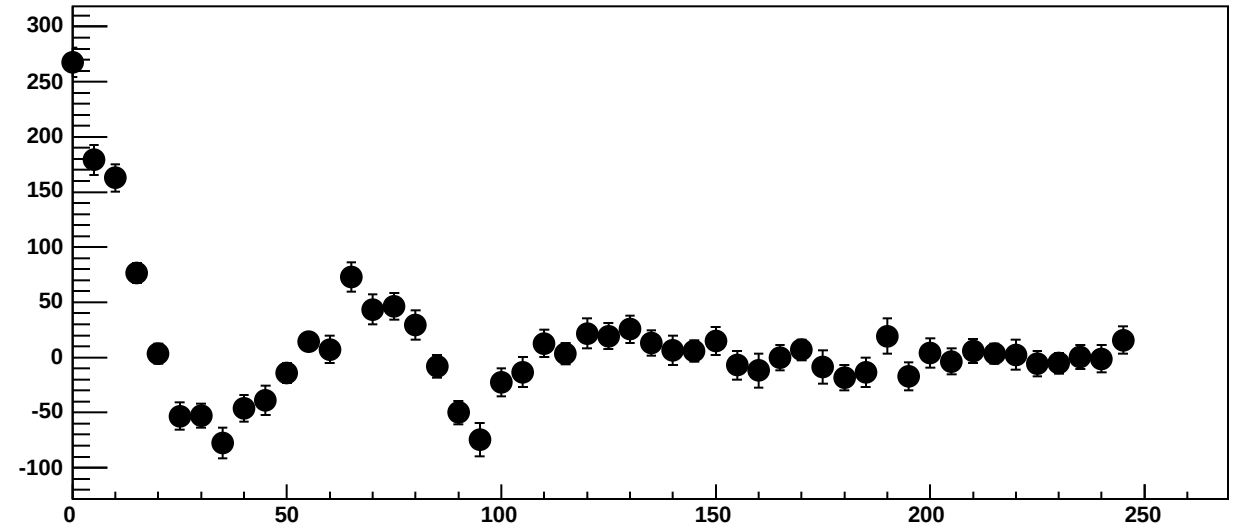
Chip 1, Channel 8, Enable 3, DAC=1600, ADC Mean vs Hold



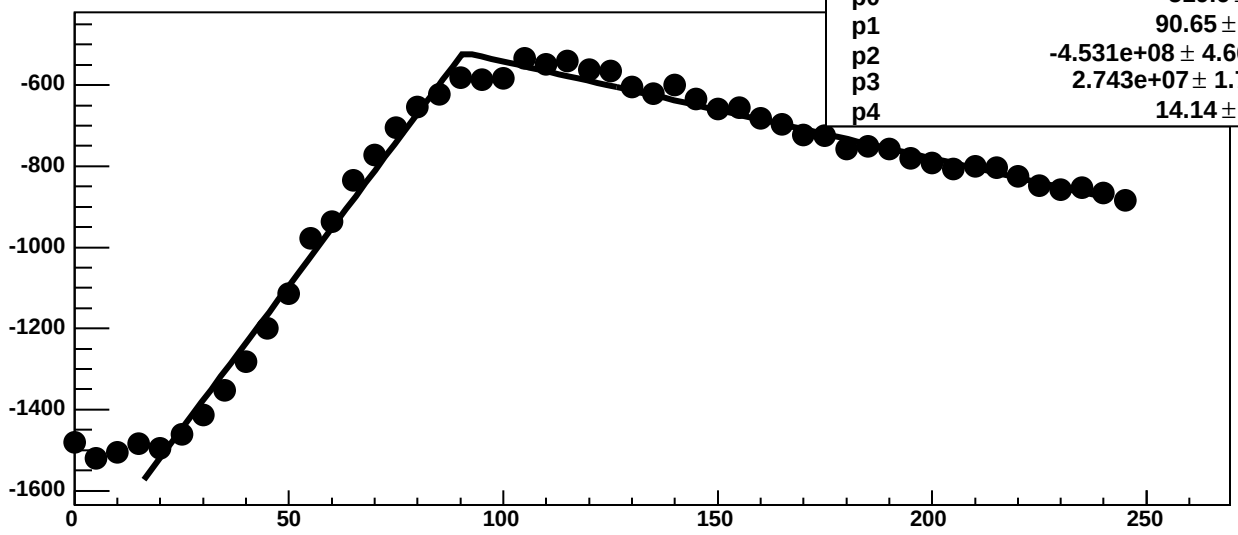
Chip 1, Channel 8, Enable 3, DAC=1600, ADC Noise vs Hold



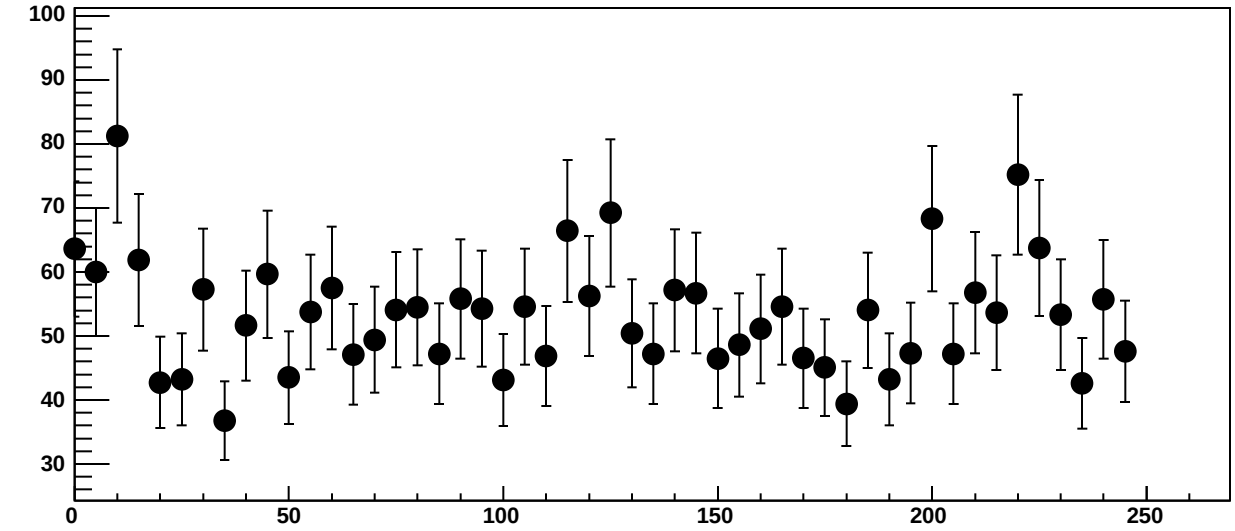
Chip 1, Channel 8, Enable 3, DAC=1600, ADC Residuals vs Hold



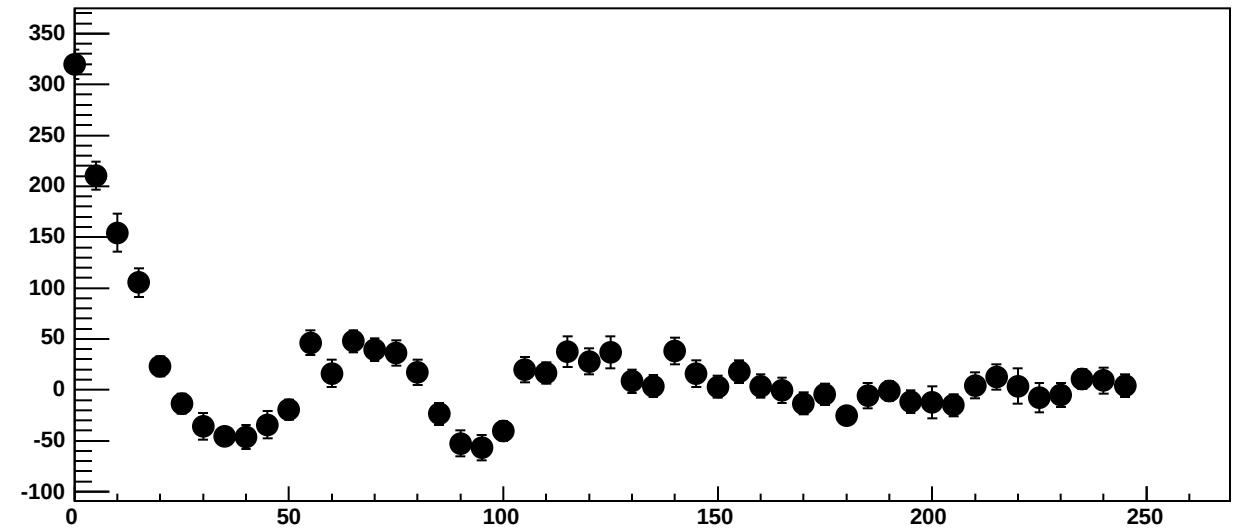
Chip 1, Channel 8, Enable 4, DAC=1600, ADC Mean vs Hold



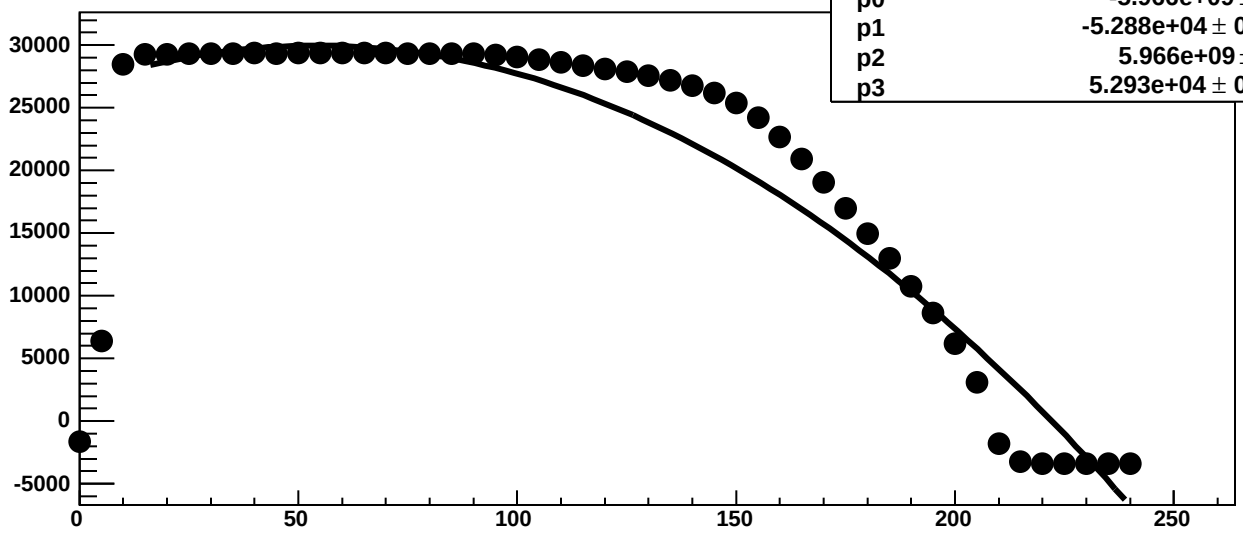
Chip 1, Channel 8, Enable 4, DAC=1600, ADC Noise vs Hold



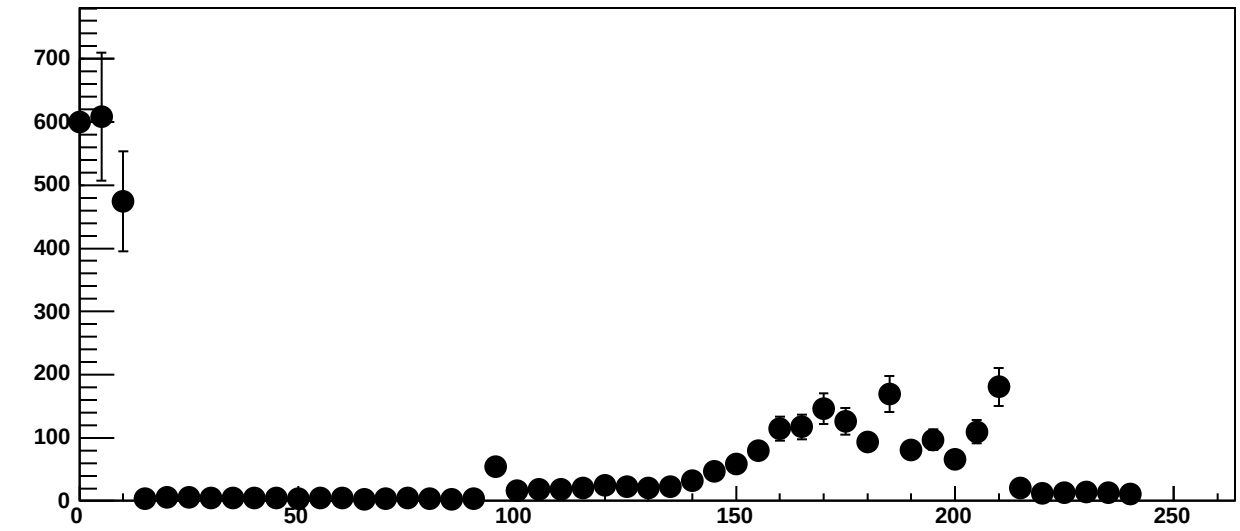
Chip 1, Channel 8, Enable 4, DAC=1600, ADC Residuals vs Hold



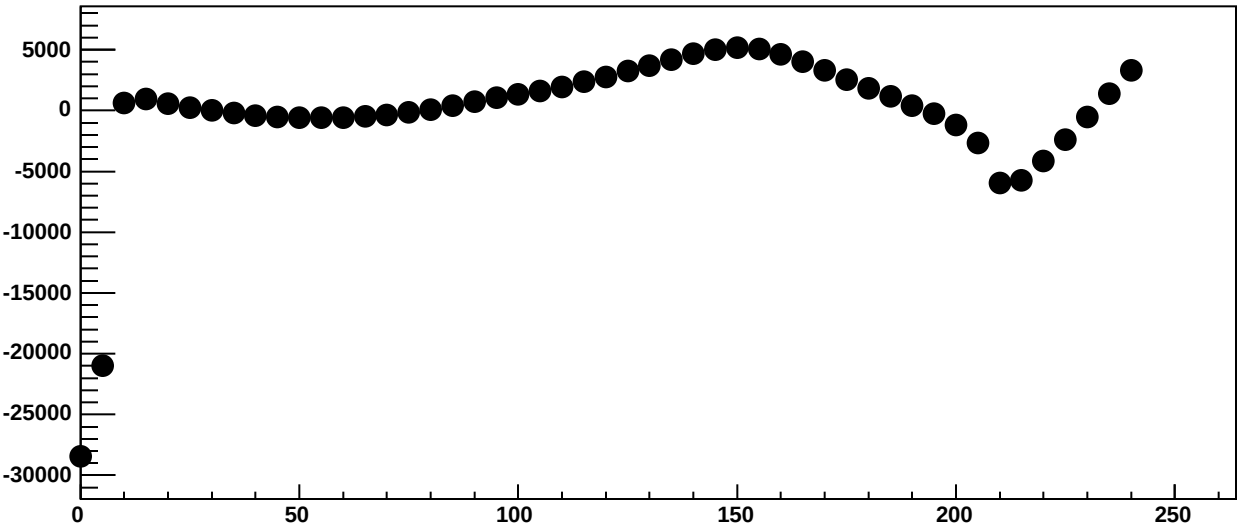
Chip 1, Channel 8, Enable 5!, DAC=1600, ADC Mean vs Hold

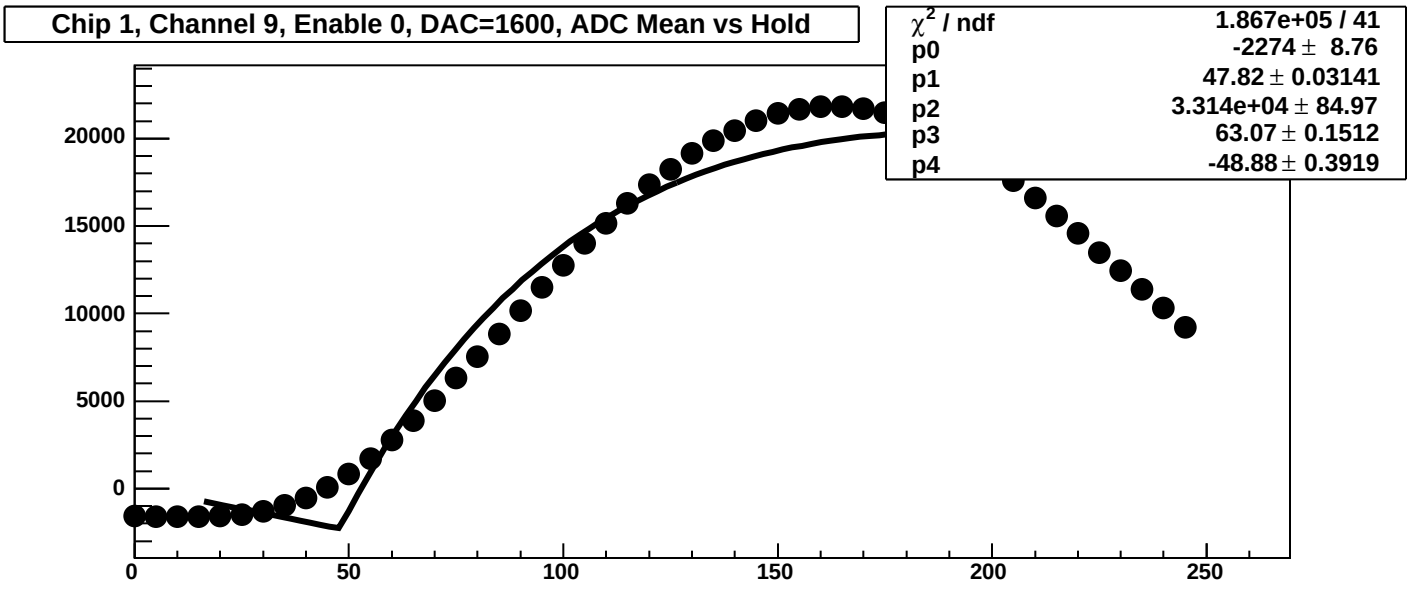


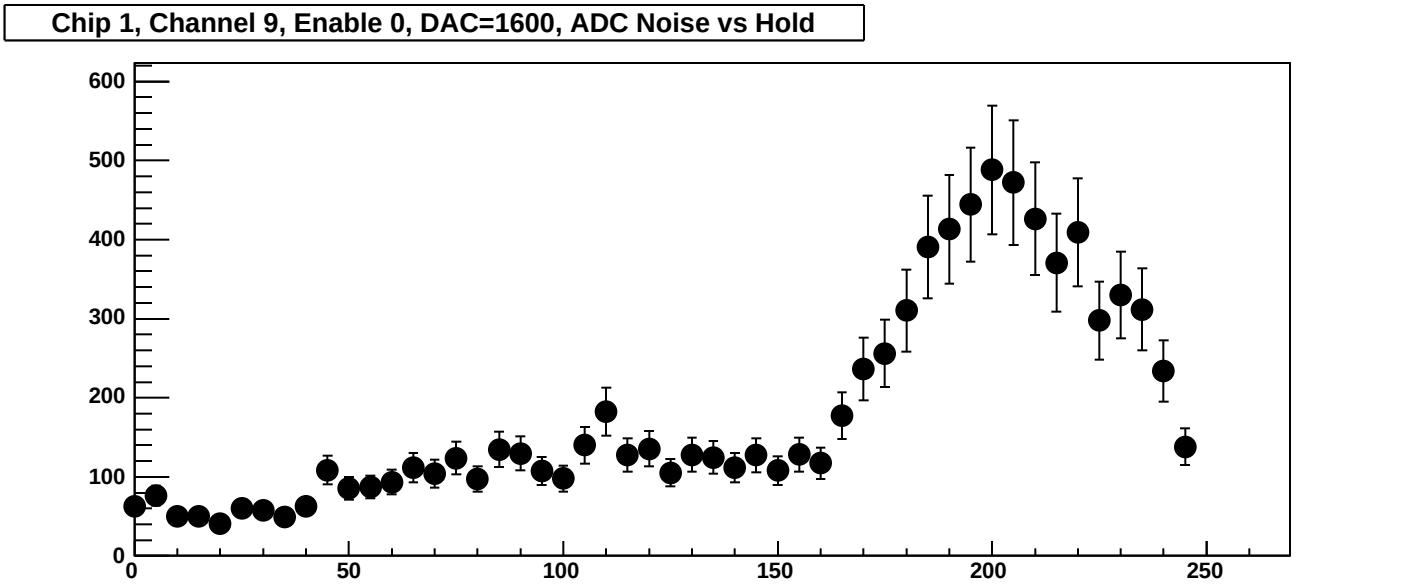
Chip 1, Channel 8, Enable 5!, DAC=1600, ADC Noise vs Hold

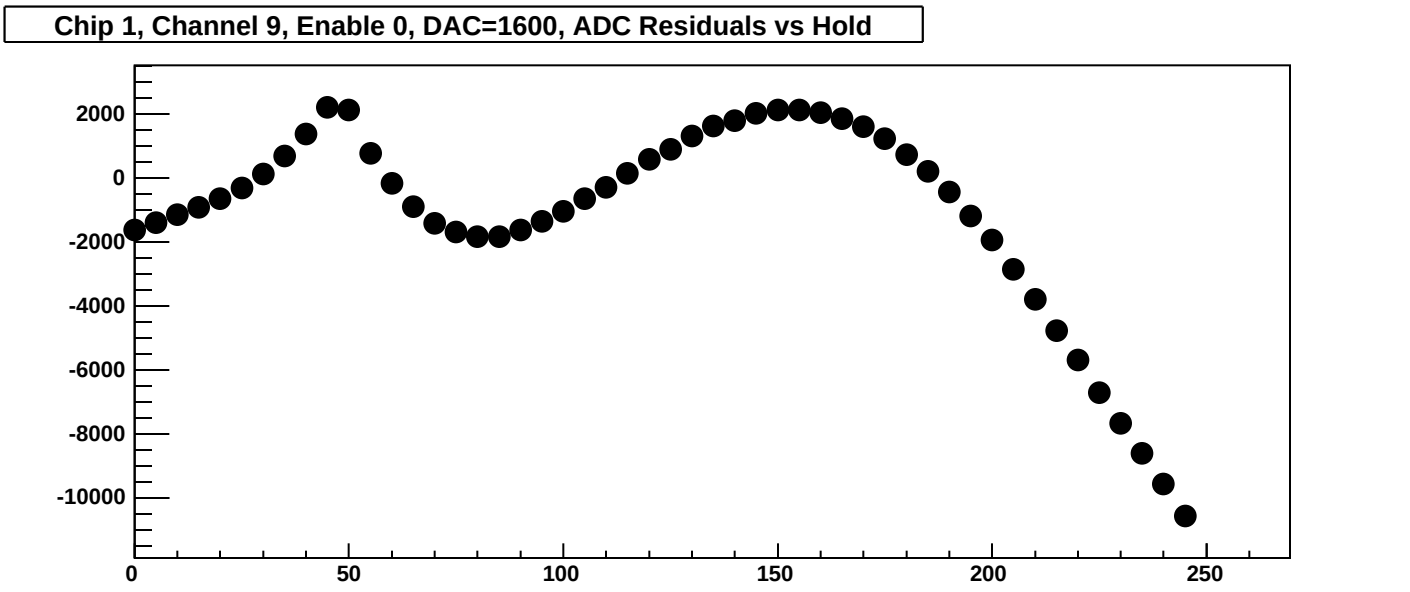


Chip 1, Channel 8, Enable 5!, DAC=1600, ADC Residuals vs Hold

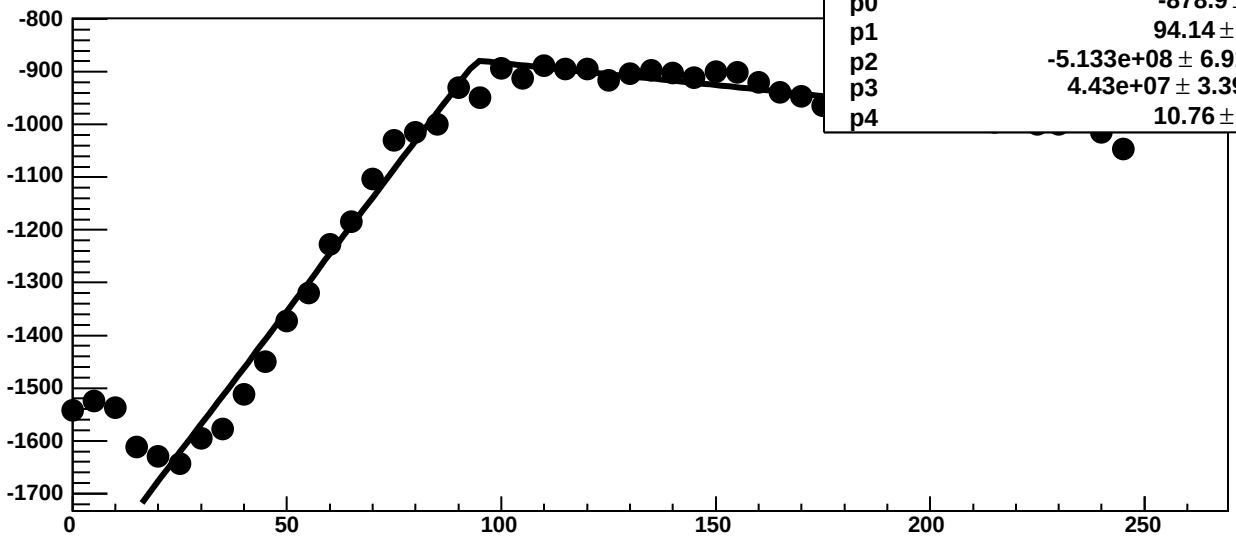



Chip 1, Channel 9, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.867e+05 / 41
p0 -2274 ± 8.76
p1 47.82 ± 0.03141
p2 3.314e+04 ± 84.97
p3 63.07 ± 0.1512
p4 -48.88 ± 0.3919
20000
15000
10000
5000
0
0
50
100
150
200
250

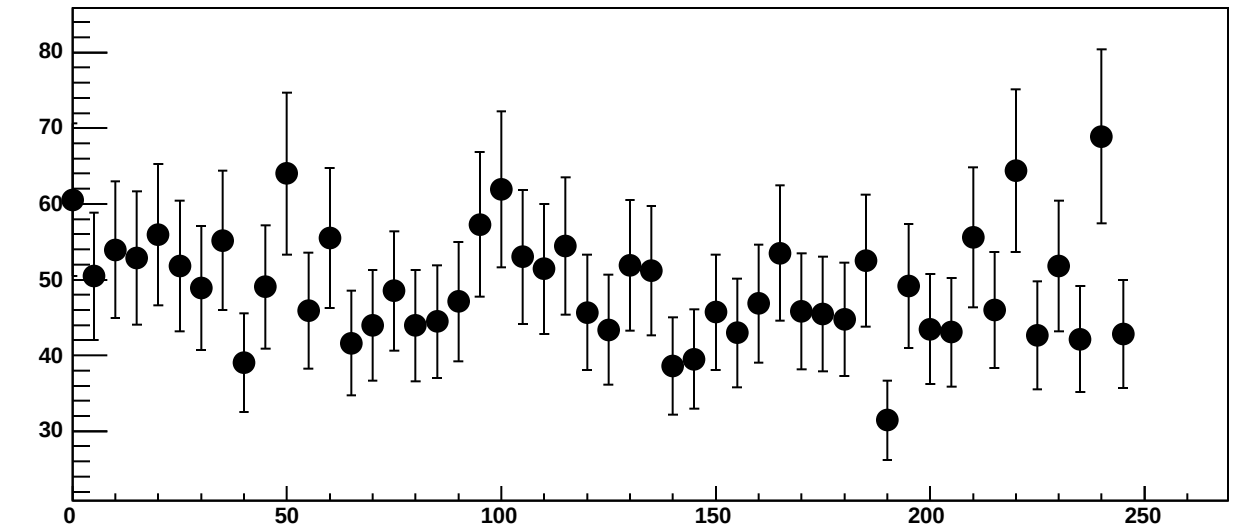

Chip 1, Channel 9, Enable 0, DAC=1600, ADC Noise vs Hold
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 1, Channel 9, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

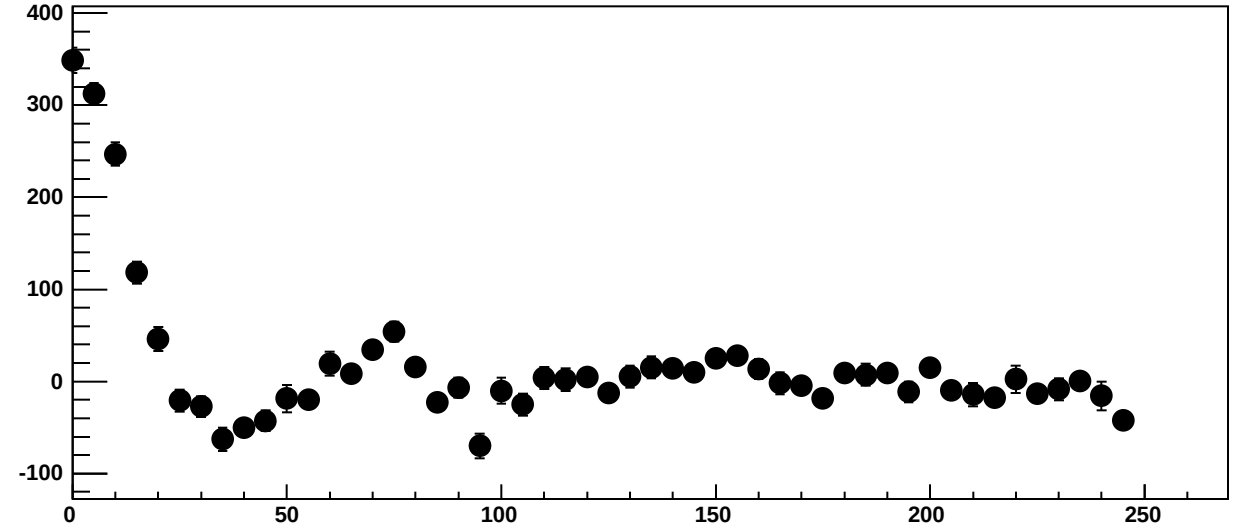
Chip 1, Channel 9, Enable 1, DAC=1600, ADC Mean vs Hold

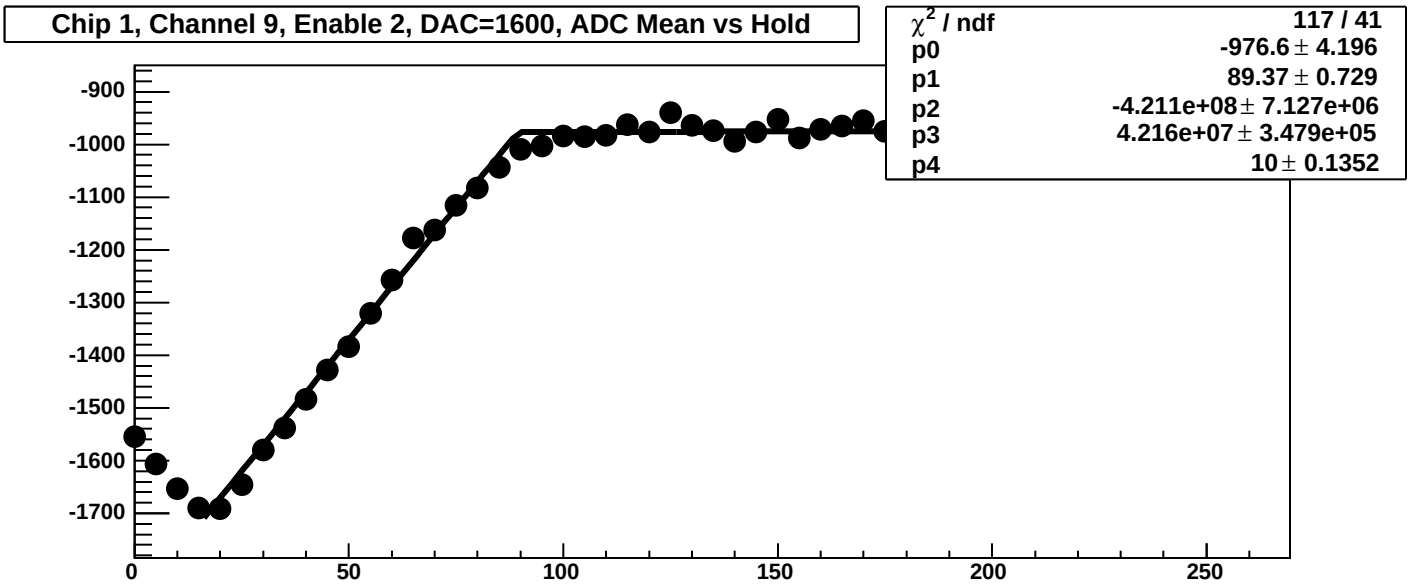


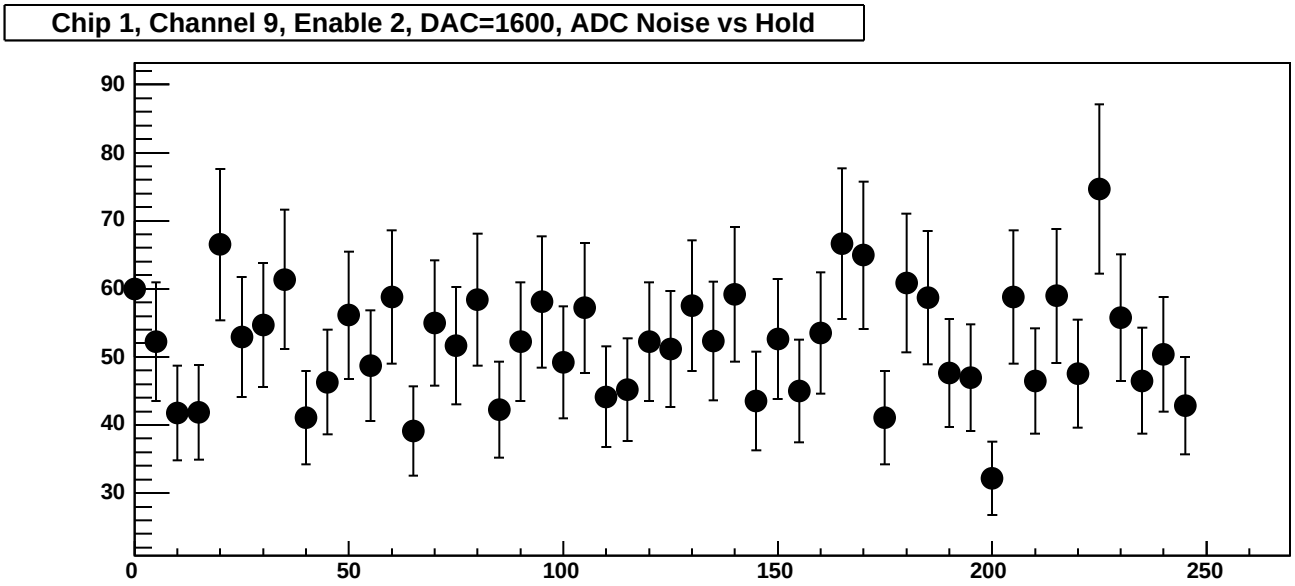
Chip 1, Channel 9, Enable 1, DAC=1600, ADC Noise vs Hold

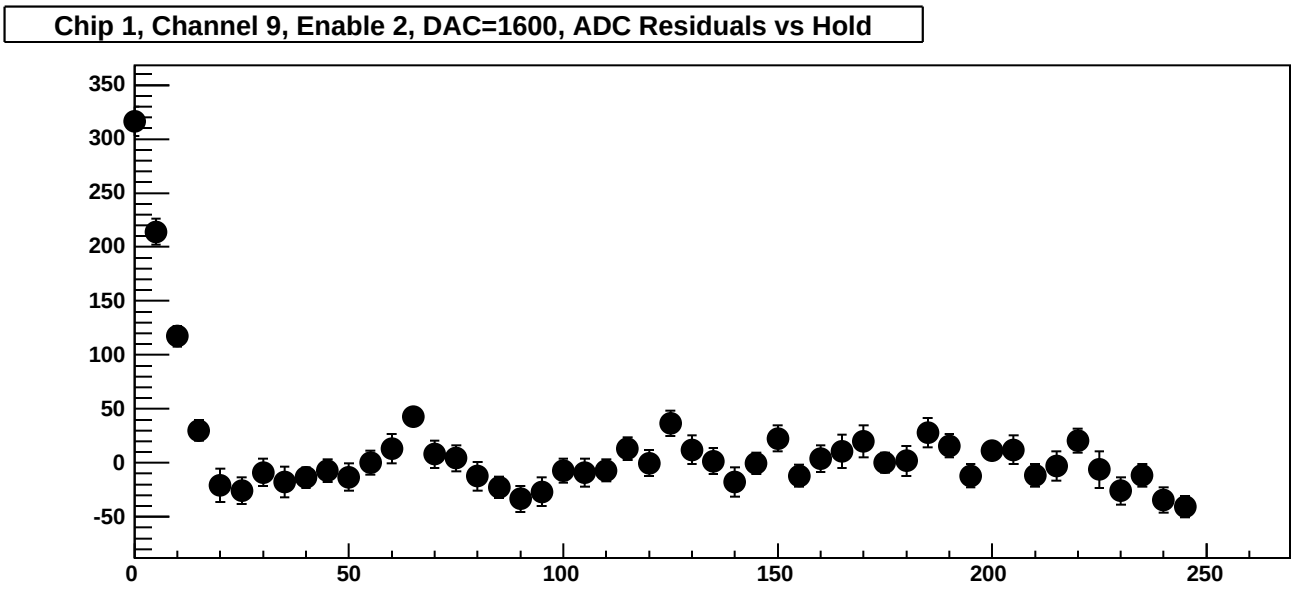


Chip 1, Channel 9, Enable 1, DAC=1600, ADC Residuals vs Hold

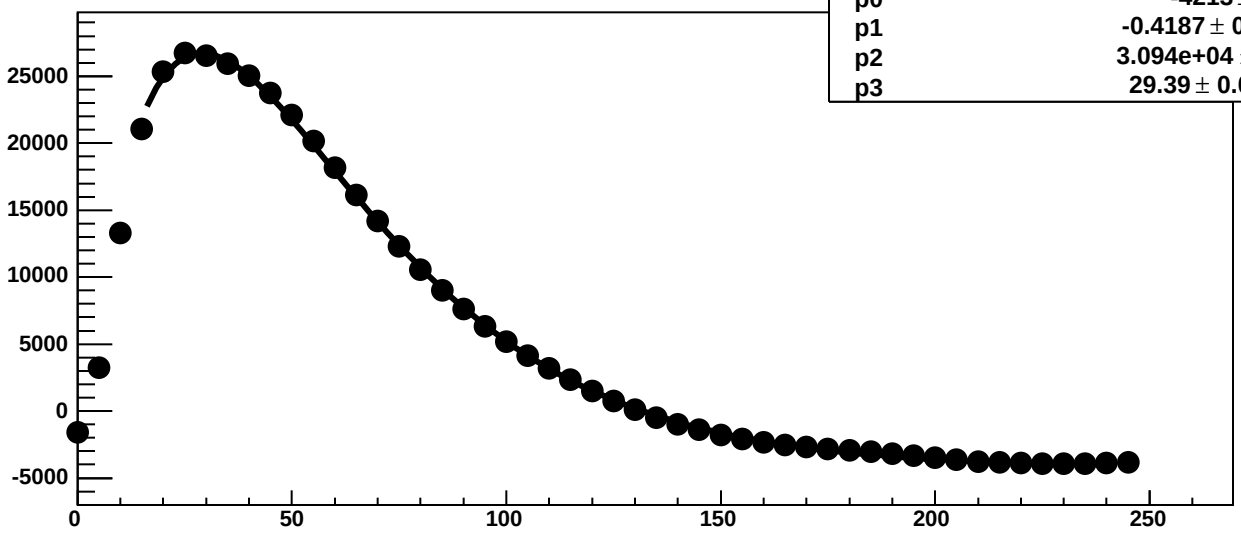



Chip 1, Channel 9, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 117 / 41
p0 -976.6 ± 4.196
p1 89.37 ± 0.729
p2 -4.211e+08 ± 7.127e+06
p3 4.216e+07 ± 3.479e+05
p4 10 ± 0.1352
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
50
100
150
200
250

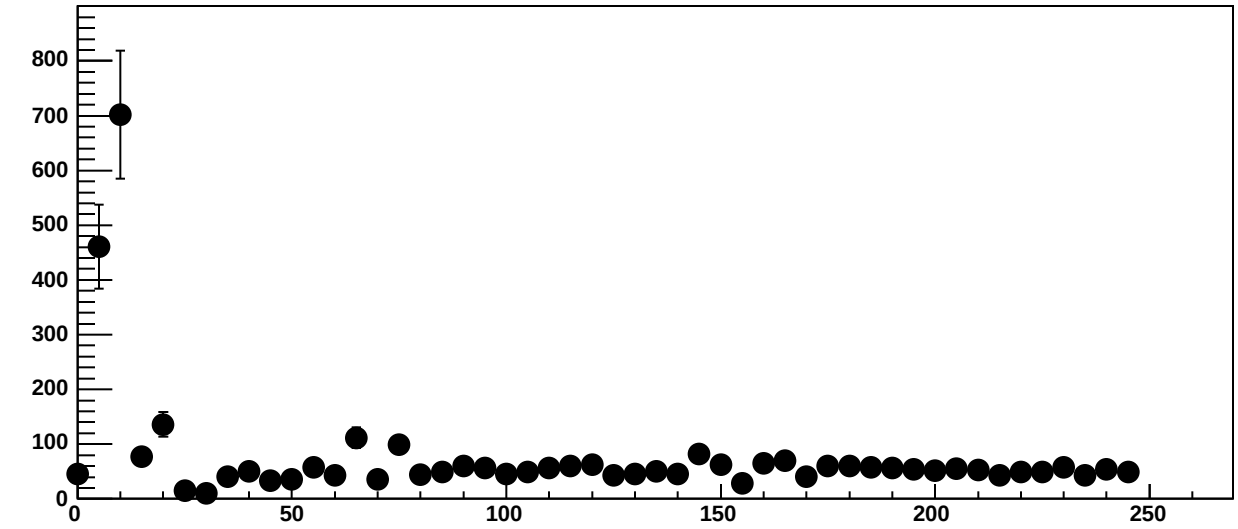

Chip 1, Channel 9, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 1, Channel 9, Enable 2, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
0
50
100
150
200
250

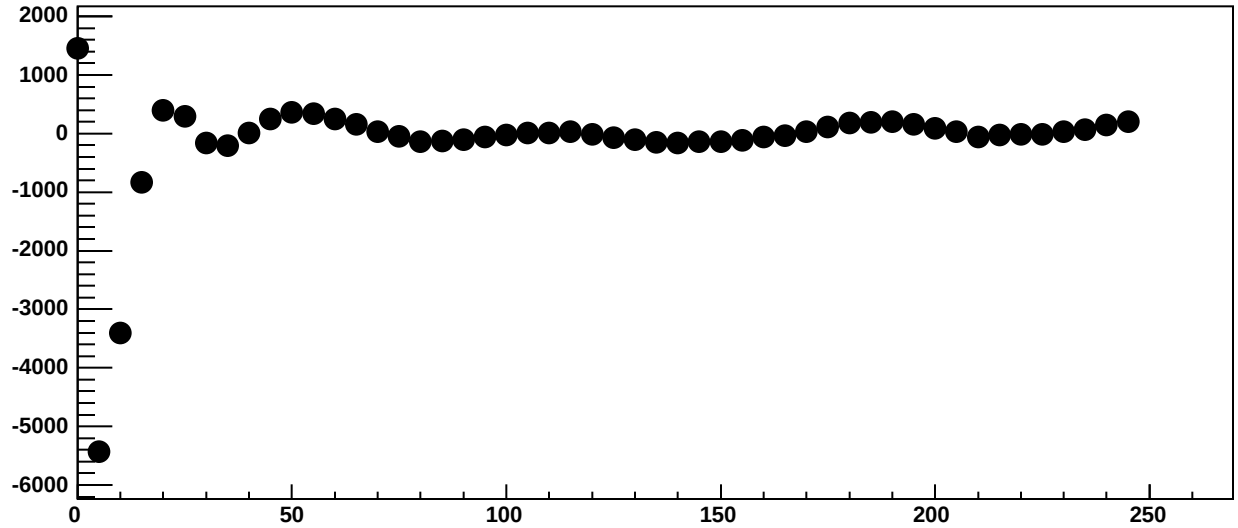
Chip 1, Channel 9, Enable 3!, DAC=1600, ADC Mean vs Hold



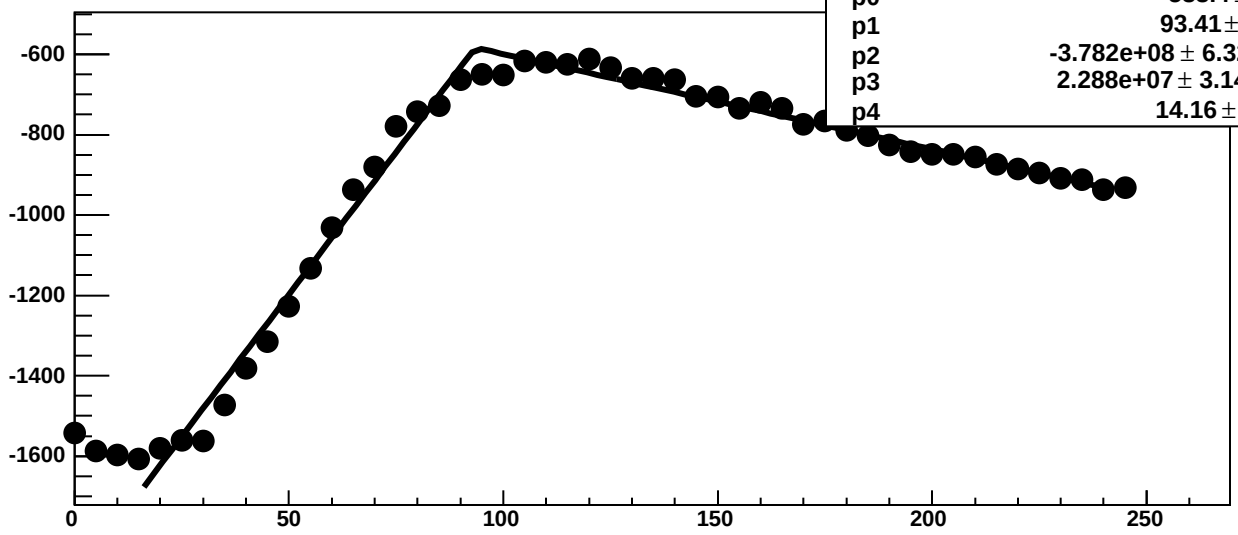
Chip 1, Channel 9, Enable 3!, DAC=1600, ADC Noise vs Hold



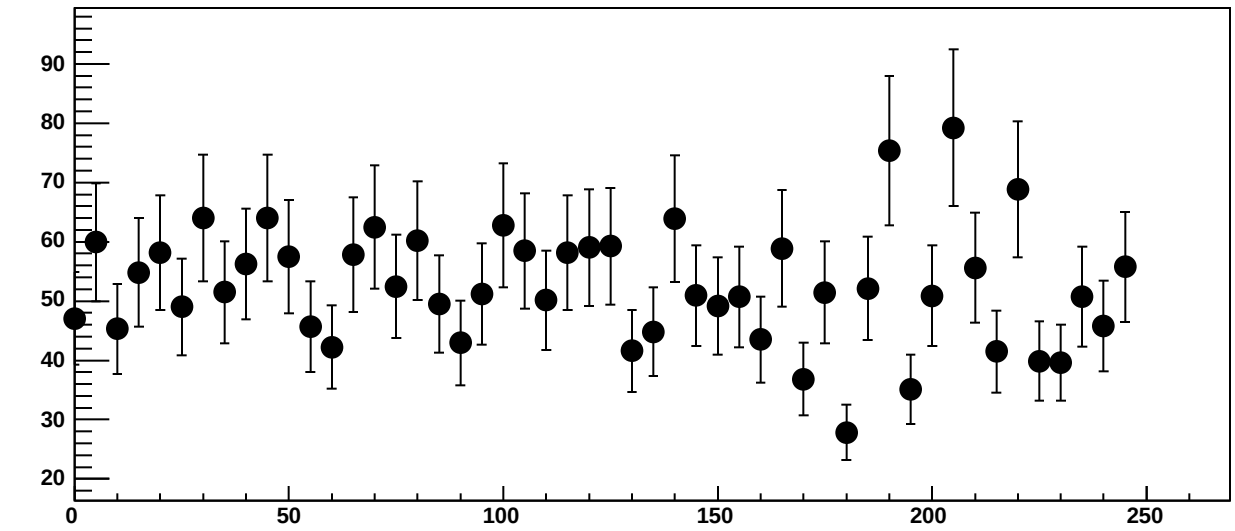
Chip 1, Channel 9, Enable 3!, DAC=1600, ADC Residuals vs Hold



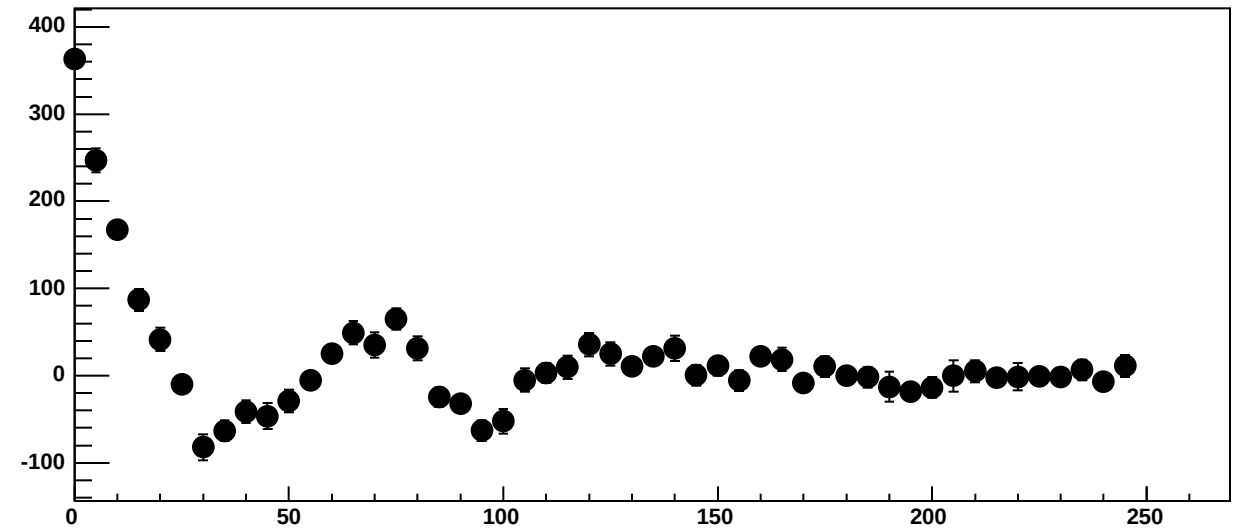
Chip 1, Channel 9, Enable 4, DAC=1600, ADC Mean vs Hold

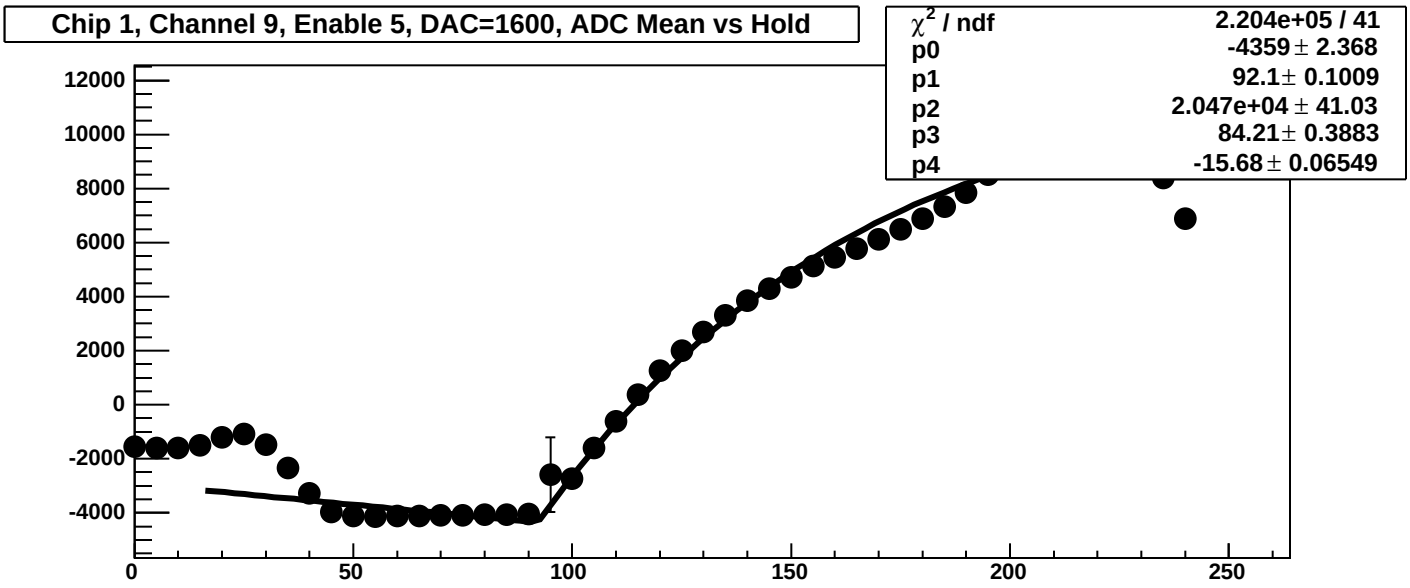


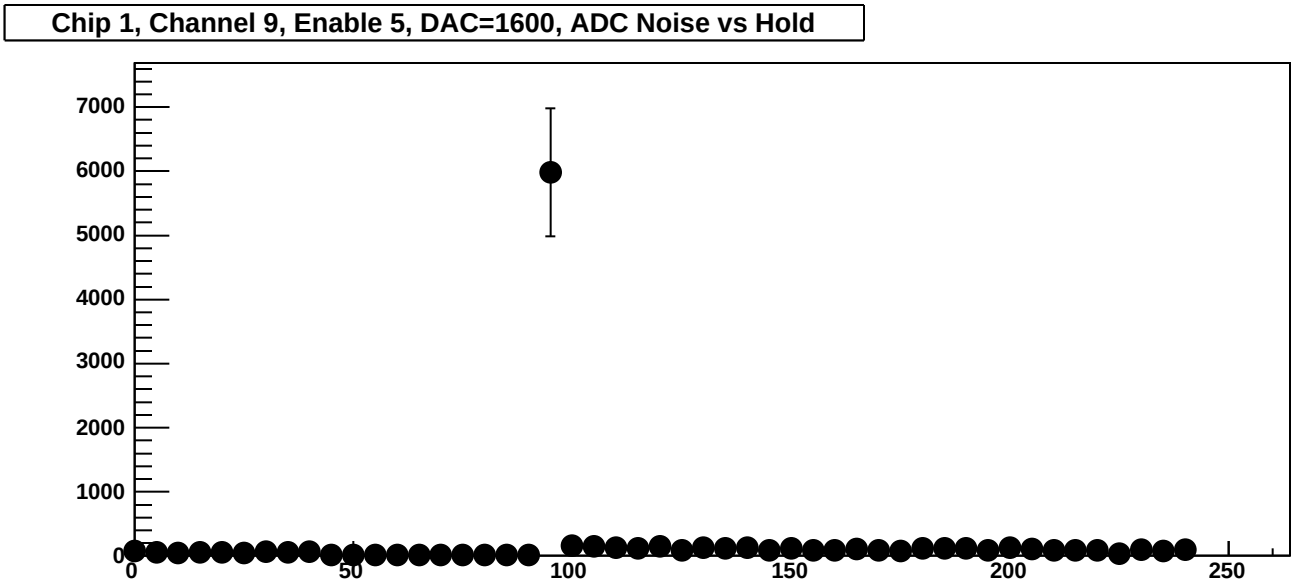
Chip 1, Channel 9, Enable 4, DAC=1600, ADC Noise vs Hold

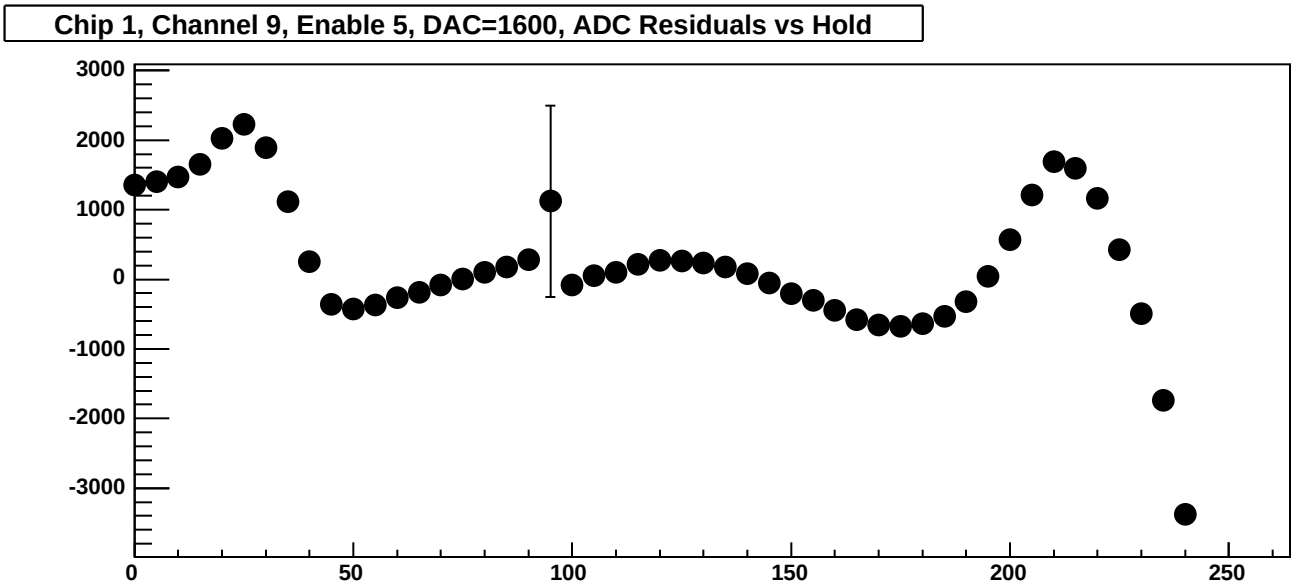


Chip 1, Channel 9, Enable 4, DAC=1600, ADC Residuals vs Hold

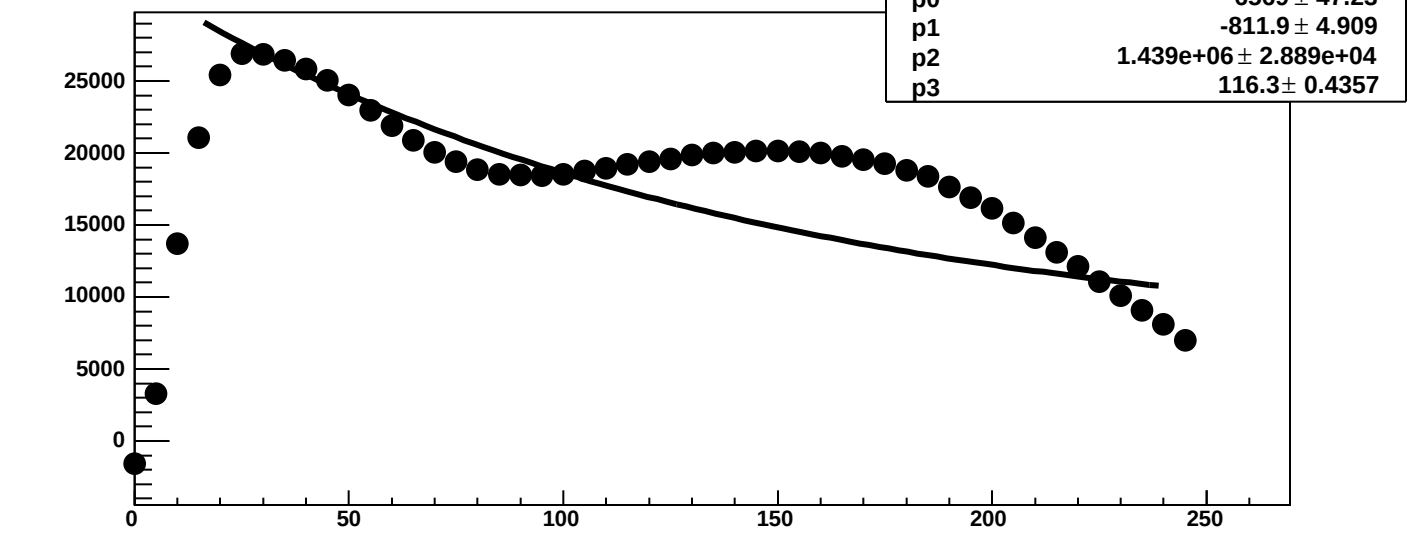



Chip 1, Channel 9, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.204e+05 / 41
p0 -4359 ± 2.368
p1 92.1 ± 0.1009
p2 2.047e+04 ± 41.03
p3 84.21 ± 0.3883
p4 -15.68 ± 0.06549
12000
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

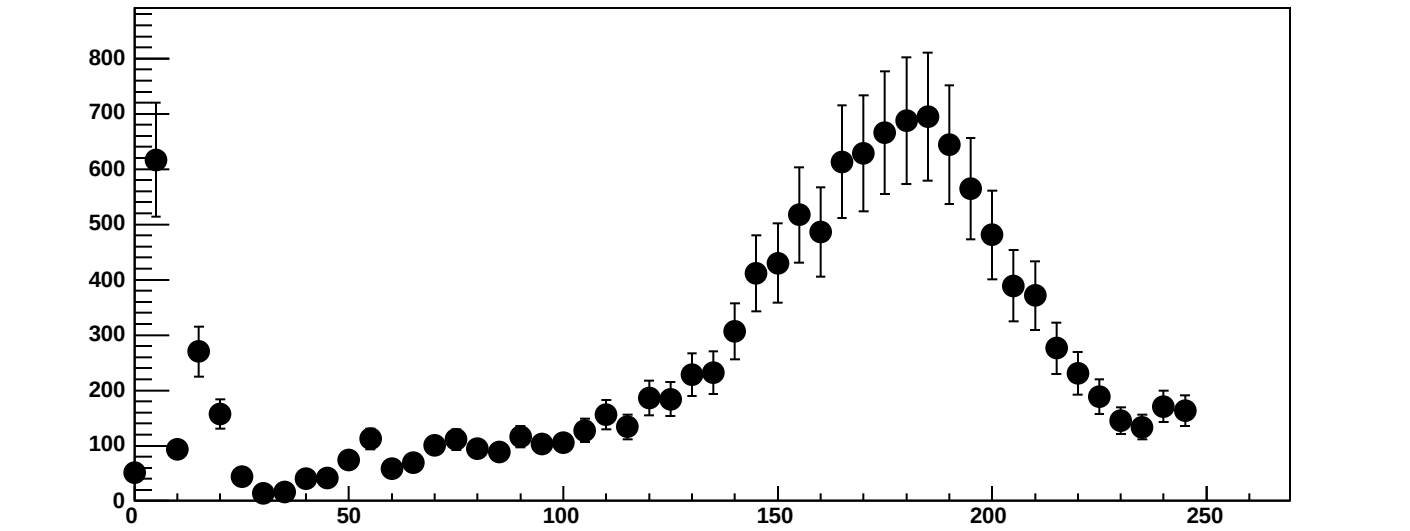

Chip 1, Channel 9, Enable 5, DAC=1600, ADC Noise vs Hold
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 1, Channel 9, Enable 5, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

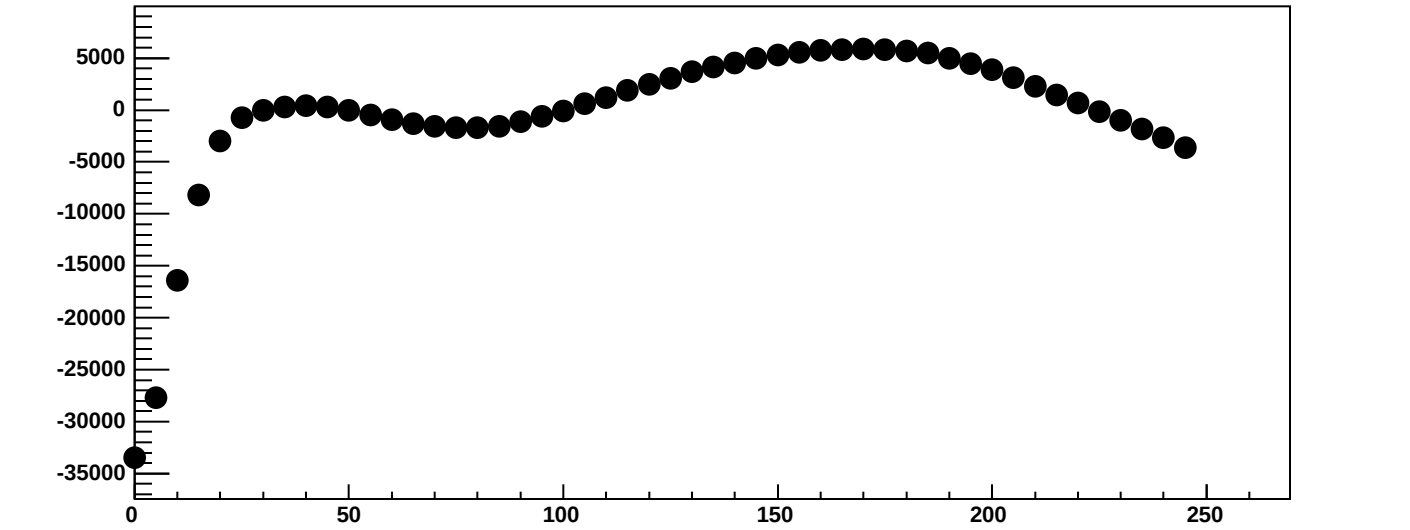
Chip 1, Channel 10, Enable 0!, DAC=1600, ADC Mean vs Hold

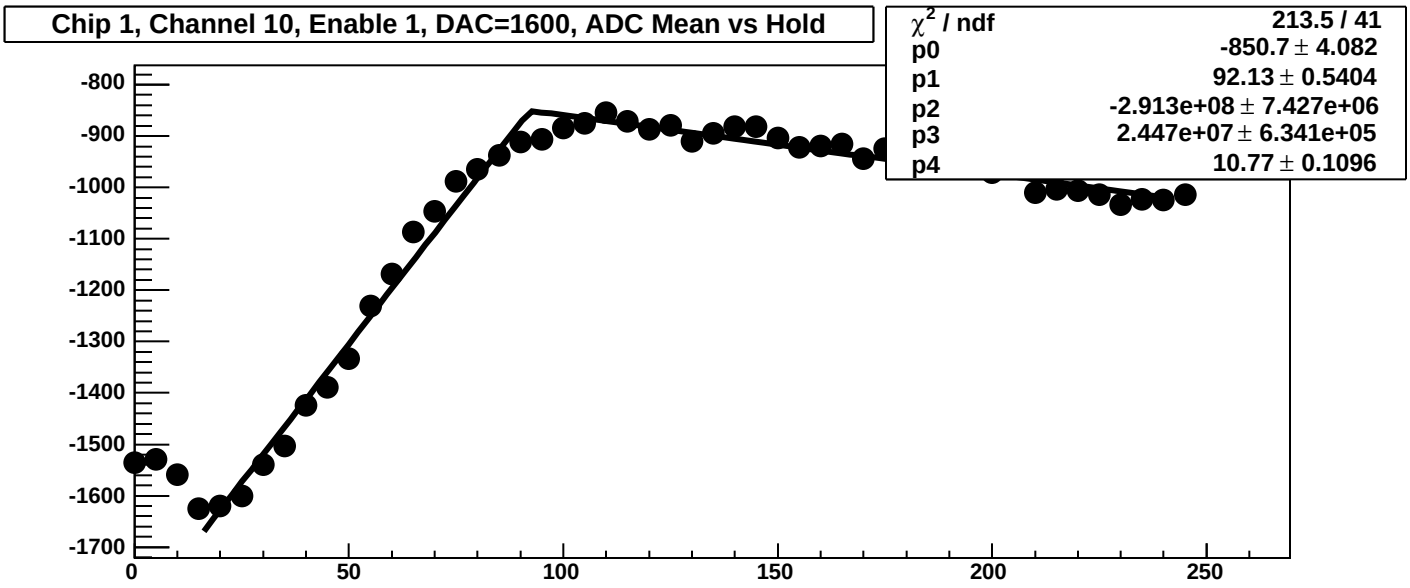


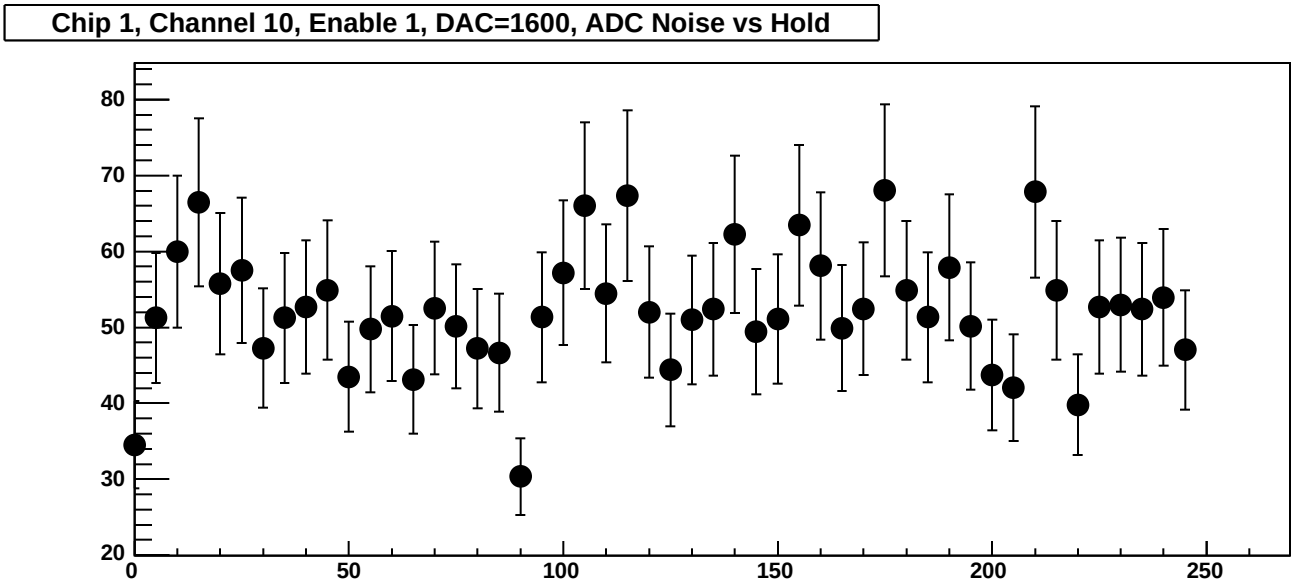
Chip 1, Channel 10, Enable 0!, DAC=1600, ADC Noise vs Hold

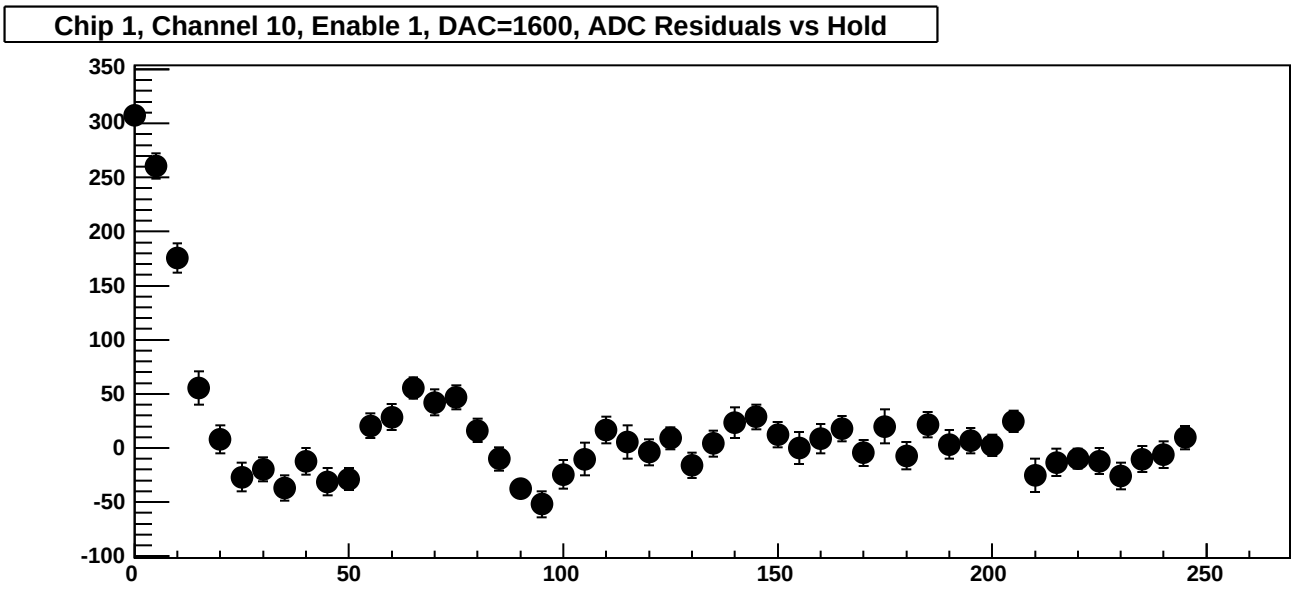


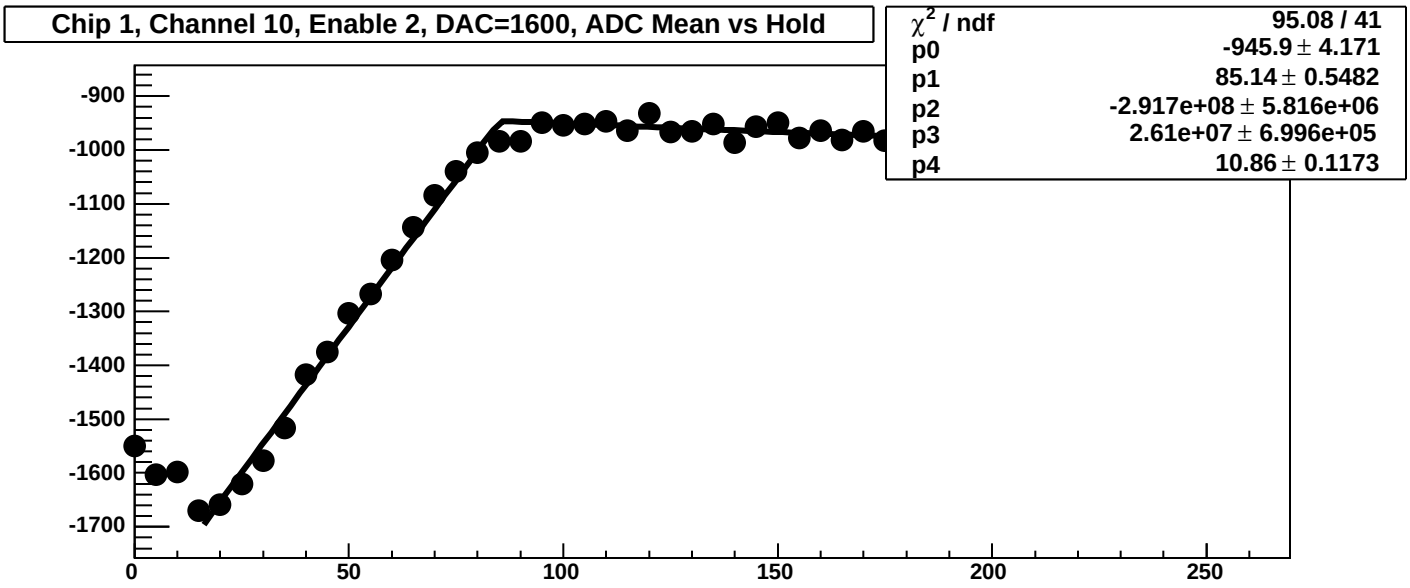
Chip 1, Channel 10, Enable 0!, DAC=1600, ADC Residuals vs Hold

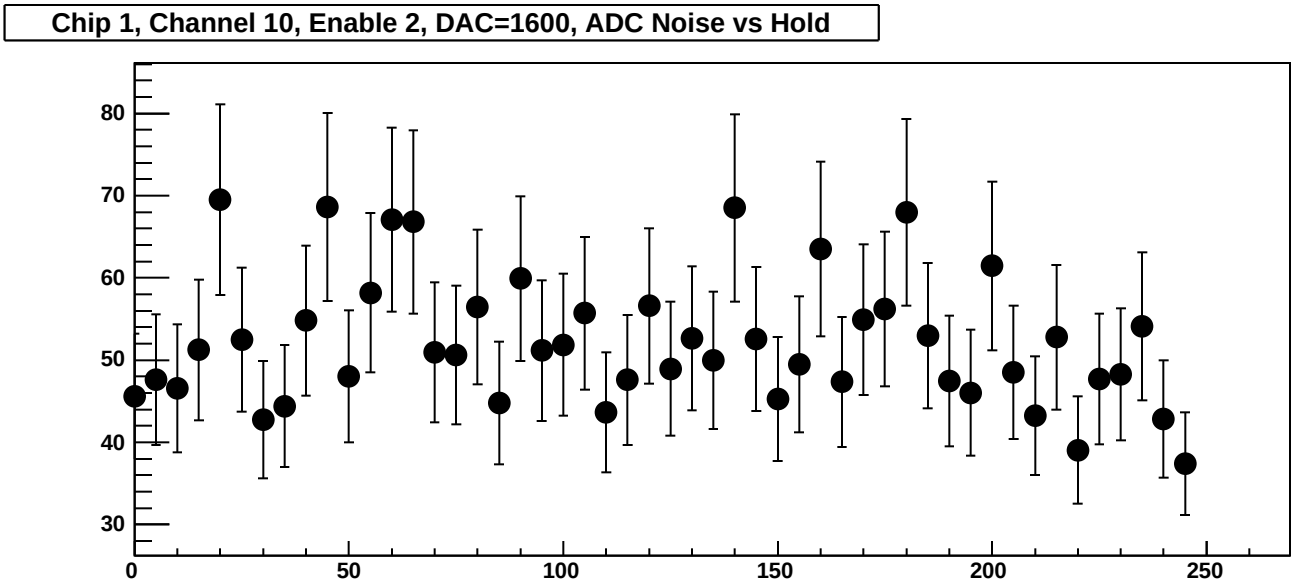


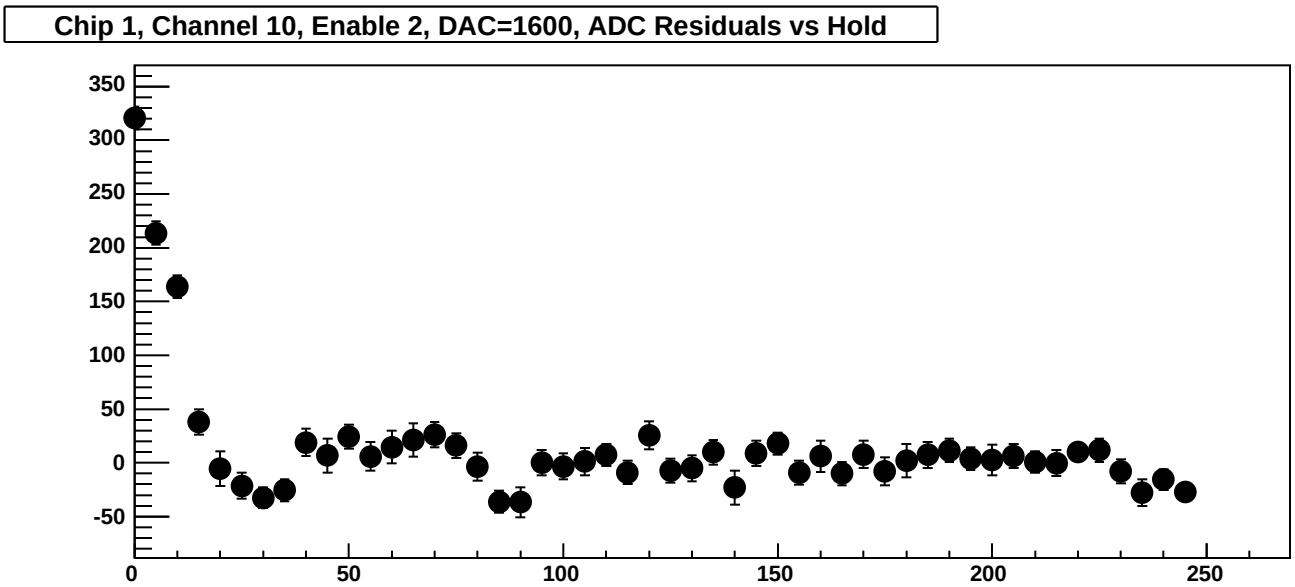

Chip 1, Channel 10, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 213.5 / 41
p0 -850.7 ± 4.082
p1 92.13 ± 0.5404
p2 -2.913e+08 ± 7.427e+06
p3 2.447e+07 ± 6.341e+05
p4 10.77 ± 0.1096
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
50
100
150
200
250

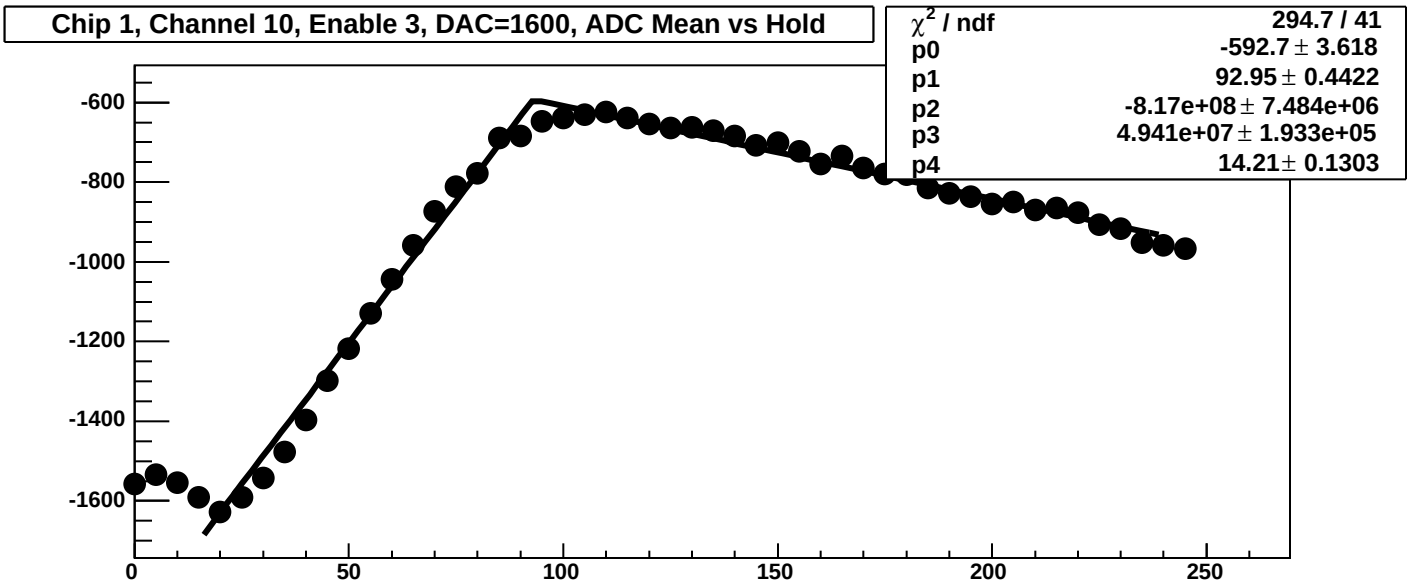

Chip 1, Channel 10, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
20
0
50
100
150
200
250

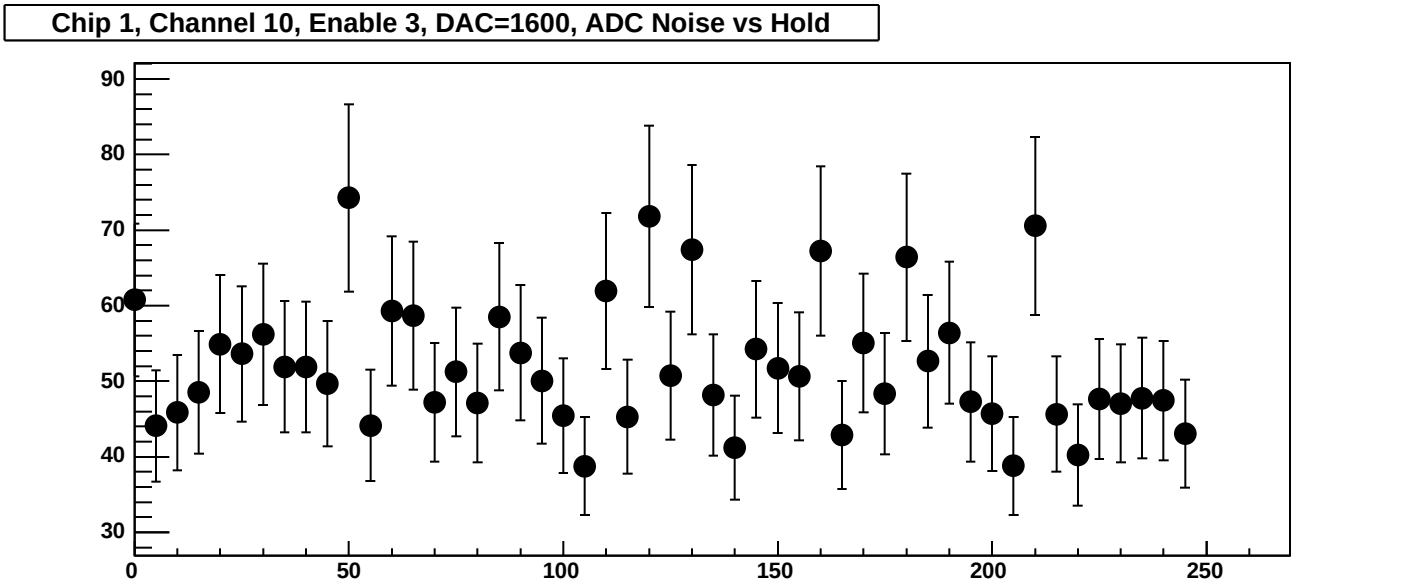

Chip 1, Channel 10, Enable 1, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

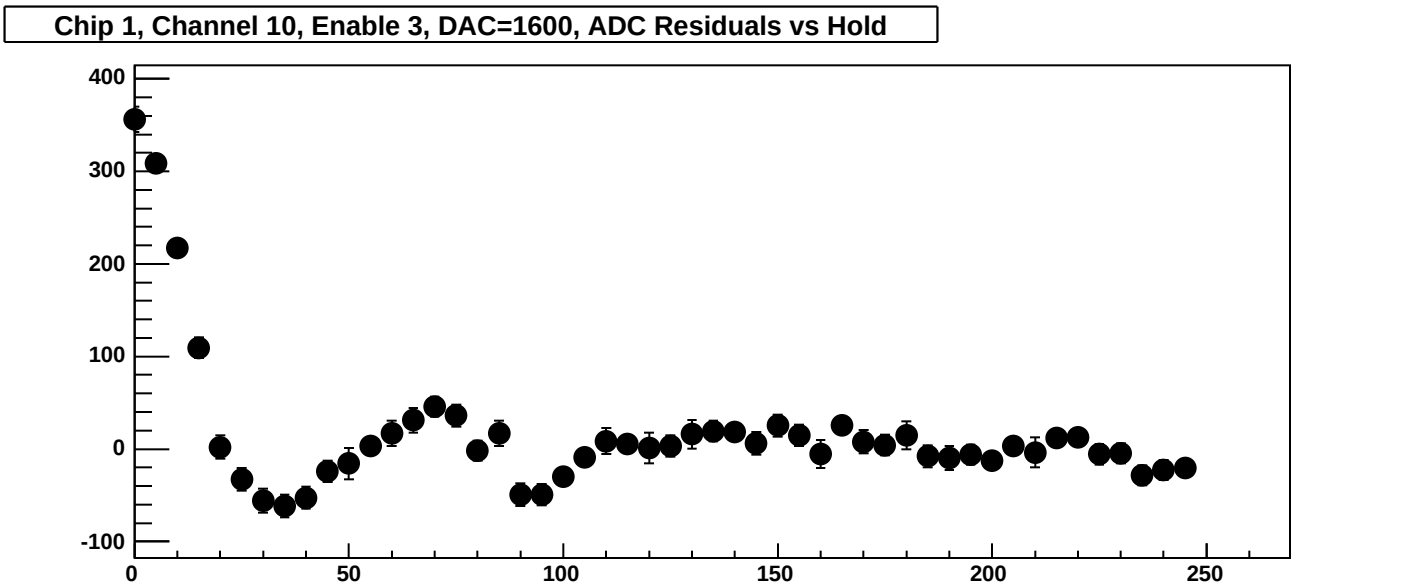

Chip 1, Channel 10, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 95.08 / 41
p0 -945.9 ± 4.171
p1 85.14 ± 0.5482
p2 -2.917e+08 ± 5.816e+06
p3 2.61e+07 ± 6.996e+05
p4 10.86 ± 0.1173
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
50
100
150
200
250


Chip 1, Channel 10, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

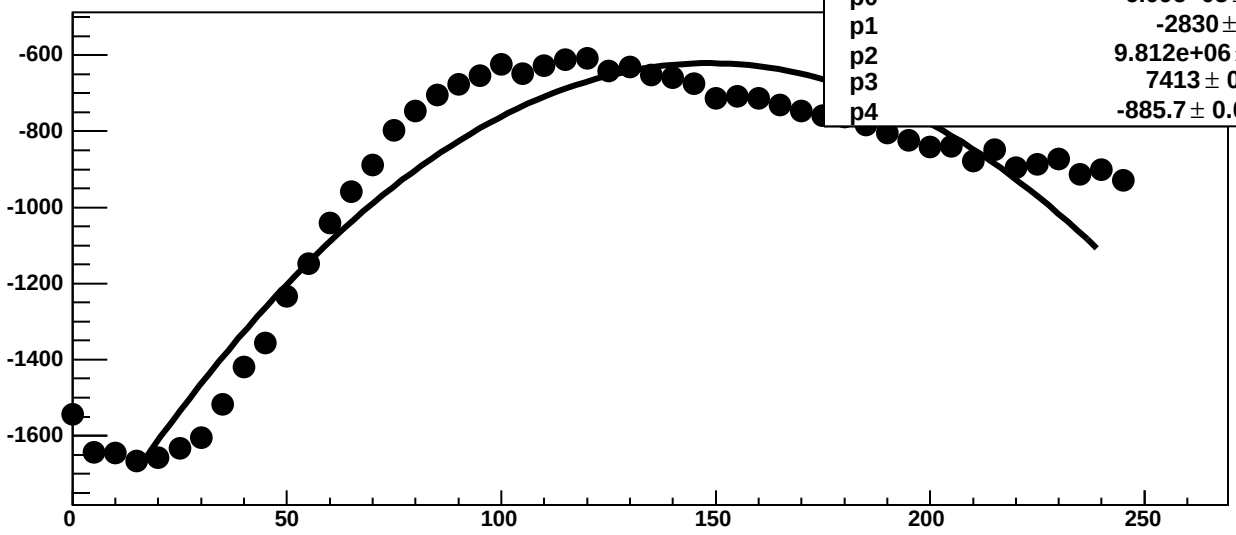

Chip 1, Channel 10, Enable 2, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
0
50
100
150
200
250


Chip 1, Channel 10, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 294.7 / 41
p0 -592.7 ± 3.618
p1 92.95 ± 0.4422
p2 -8.17e+08 ± 7.484e+06
p3 4.941e+07 ± 1.933e+05
p4 14.21 ± 0.1303
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250

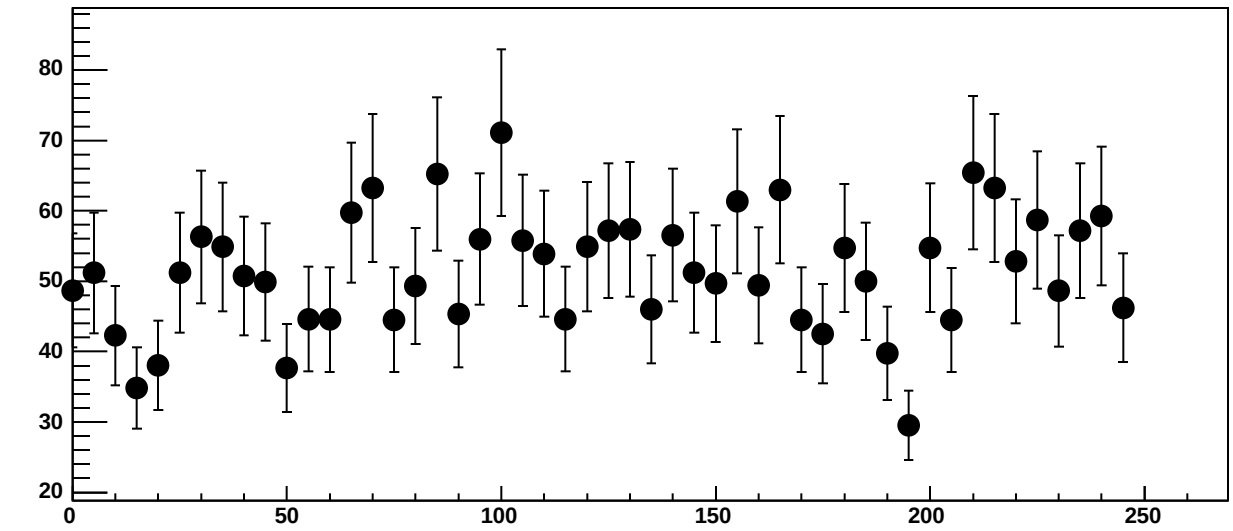

Chip 1, Channel 10, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 1, Channel 10, Enable 3, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

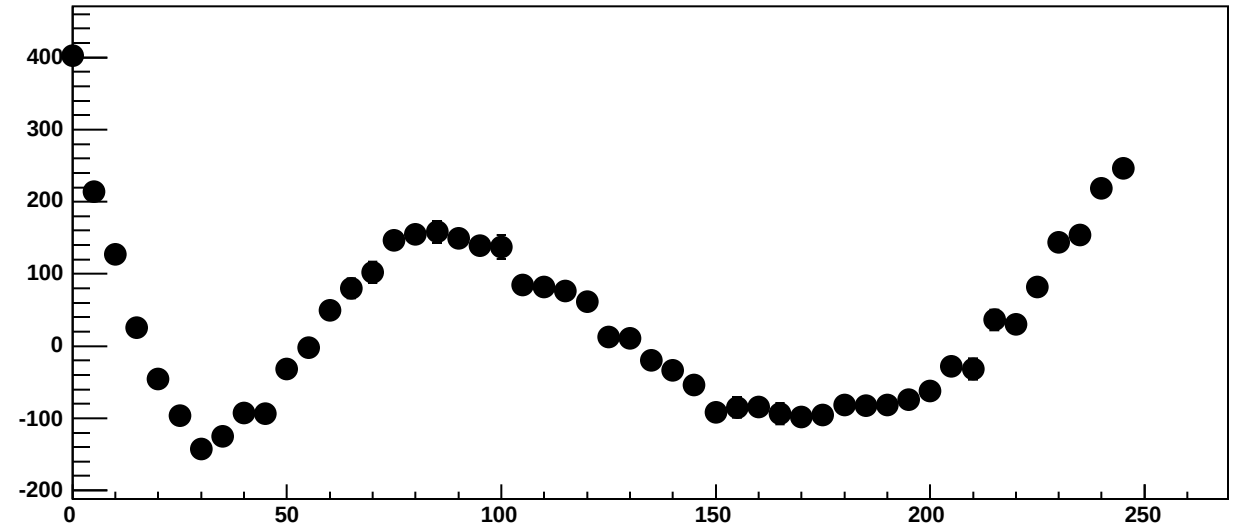
Chip 1, Channel 10, Enable 4, DAC=1600, ADC Mean vs Hold

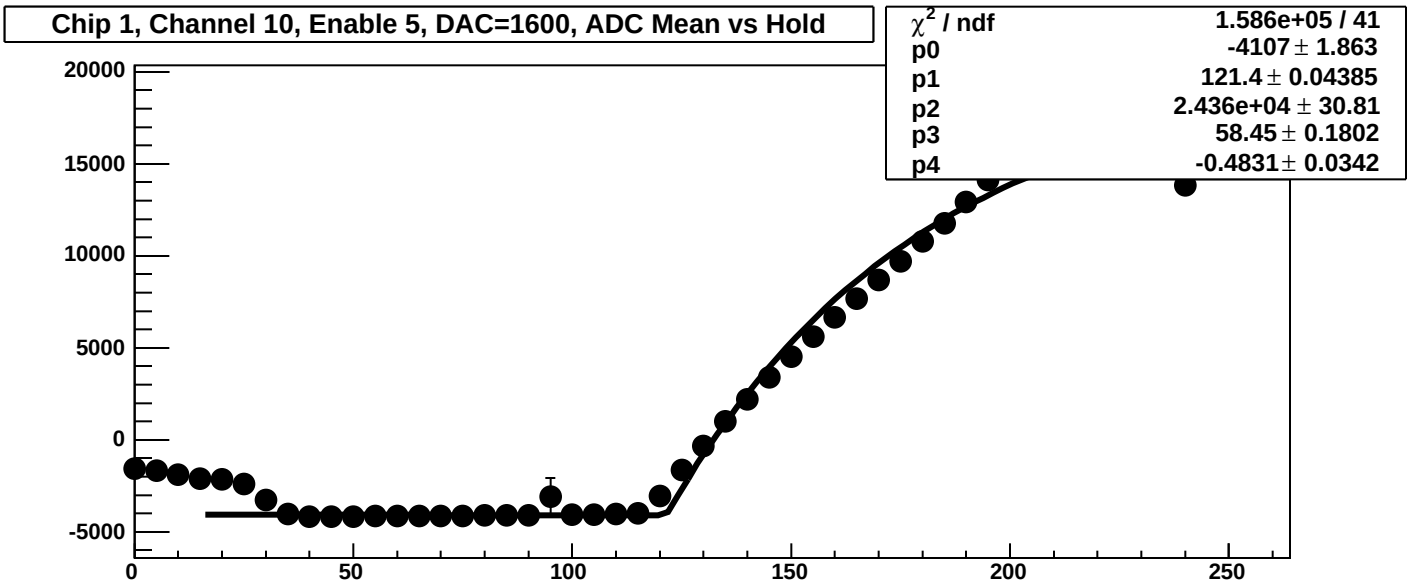


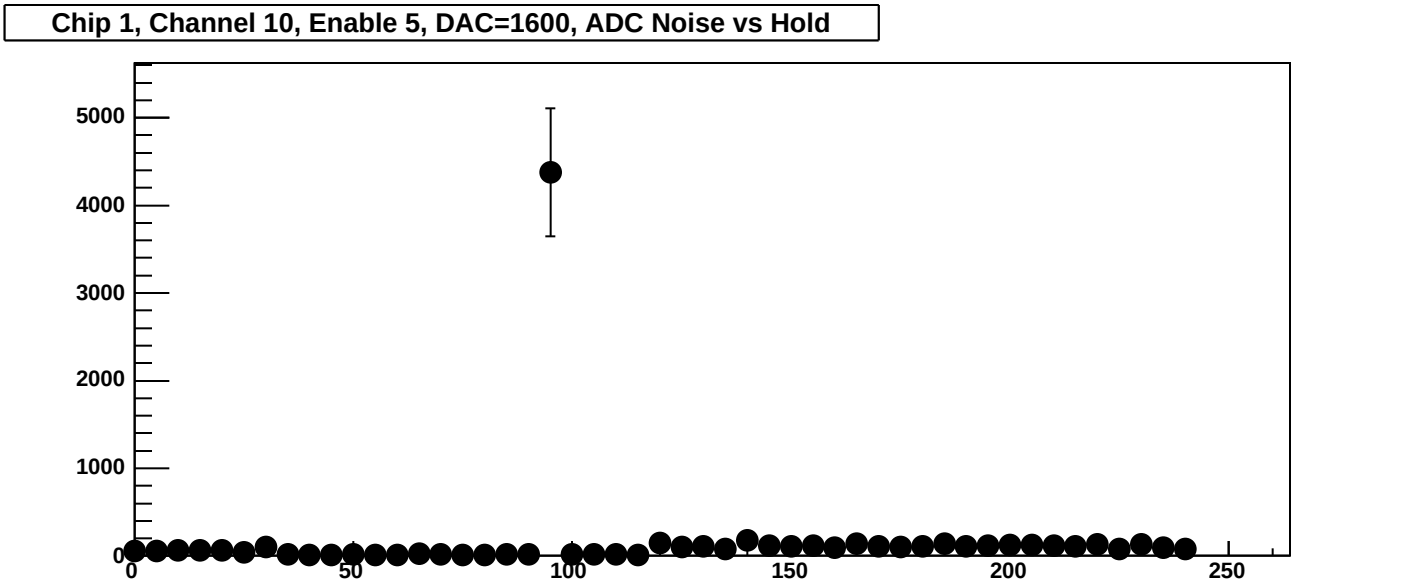
Chip 1, Channel 10, Enable 4, DAC=1600, ADC Noise vs Hold

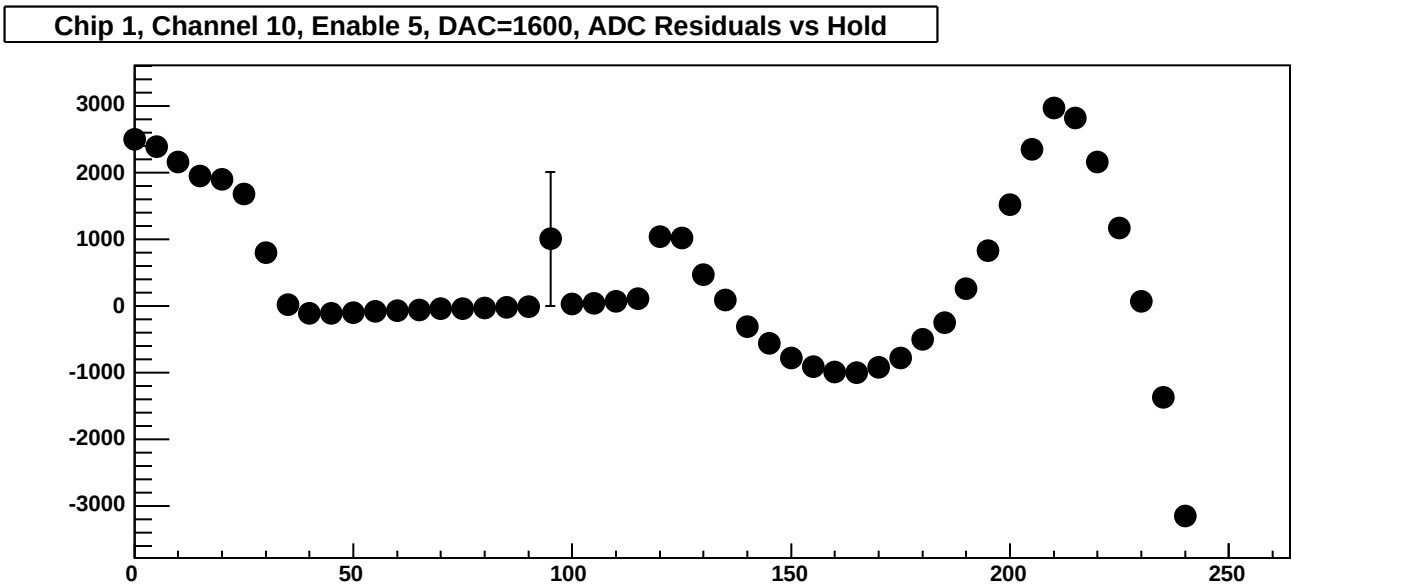


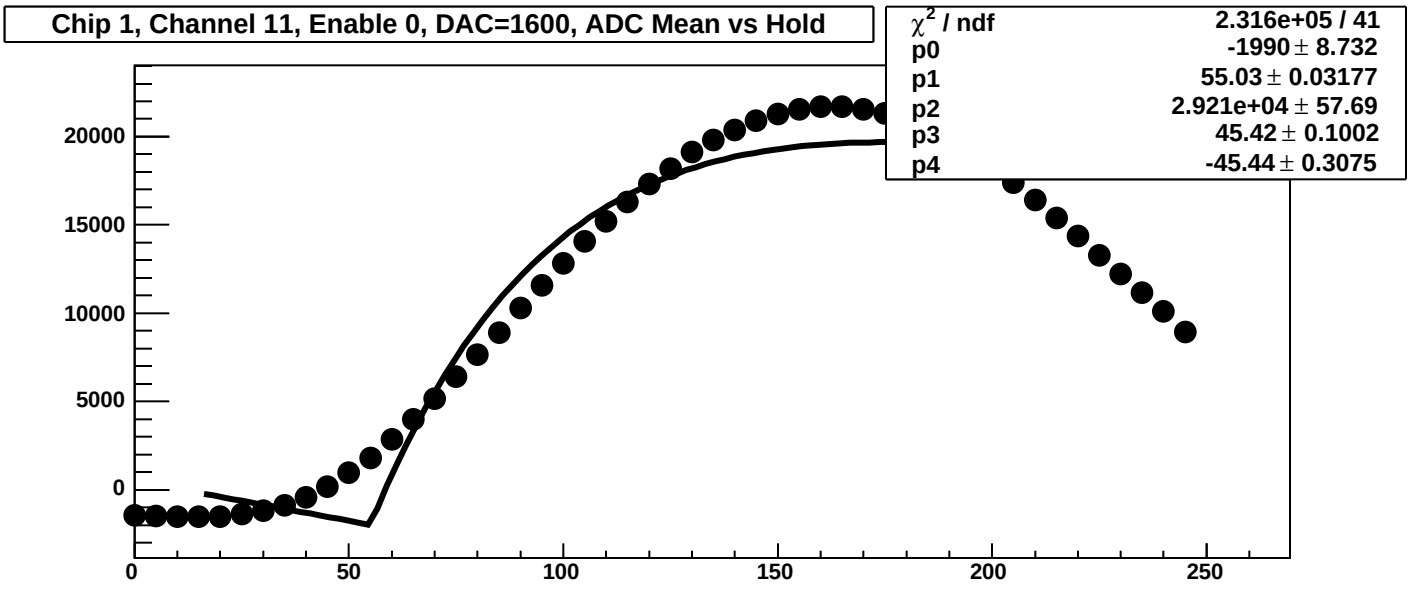
Chip 1, Channel 10, Enable 4, DAC=1600, ADC Residuals vs Hold

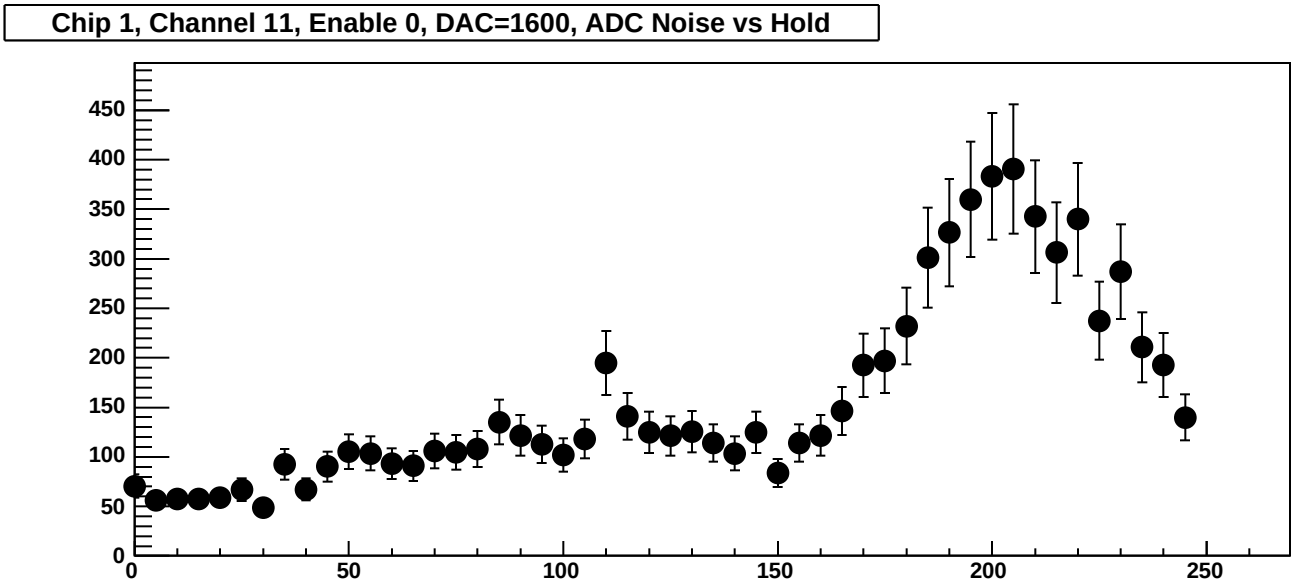


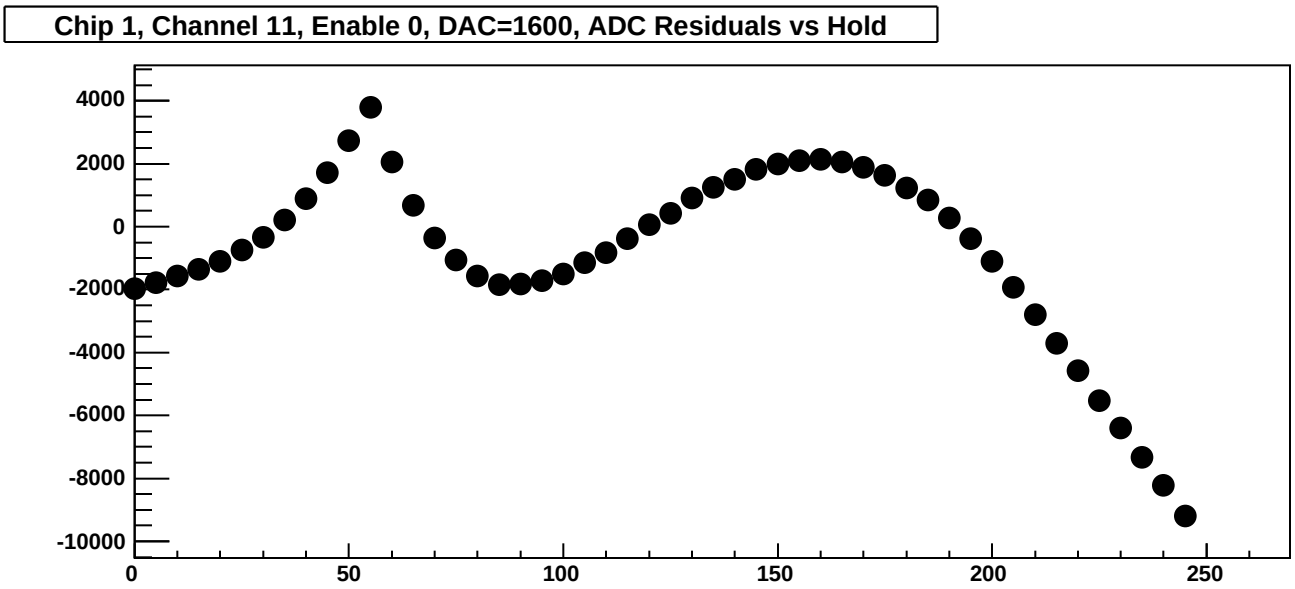

Chip 1, Channel 10, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.586e+05 / 41
p0 -4107 ± 1.863
p1 121.4 ± 0.04385
p2 2.436e+04 ± 30.81
p3 58.45 ± 0.1802
p4 -0.4831 ± 0.0342
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 1, Channel 10, Enable 5, DAC=1600, ADC Noise vs Hold
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

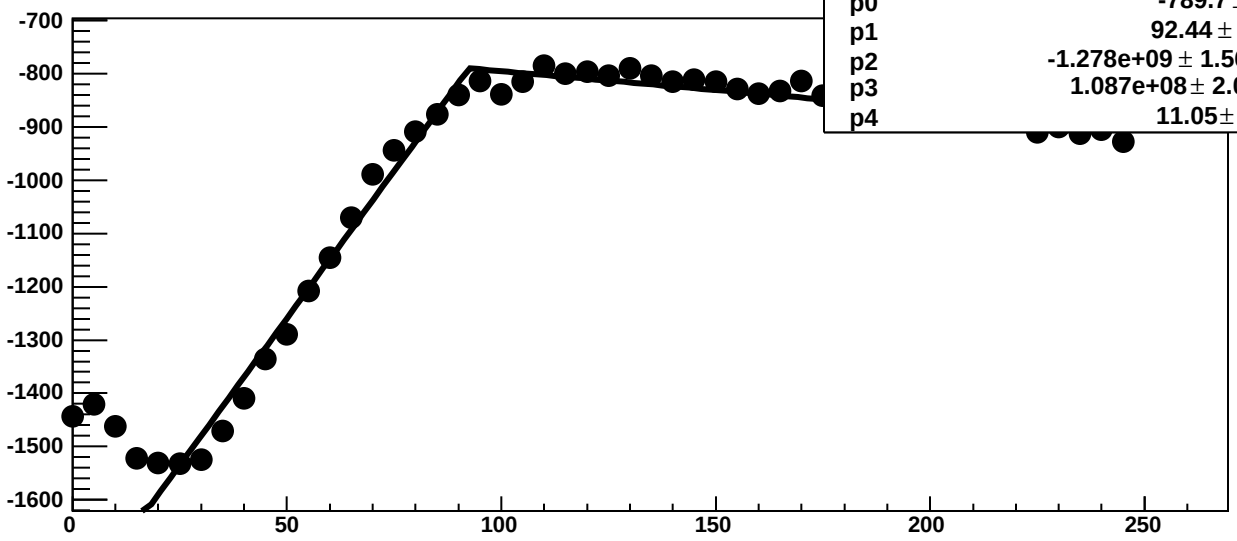

Chip 1, Channel 10, Enable 5, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250


Chip 1, Channel 11, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.316e+05 / 41
p0 -1990 ± 8.732
p1 55.03 ± 0.03177
p2 2.921e+04 ± 57.69
p3 45.42 ± 0.1002
p4 -45.44 ± 0.3075
20000
15000
10000
5000
0
0
50
100
150
200
250

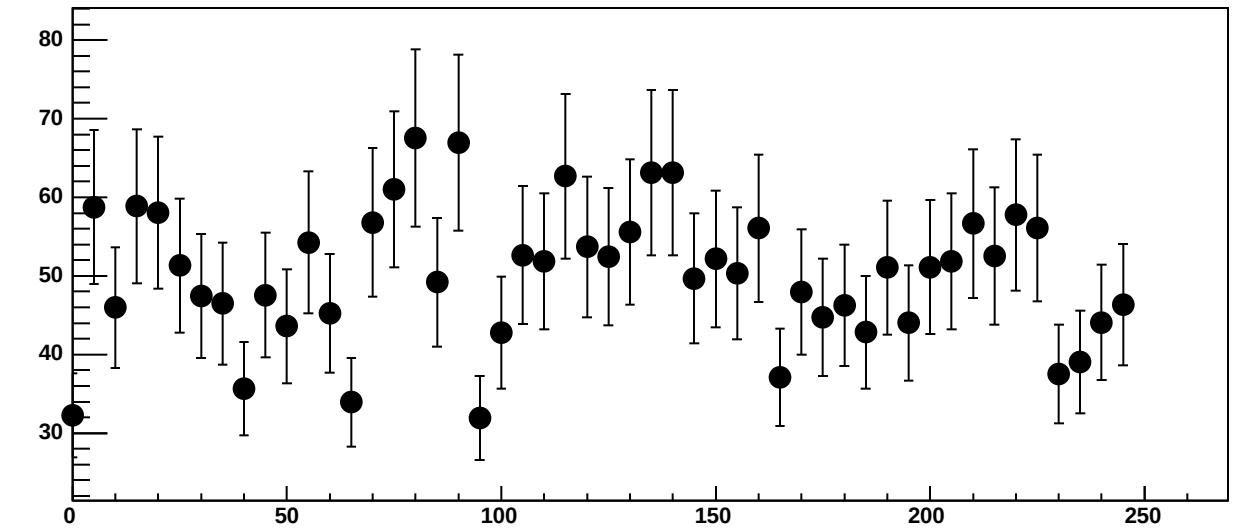

Chip 1, Channel 11, Enable 0, DAC=1600, ADC Noise vs Hold
450
400
350
300
250
200
150
100
50
0
0
50
100
150
200
250


Chip 1, Channel 11, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

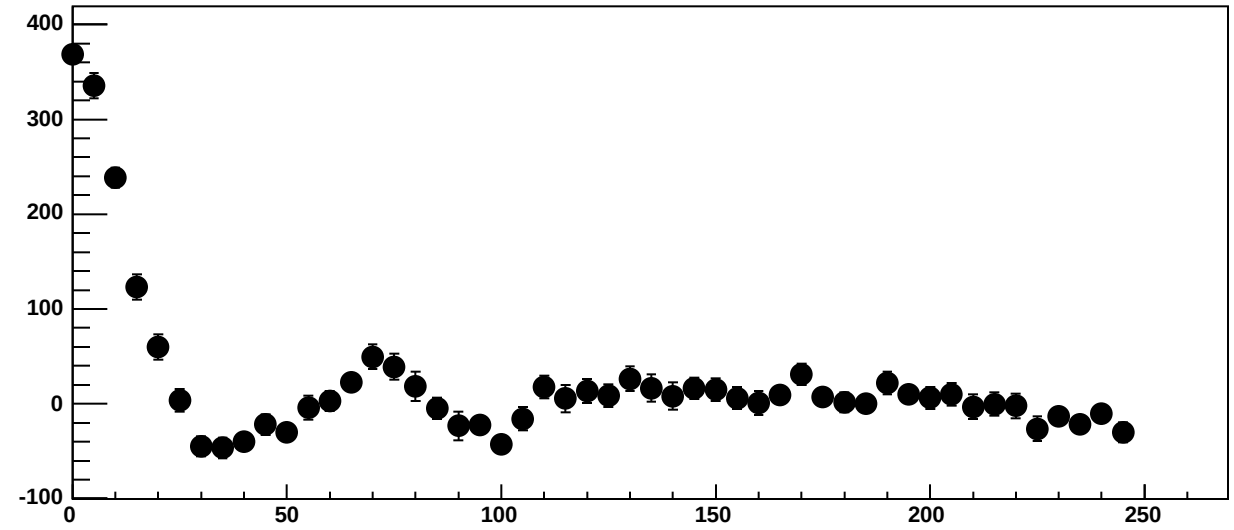
Chip 1, Channel 11, Enable 1, DAC=1600, ADC Mean vs Hold

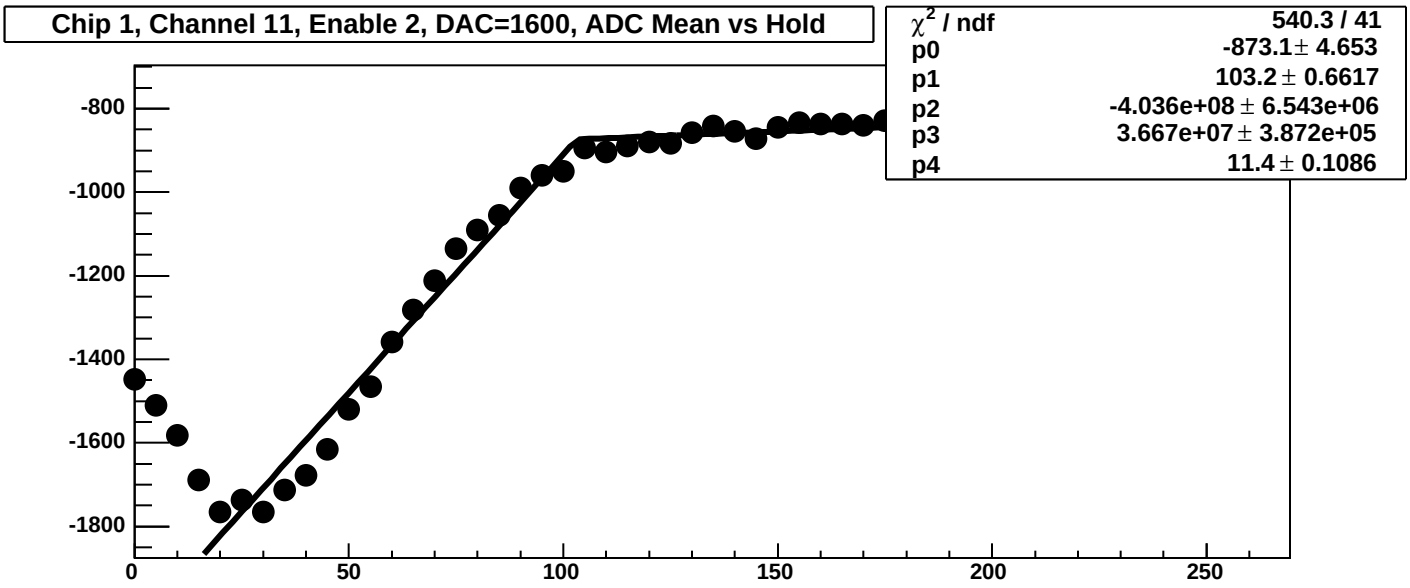


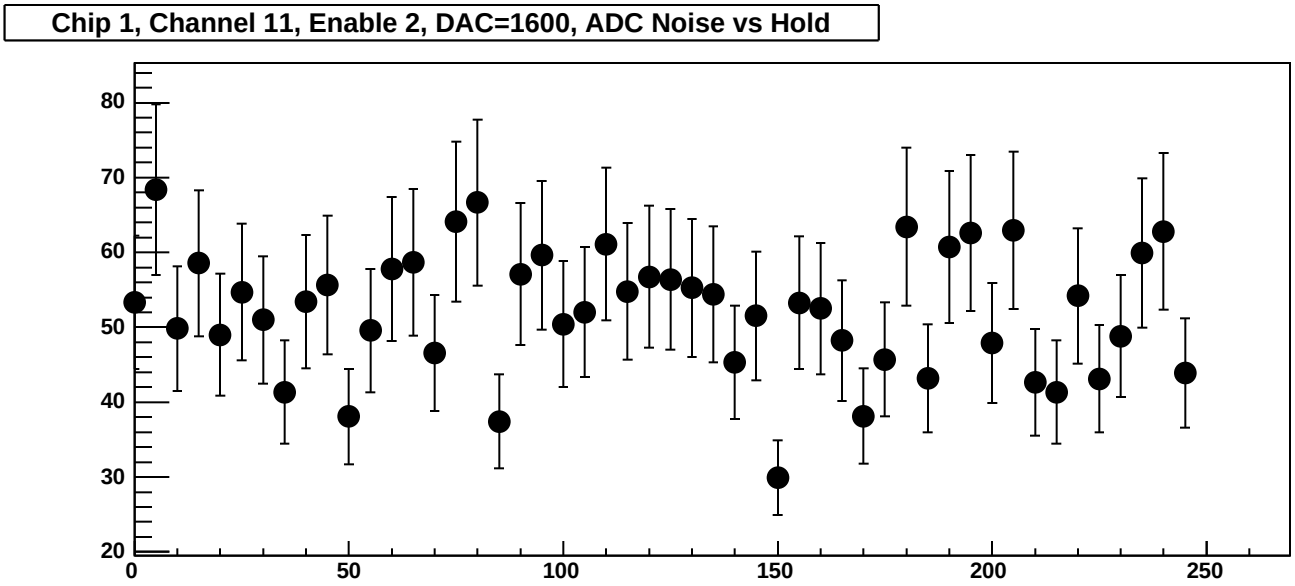
Chip 1, Channel 11, Enable 1, DAC=1600, ADC Noise vs Hold

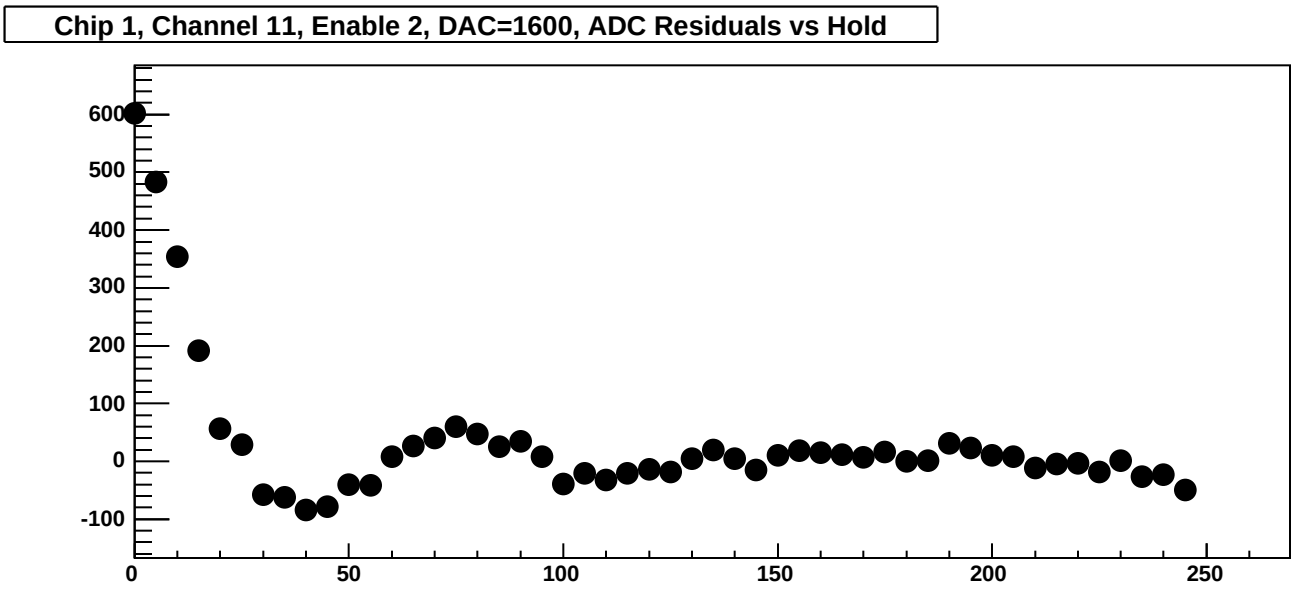


Chip 1, Channel 11, Enable 1, DAC=1600, ADC Residuals vs Hold

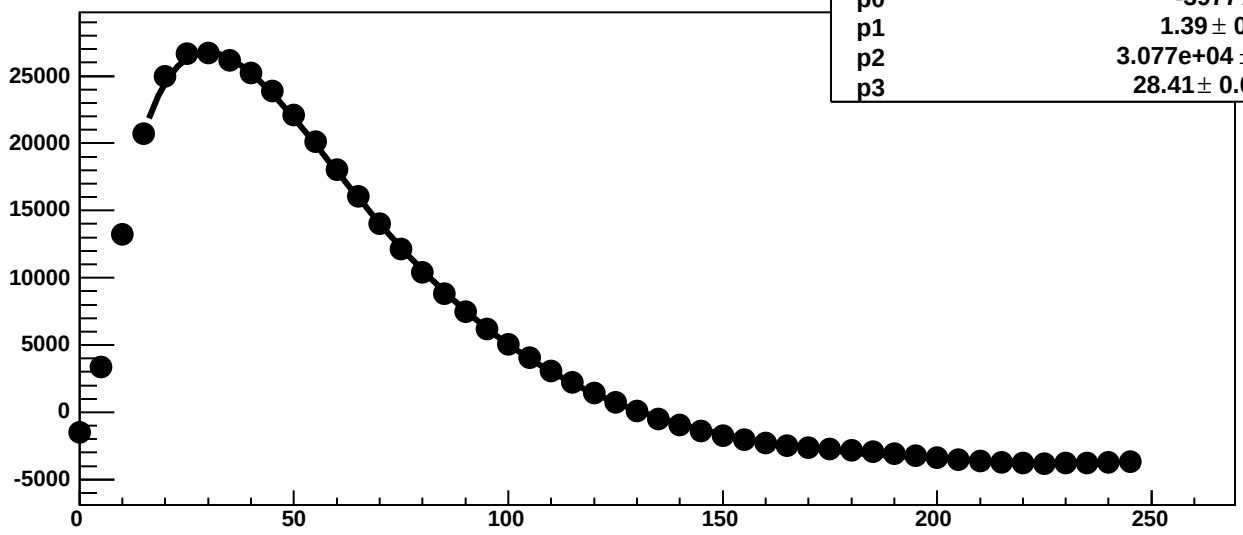



Chip 1, Channel 11, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 540.3 / 41
p0 -873.1 ± 4.653
p1 103.2 ± 0.6617
p2 -4.036e+08 ± 6.543e+06
p3 3.667e+07 ± 3.872e+05
p4 11.4 ± 0.1086
-800
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250

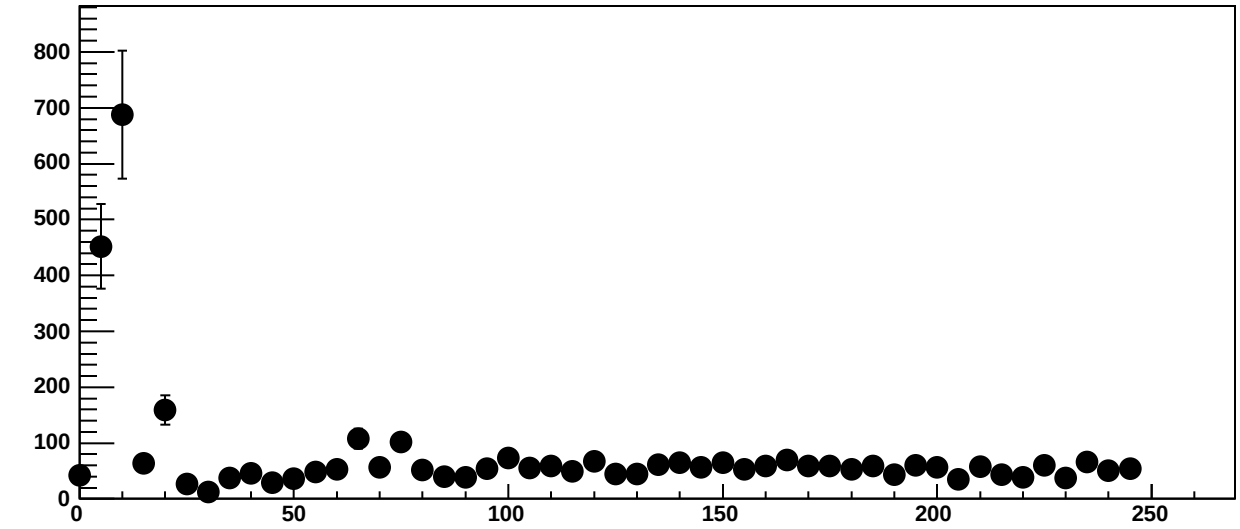

Chip 1, Channel 11, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 1, Channel 11, Enable 2, DAC=1600, ADC Residuals vs Hold
600
500
400
300
200
100
0
-100
0
50
100
150
200
250

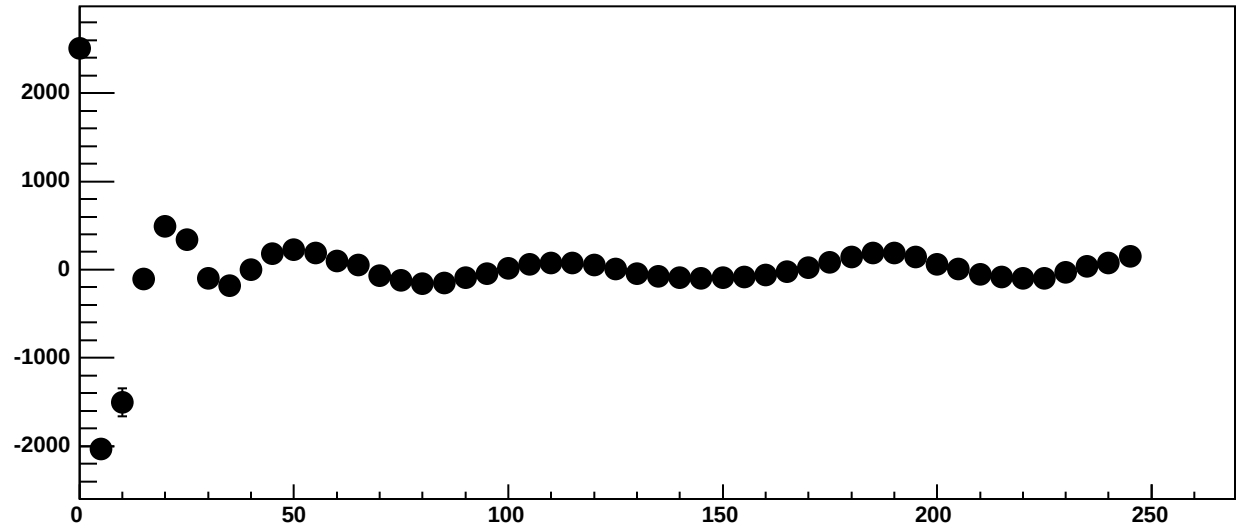
Chip 1, Channel 11, Enable 3!, DAC=1600, ADC Mean vs Hold



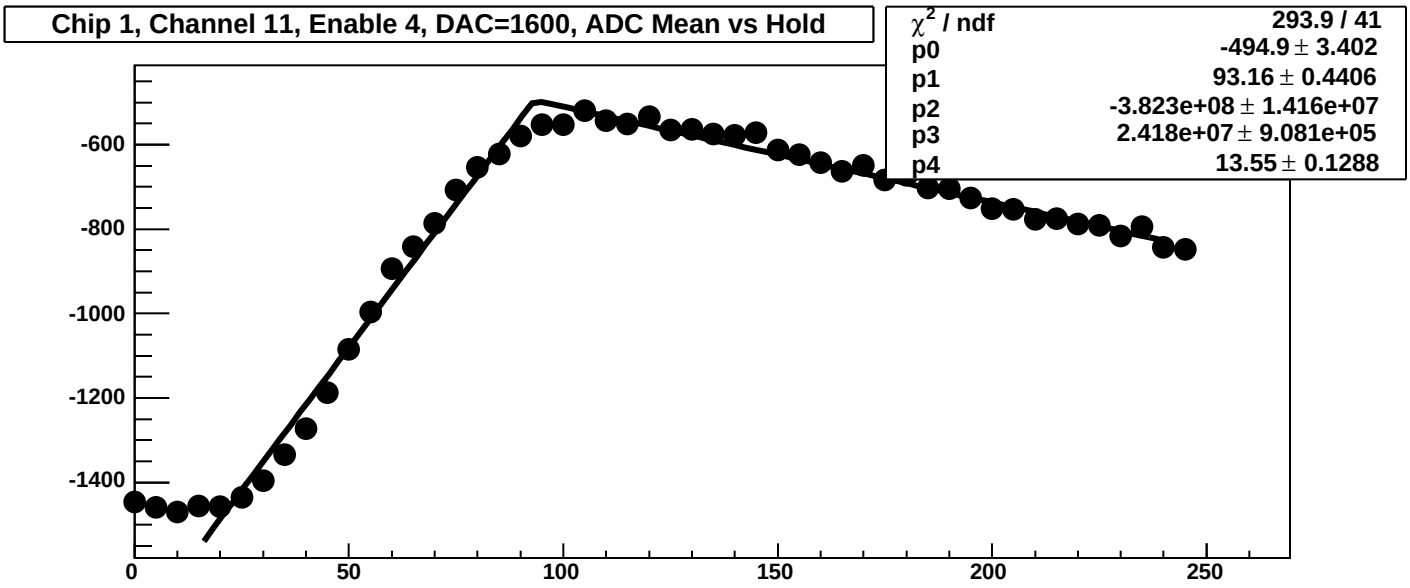
Chip 1, Channel 11, Enable 3!, DAC=1600, ADC Noise vs Hold



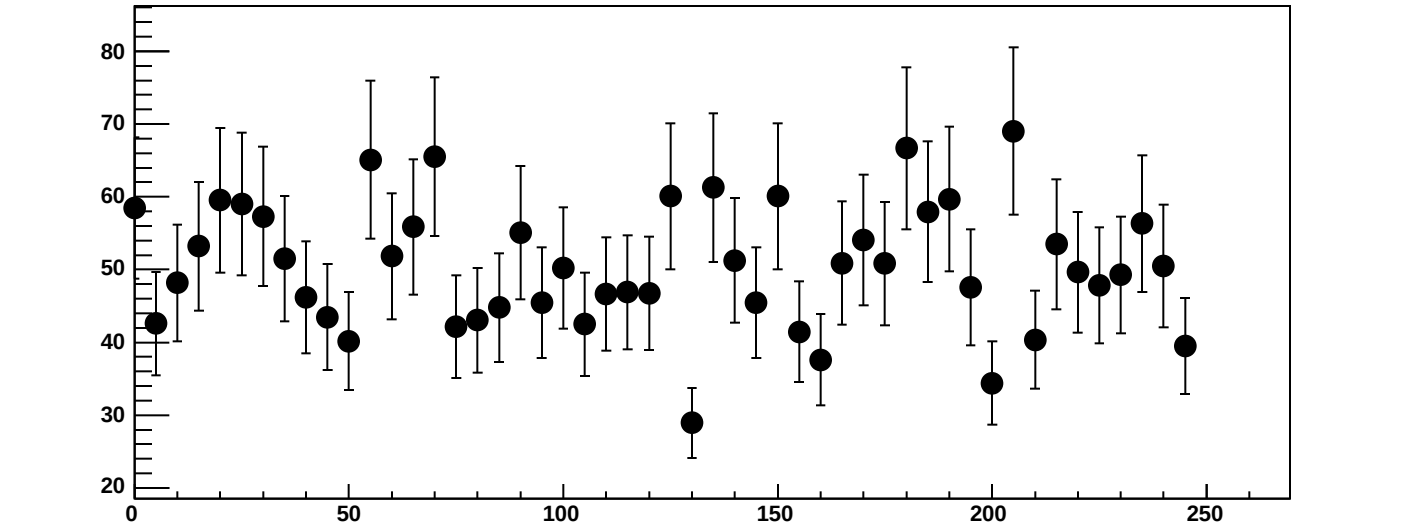
Chip 1, Channel 11, Enable 3!, DAC=1600, ADC Residuals vs Hold



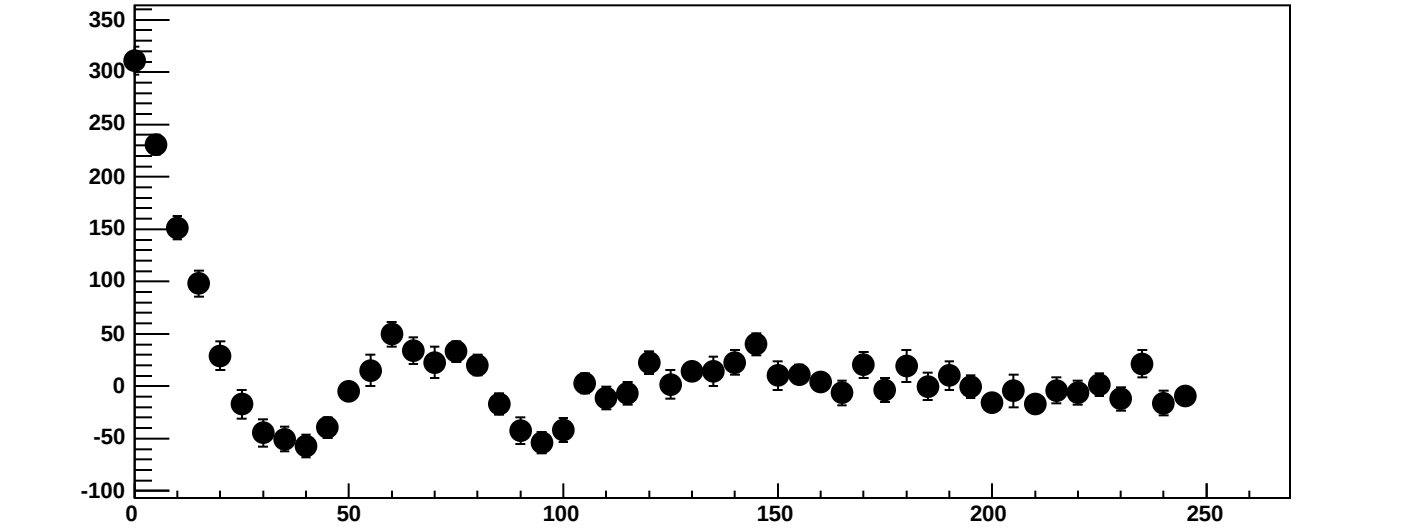
Chip 1, Channel 11, Enable 4, DAC=1600, ADC Mean vs Hold

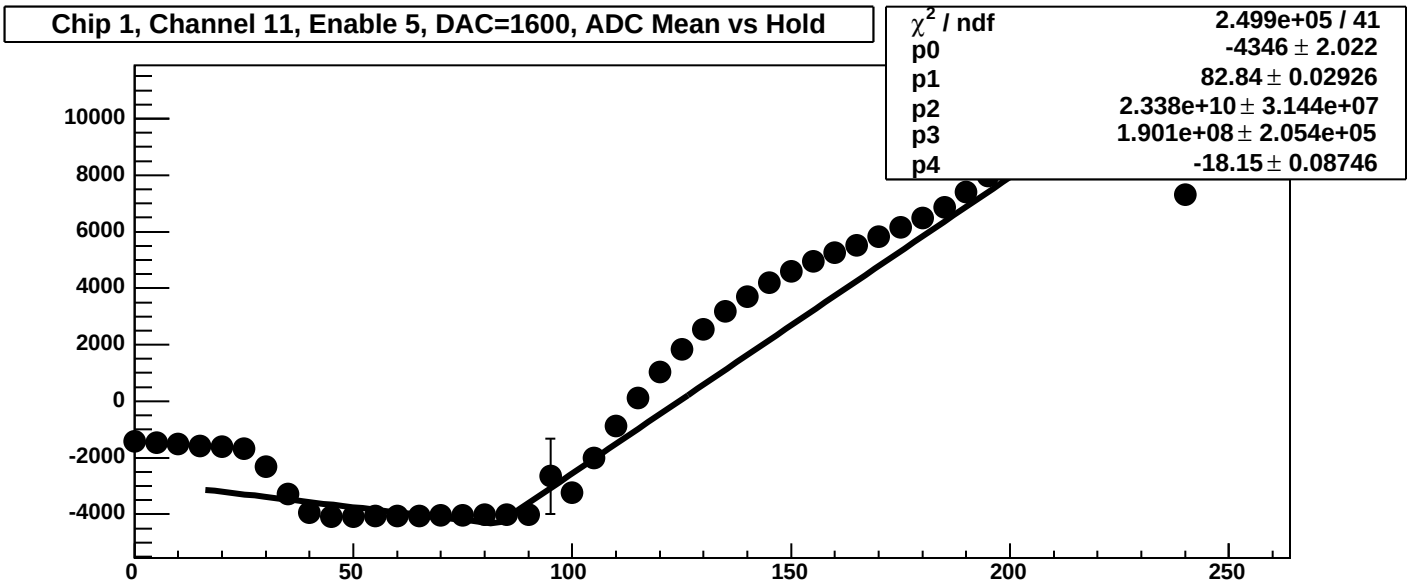


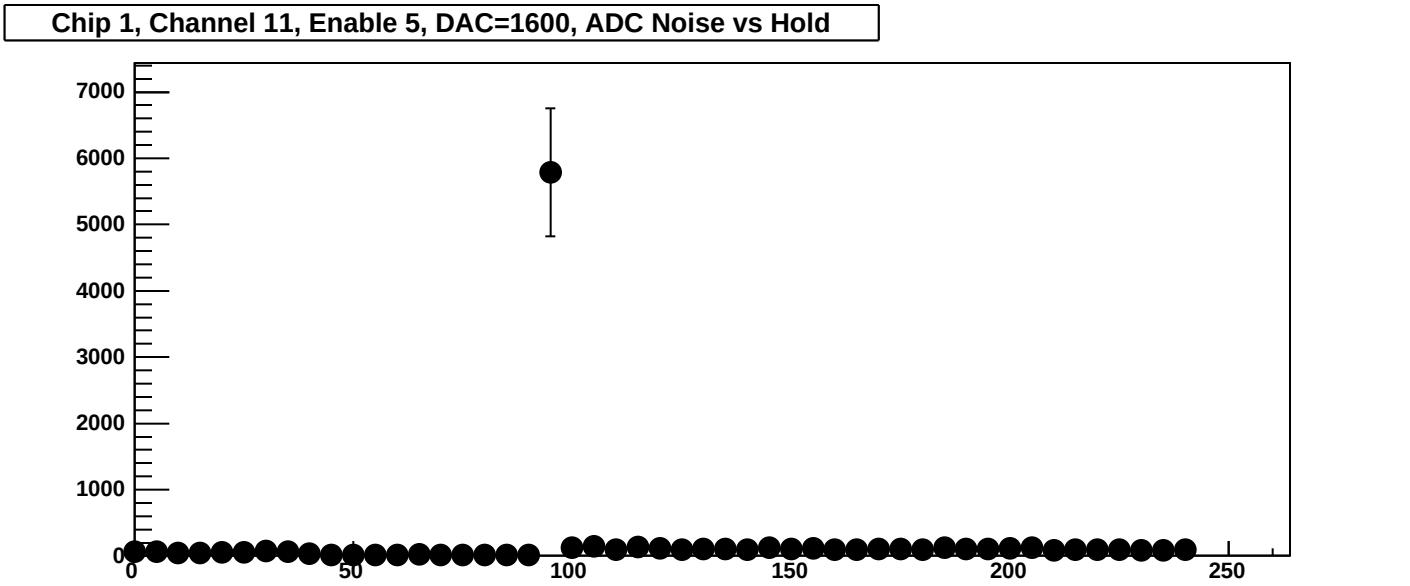
Chip 1, Channel 11, Enable 4, DAC=1600, ADC Noise vs Hold

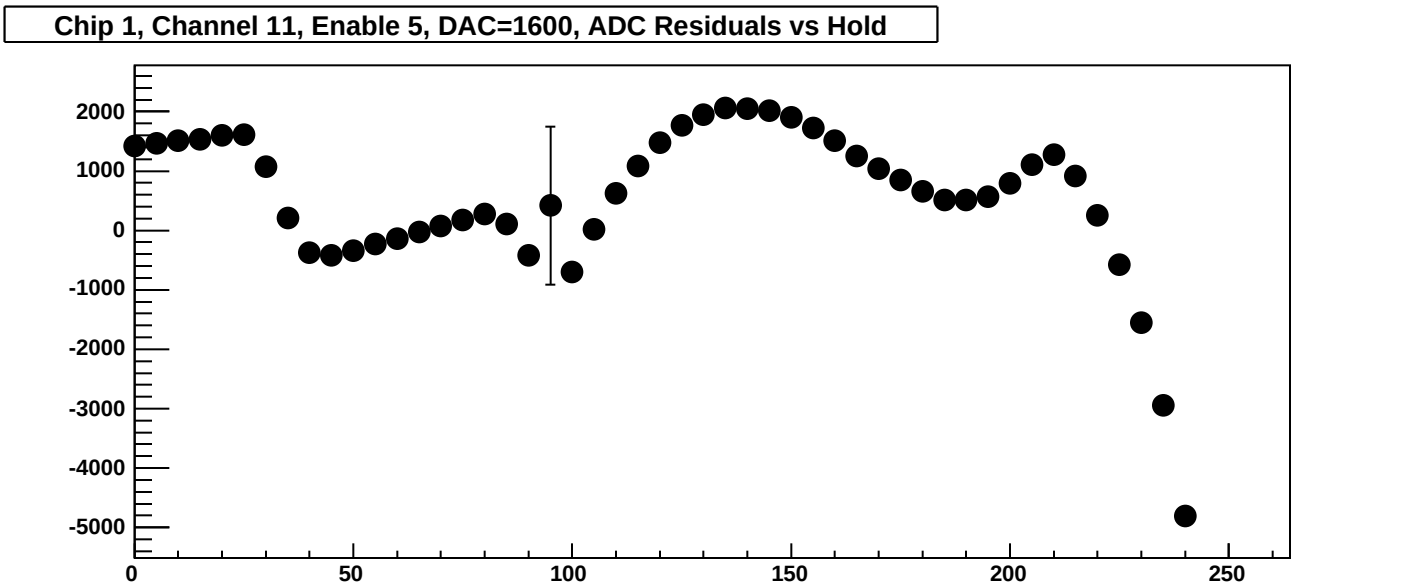


Chip 1, Channel 11, Enable 4, DAC=1600, ADC Residuals vs Hold

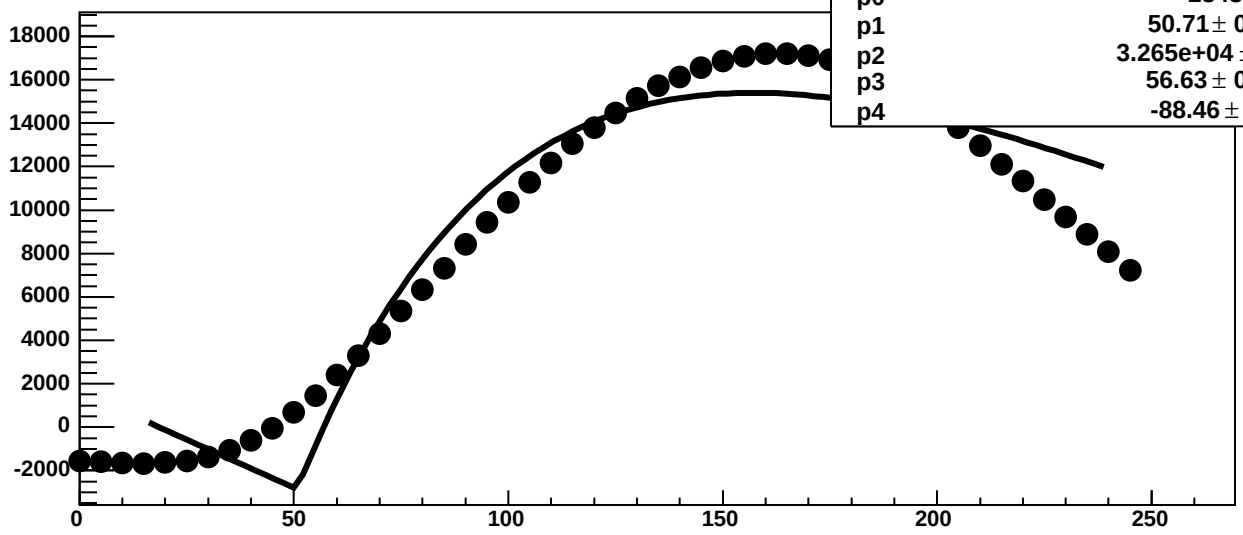



Chip 1, Channel 11, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.499e+05 / 41
p0 -4346 ± 2.022
p1 82.84 ± 0.02926
p2 2.338e+10 ± 3.144e+07
p3 1.901e+08 ± 2.054e+05
p4 -18.15 ± 0.08746
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

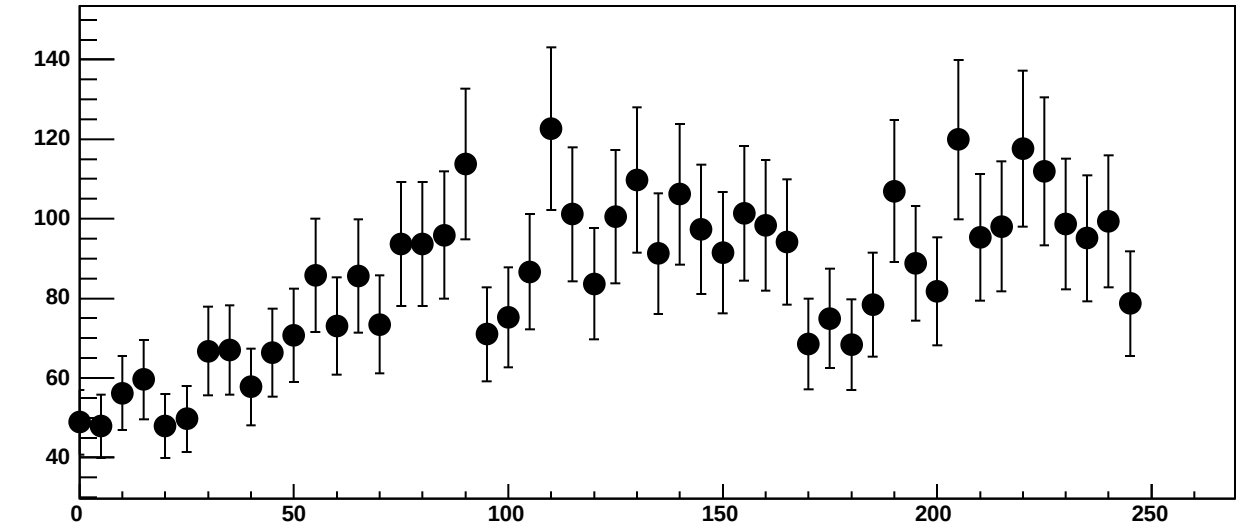

Chip 1, Channel 11, Enable 5, DAC=1600, ADC Noise vs Hold
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 1, Channel 11, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

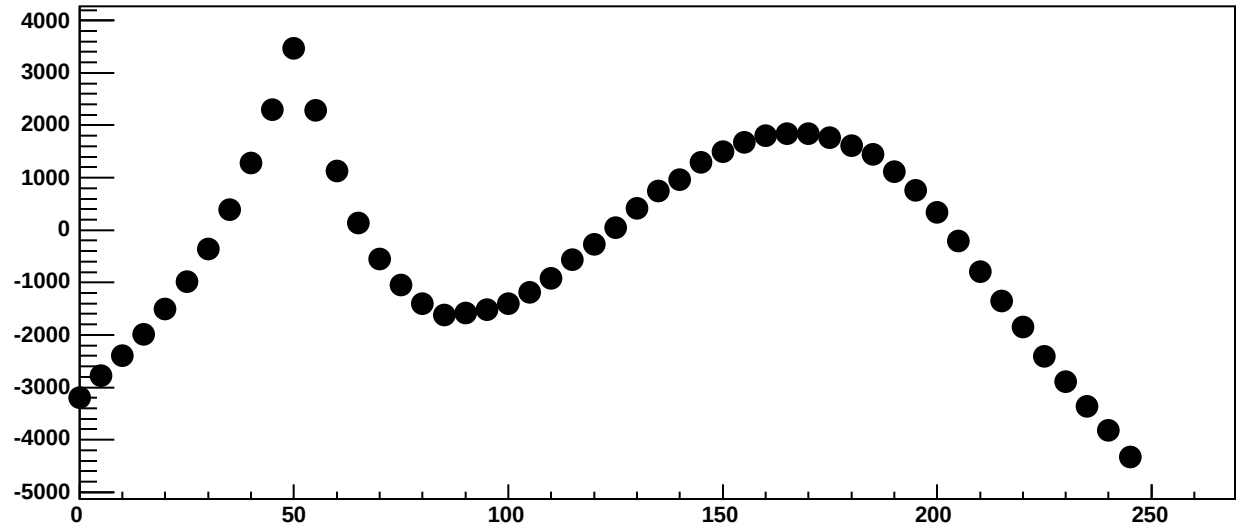
Chip 1, Channel 12, Enable 0, DAC=1600, ADC Mean vs Hold



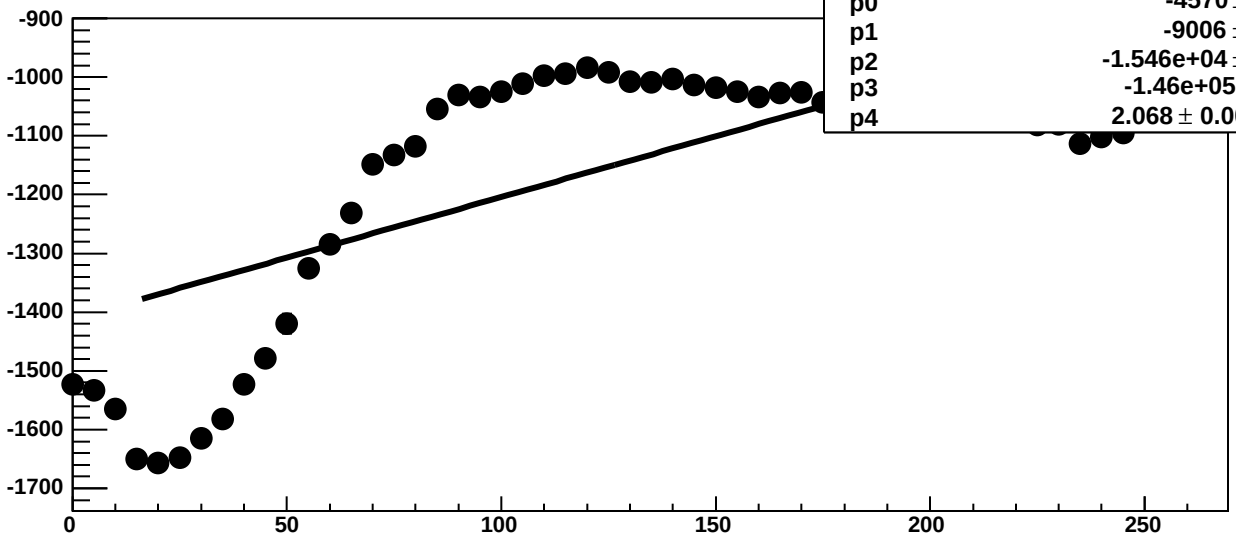
Chip 1, Channel 12, Enable 0, DAC=1600, ADC Noise vs Hold



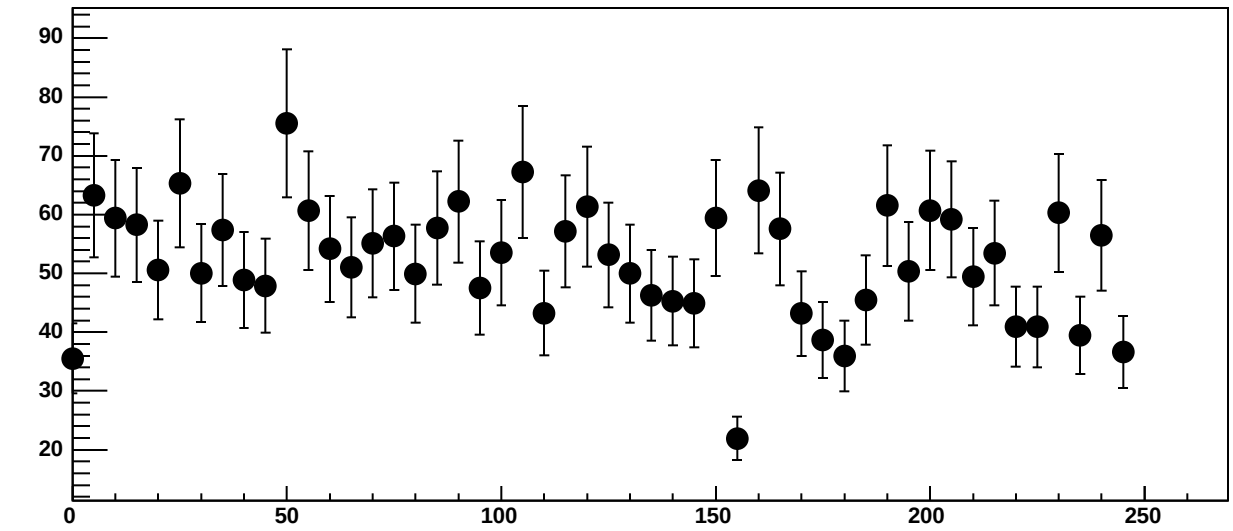
Chip 1, Channel 12, Enable 0, DAC=1600, ADC Residuals vs Hold



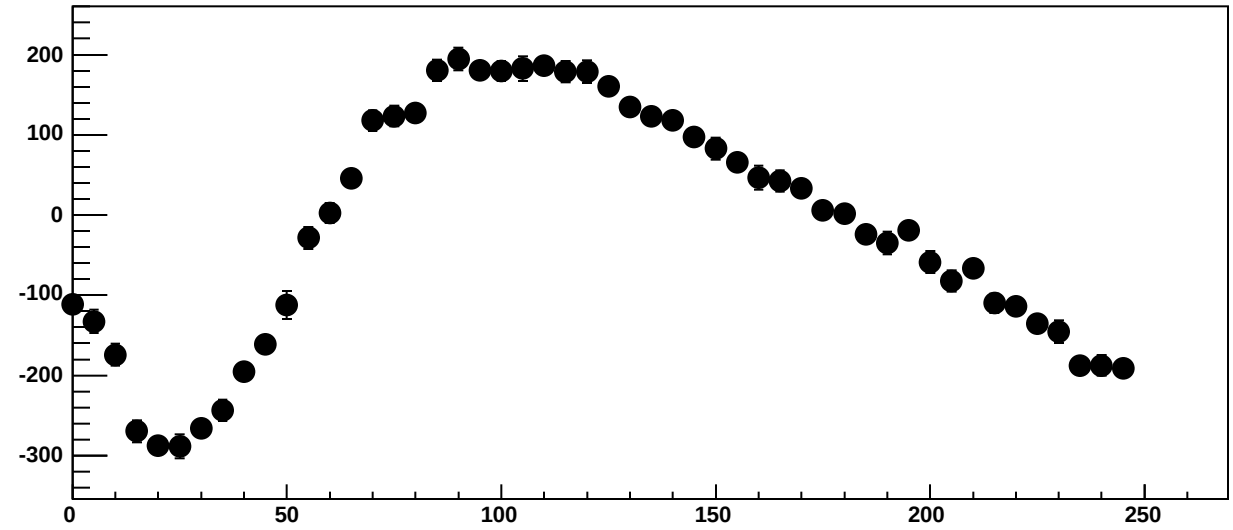
Chip 1, Channel 12, Enable 1, DAC=1600, ADC Mean vs Hold

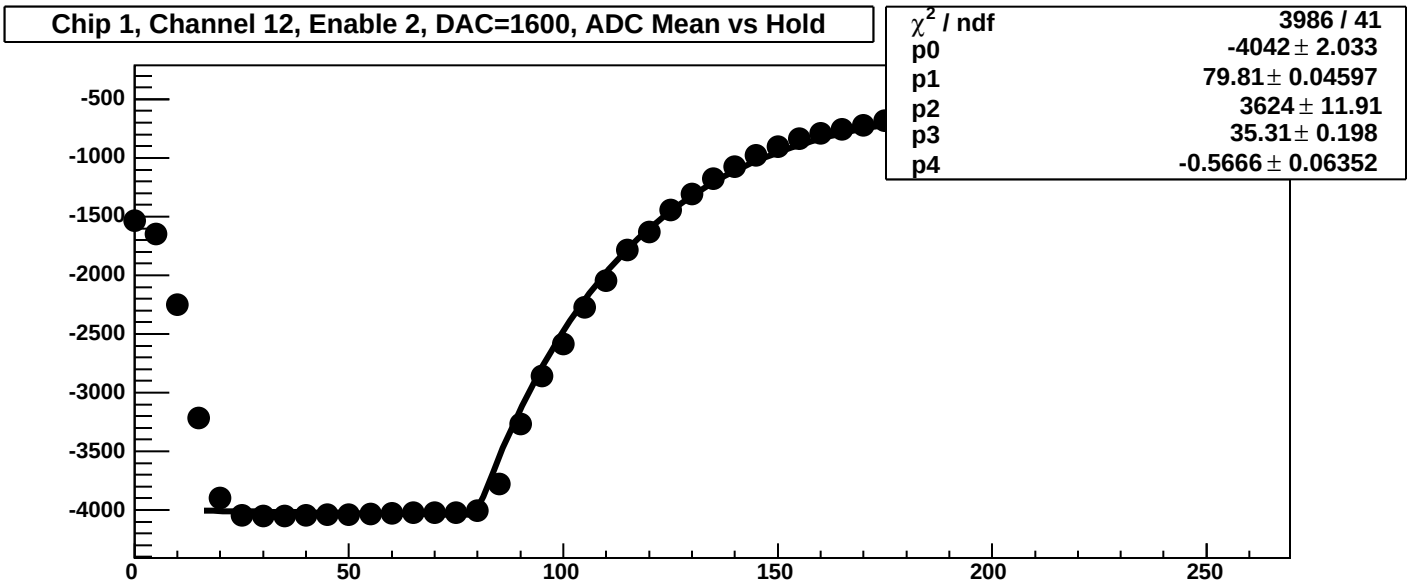


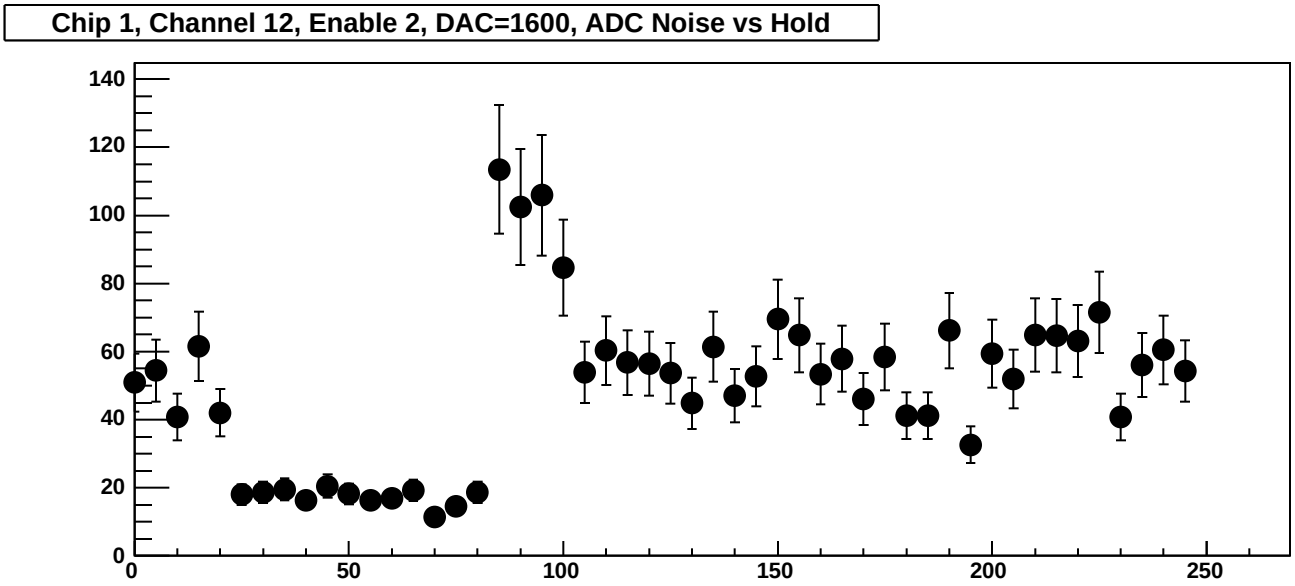
Chip 1, Channel 12, Enable 1, DAC=1600, ADC Noise vs Hold

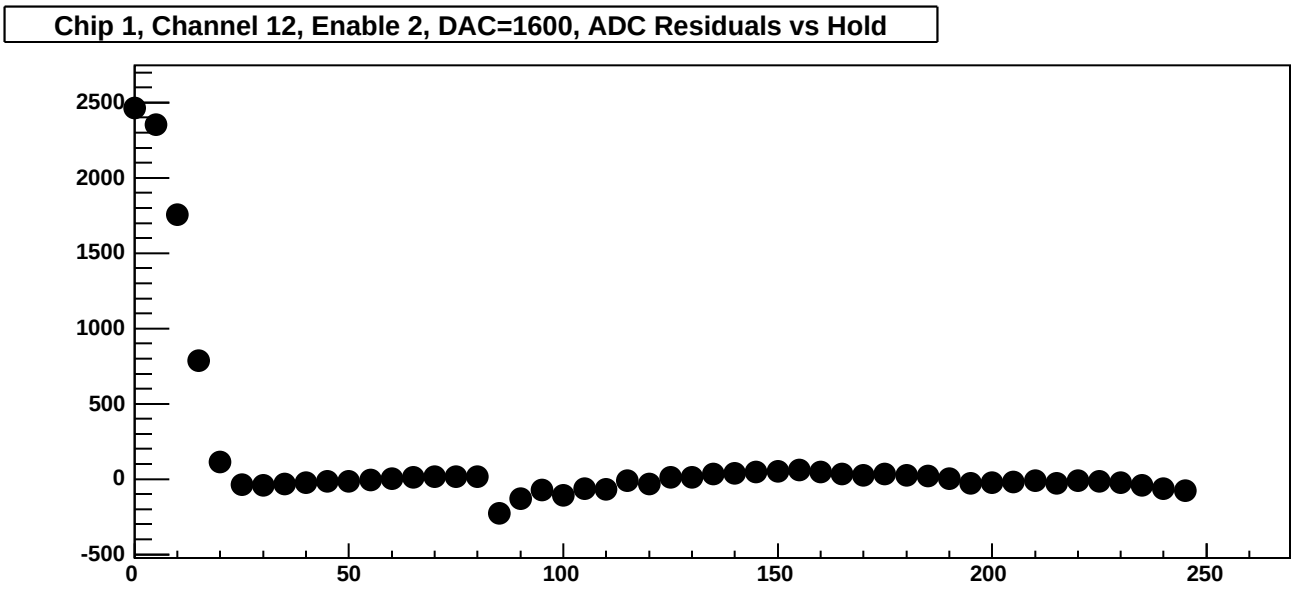


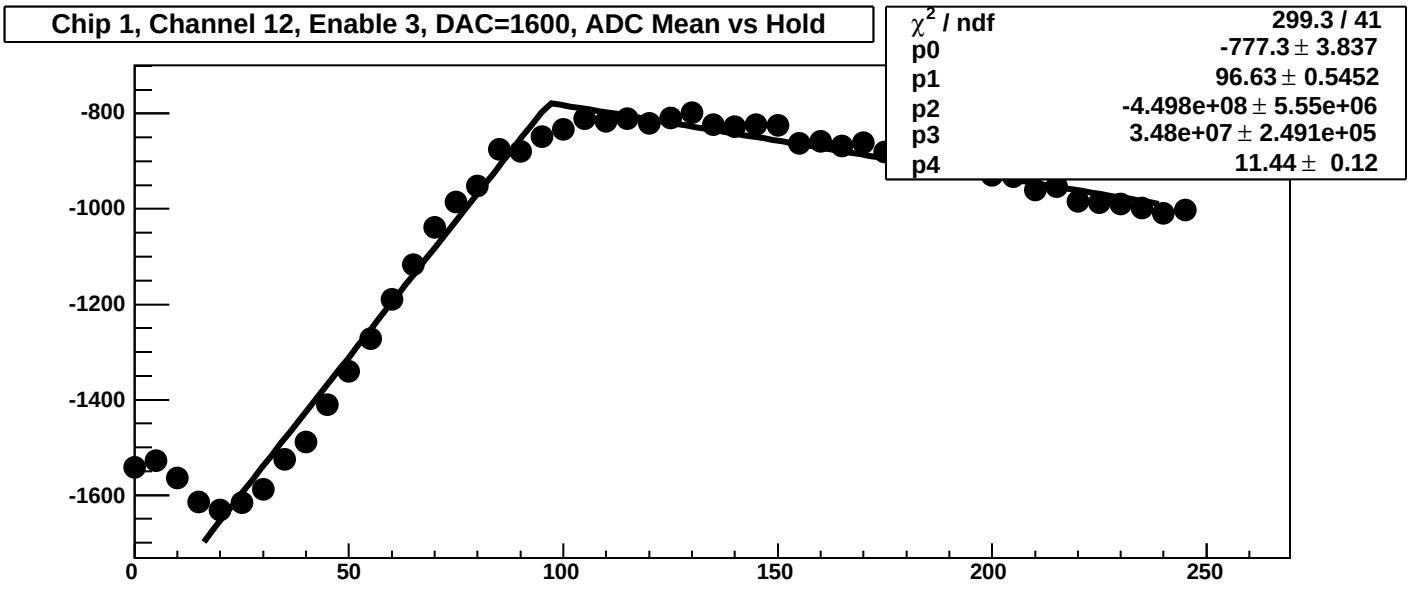
Chip 1, Channel 12, Enable 1, DAC=1600, ADC Residuals vs Hold

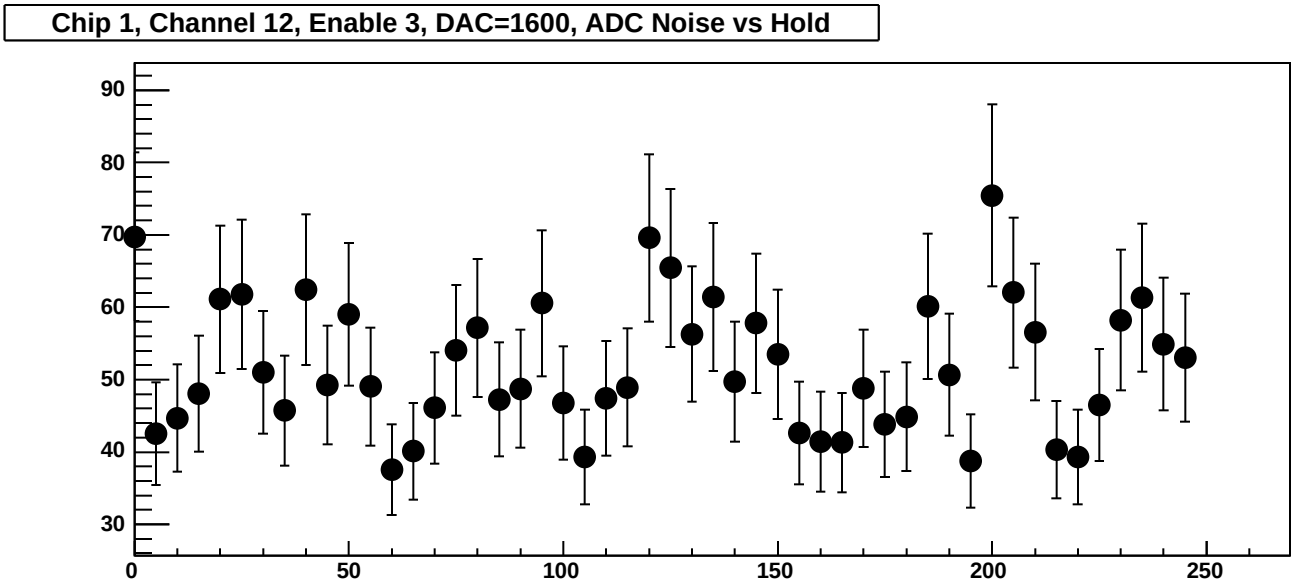


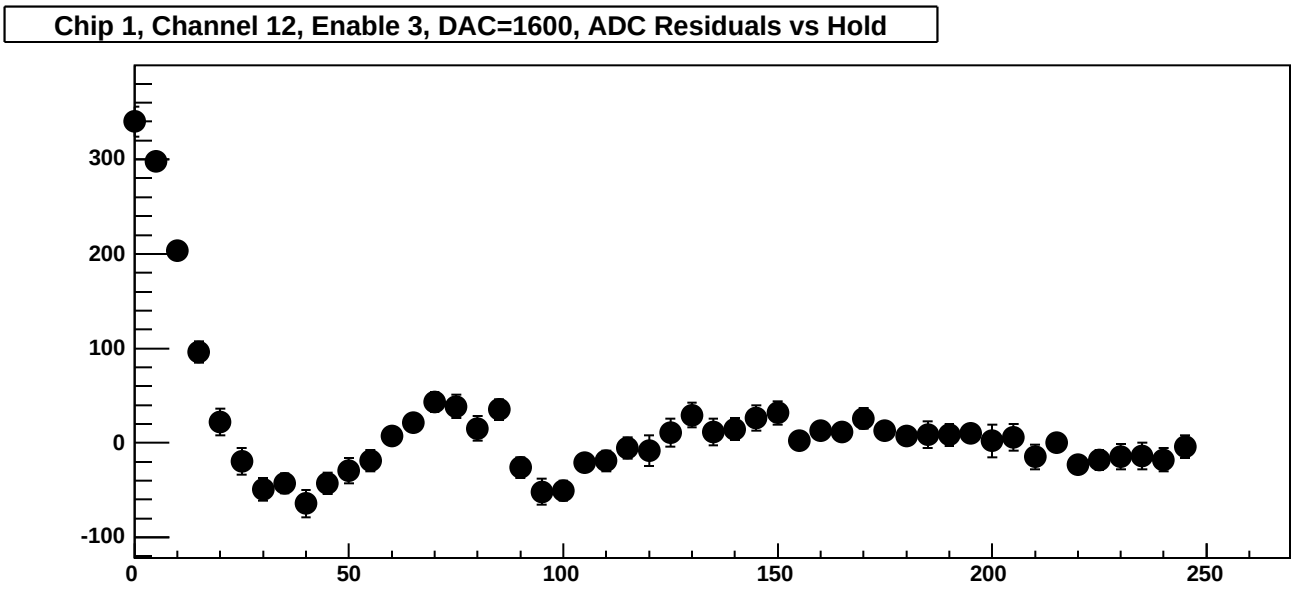

Chip 1, Channel 12, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 3986 / 41
p0 -4042 ± 2.033
p1 79.81 ± 0.04597
p2 3624 ± 11.91
p3 35.31 ± 0.198
p4 -0.5666 ± 0.06352
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
0
50
100
150
200
250


Chip 1, Channel 12, Enable 2, DAC=1600, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 1, Channel 12, Enable 2, DAC=1600, ADC Residuals vs Hold
2500
2000
1500
1000
500
0
-500
0
50
100
150
200
250

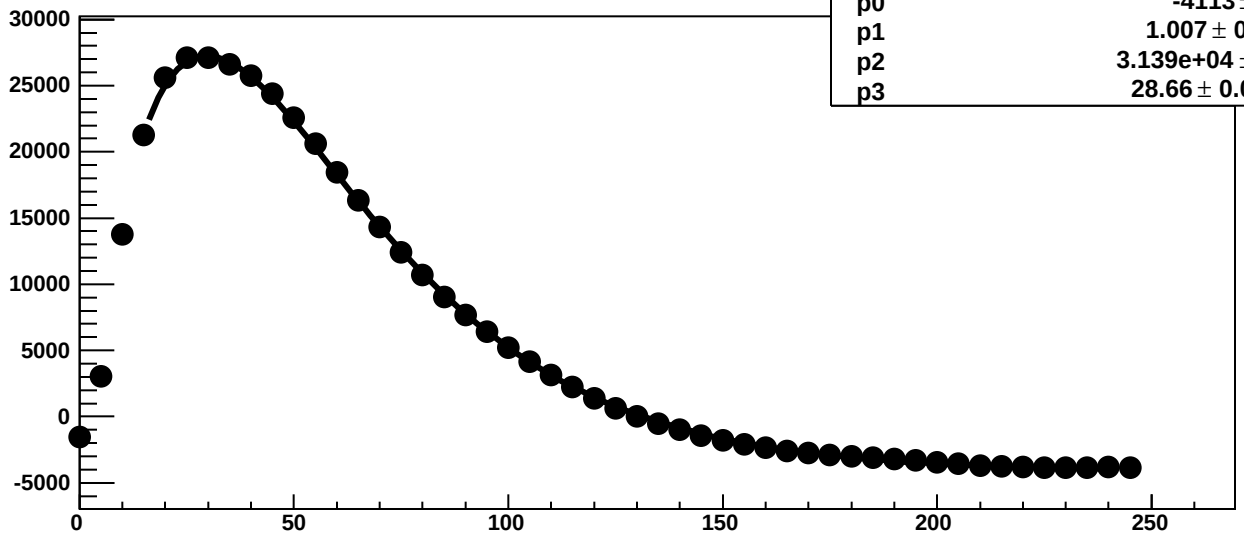

Chip 1, Channel 12, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 299.3 / 41
p0 -777.3 ± 3.837
p1 96.63 ± 0.5452
p2 -4.498e+08 ± 5.55e+06
p3 3.48e+07 ± 2.491e+05
p4 11.44 ± 0.12
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250


Chip 1, Channel 12, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

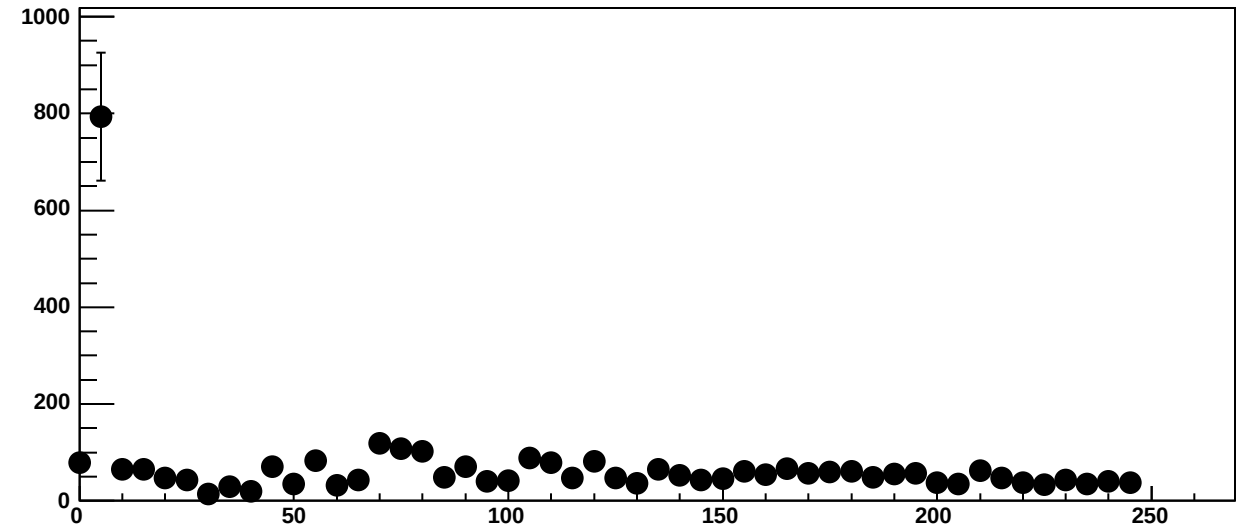

Chip 1, Channel 12, Enable 3, DAC=1600, ADC Residuals vs Hold
300
200
100
0
-100
0
50
100
150
200
250

Chip 1, Channel 12, Enable 4!, DAC=1600, ADC Mean vs Hold

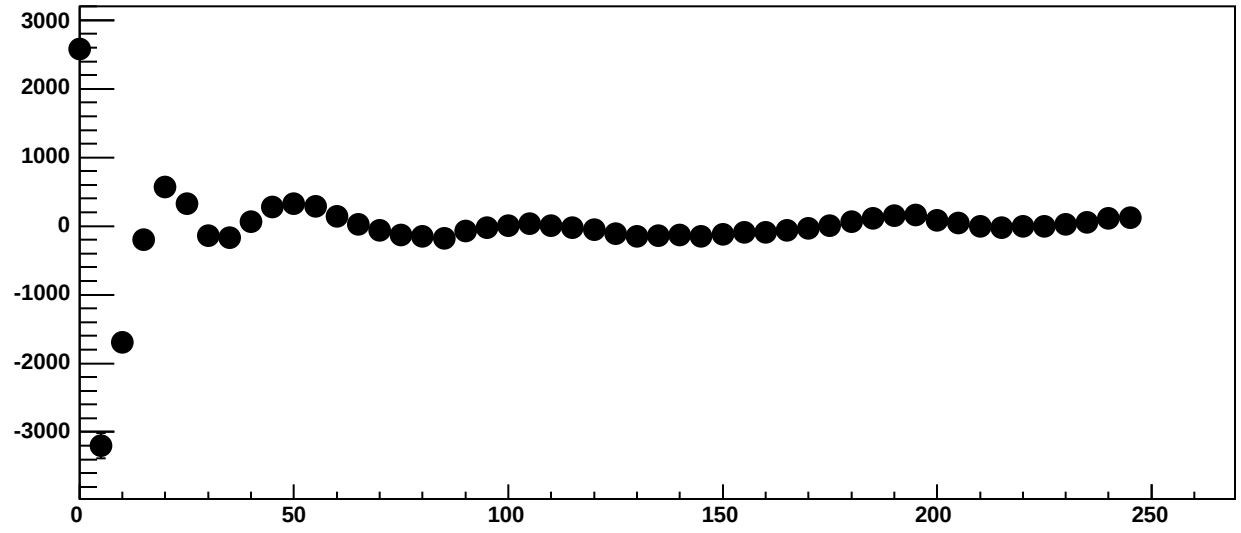
χ^2 / ndf	1.129e+04 / 42
p0	-4113 ± 2.822
p1	1.007 ± 0.01399
p2	3.139e+04 ± 3.476
p3	28.66 ± 0.008413



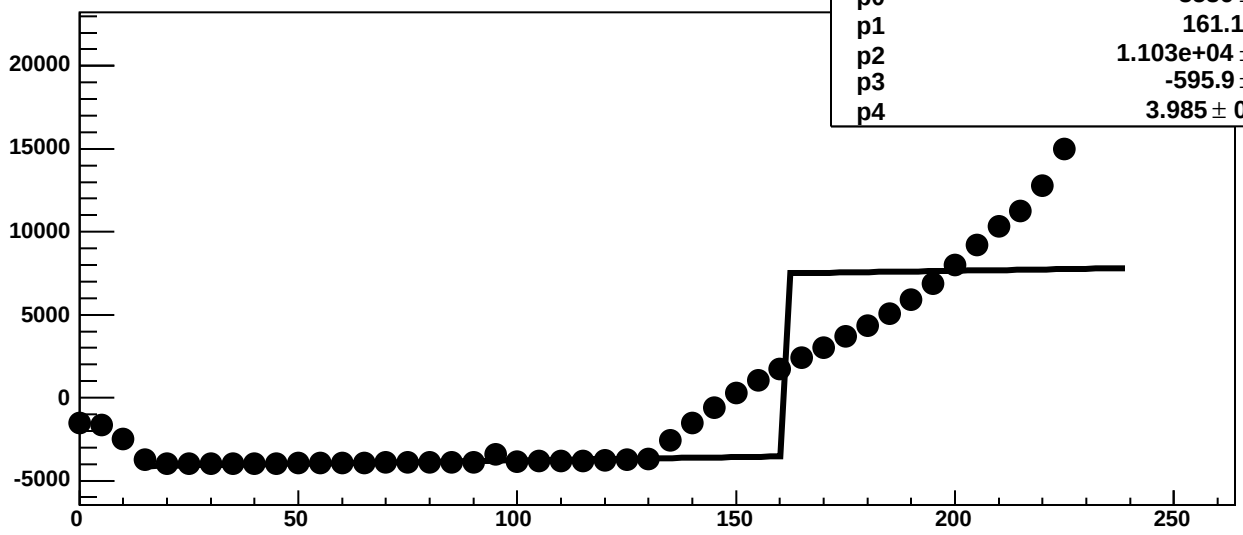
Chip 1, Channel 12, Enable 4!, DAC=1600, ADC Noise vs Hold



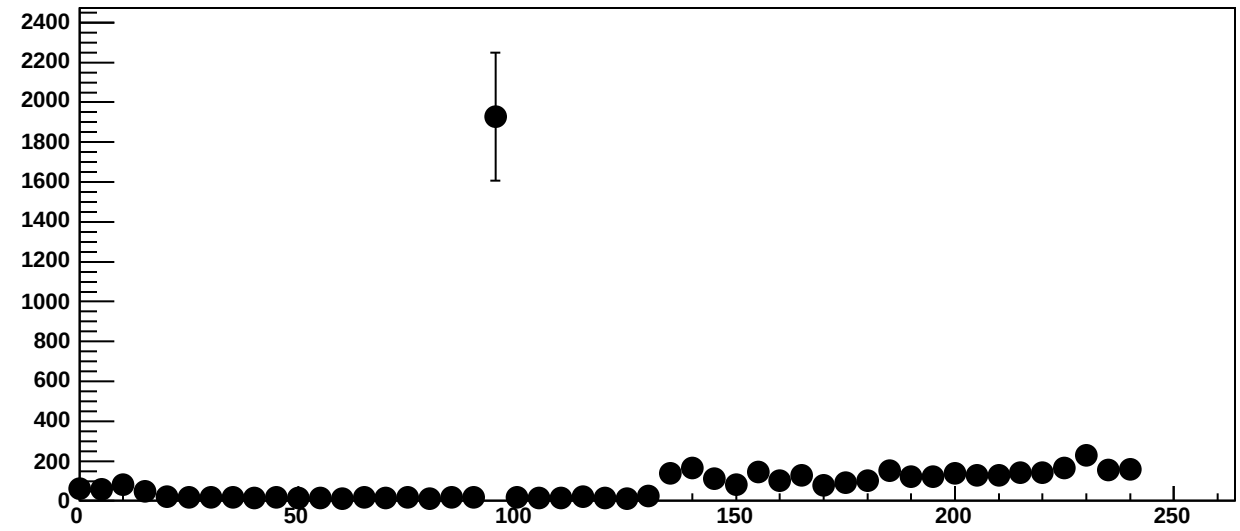
Chip 1, Channel 12, Enable 4!, DAC=1600, ADC Residuals vs Hold



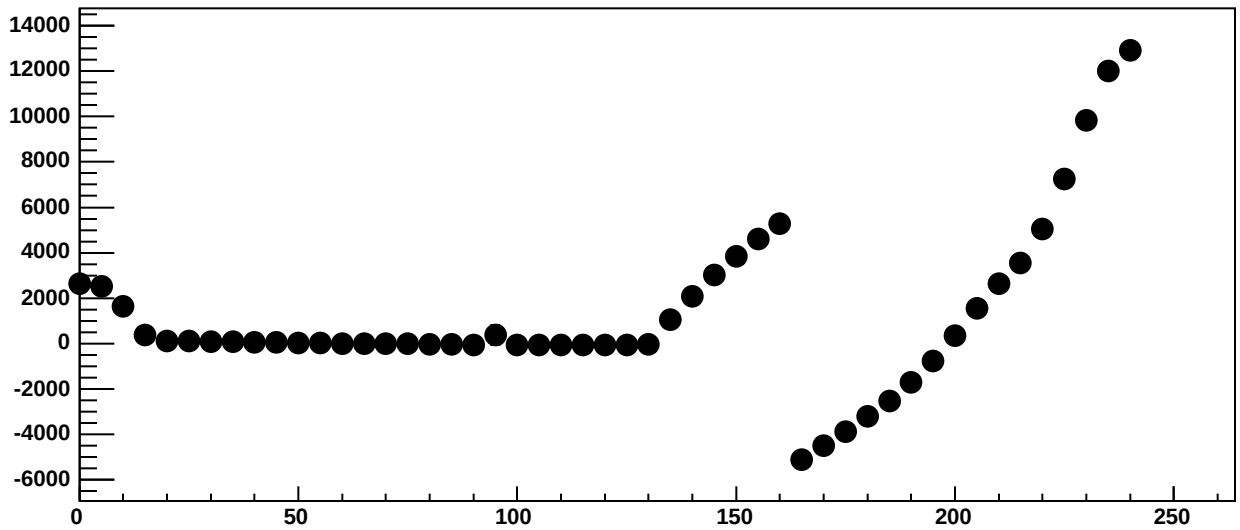
Chip 1, Channel 12, Enable 5, DAC=1600, ADC Mean vs Hold

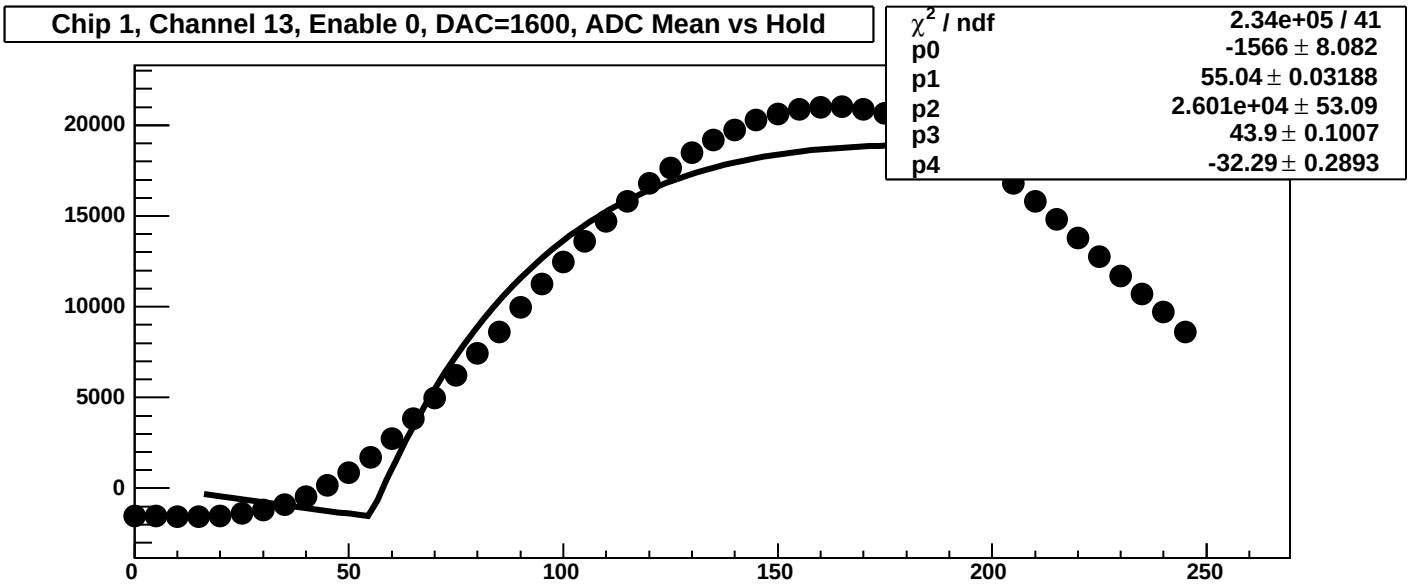


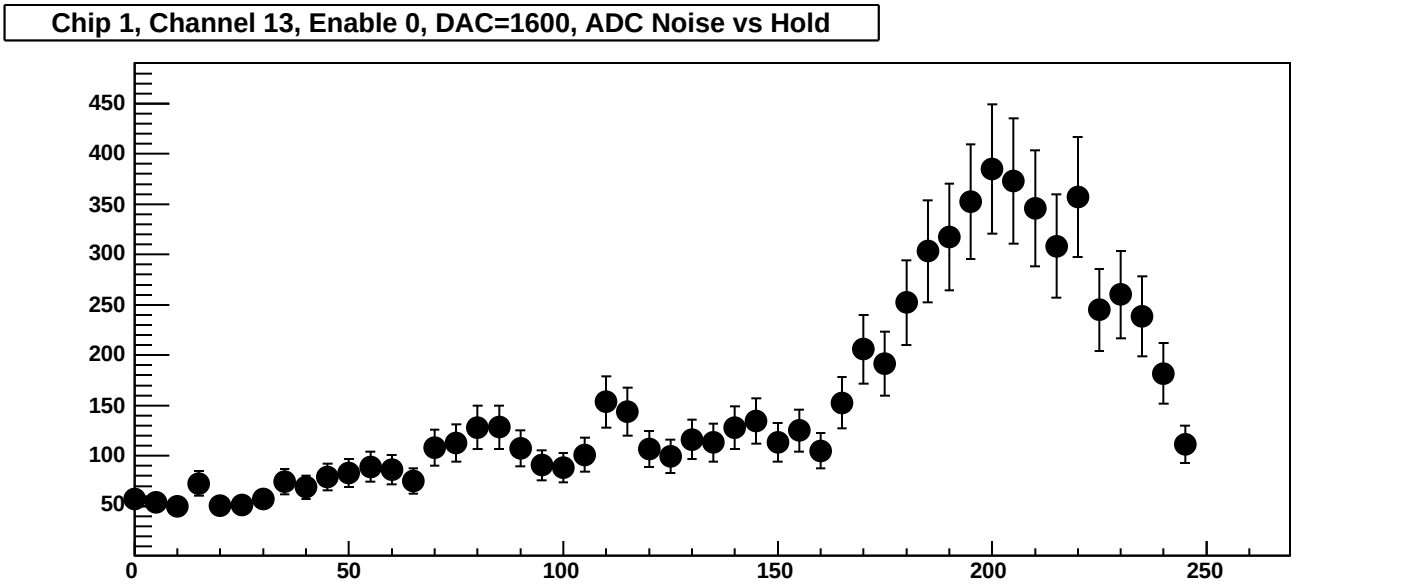
Chip 1, Channel 12, Enable 5, DAC=1600, ADC Noise vs Hold

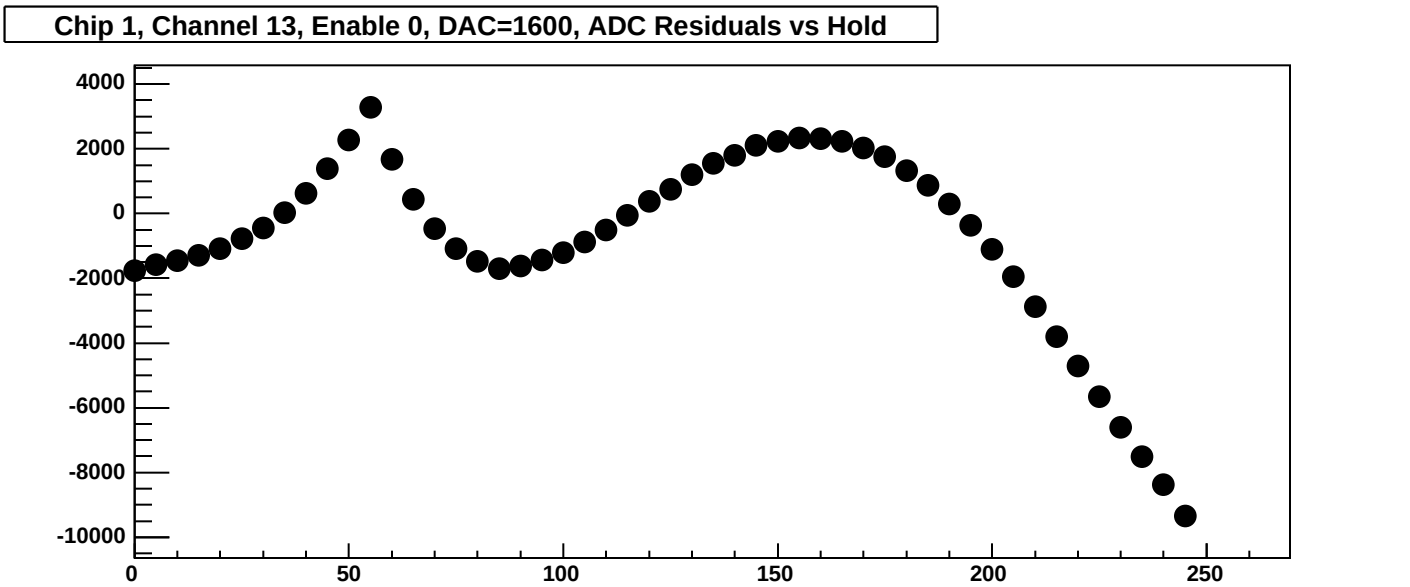


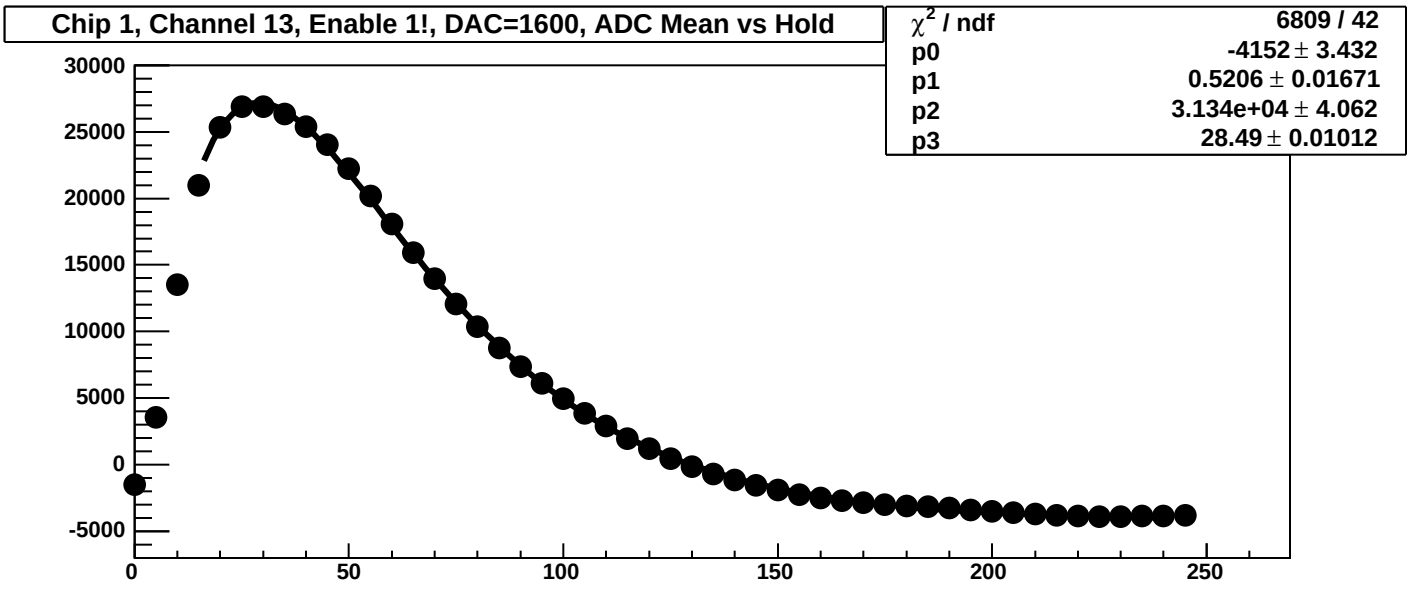
Chip 1, Channel 12, Enable 5, DAC=1600, ADC Residuals vs Hold

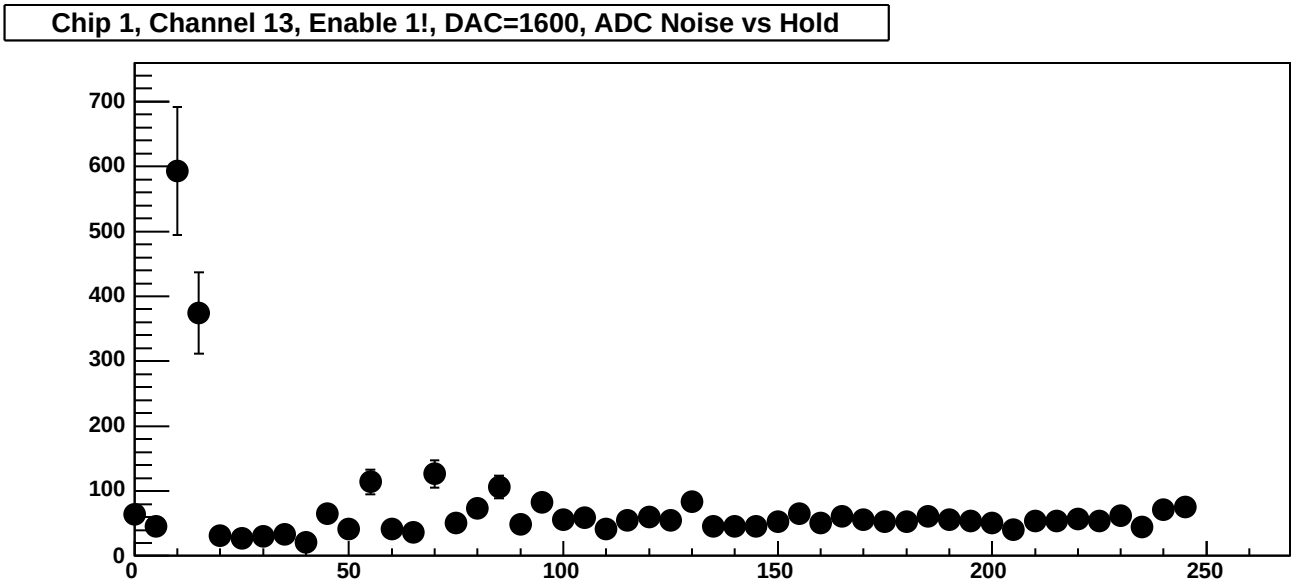


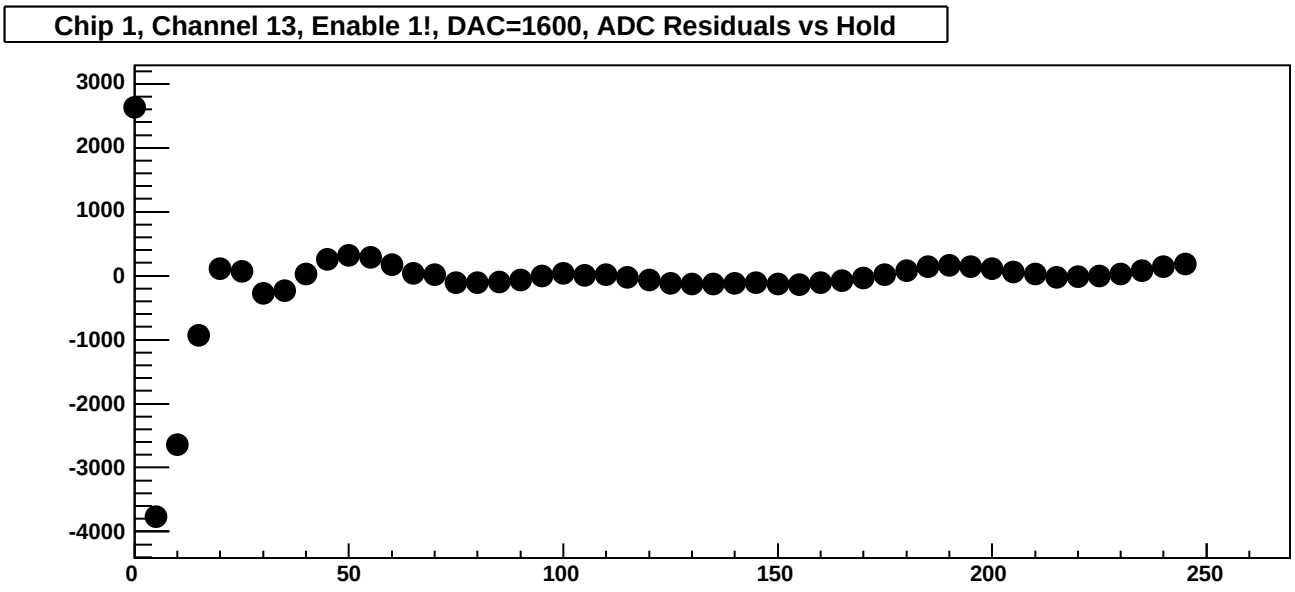

Chip 1, Channel 13, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.34e+05 / 41
p0 -1566 ± 8.082
p1 55.04 ± 0.03188
p2 2.601e+04 ± 53.09
p3 43.9 ± 0.1007
p4 -32.29 ± 0.2893
20000
15000
10000
5000
0
0
50
100
150
200
250

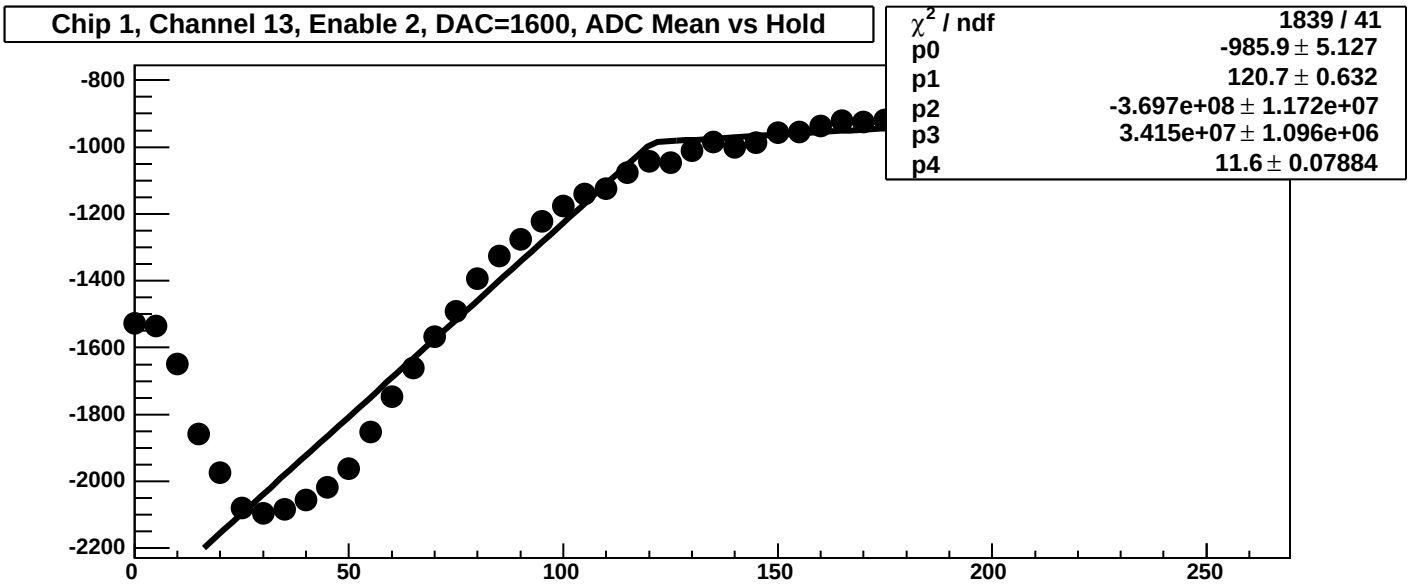

Chip 1, Channel 13, Enable 0, DAC=1600, ADC Noise vs Hold
450
400
350
300
250
200
150
100
50
0
50
100
150
200
250

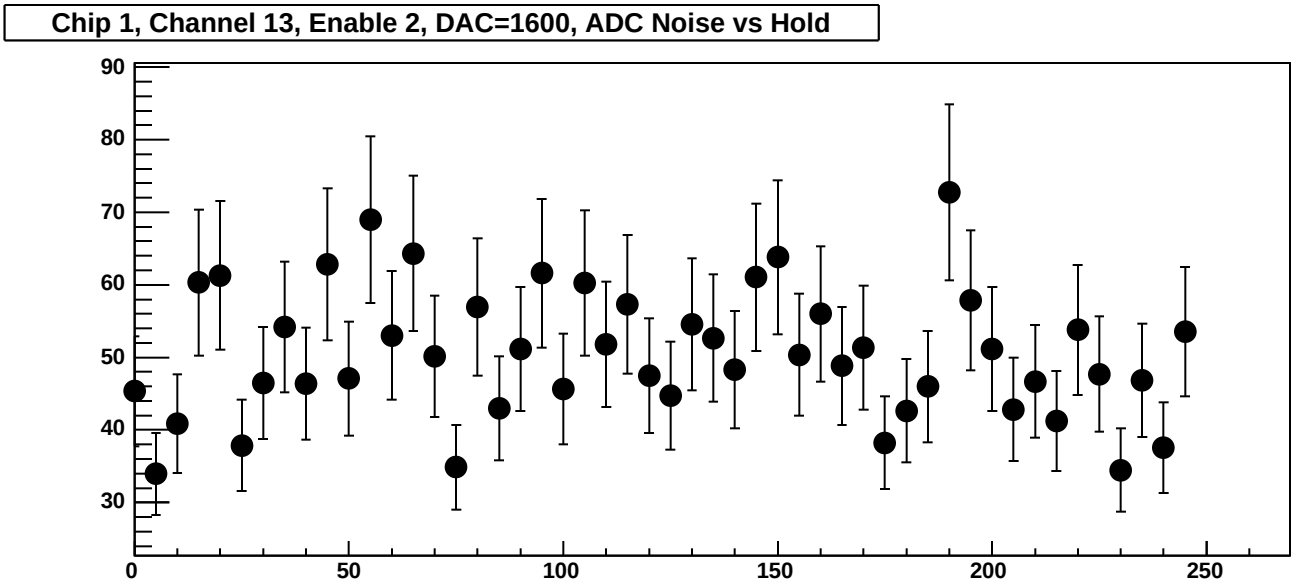

Chip 1, Channel 13, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

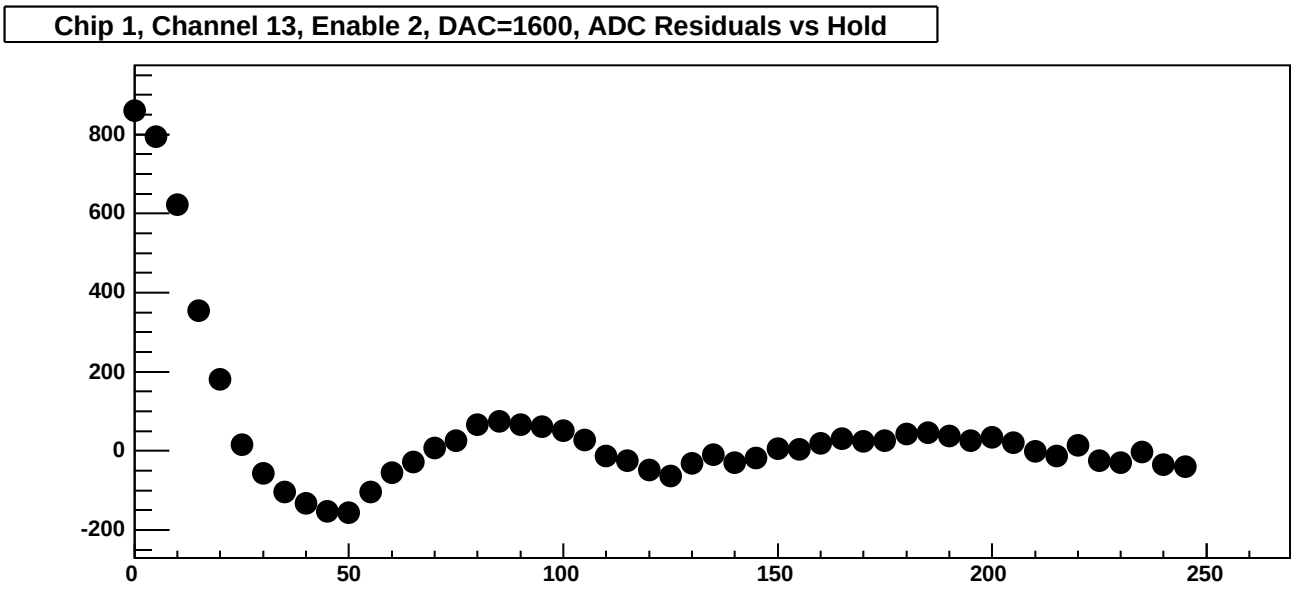

Chip 1, Channel 13, Enable 1!, DAC=1600, ADC Mean vs Hold
χ² / ndf 6809 / 42
p0 -4152 ± 3.432
p1 0.5206 ± 0.01671
p2 3.134e+04 ± 4.062
p3 28.49 ± 0.01012
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 1, Channel 13, Enable 1!, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250

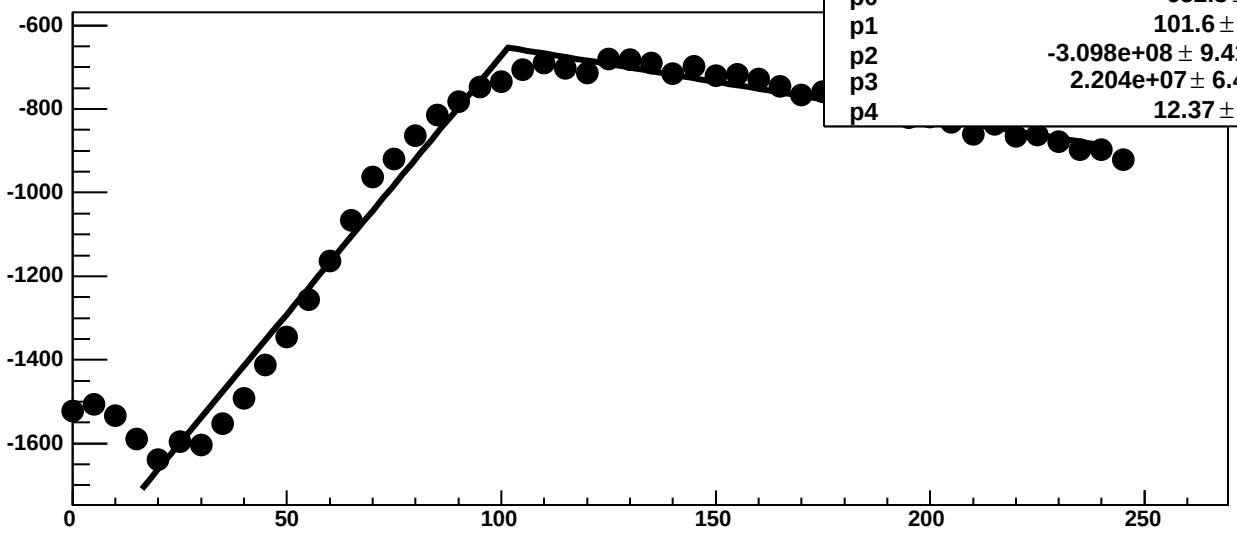

Chip 1, Channel 13, Enable 1!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250


Chip 1, Channel 13, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 1839 / 41
p0 -985.9 ± 5.127
p1 120.7 ± 0.632
p2 -3.697e+08 ± 1.172e+07
p3 3.415e+07 ± 1.096e+06
p4 11.6 ± 0.07884
-800
-1000
-1200
-1400
-1600
-1800
-2000
-2200
0
50
100
150
200
250

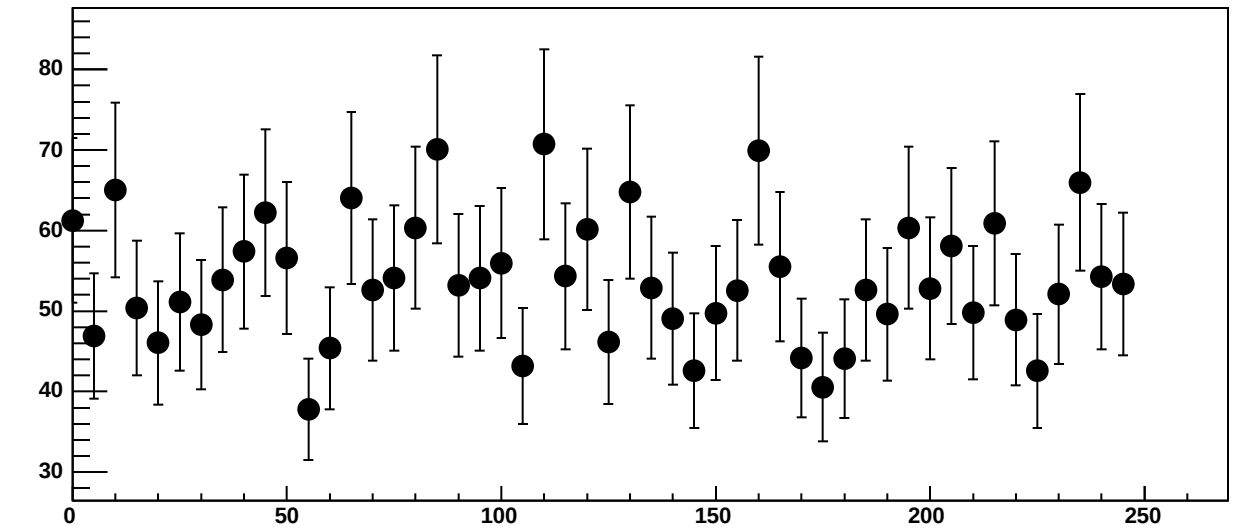

Chip 1, Channel 13, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 1, Channel 13, Enable 2, DAC=1600, ADC Residuals vs Hold
800
600
400
200
0
-200
0
50
100
150
200
250

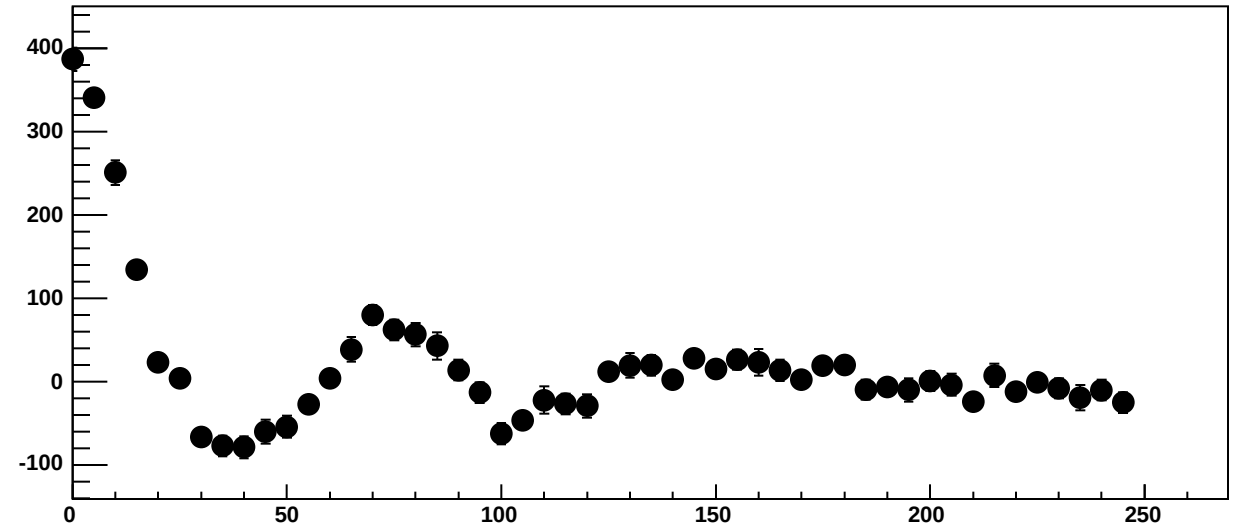
Chip 1, Channel 13, Enable 3, DAC=1600, ADC Mean vs Hold

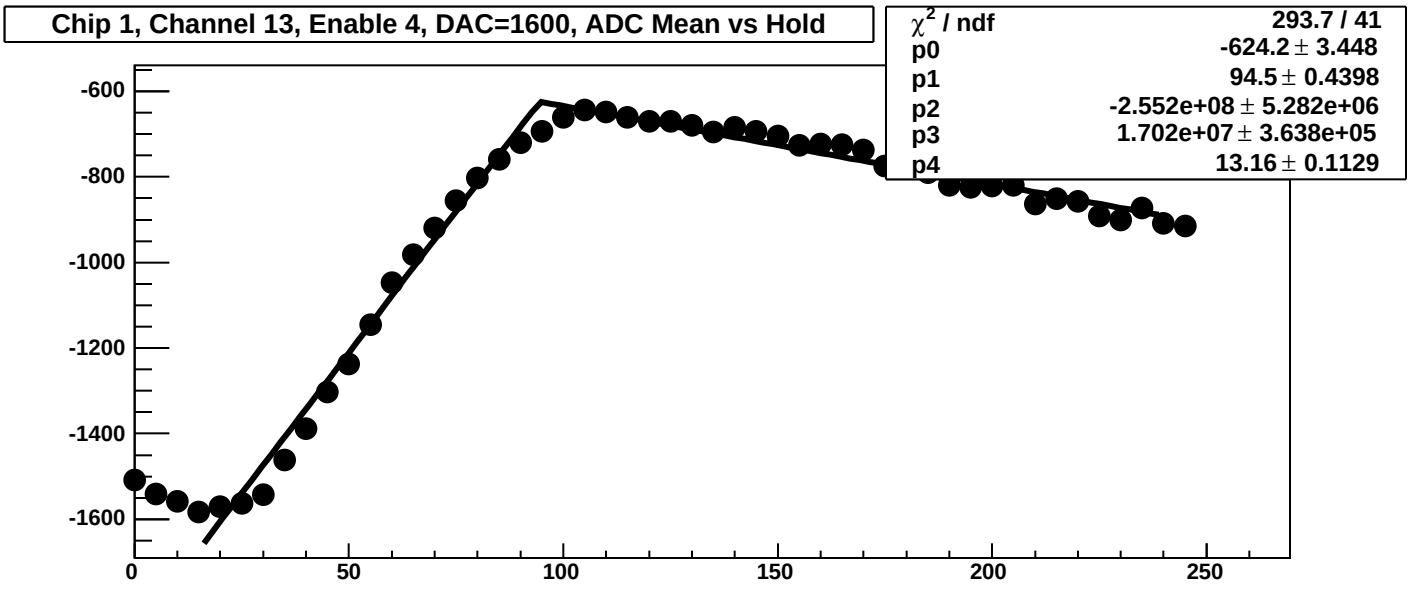


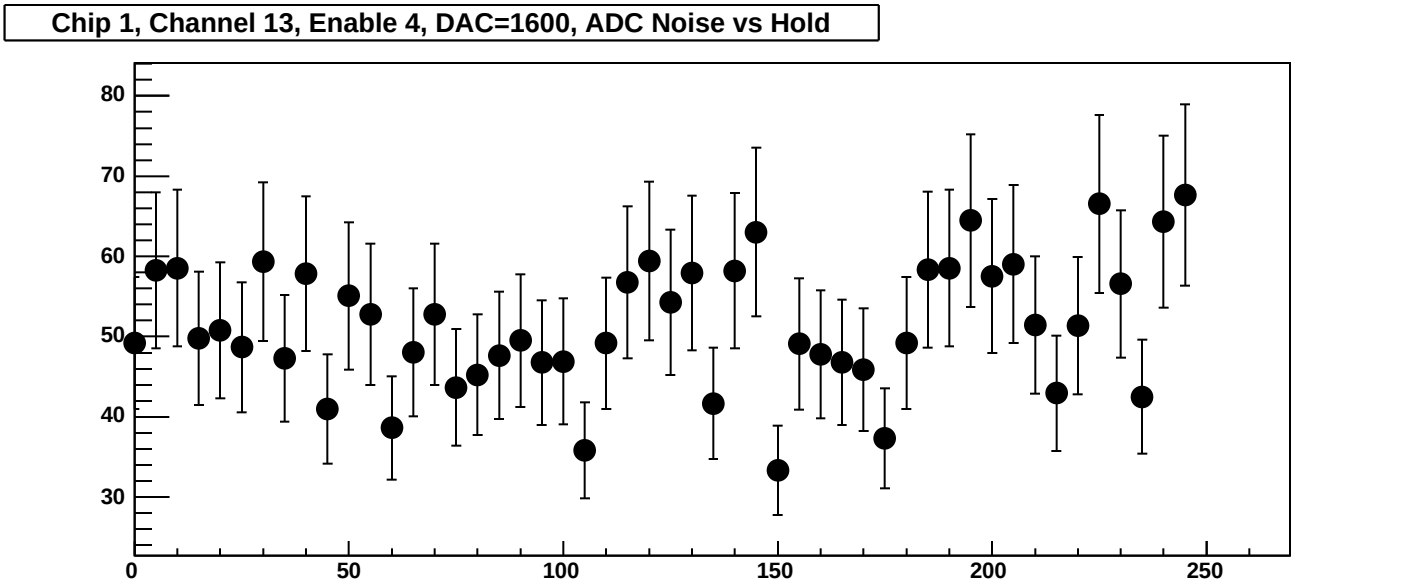
Chip 1, Channel 13, Enable 3, DAC=1600, ADC Noise vs Hold

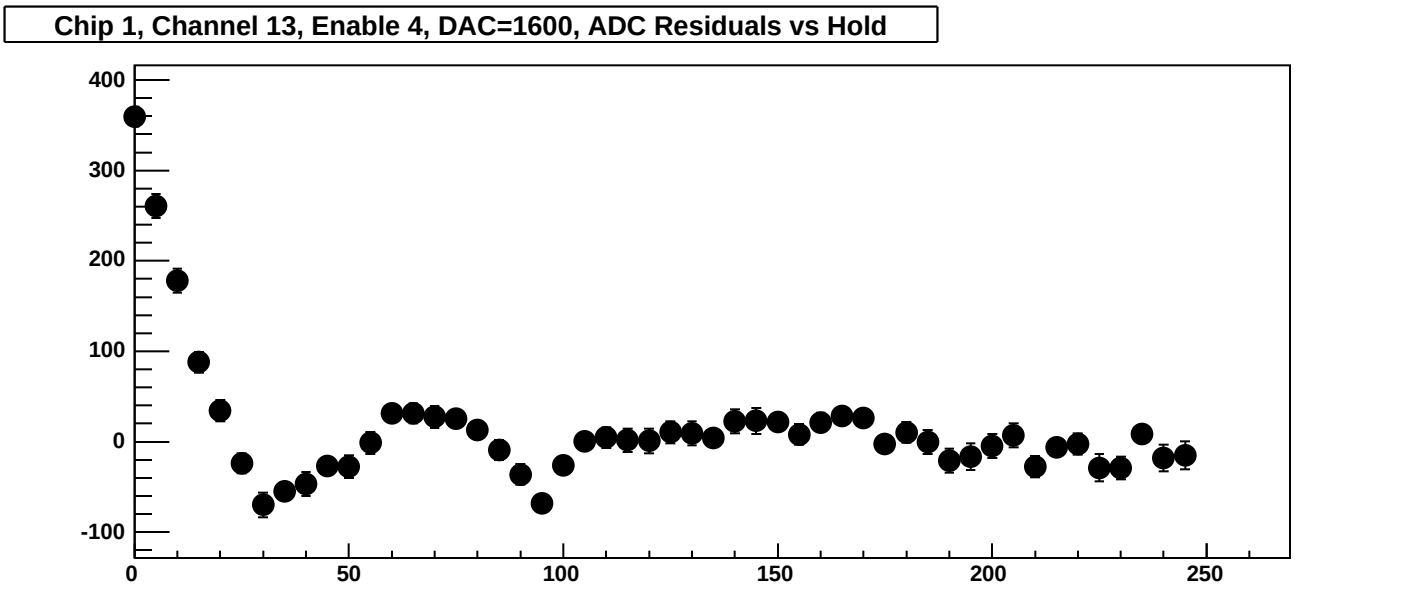


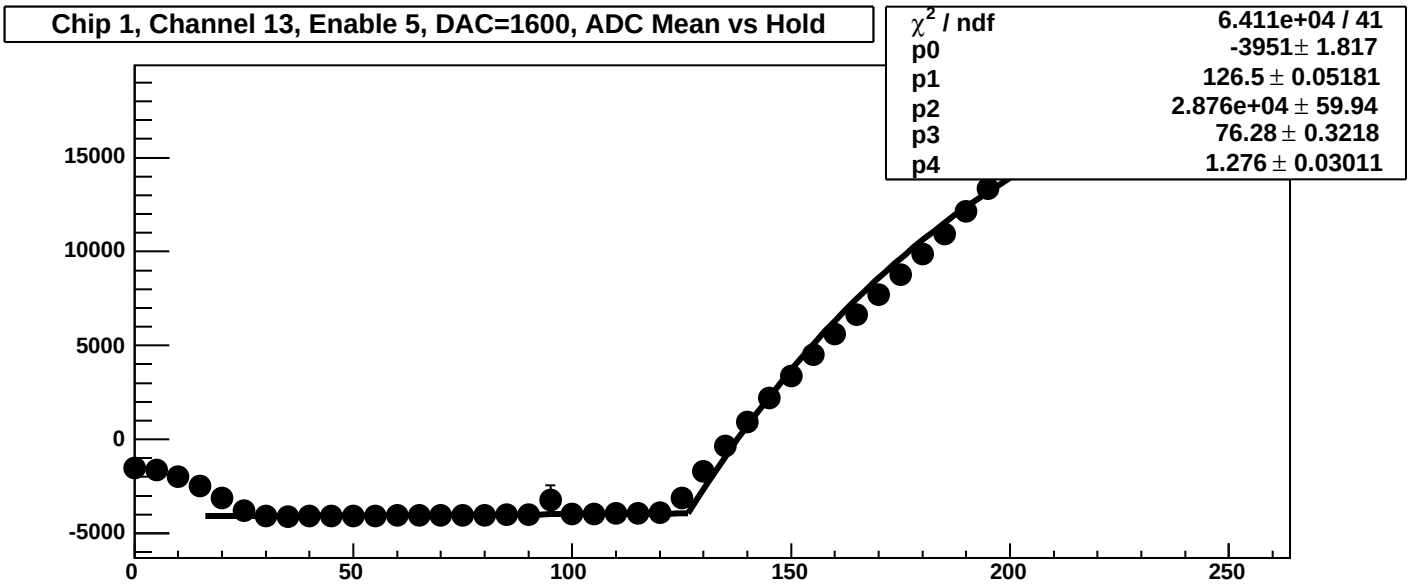
Chip 1, Channel 13, Enable 3, DAC=1600, ADC Residuals vs Hold

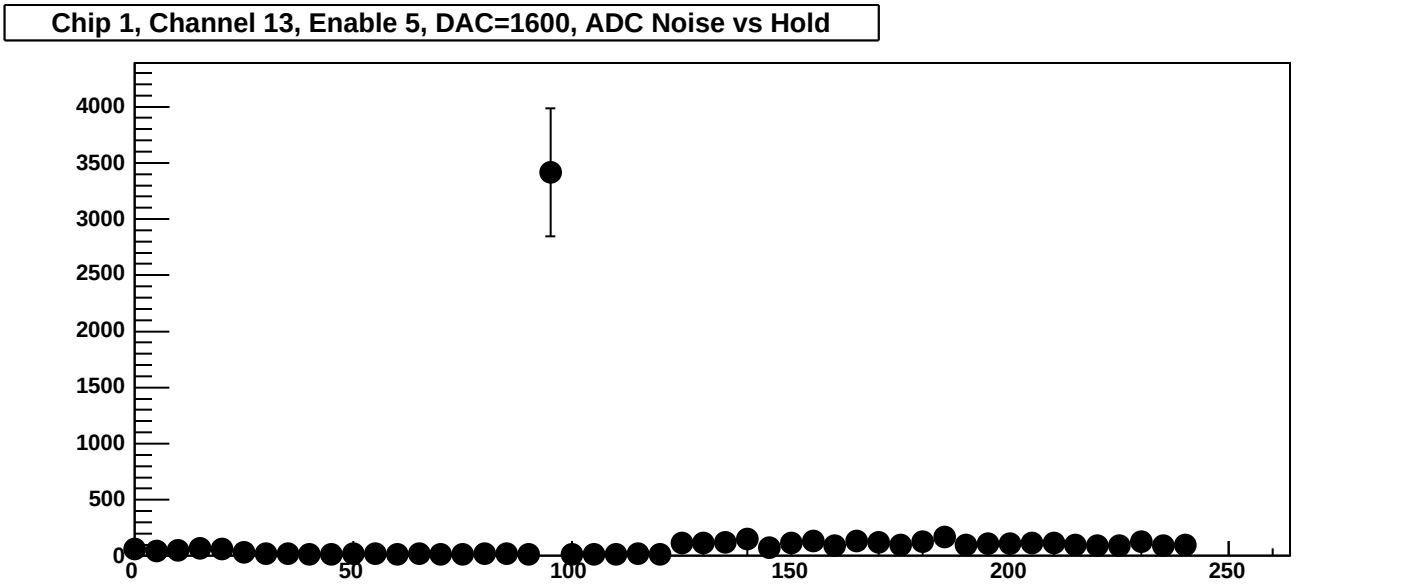


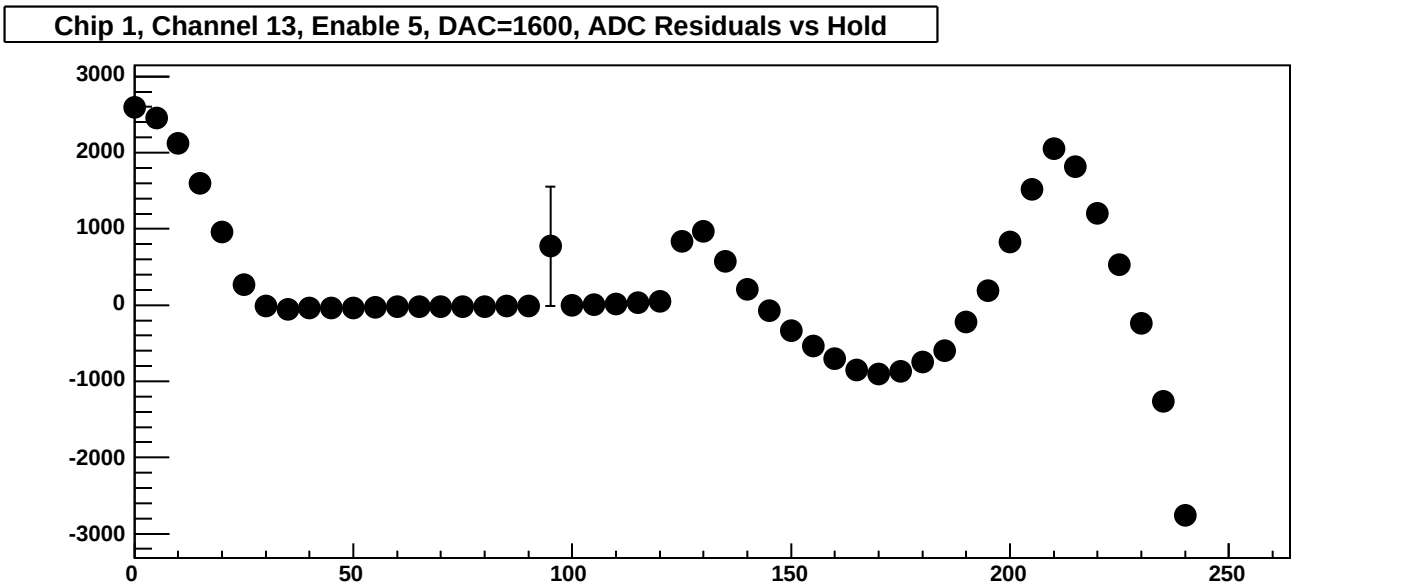

Chip 1, Channel 13, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 293.7 / 41
p0 -624.2 ± 3.448
p1 94.5 ± 0.4398
p2 -2.552e+08 ± 5.282e+06
p3 1.702e+07 ± 3.638e+05
p4 13.16 ± 0.1129
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250


Chip 1, Channel 13, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

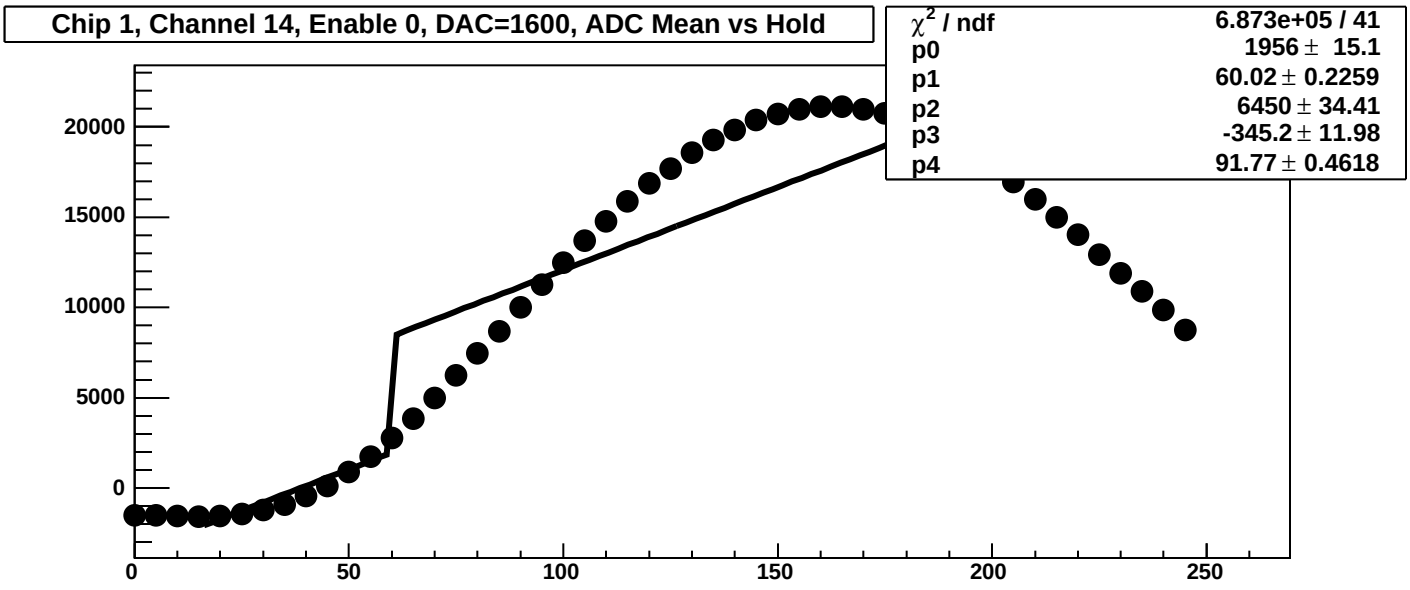

Chip 1, Channel 13, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 1, Channel 13, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 6.411e+04 / 41
p0 -3951 ± 1.817
p1 126.5 ± 0.05181
p2 2.876e+04 ± 59.94
p3 76.28 ± 0.3218
p4 1.276 ± 0.03011
15000
10000
5000
0
-5000
0
50
100
150
200
250

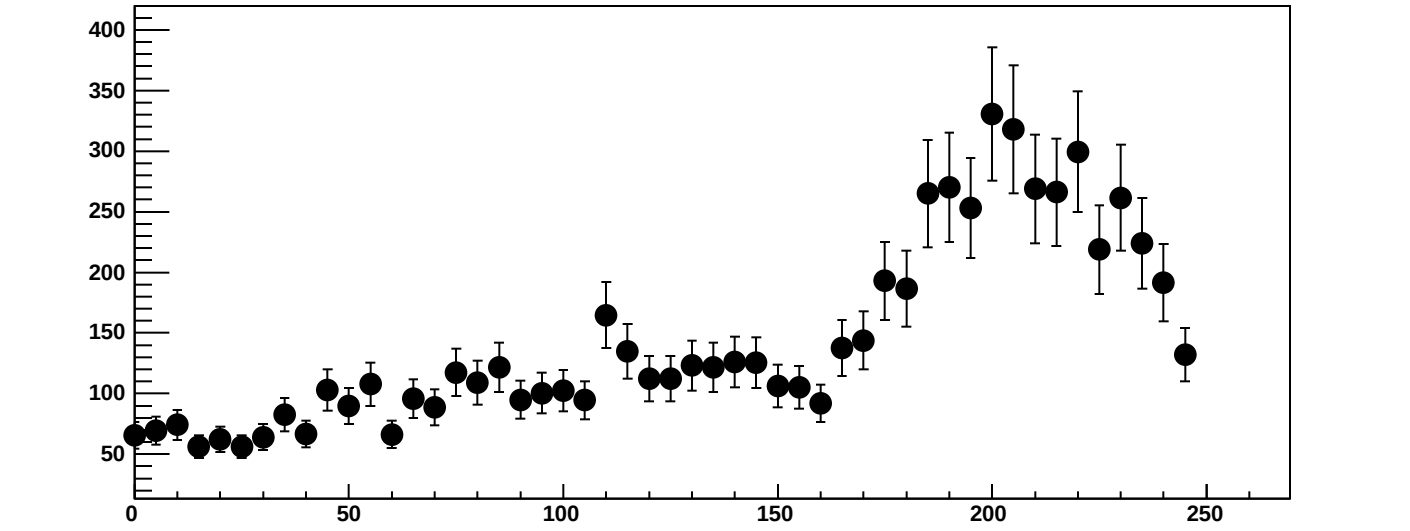

Chip 1, Channel 13, Enable 5, DAC=1600, ADC Noise vs Hold
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 1, Channel 13, Enable 5, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

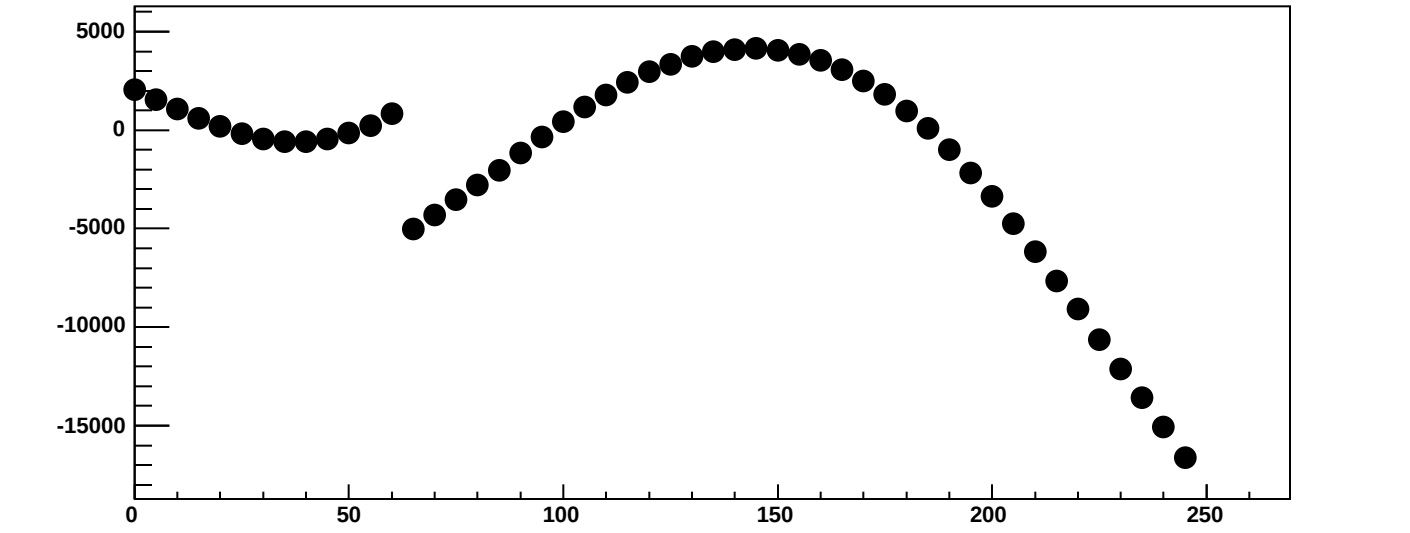
Chip 1, Channel 14, Enable 0, DAC=1600, ADC Mean vs Hold



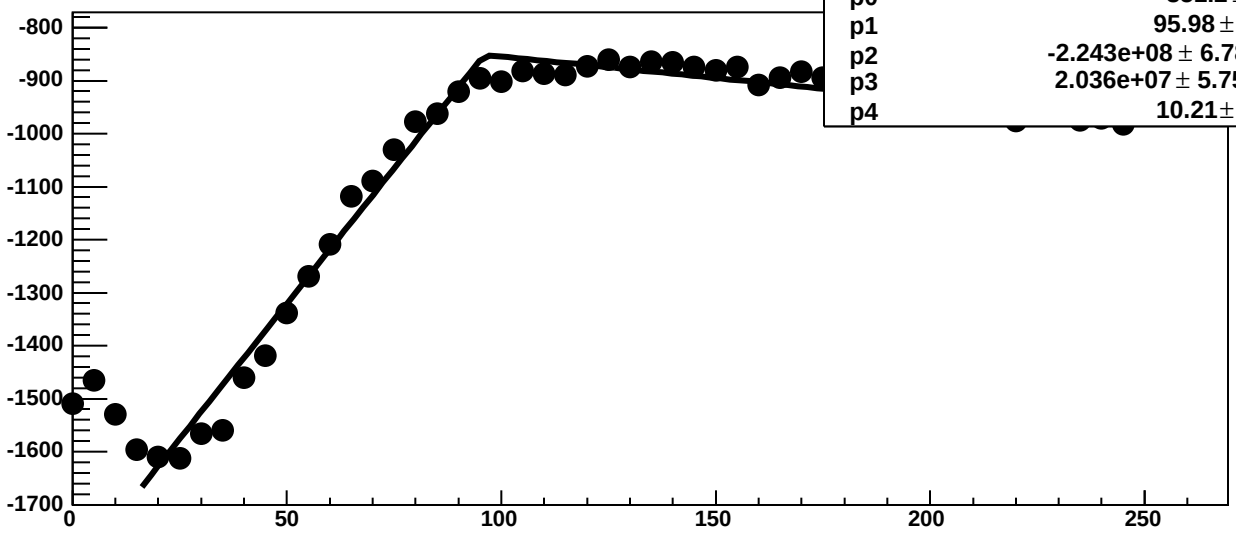
Chip 1, Channel 14, Enable 0, DAC=1600, ADC Noise vs Hold



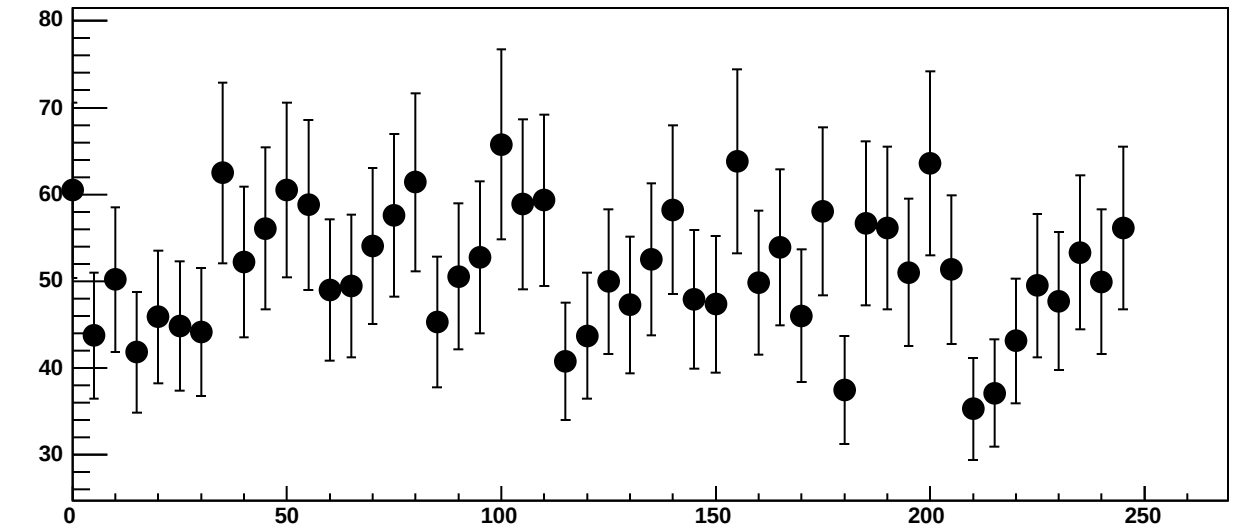
Chip 1, Channel 14, Enable 0, DAC=1600, ADC Residuals vs Hold



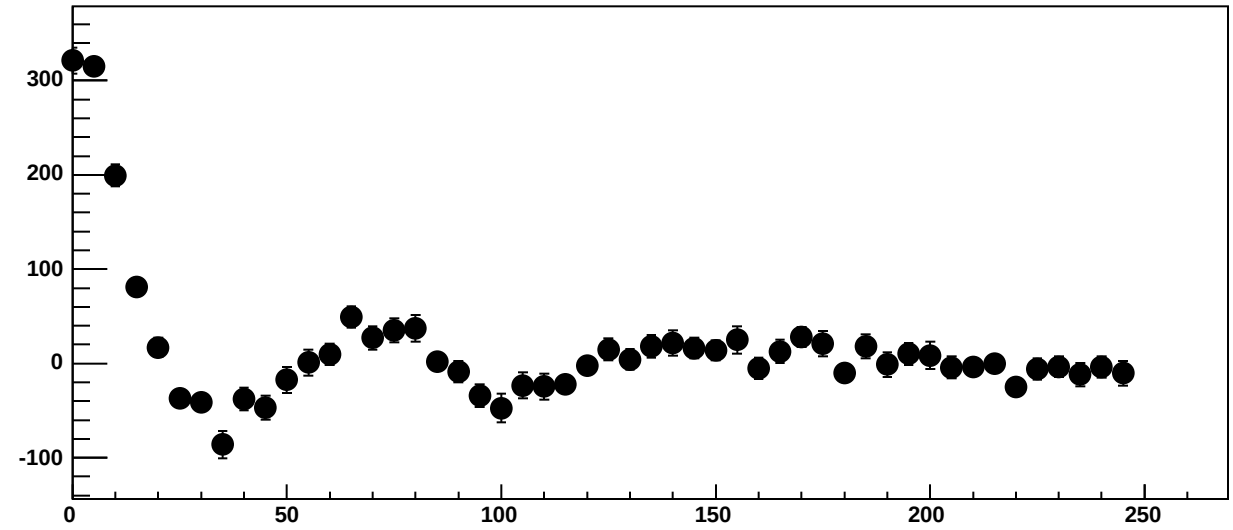
Chip 1, Channel 14, Enable 1, DAC=1600, ADC Mean vs Hold

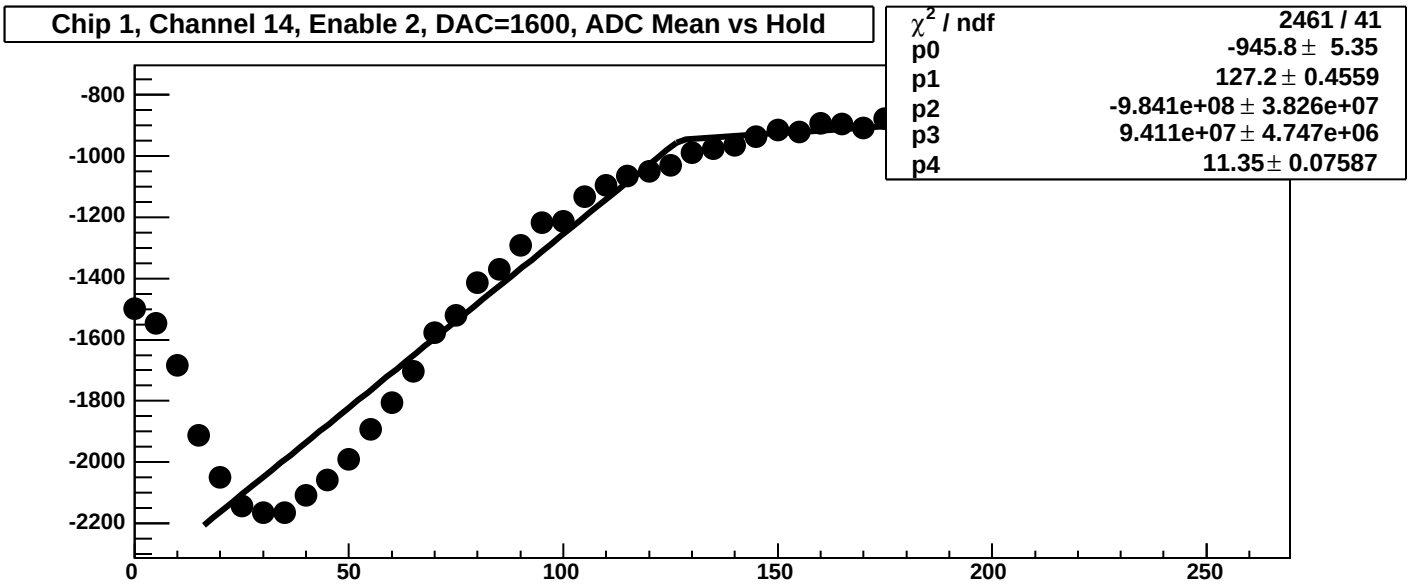


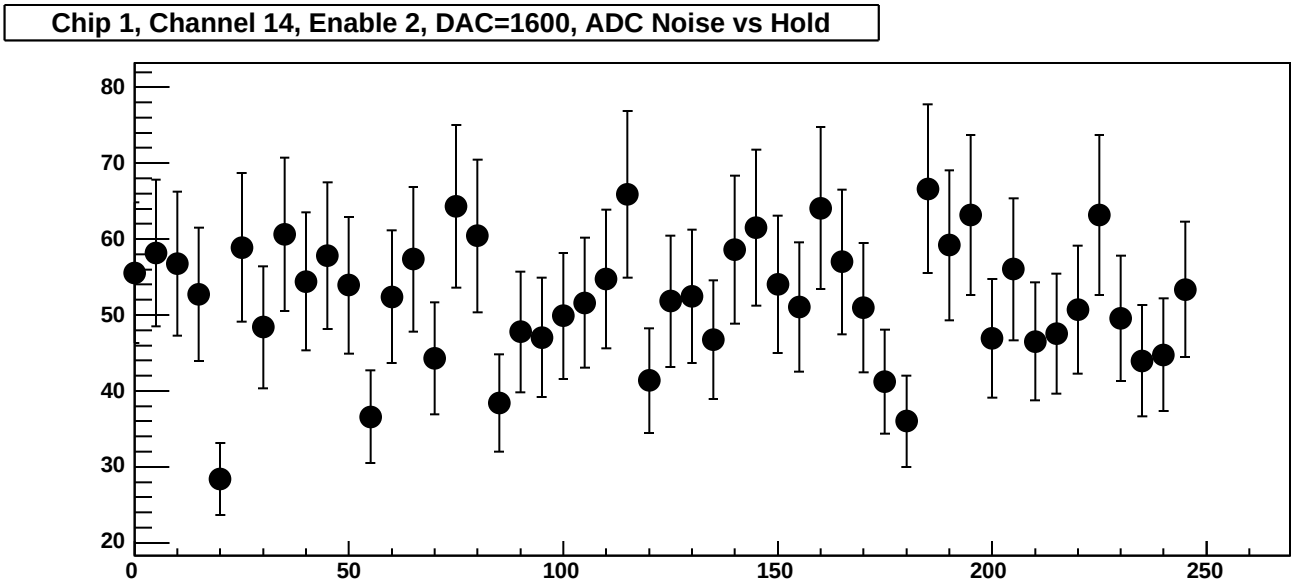
Chip 1, Channel 14, Enable 1, DAC=1600, ADC Noise vs Hold

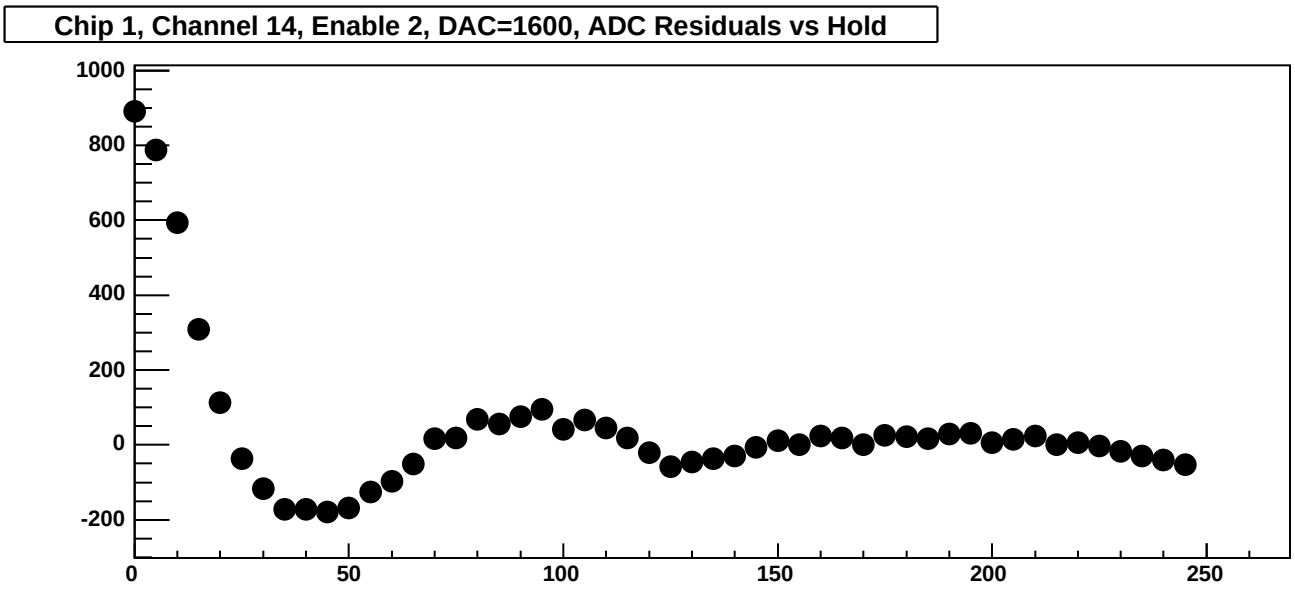


Chip 1, Channel 14, Enable 1, DAC=1600, ADC Residuals vs Hold

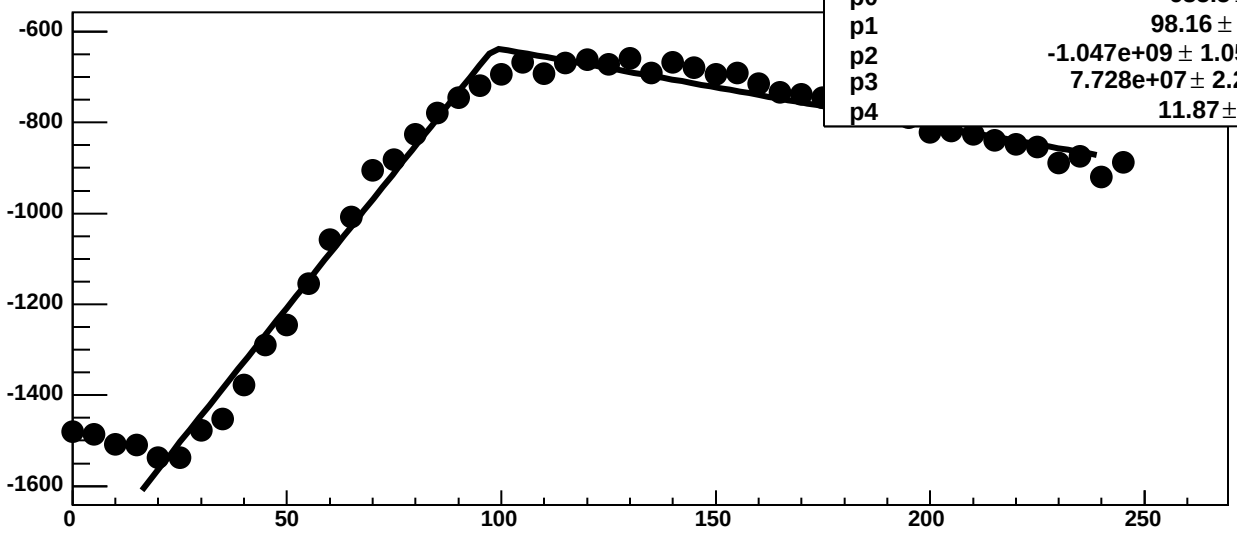



Chip 1, Channel 14, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 2461 / 41
p0 -945.8 ± 5.35
p1 127.2 ± 0.4559
p2 -9.841e+08 ± 3.826e+07
p3 9.411e+07 ± 4.747e+06
p4 11.35 ± 0.07587
-800
-1000
-1200
-1400
-1600
-1800
-2000
-2200
0
50
100
150
200
250

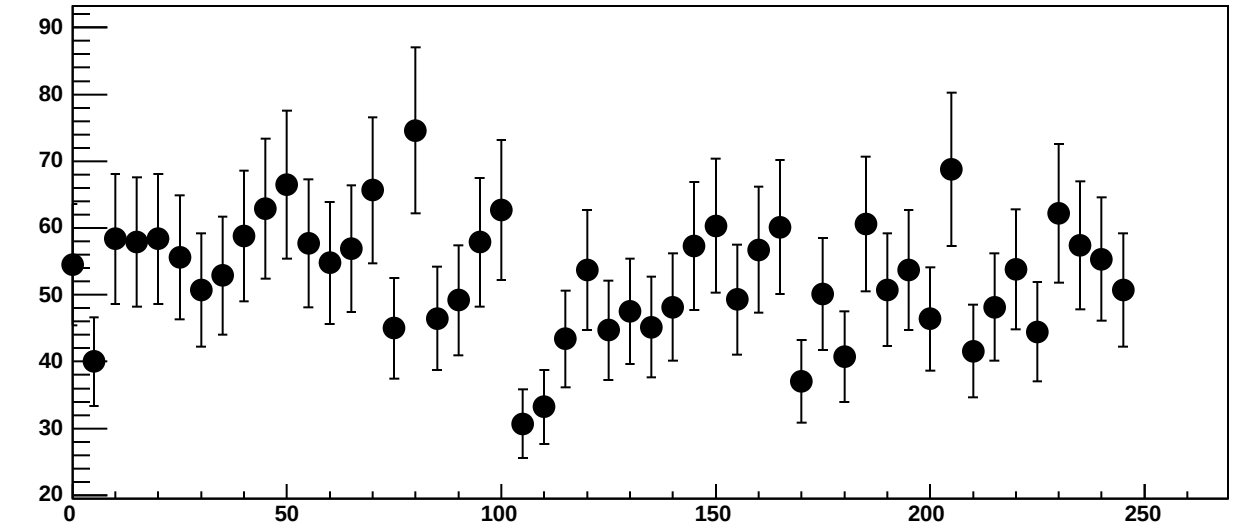

Chip 1, Channel 14, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 1, Channel 14, Enable 2, DAC=1600, ADC Residuals vs Hold
1000
800
600
400
200
0
-200
0
50
100
150
200
250

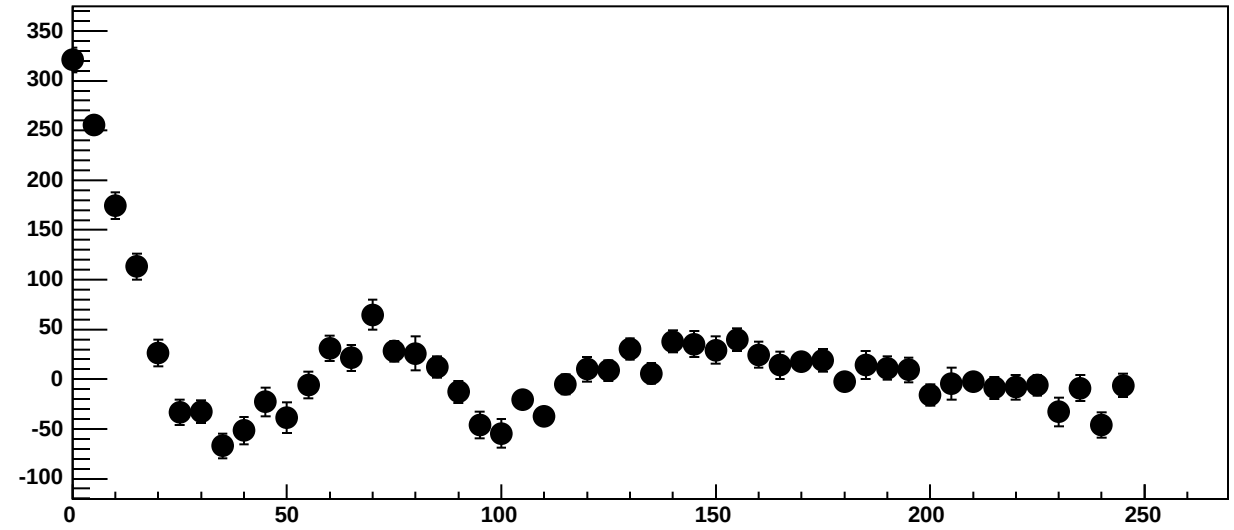
Chip 1, Channel 14, Enable 3, DAC=1600, ADC Mean vs Hold

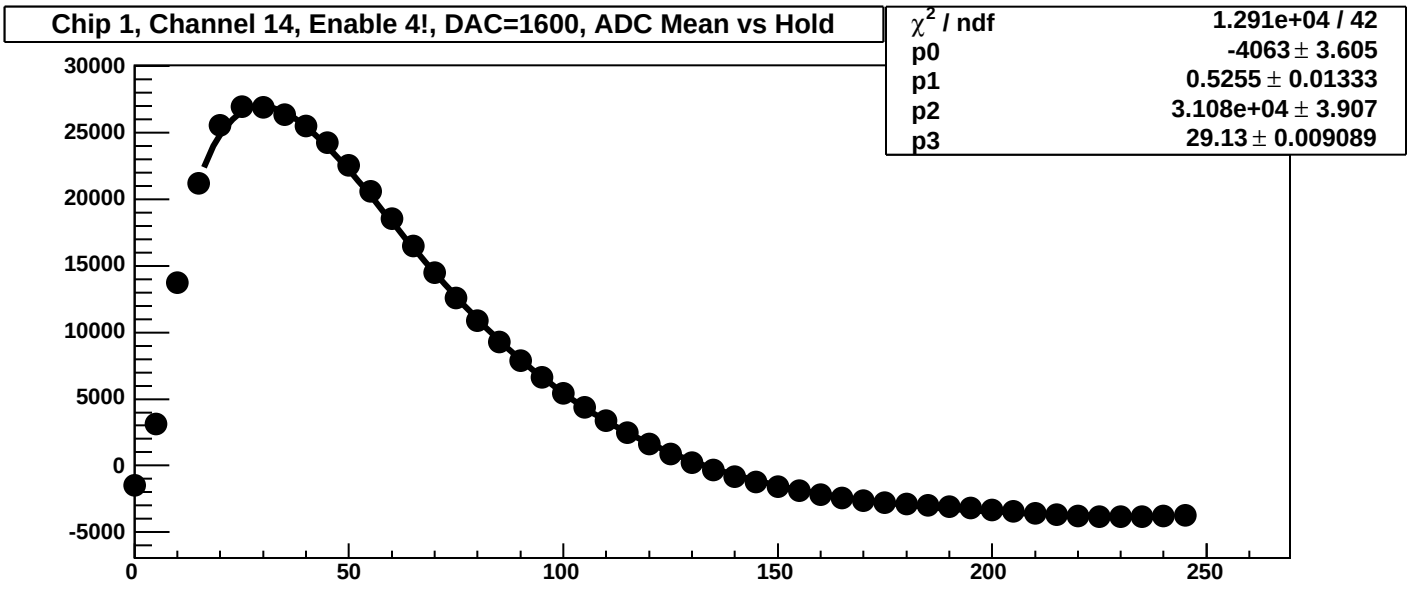


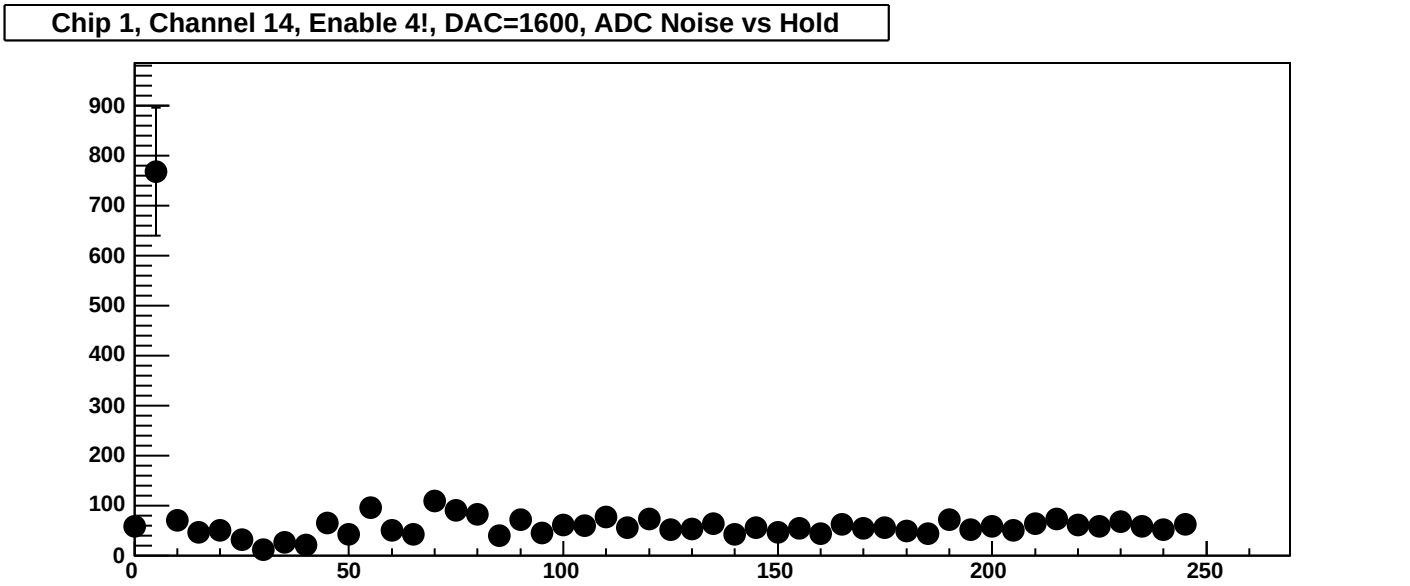
Chip 1, Channel 14, Enable 3, DAC=1600, ADC Noise vs Hold

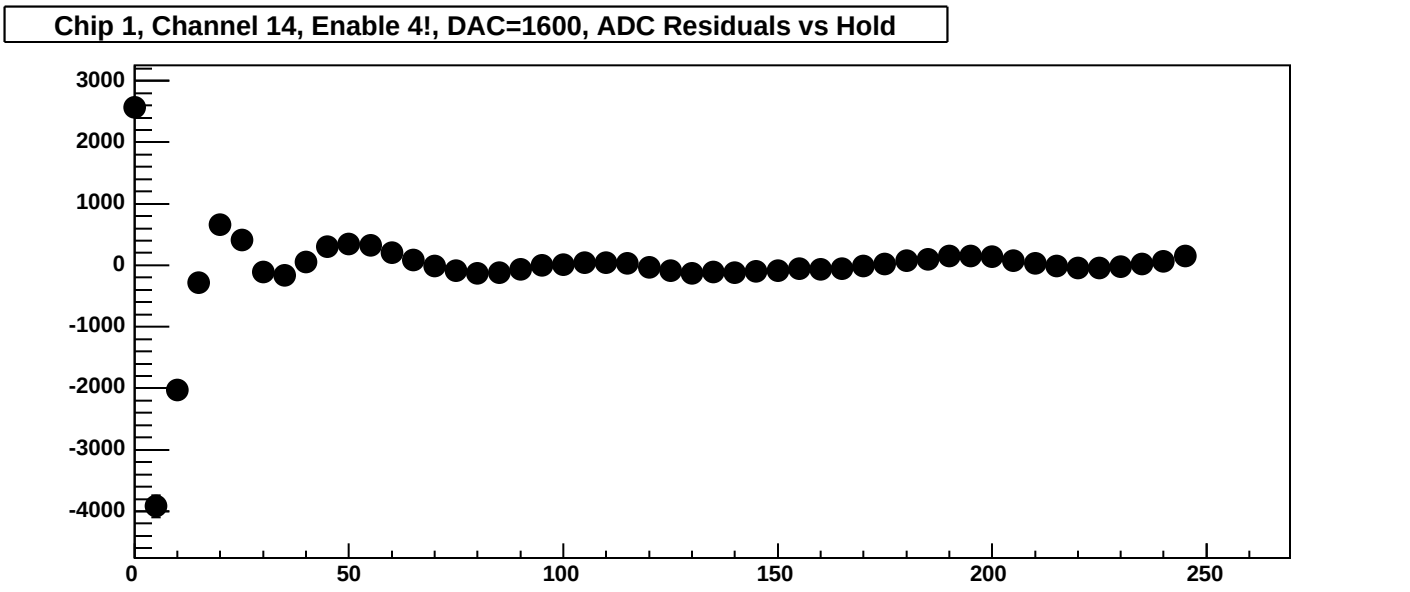


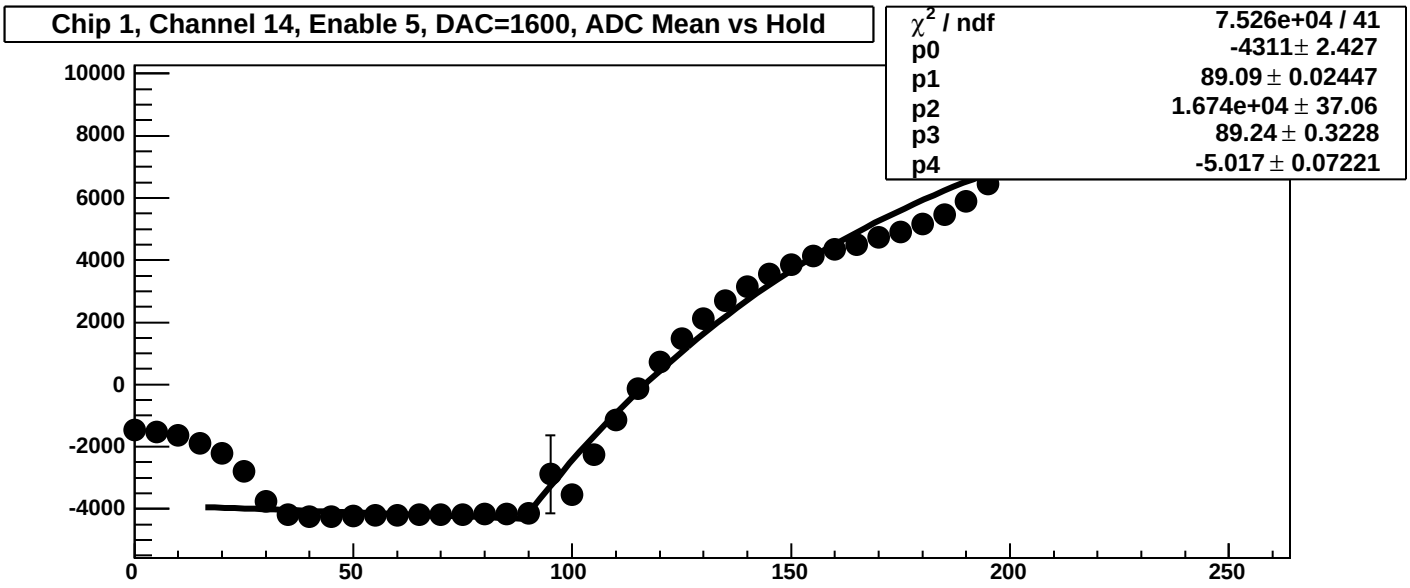
Chip 1, Channel 14, Enable 3, DAC=1600, ADC Residuals vs Hold

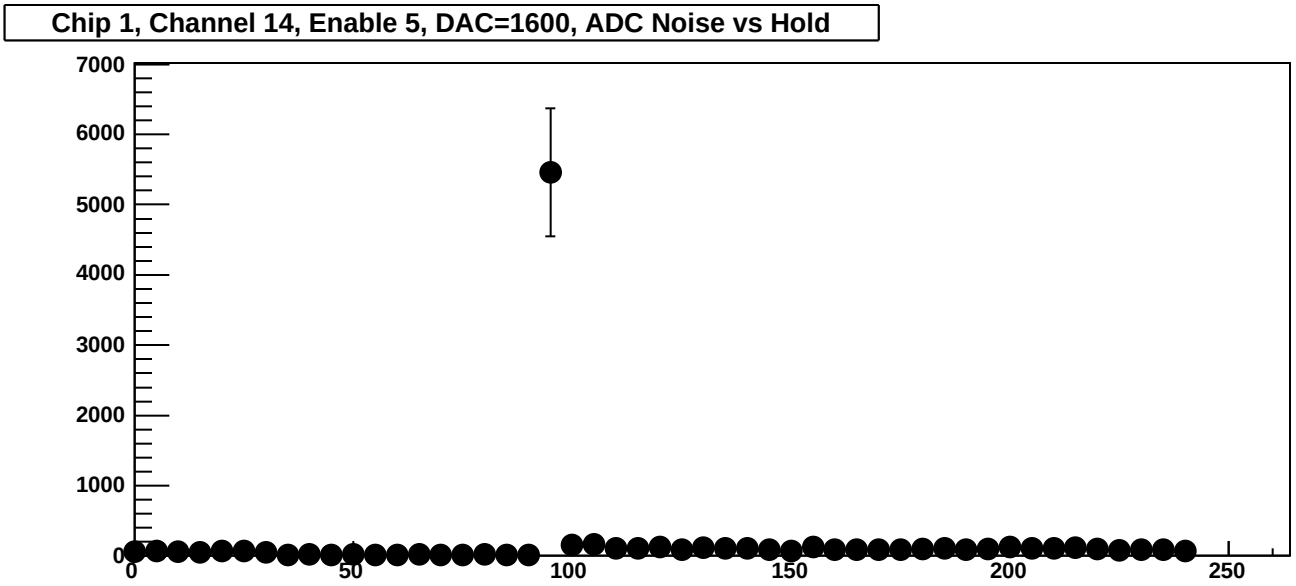


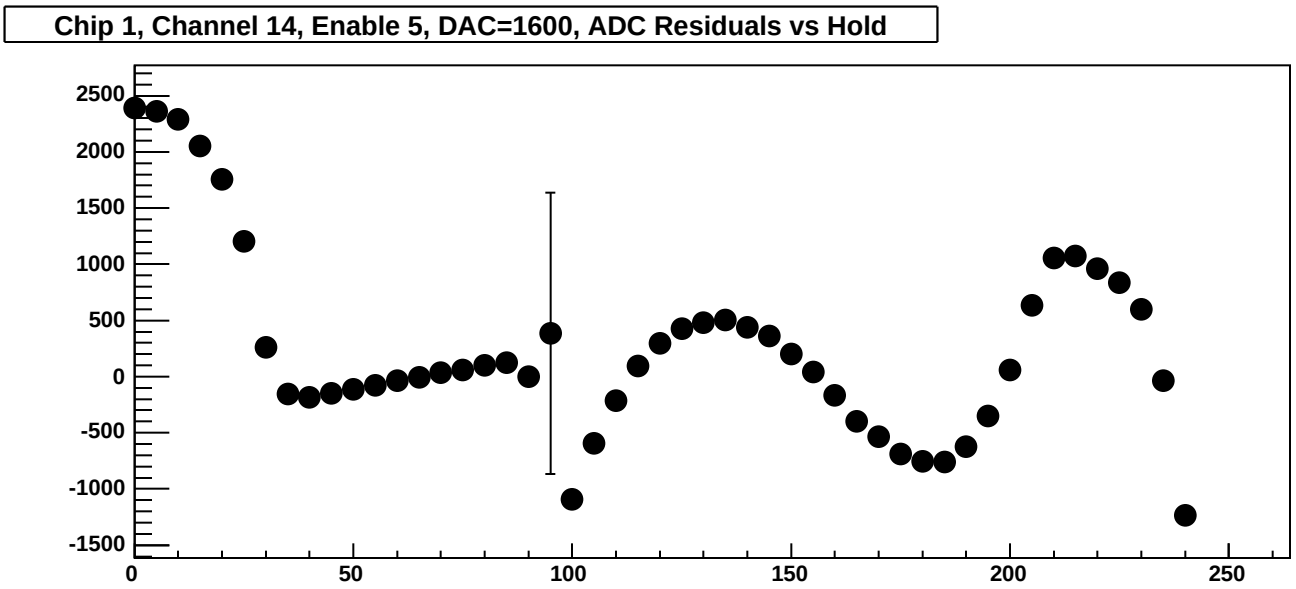

Chip 1, Channel 14, Enable 4!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.291e+04 / 42
p0 -4063 ± 3.605
p1 0.5255 ± 0.01333
p2 3.108e+04 ± 3.907
p3 29.13 ± 0.009089
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

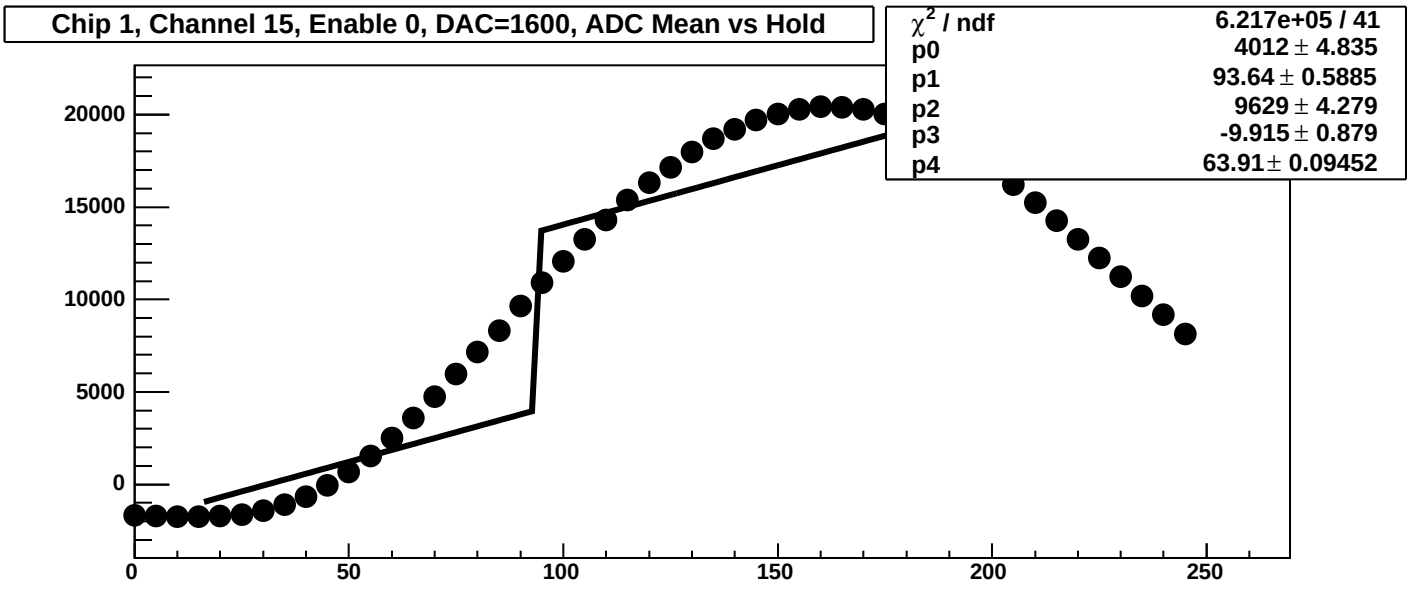

Chip 1, Channel 14, Enable 4!, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250

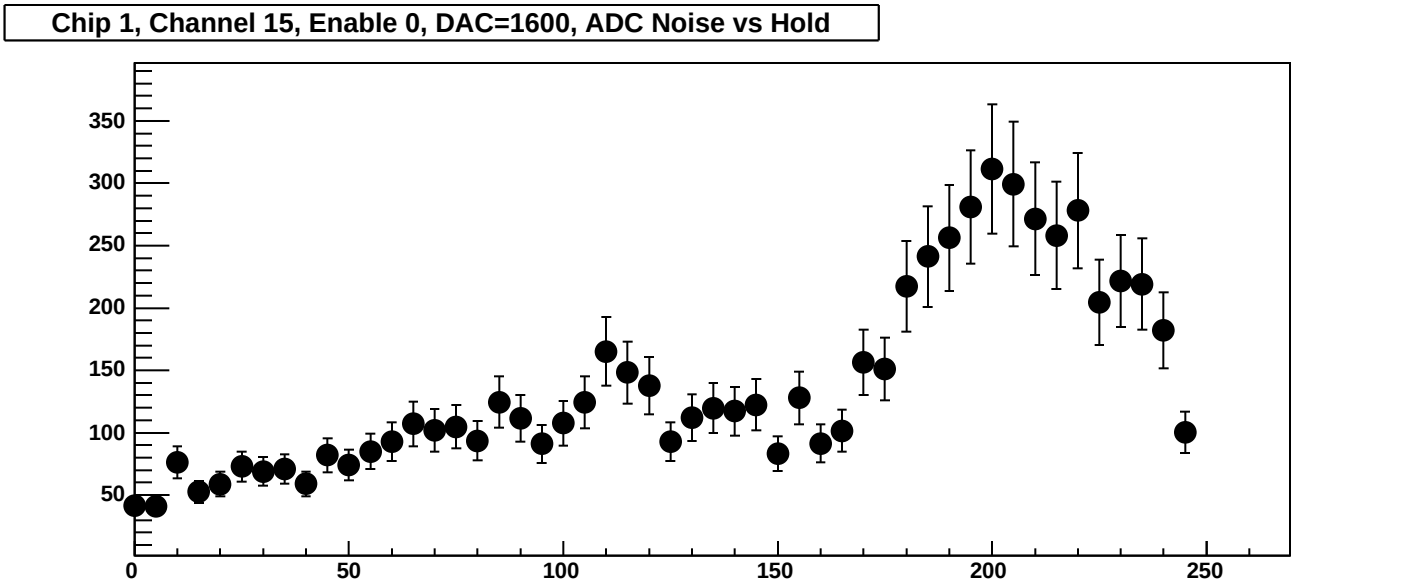

Chip 1, Channel 14, Enable 4!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

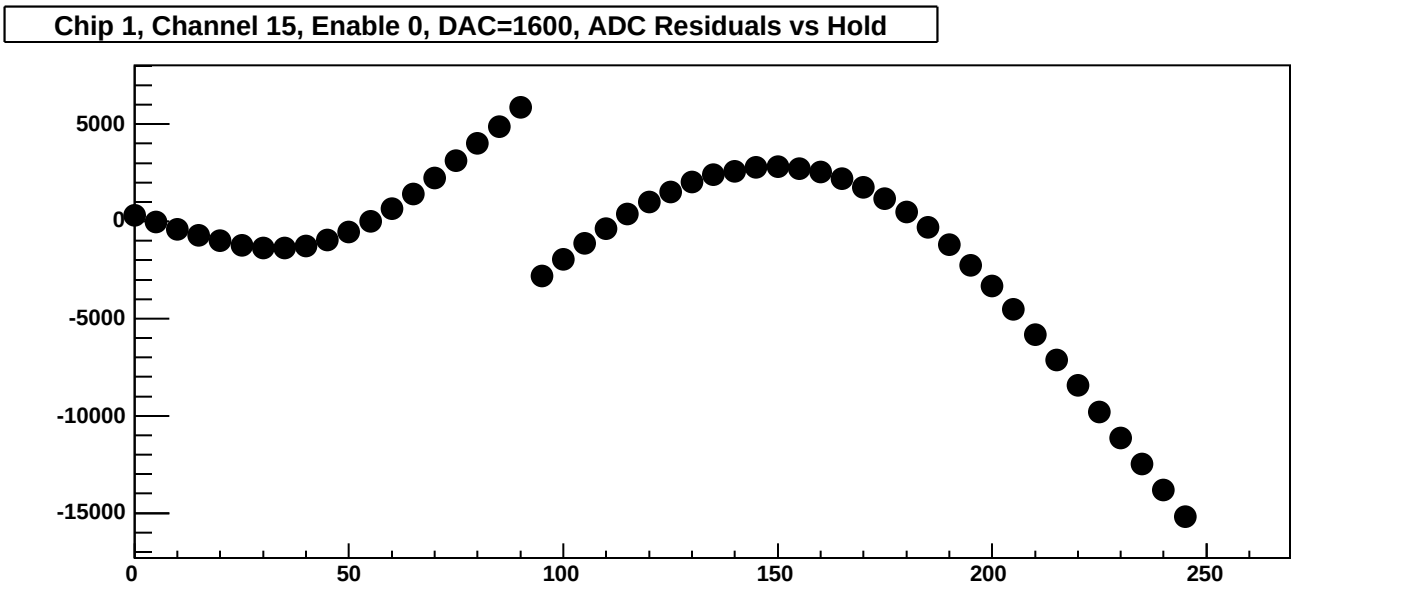

Chip 1, Channel 14, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.526e+04 / 41
p0 -4311 ± 2.427
p1 89.09 ± 0.02447
p2 1.674e+04 ± 37.06
p3 89.24 ± 0.3228
p4 -5.017 ± 0.07221
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

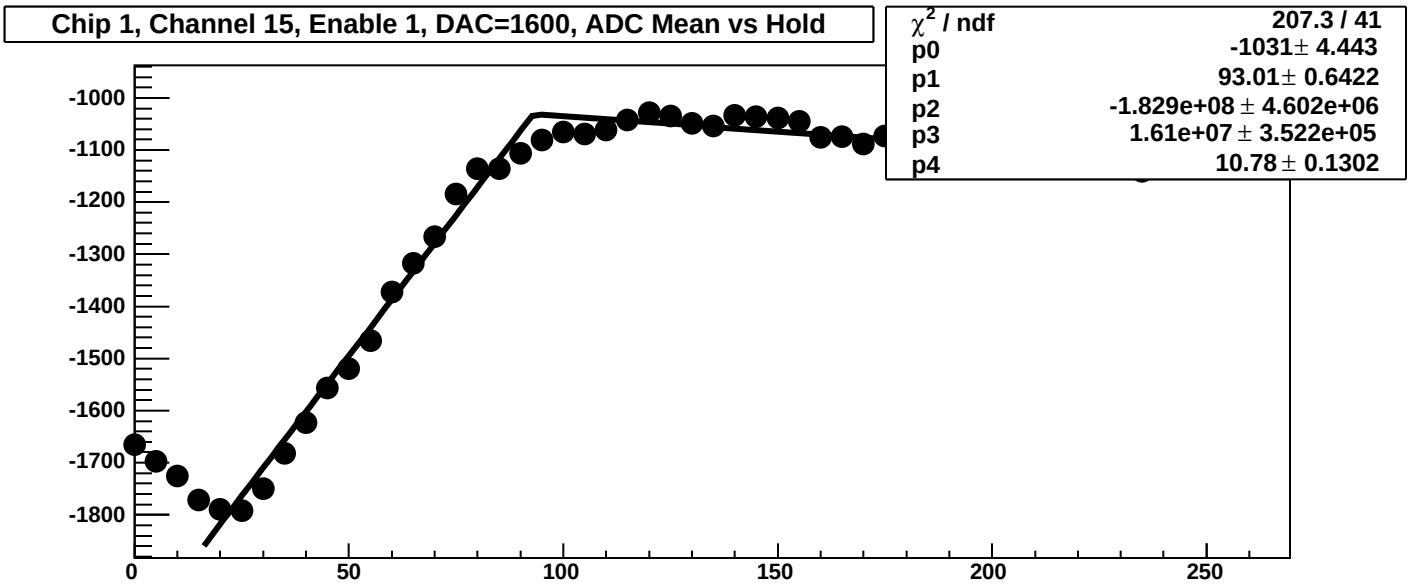

Chip 1, Channel 14, Enable 5, DAC=1600, ADC Noise vs Hold
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

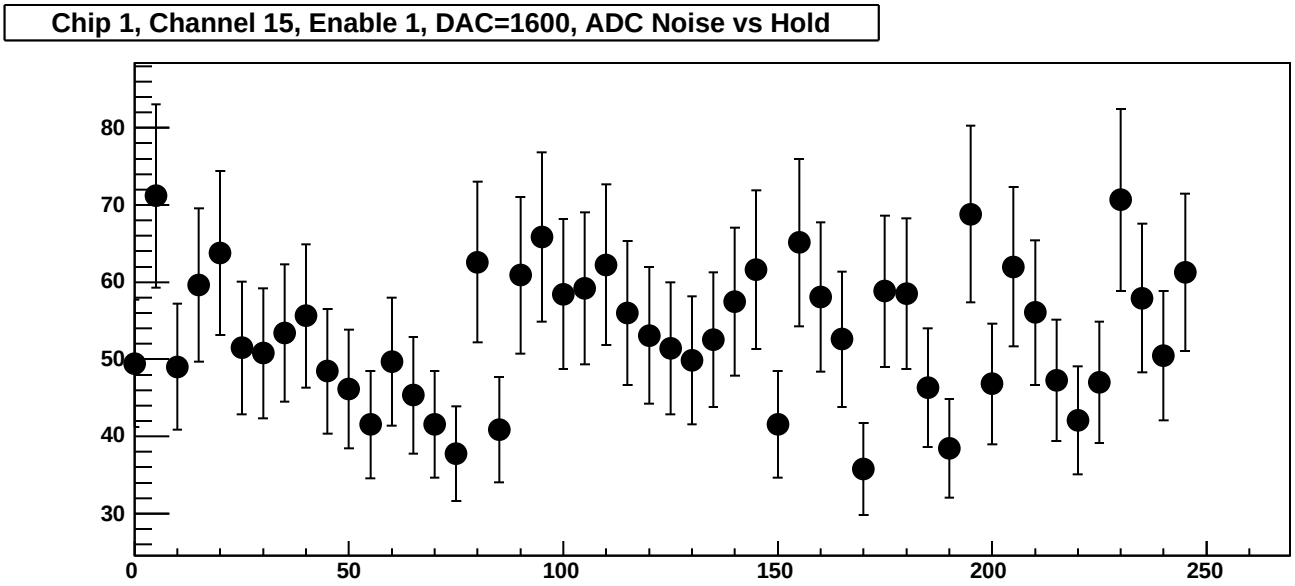

Chip 1, Channel 14, Enable 5, DAC=1600, ADC Residuals vs Hold
2500
2000
1500
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

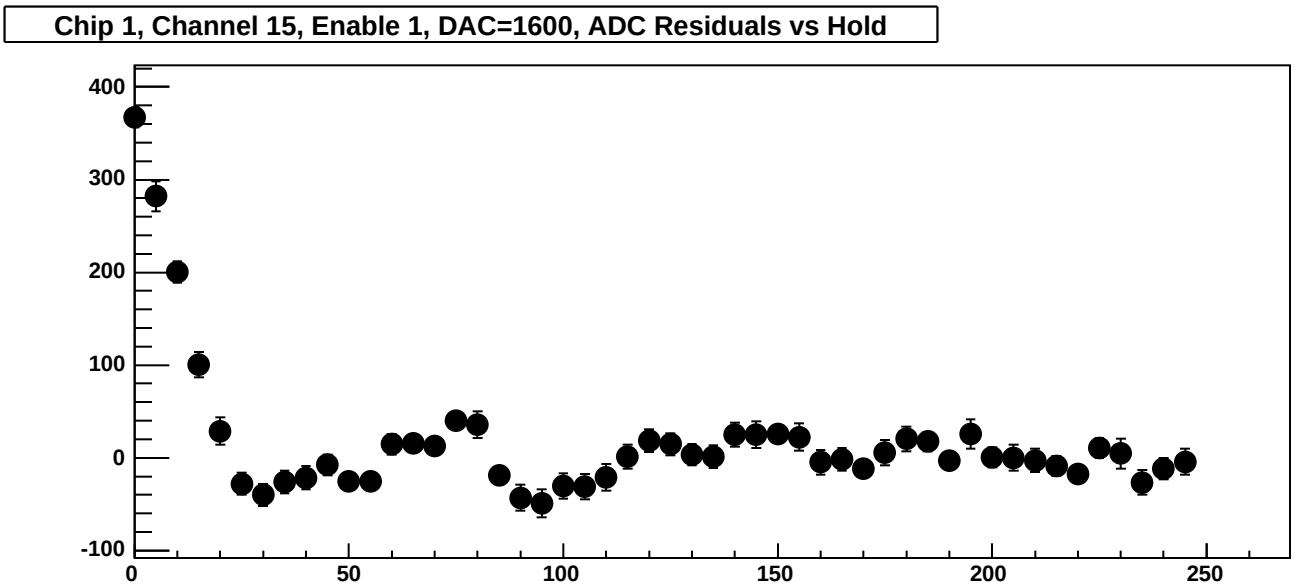

Chip 1, Channel 15, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 6.217e+05 / 41
p0 4012 ± 4.835
p1 93.64 ± 0.5885
p2 9629 ± 4.279
p3 -9.915 ± 0.879
p4 63.91 ± 0.09452
20000
15000
10000
5000
0
0
50
100
150
200
250

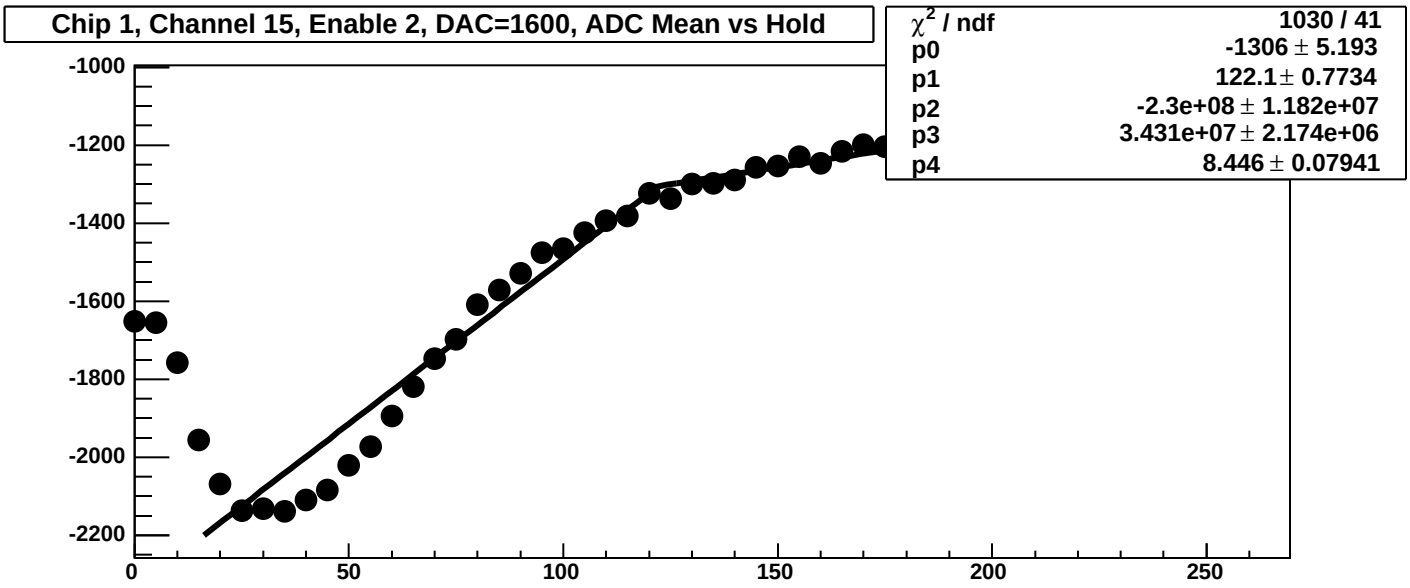

Chip 1, Channel 15, Enable 0, DAC=1600, ADC Noise vs Hold
350
300
250
200
150
100
50
0
50
100
150
200
250

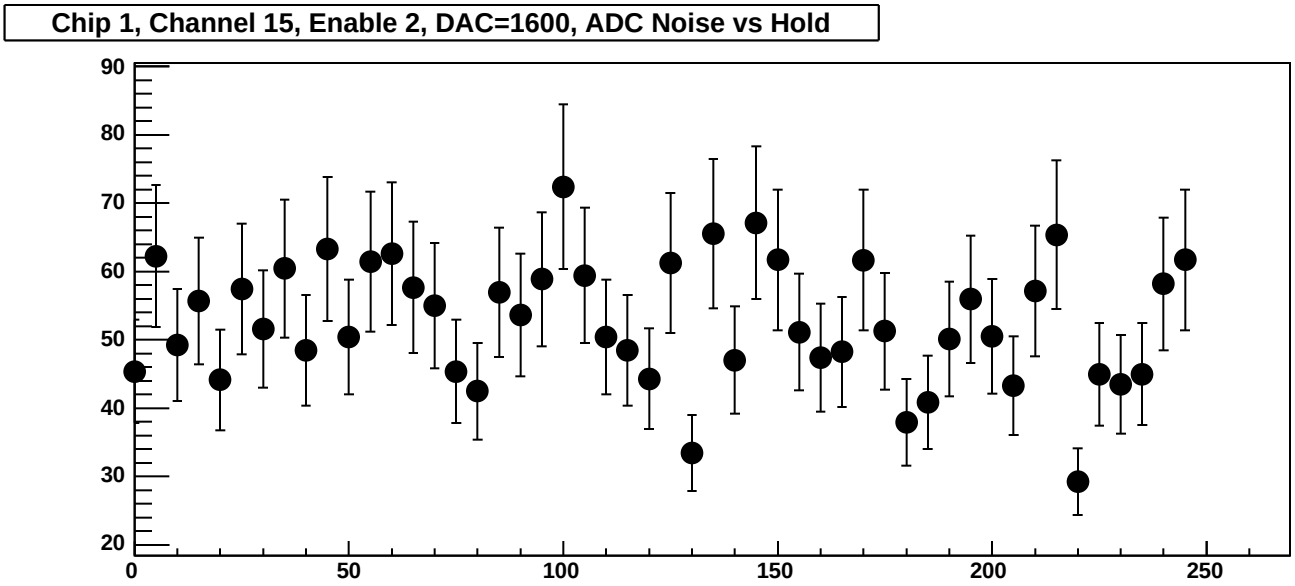

Chip 1, Channel 15, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
0
50
100
150
200
250

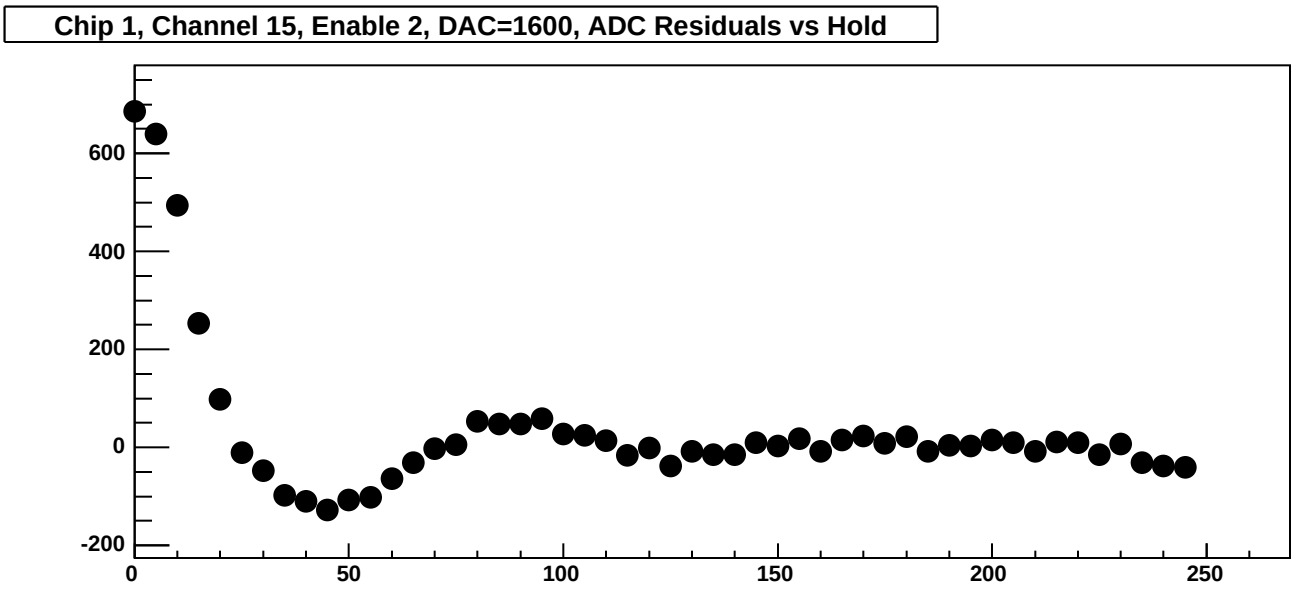

Chip 1, Channel 15, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 207.3 / 41
p0 -1031 ± 4.443
p1 93.01 ± 0.6422
p2 -1.829e+08 ± 4.602e+06
p3 1.61e+07 ± 3.522e+05
p4 10.78 ± 0.1302
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
-1800
0
50
100
150
200
250

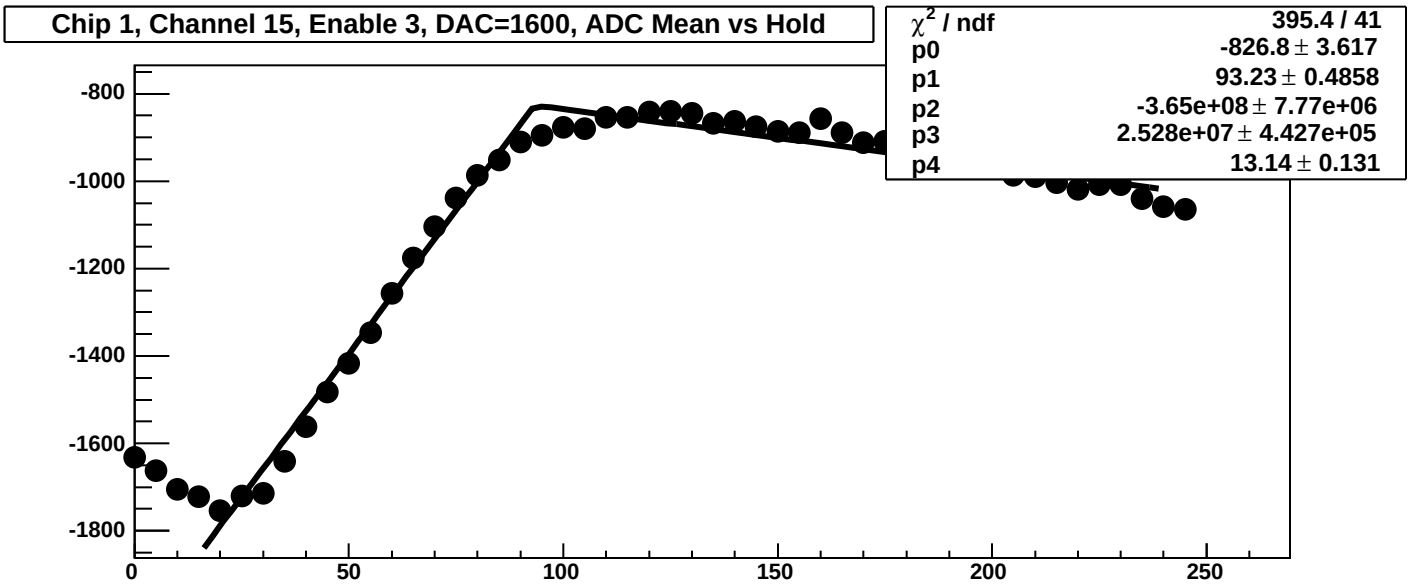

Chip 1, Channel 15, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

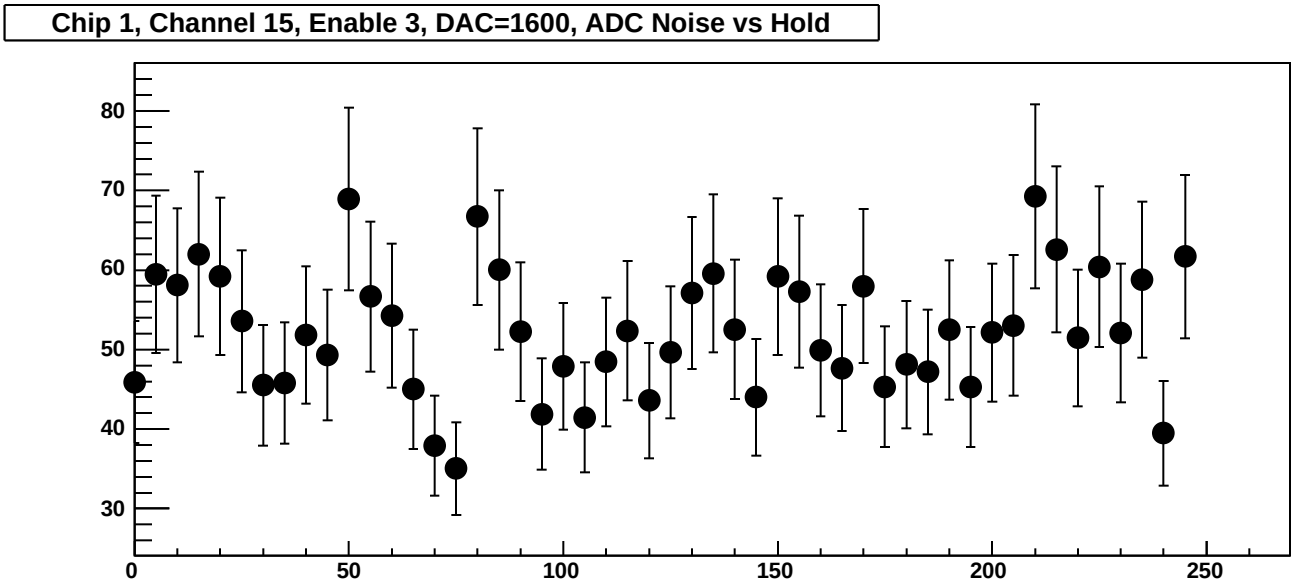

Chip 1, Channel 15, Enable 1, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

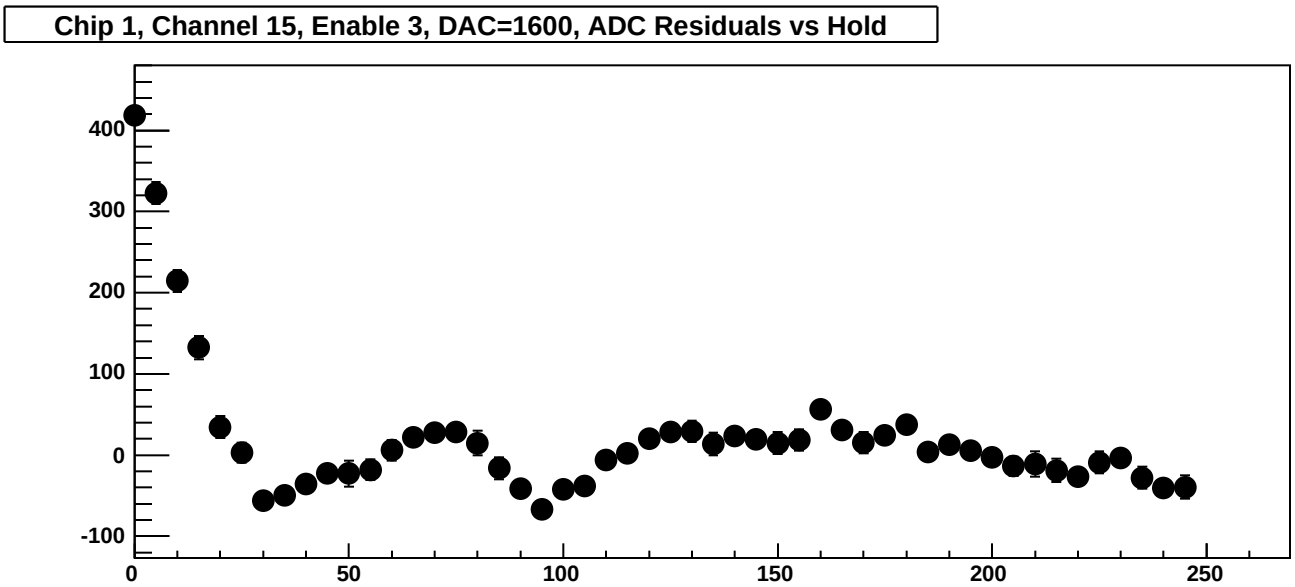

Chip 1, Channel 15, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 1030 / 41
p0 -1306 ± 5.193
p1 122.1 ± 0.7734
p2 -2.3e+08 ± 1.182e+07
p3 3.431e+07 ± 2.174e+06
p4 8.446 ± 0.07941
-1000
-1200
-1400
-1600
-1800
-2000
-2200
0
50
100
150
200
250


Chip 1, Channel 15, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
20
0
50
100
150
200
250

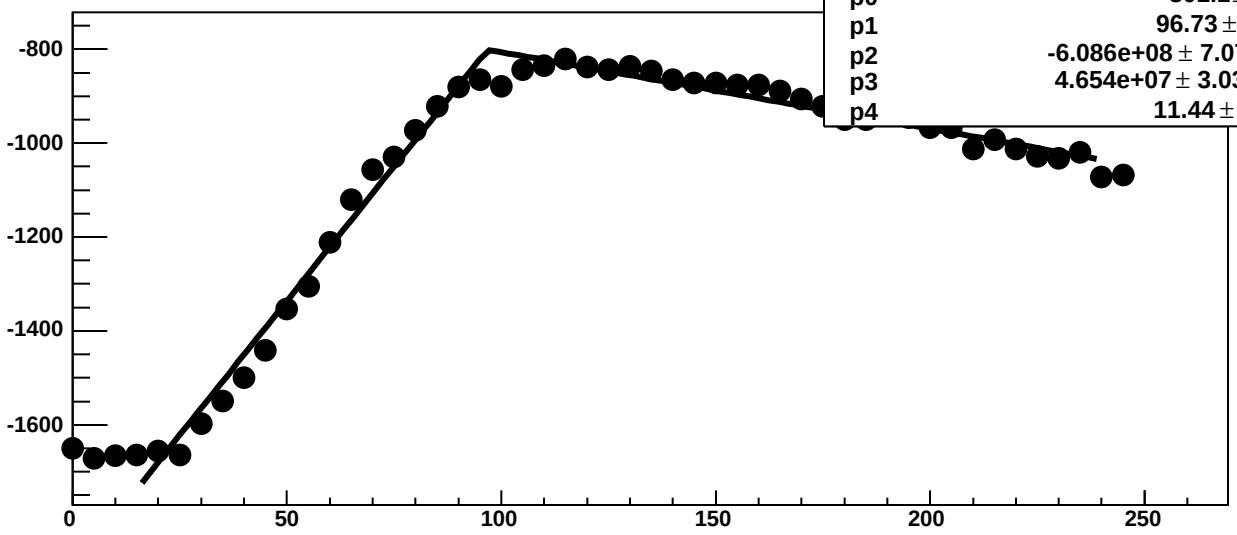

Chip 1, Channel 15, Enable 2, DAC=1600, ADC Residuals vs Hold
600
400
200
0
-200
0
50
100
150
200
250


Chip 1, Channel 15, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 395.4 / 41
p0 -826.8 ± 3.617
p1 93.23 ± 0.4858
p2 -3.65e+08 ± 7.77e+06
p3 2.528e+07 ± 4.427e+05
p4 13.14 ± 0.131
-800
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250

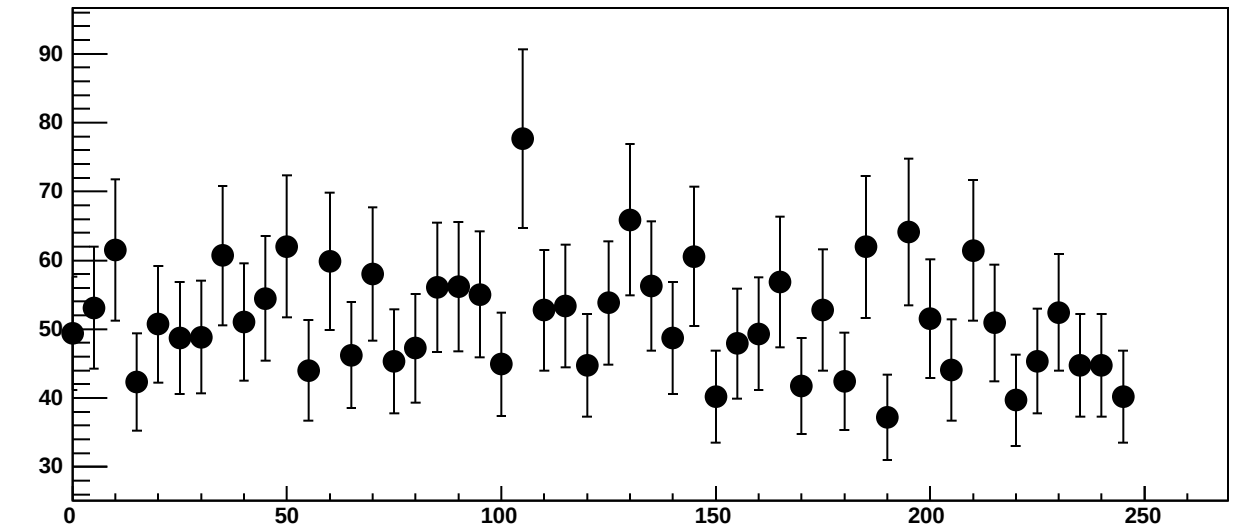

Chip 1, Channel 15, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 1, Channel 15, Enable 3, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

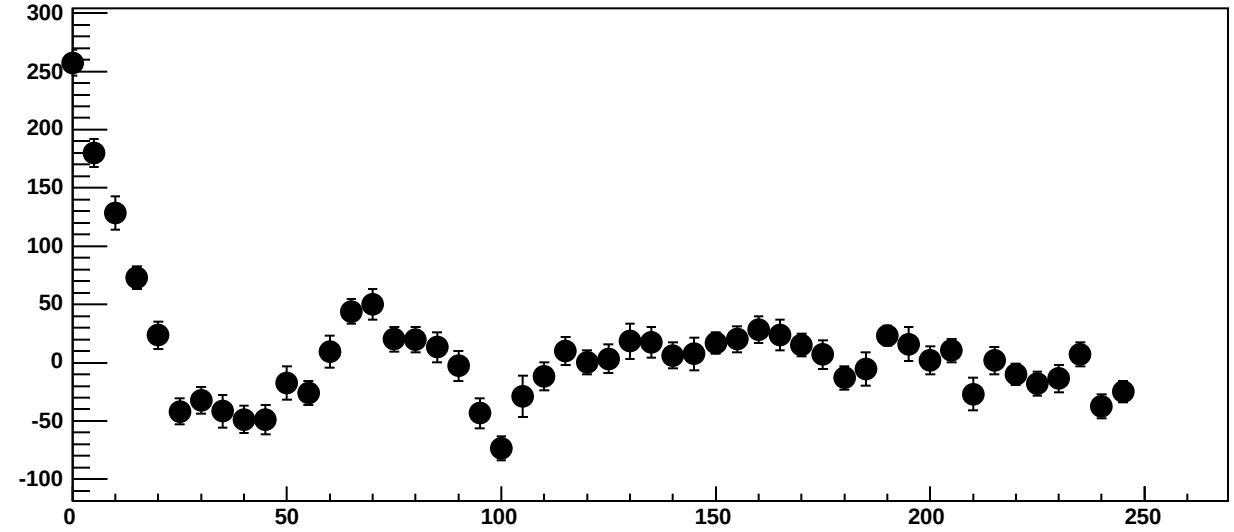
Chip 1, Channel 15, Enable 4, DAC=1600, ADC Mean vs Hold



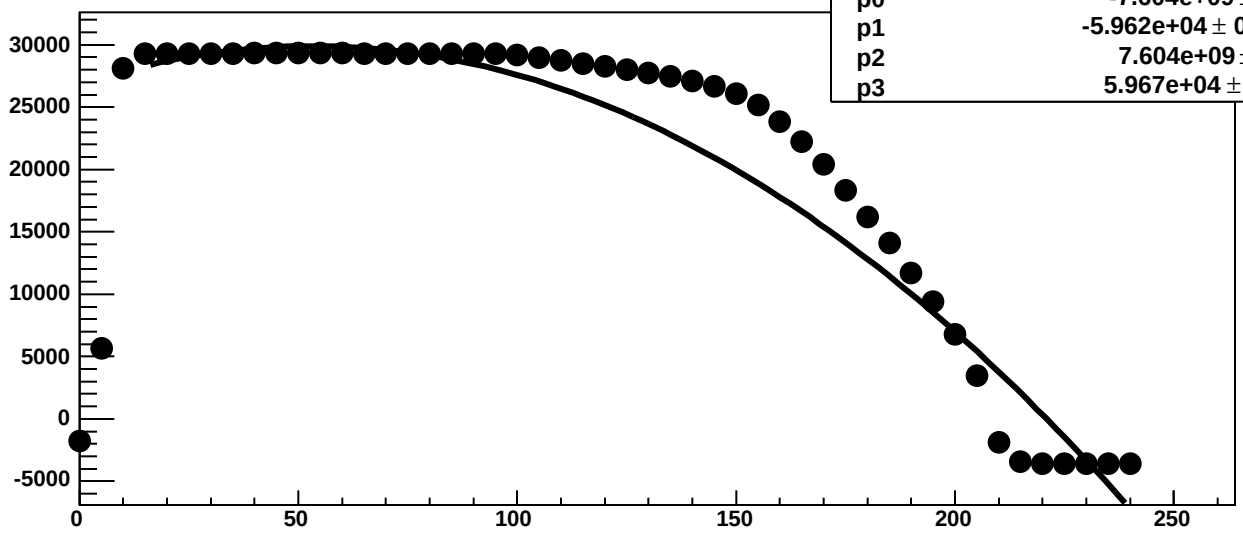
Chip 1, Channel 15, Enable 4, DAC=1600, ADC Noise vs Hold



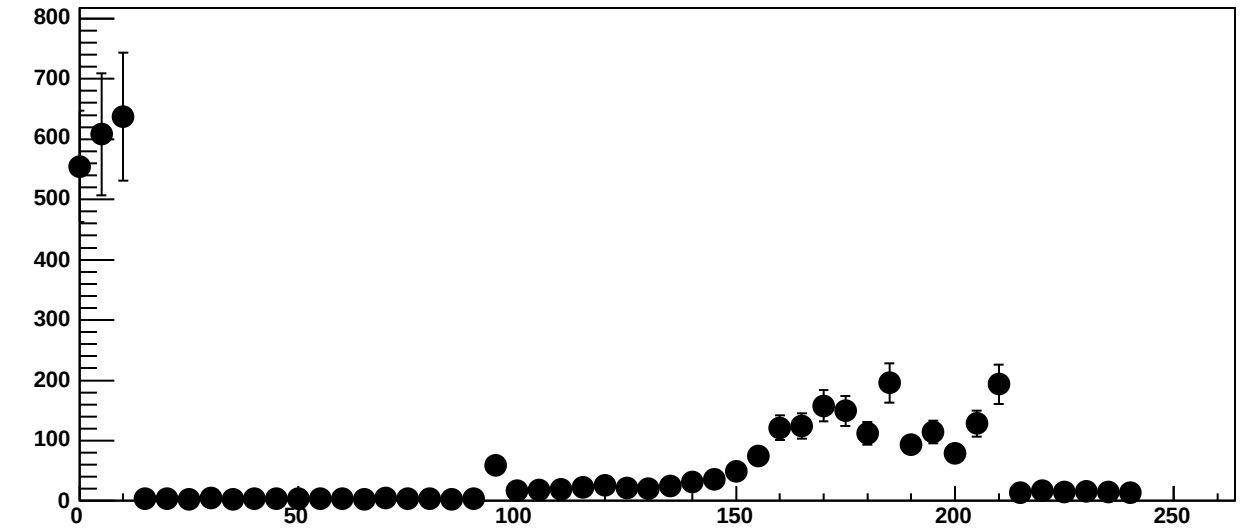
Chip 1, Channel 15, Enable 4, DAC=1600, ADC Residuals vs Hold



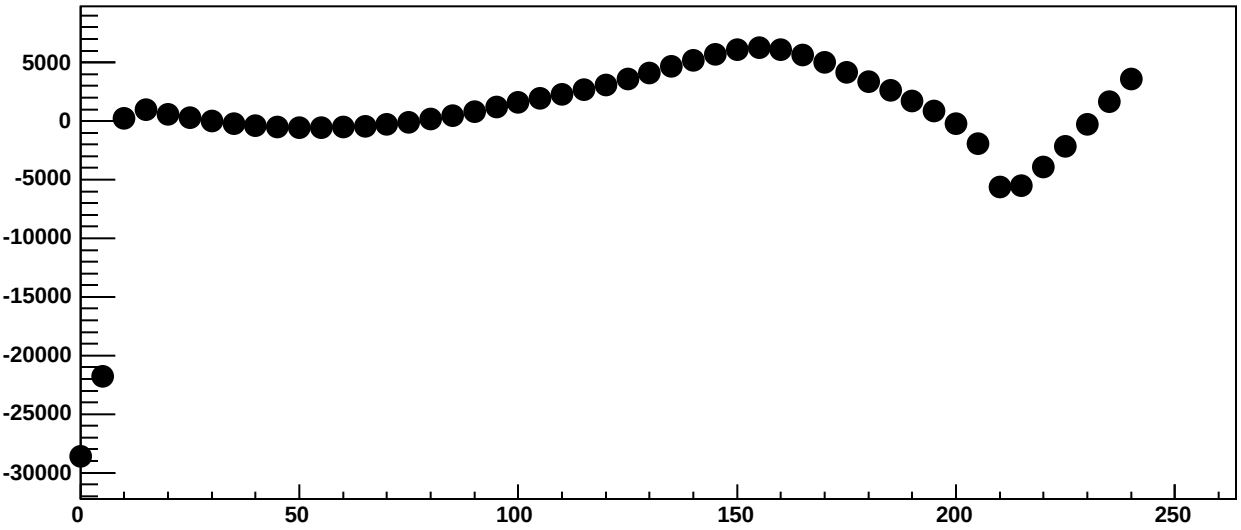
Chip 1, Channel 15, Enable 5!, DAC=1600, ADC Mean vs Hold

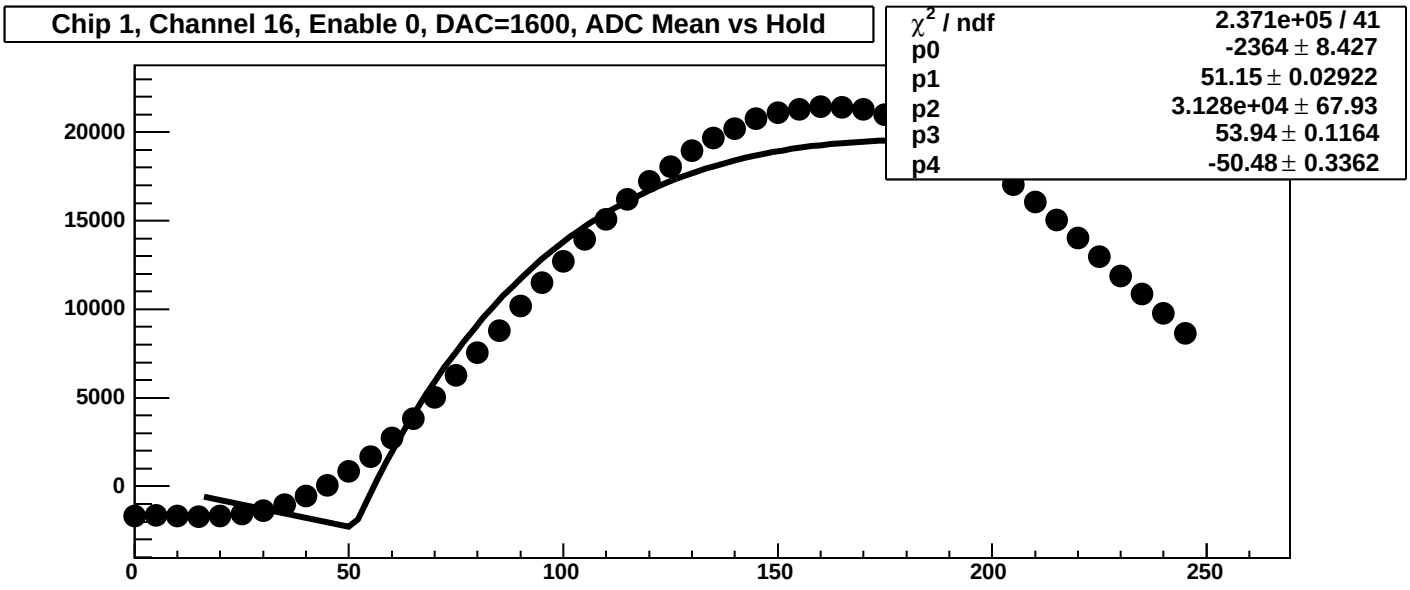


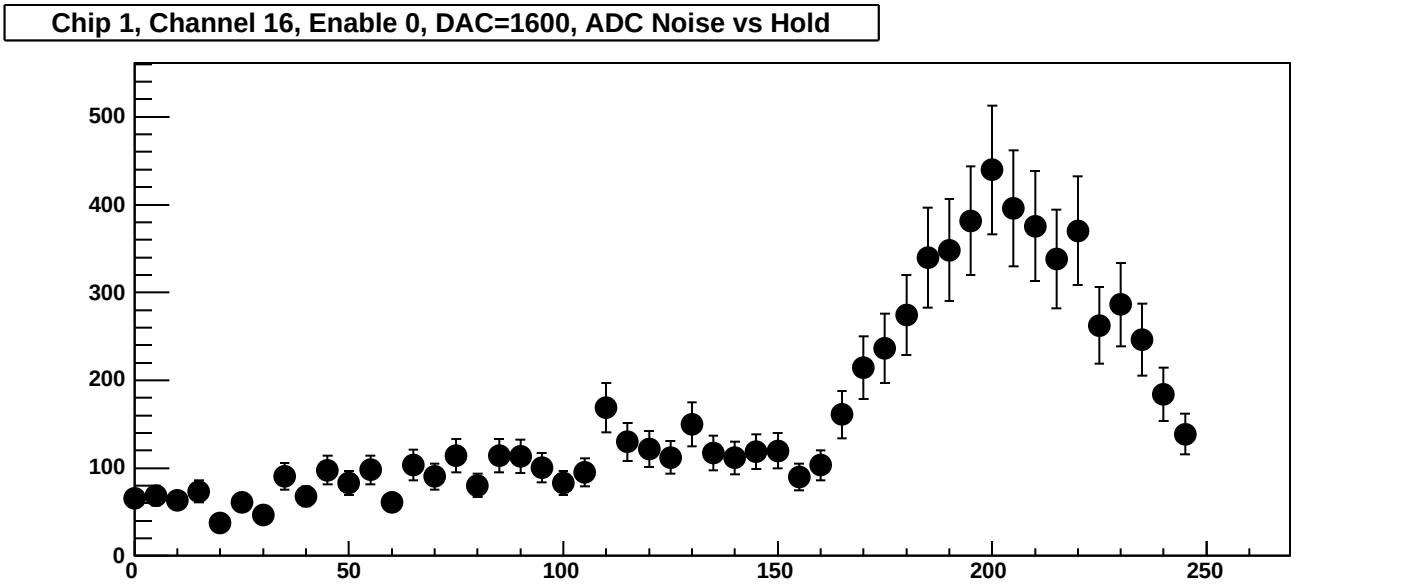
Chip 1, Channel 15, Enable 5!, DAC=1600, ADC Noise vs Hold

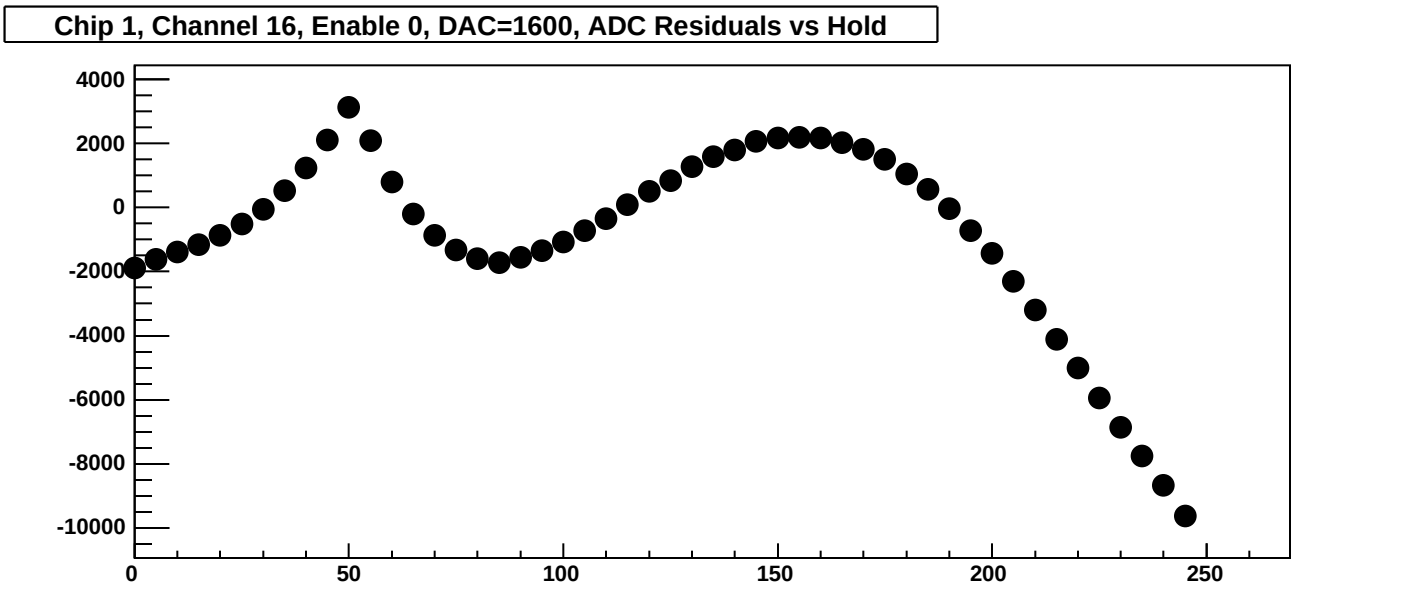


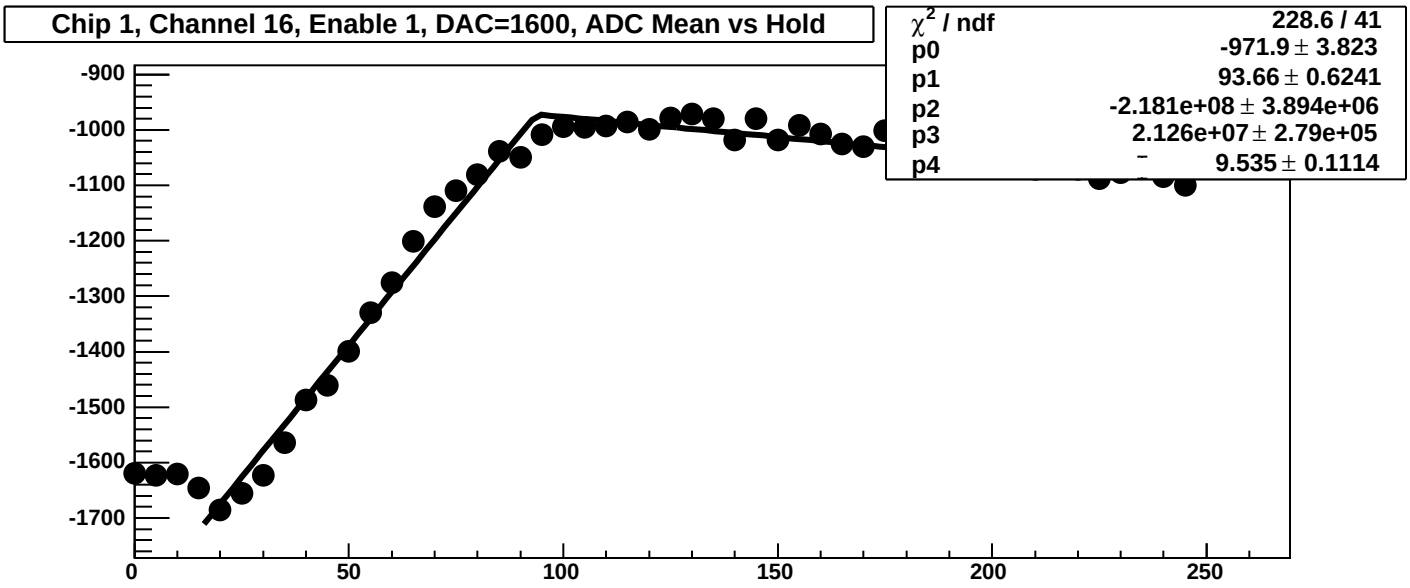
Chip 1, Channel 15, Enable 5!, DAC=1600, ADC Residuals vs Hold

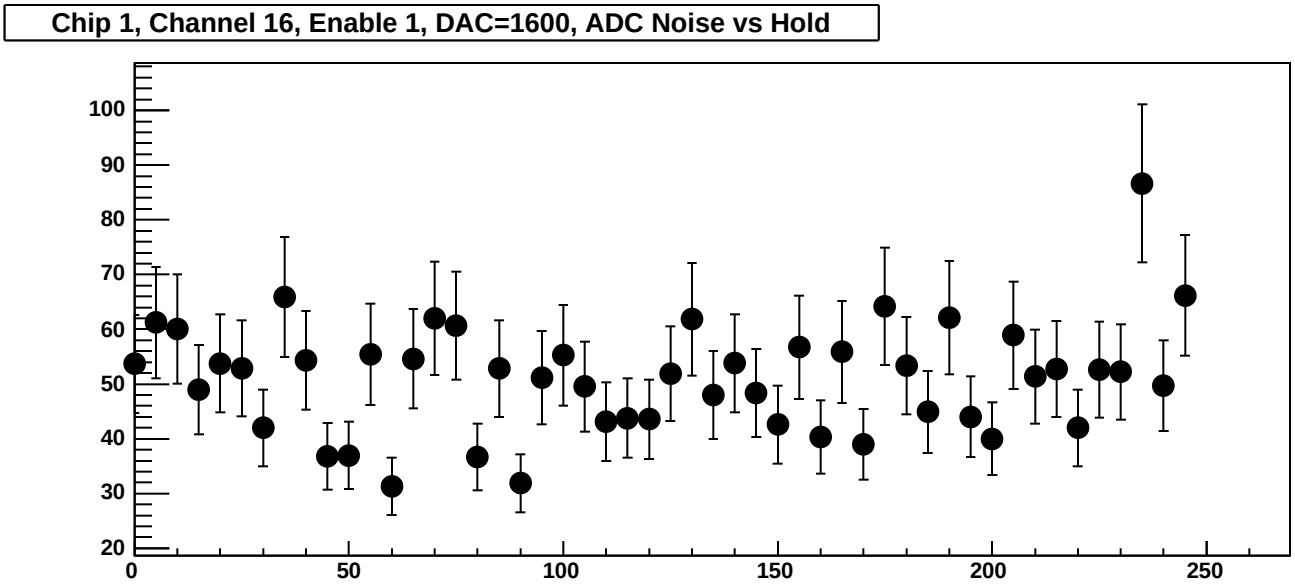


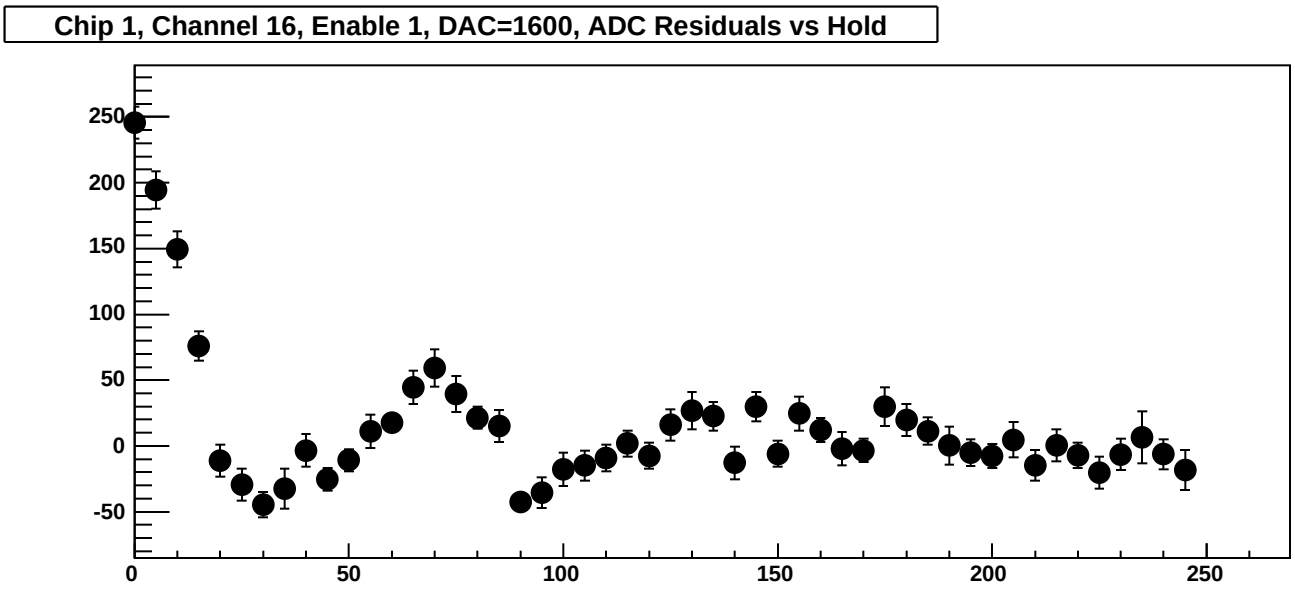

Chip 1, Channel 16, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.371e+05 / 41
p0 -2364 ± 8.427
p1 51.15 ± 0.02922
p2 3.128e+04 ± 67.93
p3 53.94 ± 0.1164
p4 -50.48 ± 0.3362
20000
15000
10000
5000
0
0
50
100
150
200
250

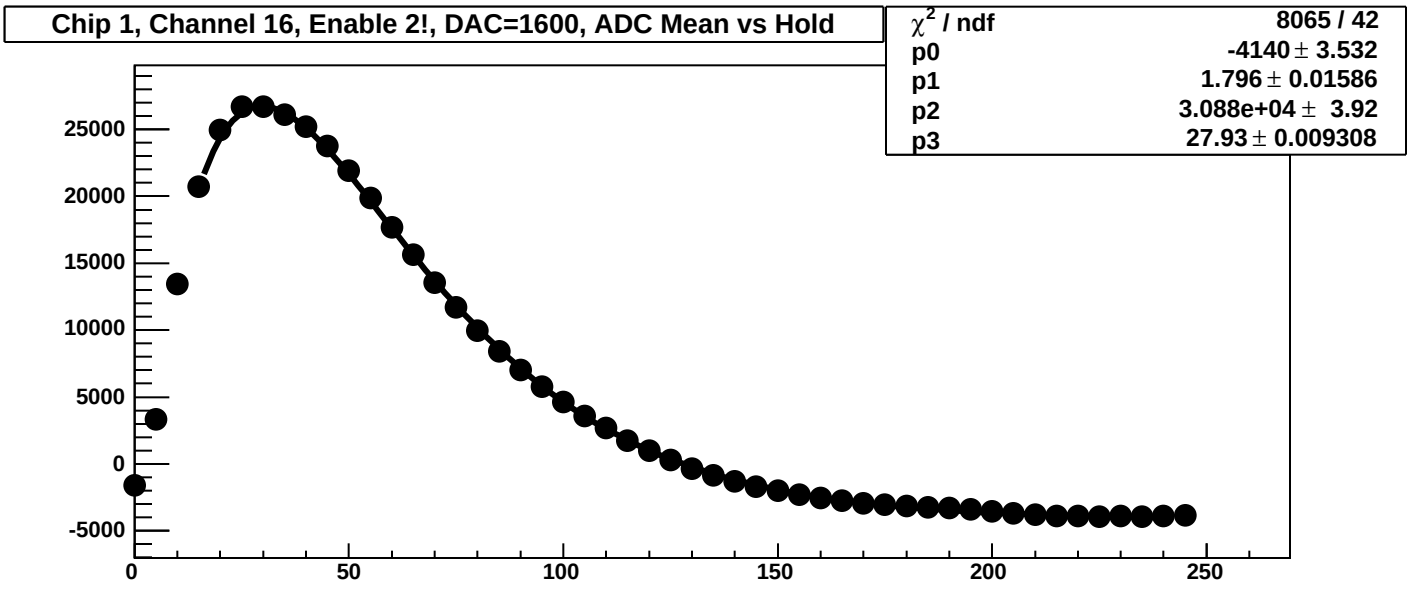

Chip 1, Channel 16, Enable 0, DAC=1600, ADC Noise vs Hold
500
400
300
200
100
0
0
50
100
150
200
250

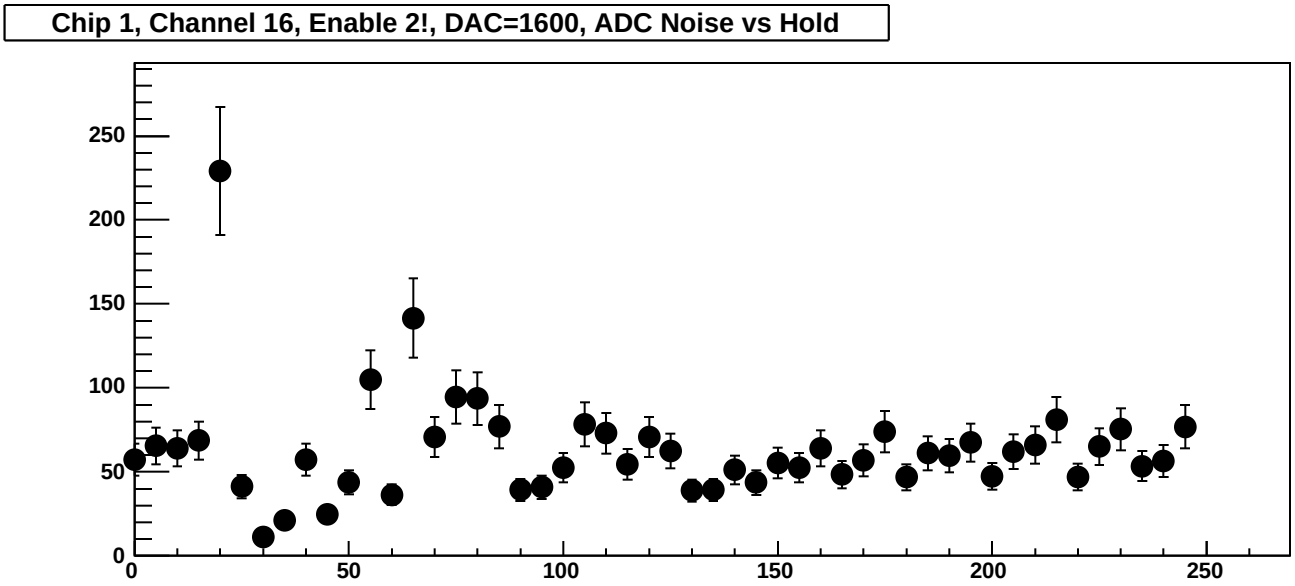

Chip 1, Channel 16, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

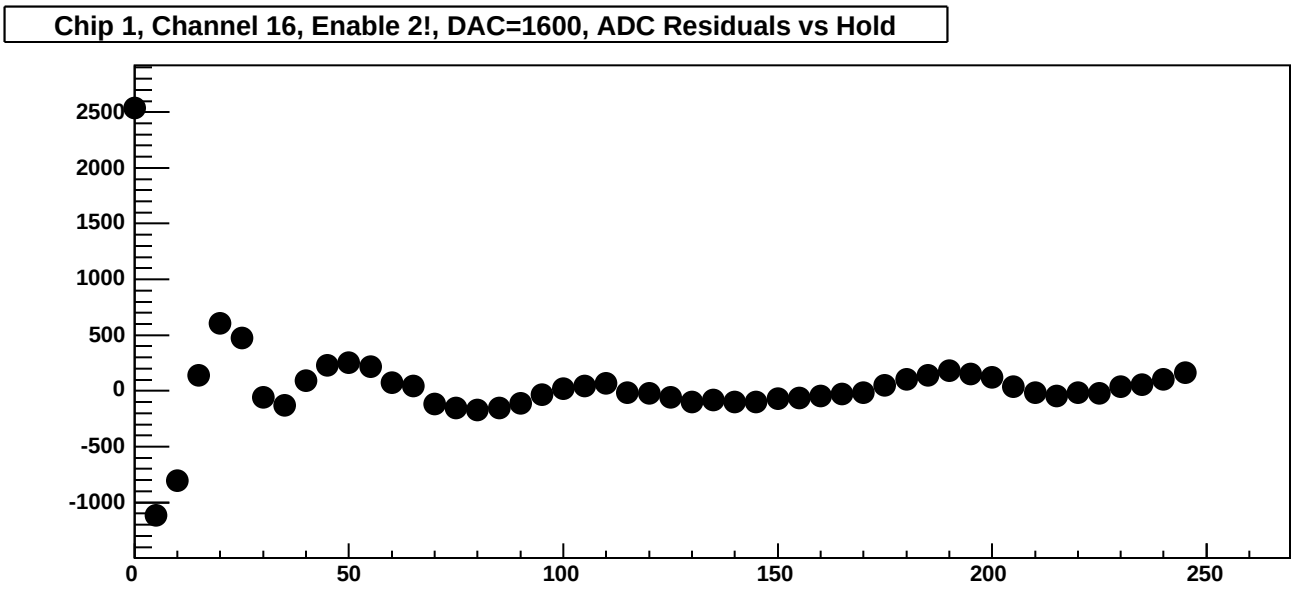

Chip 1, Channel 16, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 228.6 / 41
p0 -971.9 ± 3.823
p1 93.66 ± 0.6241
p2 -2.181e+08 ± 3.894e+06
p3 2.126e+07 ± 2.79e+05
p4 9.535 ± 0.1114
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
50
100
150
200
250


Chip 1, Channel 16, Enable 1, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
20
0
50
100
150
200
250

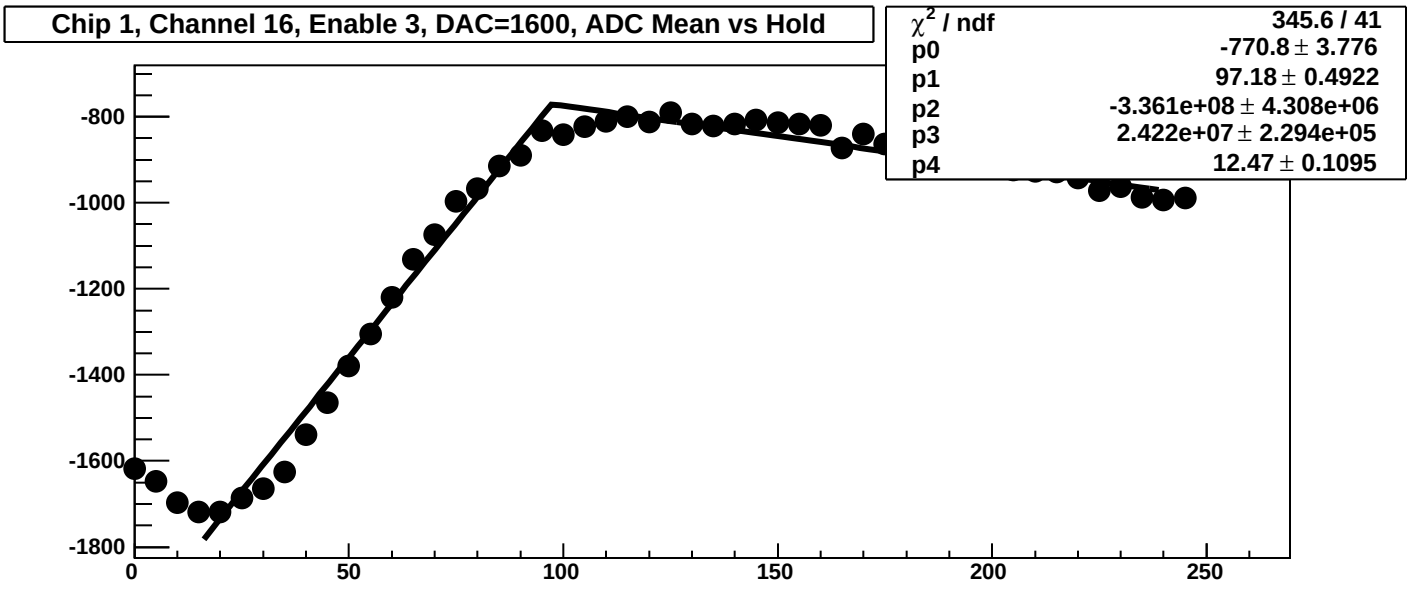

Chip 1, Channel 16, Enable 1, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
0
50
100
150
200
250


Chip 1, Channel 16, Enable 2!, DAC=1600, ADC Mean vs Hold
χ² / ndf 8065 / 42
p0 -4140 ± 3.532
p1 1.796 ± 0.01586
p2 3.088e+04 ± 3.92
p3 27.93 ± 0.009308
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

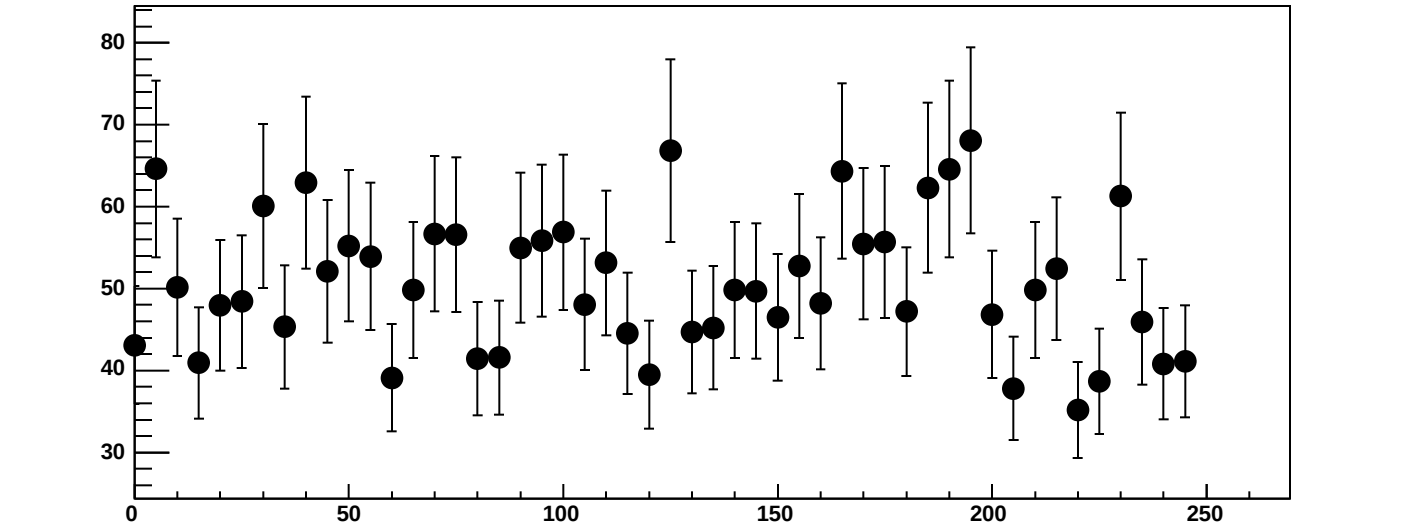

Chip 1, Channel 16, Enable 2!, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250


Chip 1, Channel 16, Enable 2!, DAC=1600, ADC Residuals vs Hold
2500
2000
1500
1000
500
0
-500
-1000
0
50
100
150
200
250

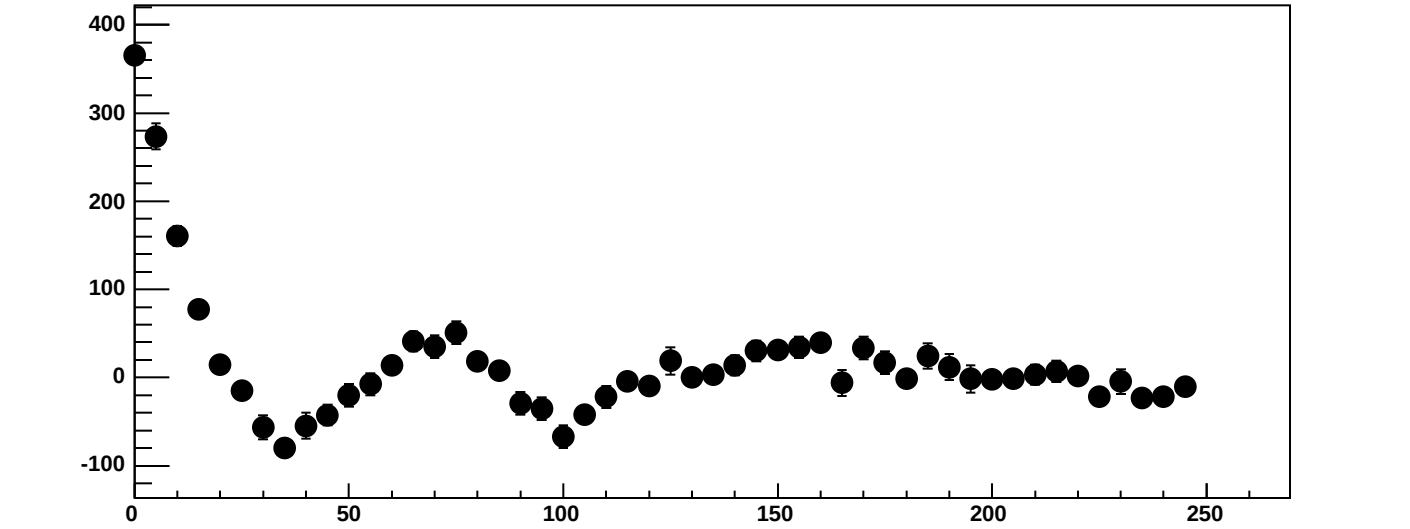
Chip 1, Channel 16, Enable 3, DAC=1600, ADC Mean vs Hold



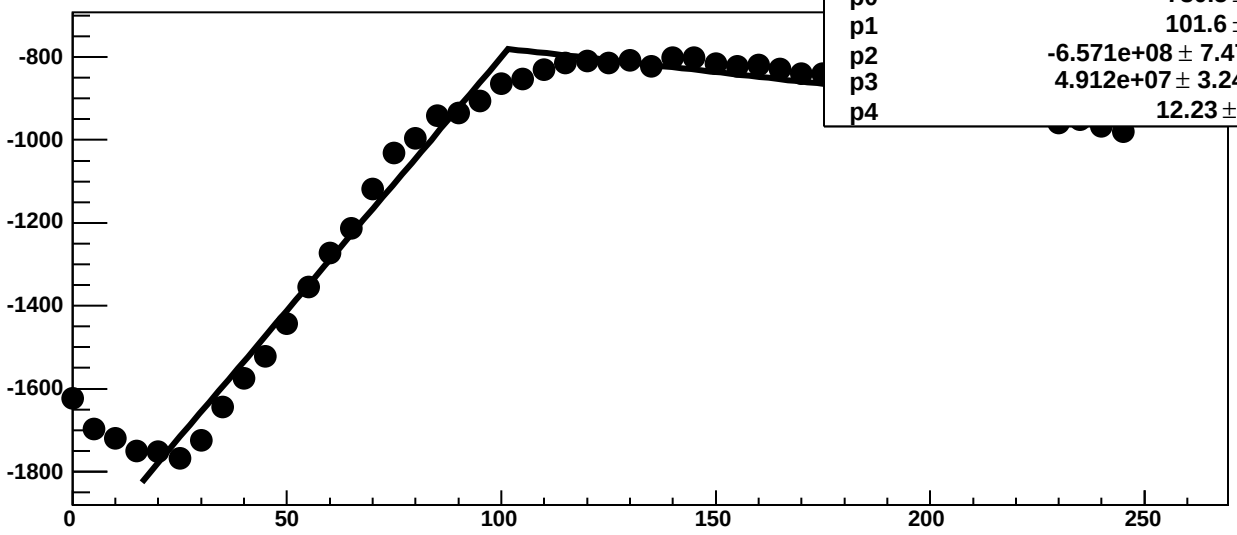
Chip 1, Channel 16, Enable 3, DAC=1600, ADC Noise vs Hold



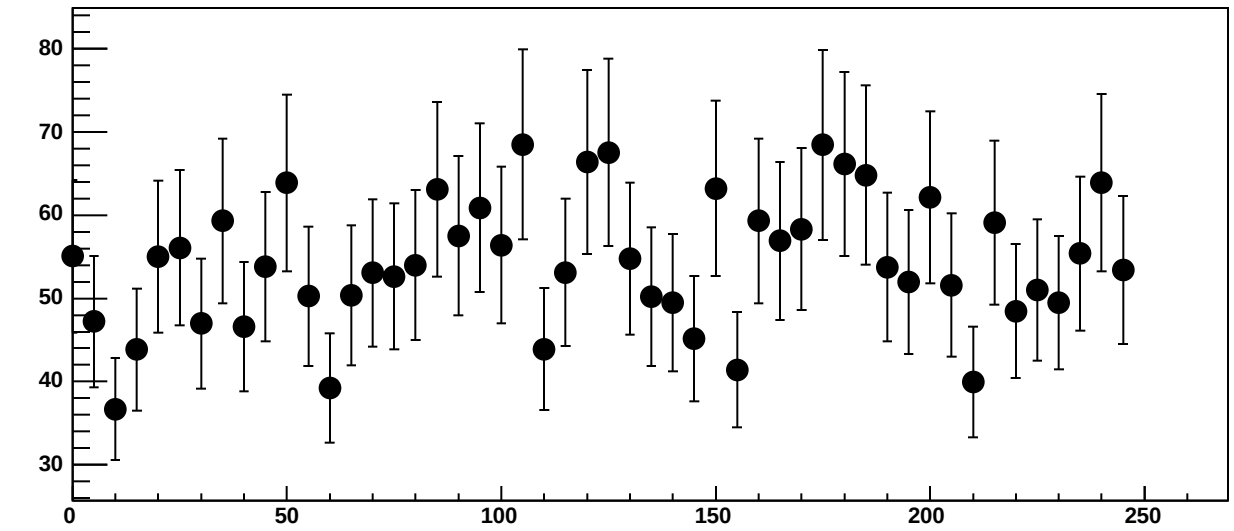
Chip 1, Channel 16, Enable 3, DAC=1600, ADC Residuals vs Hold



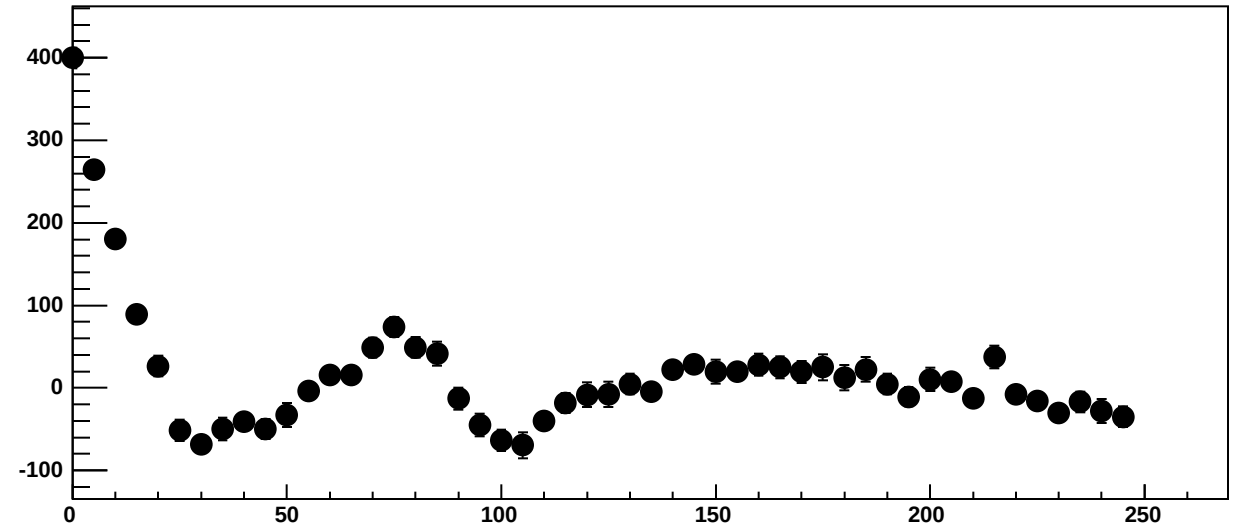
Chip 1, Channel 16, Enable 4, DAC=1600, ADC Mean vs Hold

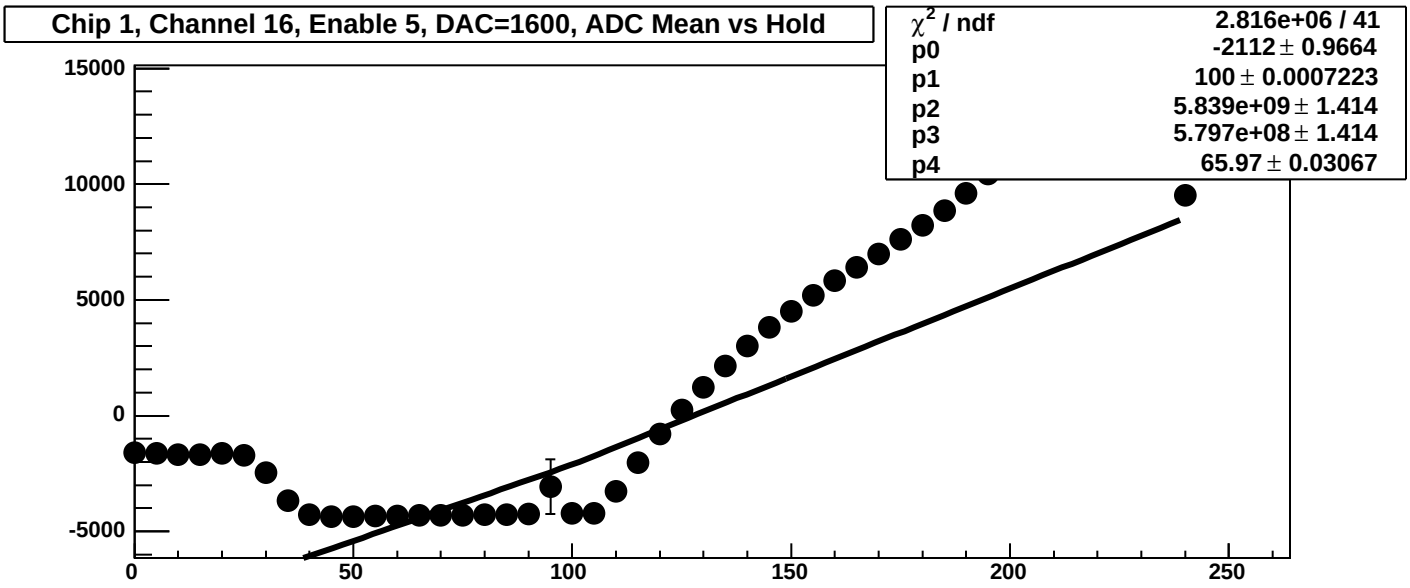


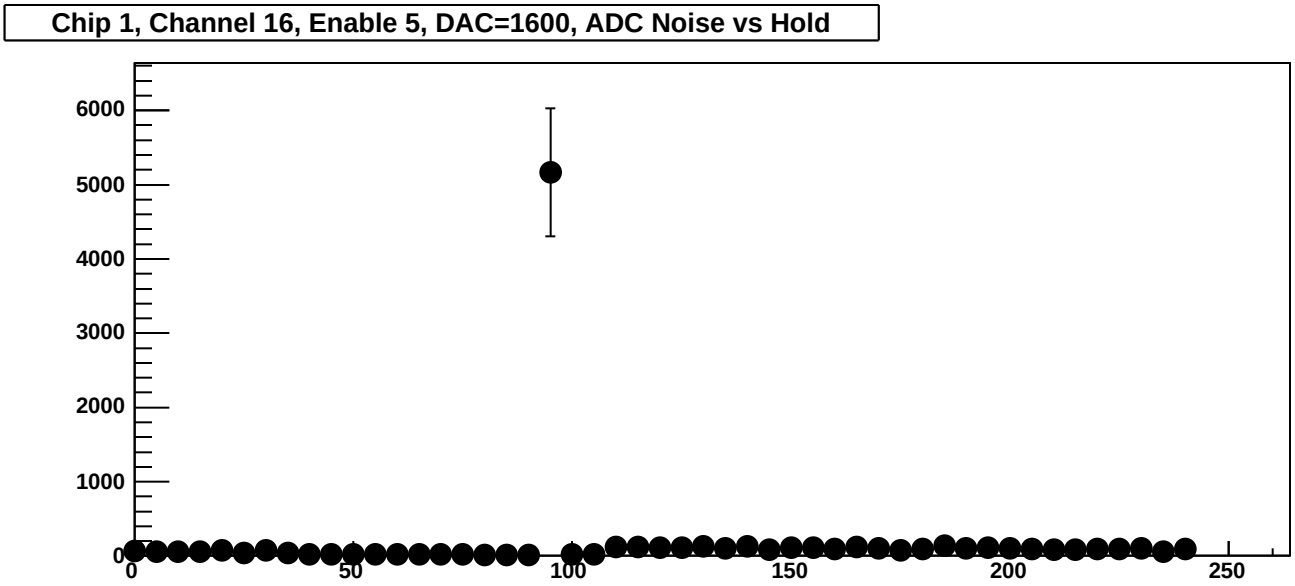
Chip 1, Channel 16, Enable 4, DAC=1600, ADC Noise vs Hold

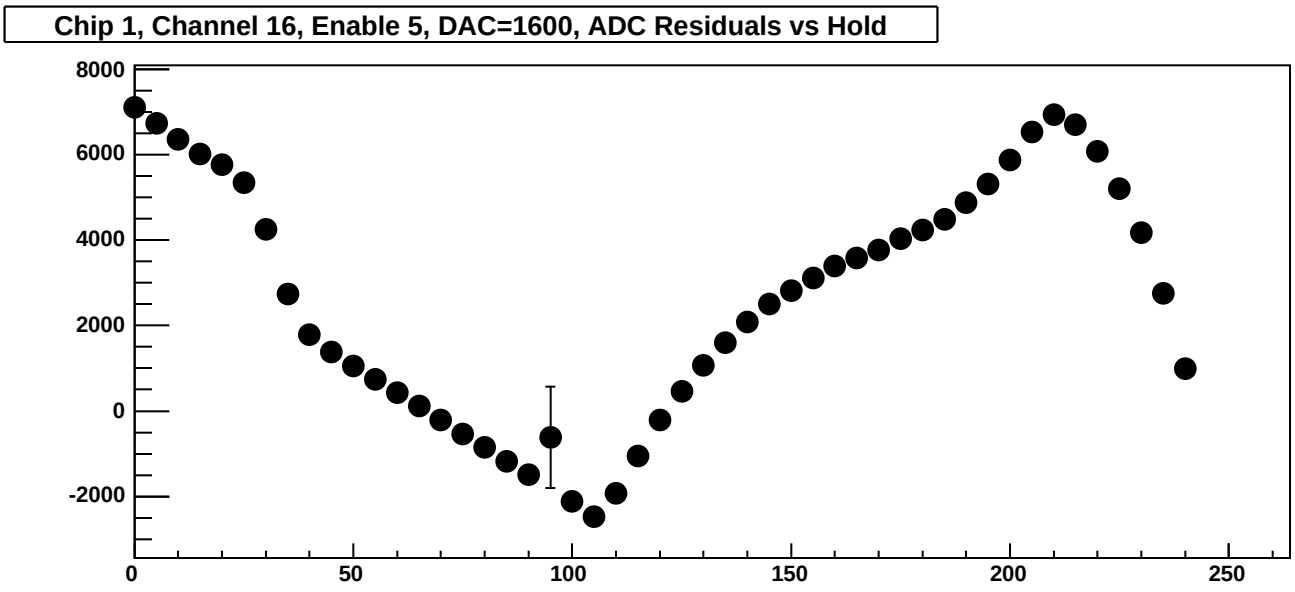


Chip 1, Channel 16, Enable 4, DAC=1600, ADC Residuals vs Hold

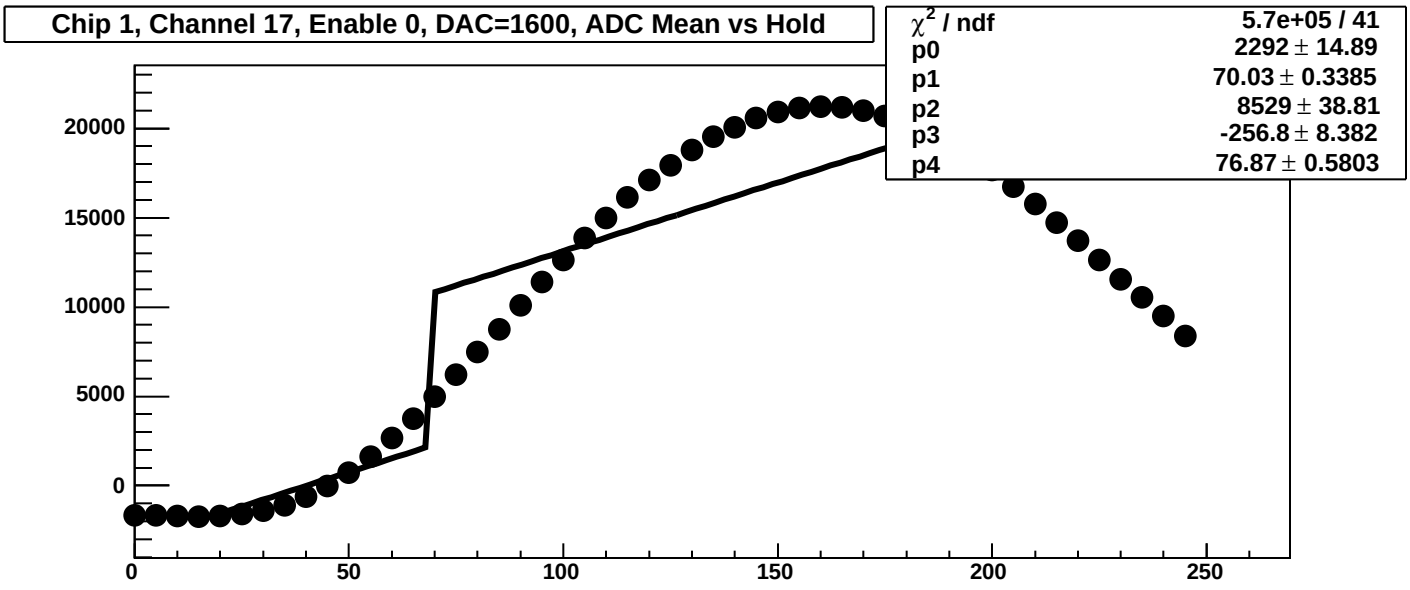



Chip 1, Channel 16, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.816e+06 / 41
p0 -2112 ± 0.9664
p1 100 ± 0.0007223
p2 5.839e+09 ± 1.414
p3 5.797e+08 ± 1.414
p4 65.97 ± 0.03067
15000
10000
5000
0
-5000
0
50
100
150
200
250

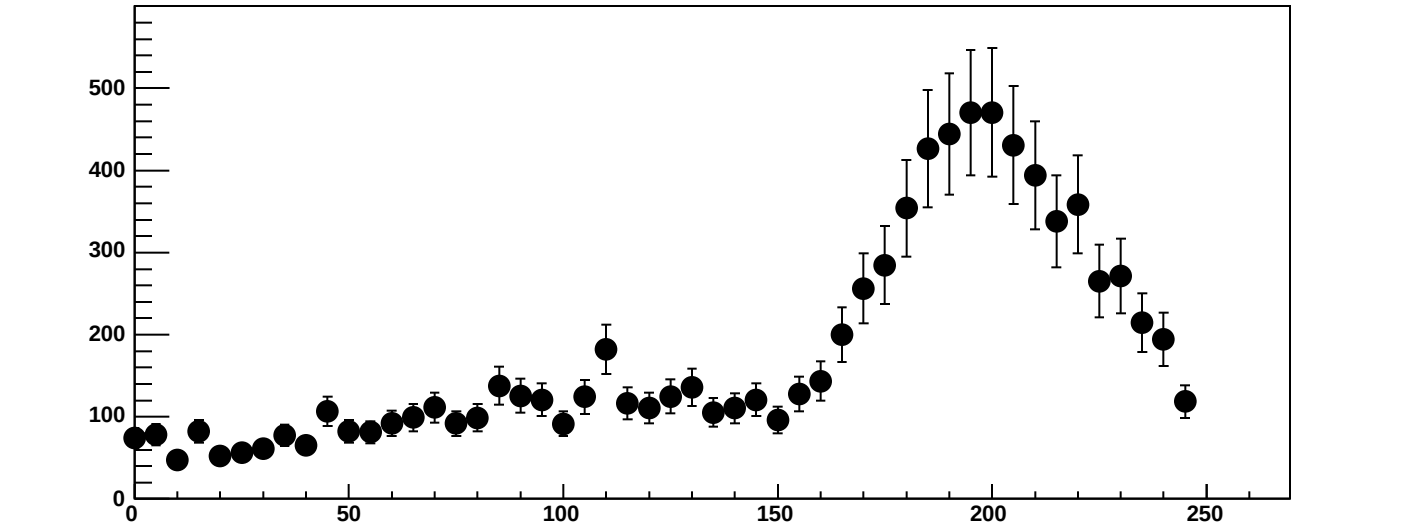

Chip 1, Channel 16, Enable 5, DAC=1600, ADC Noise vs Hold
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 1, Channel 16, Enable 5, DAC=1600, ADC Residuals vs Hold
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

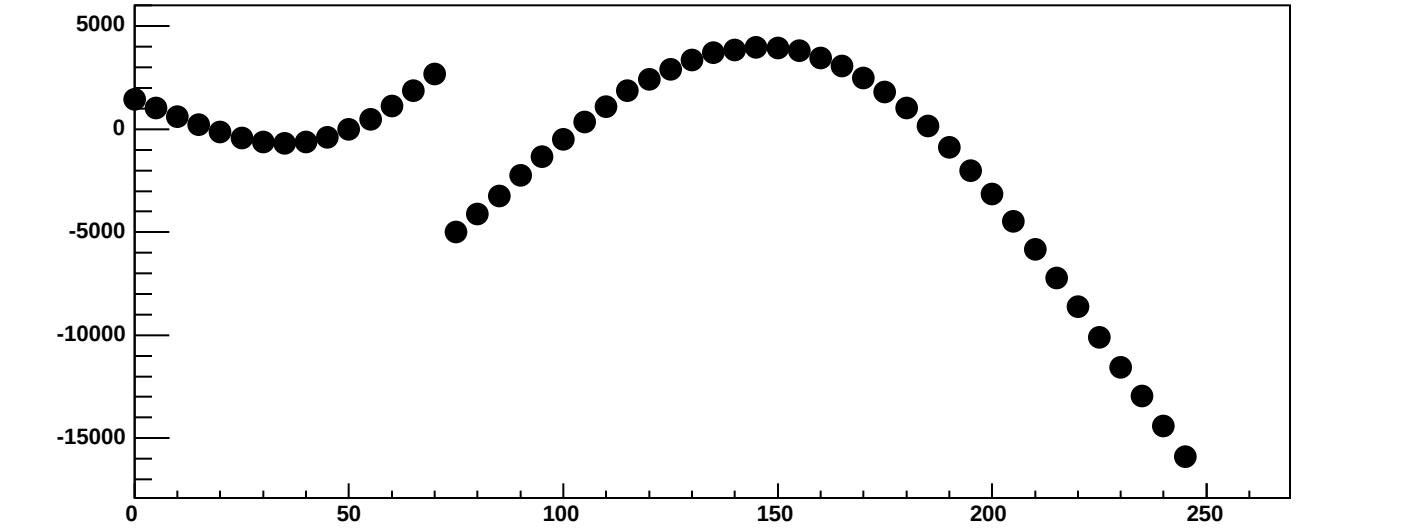
Chip 1, Channel 17, Enable 0, DAC=1600, ADC Mean vs Hold



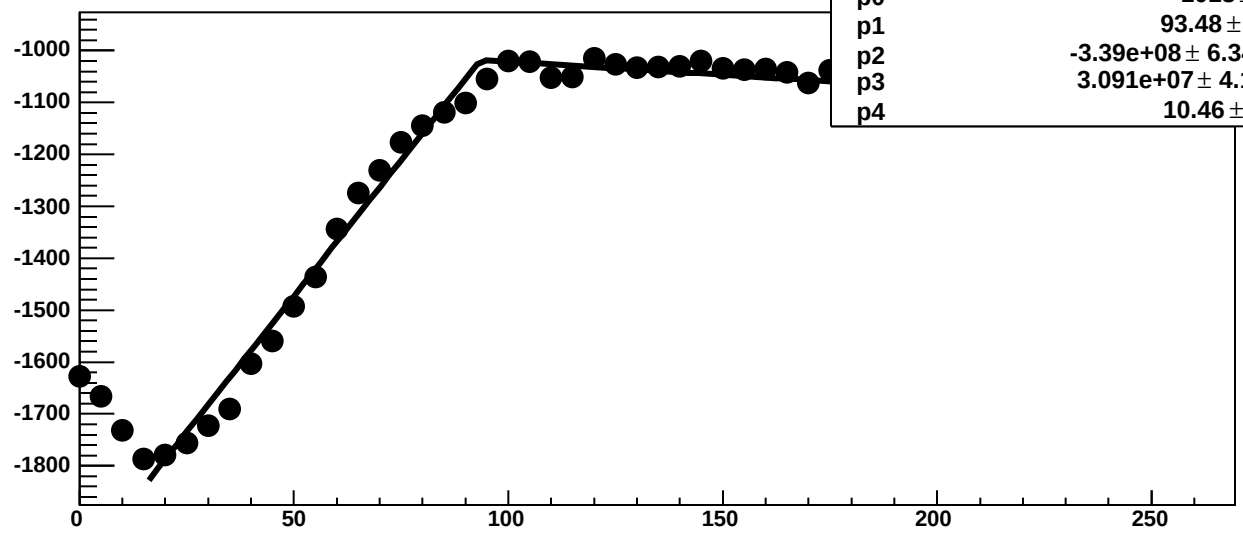
Chip 1, Channel 17, Enable 0, DAC=1600, ADC Noise vs Hold



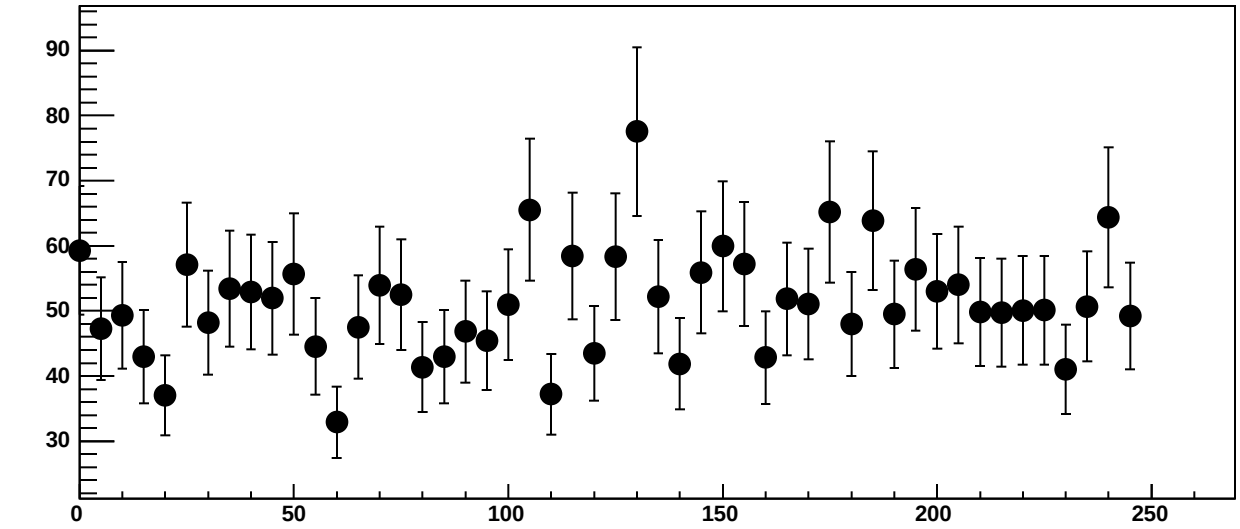
Chip 1, Channel 17, Enable 0, DAC=1600, ADC Residuals vs Hold



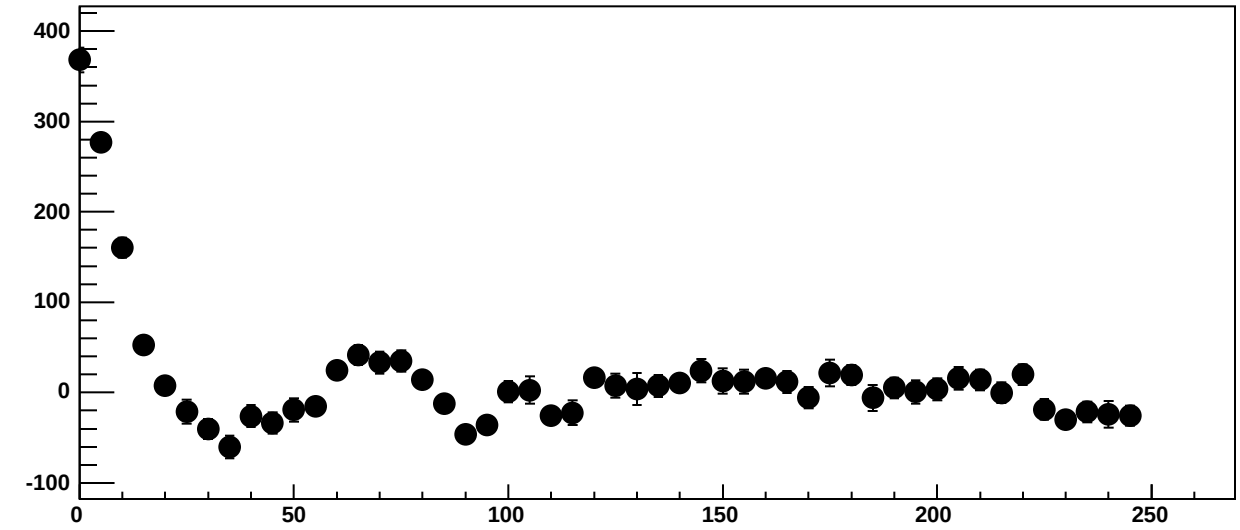
Chip 1, Channel 17, Enable 1, DAC=1600, ADC Mean vs Hold

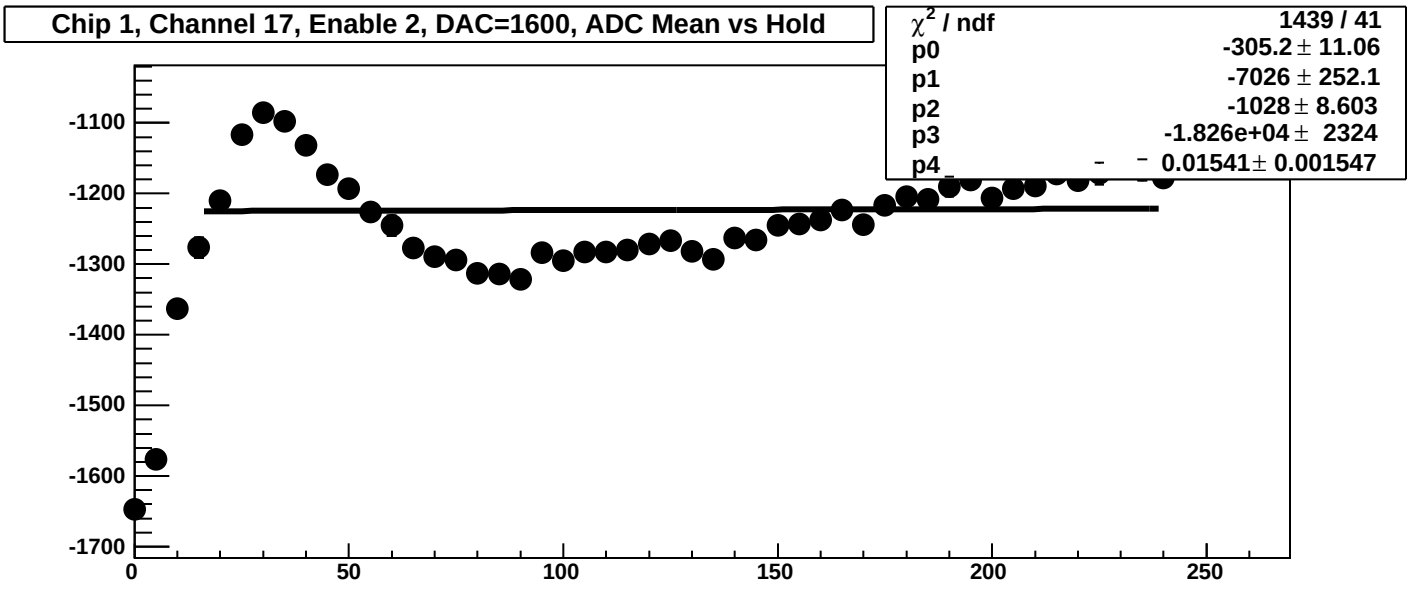


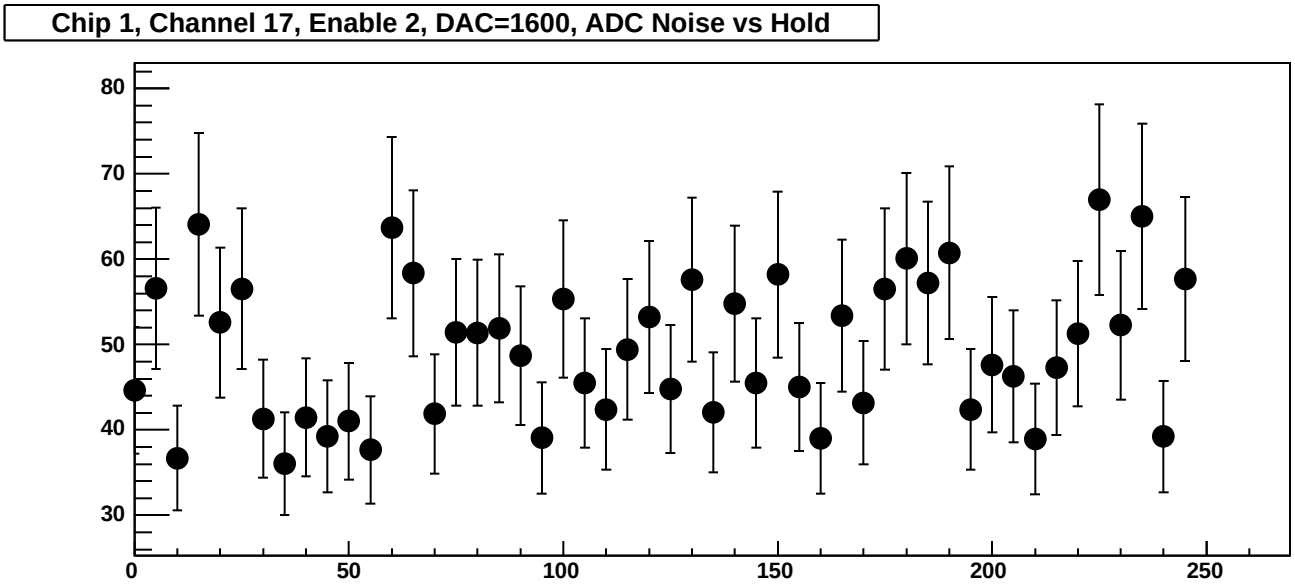
Chip 1, Channel 17, Enable 1, DAC=1600, ADC Noise vs Hold

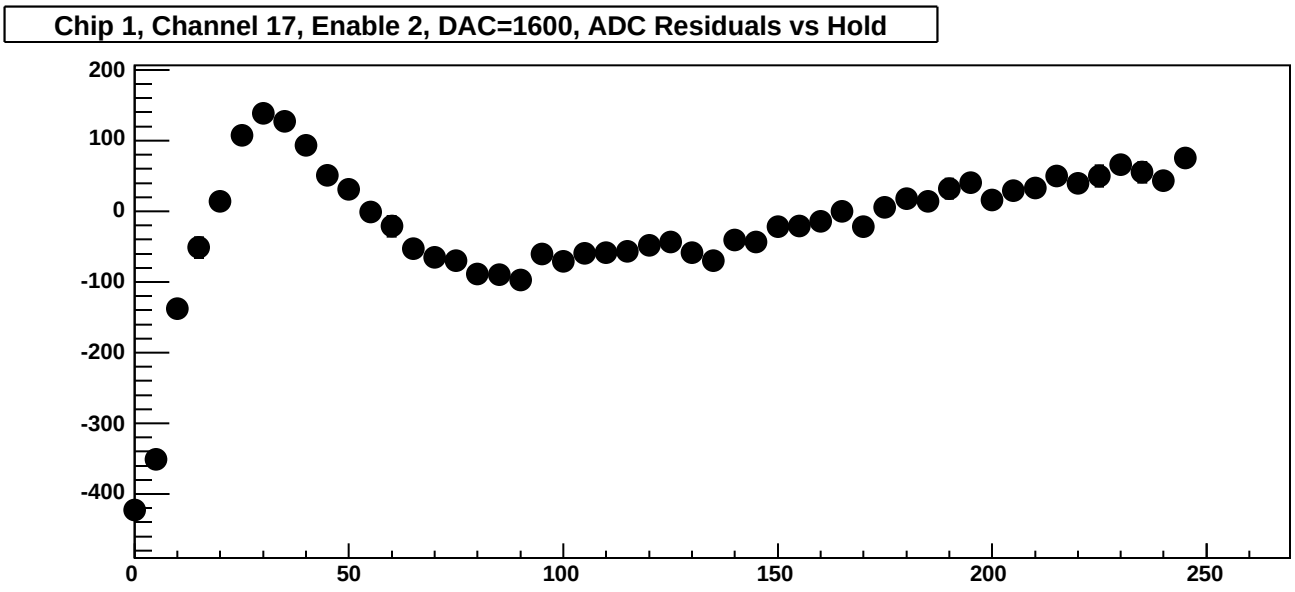


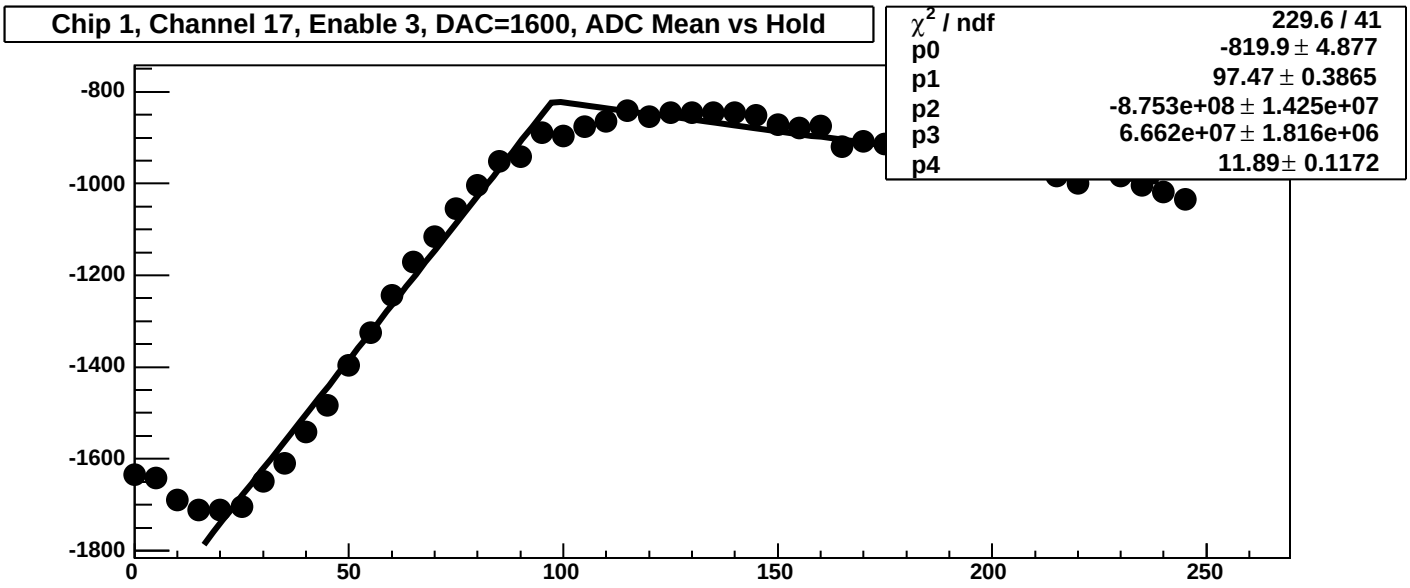
Chip 1, Channel 17, Enable 1, DAC=1600, ADC Residuals vs Hold

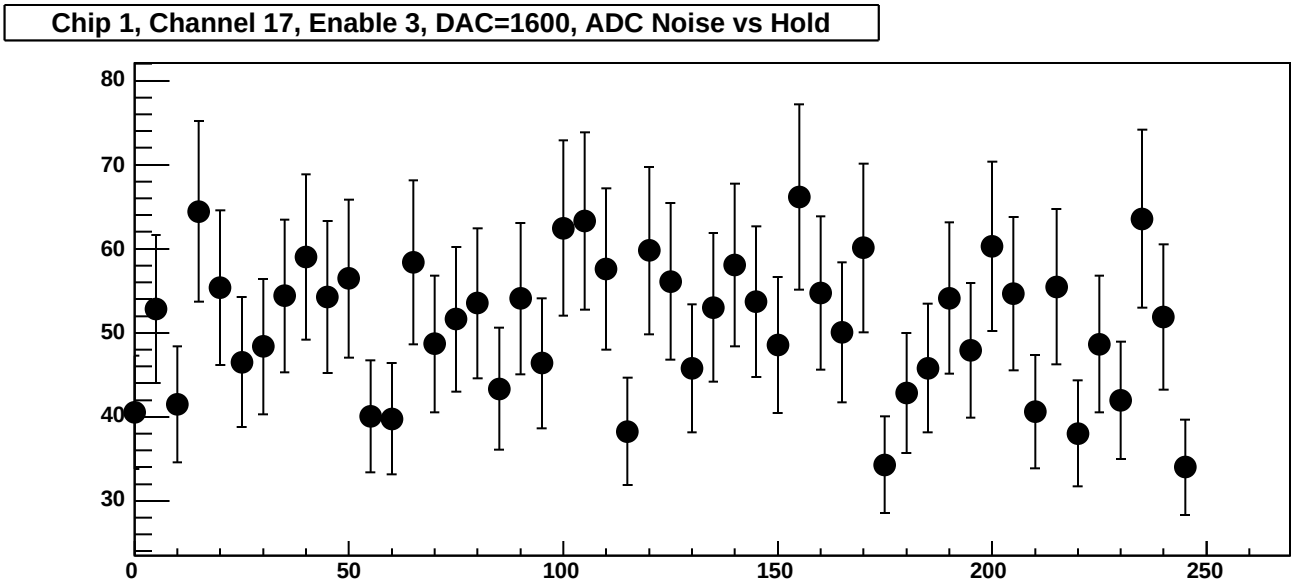


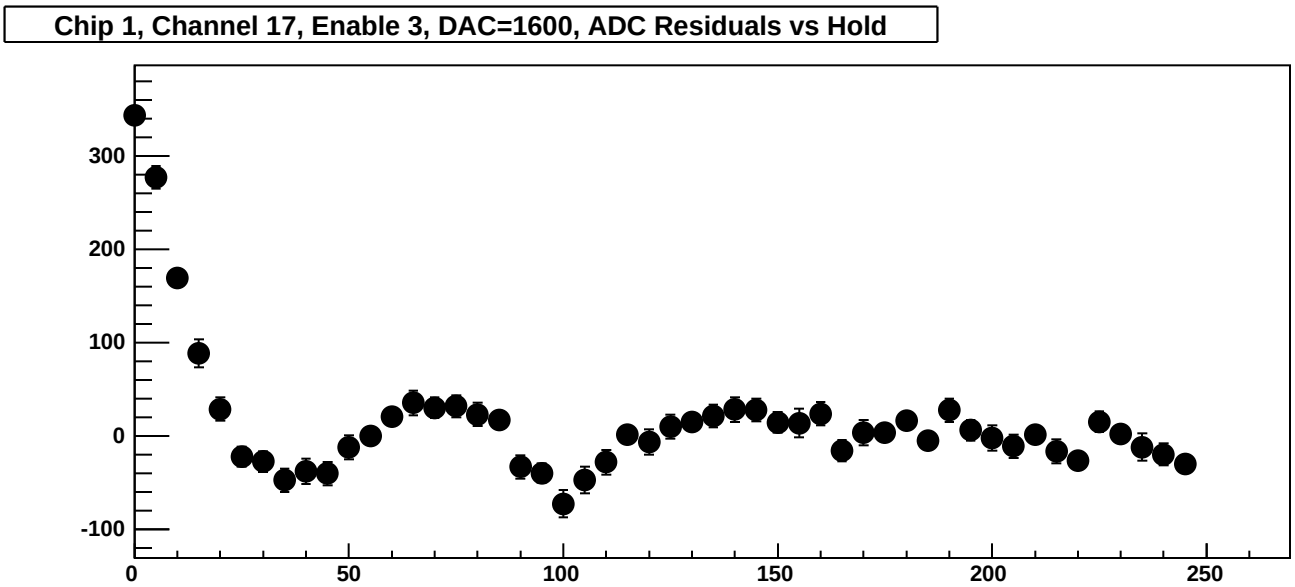

Chip 1, Channel 17, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 1439 / 41
p0 -305.2 ± 11.06
p1 -7026 ± 252.1
p2 -1028 ± 8.603
p3 -1.826e+04 ± 2324
p4 0.01541 ± 0.001547
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
50
100
150
200
250


Chip 1, Channel 17, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

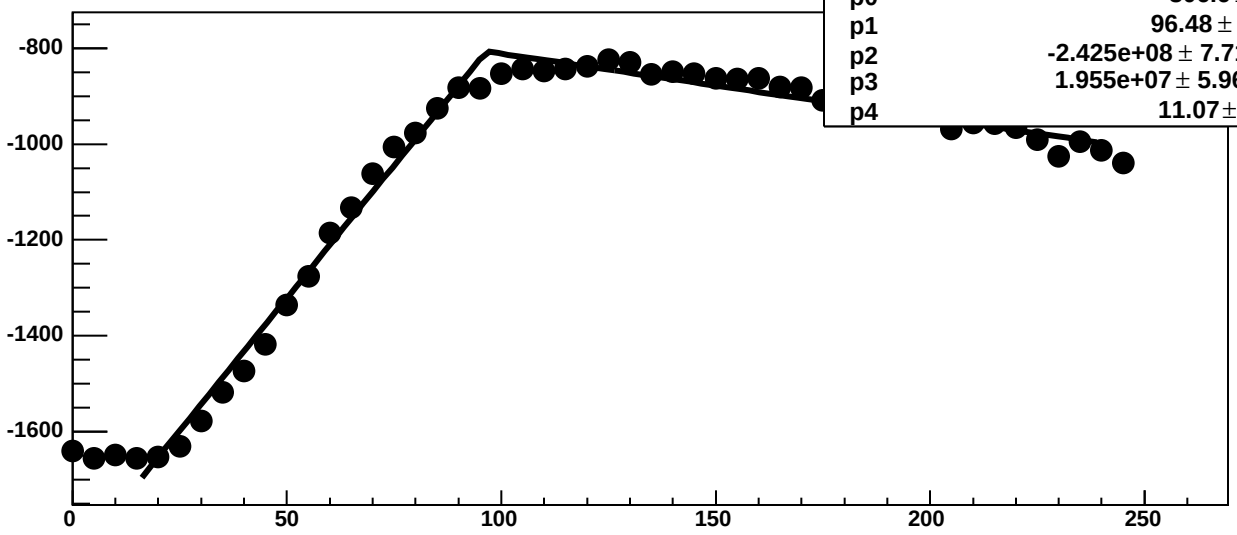

Chip 1, Channel 17, Enable 2, DAC=1600, ADC Residuals vs Hold
200
100
0
-100
-200
-300
-400
0
50
100
150
200
250


Chip 1, Channel 17, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 229.6 / 41
p0 -819.9 ± 4.877
p1 97.47 ± 0.3865
p2 -8.753e+08 ± 1.425e+07
p3 6.662e+07 ± 1.816e+06
p4 11.89 ± 0.1172
-800
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250

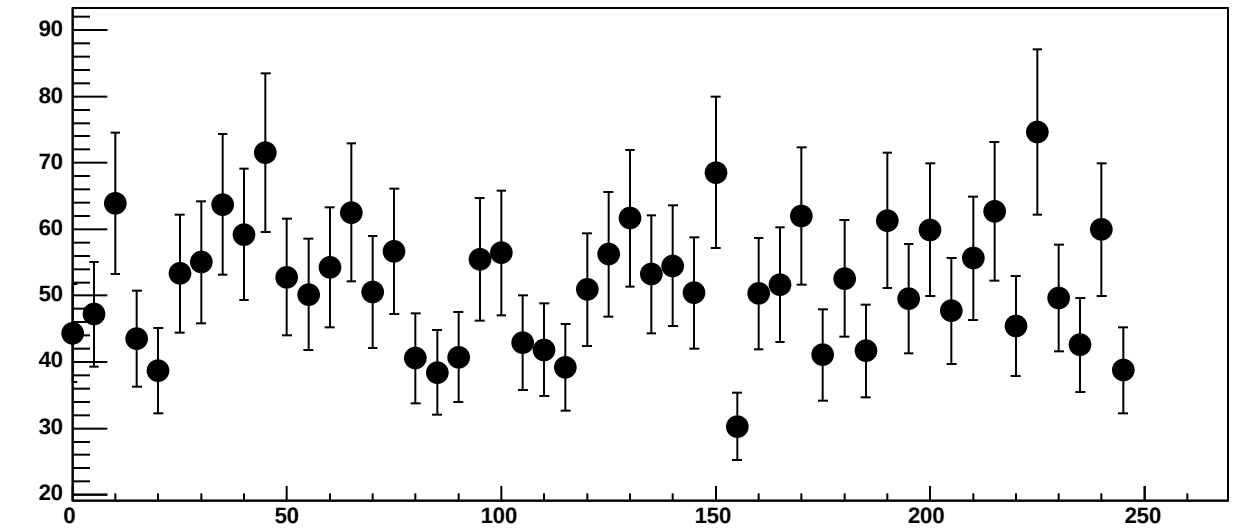

Chip 1, Channel 17, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 1, Channel 17, Enable 3, DAC=1600, ADC Residuals vs Hold
300
200
100
0
-100
0
50
100
150
200
250

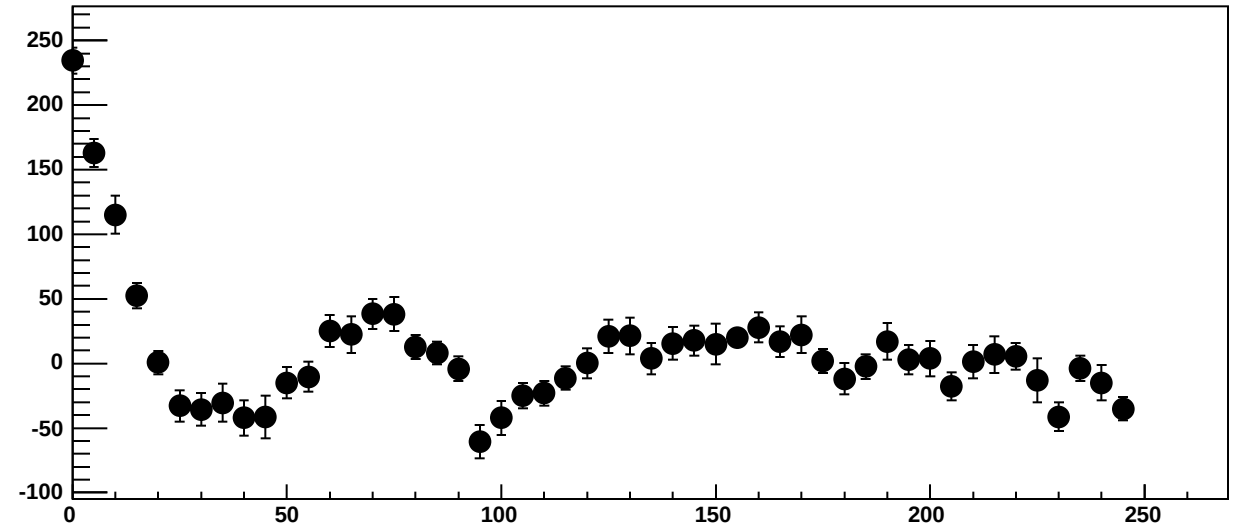
Chip 1, Channel 17, Enable 4, DAC=1600, ADC Mean vs Hold

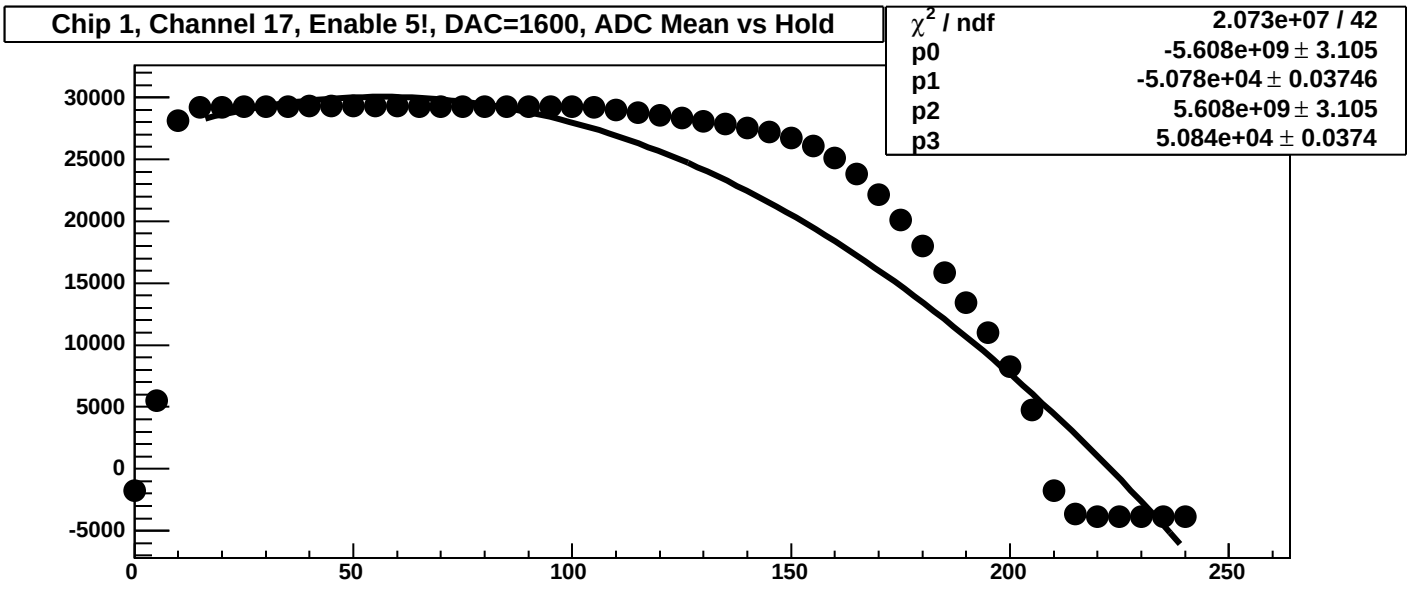


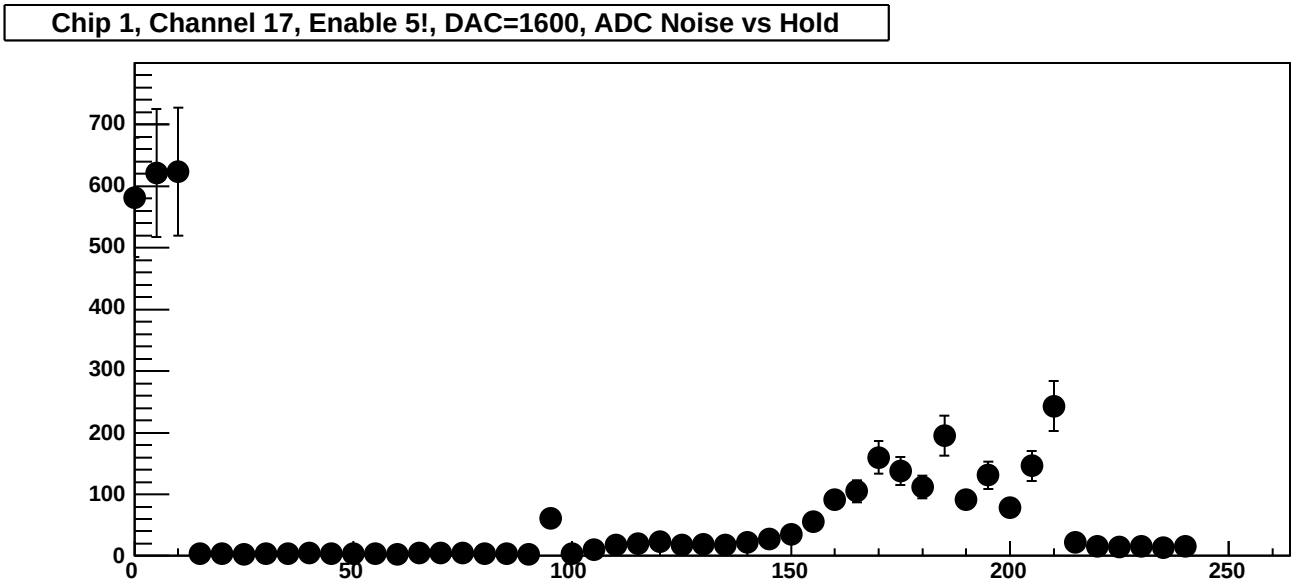
Chip 1, Channel 17, Enable 4, DAC=1600, ADC Noise vs Hold

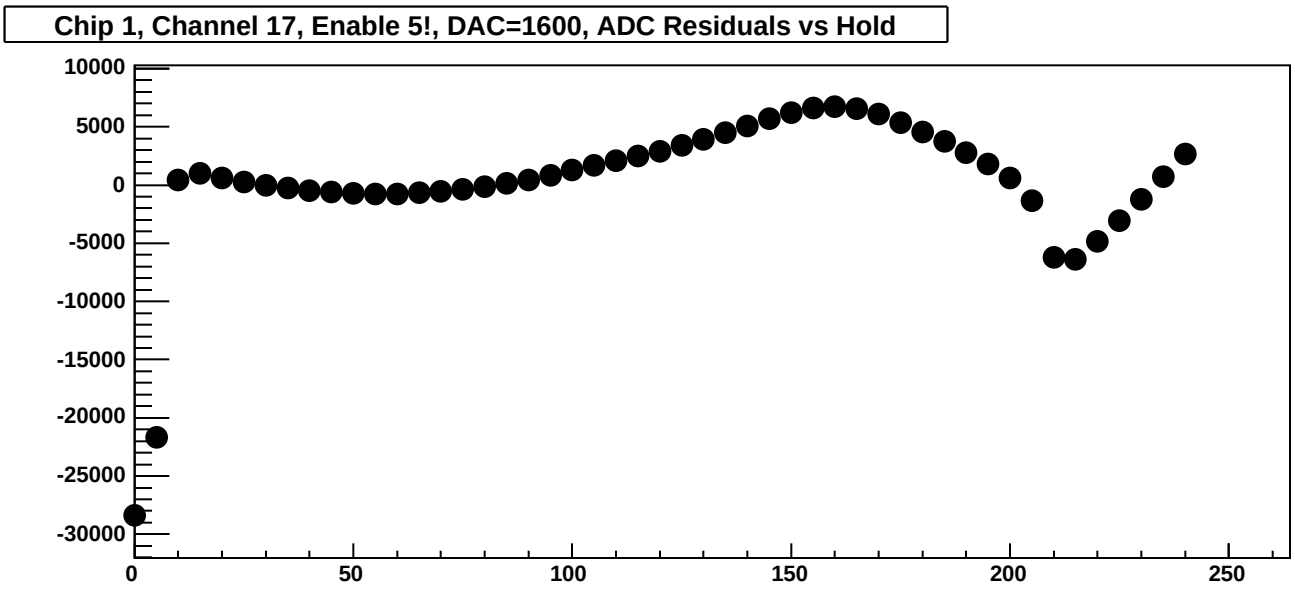


Chip 1, Channel 17, Enable 4, DAC=1600, ADC Residuals vs Hold

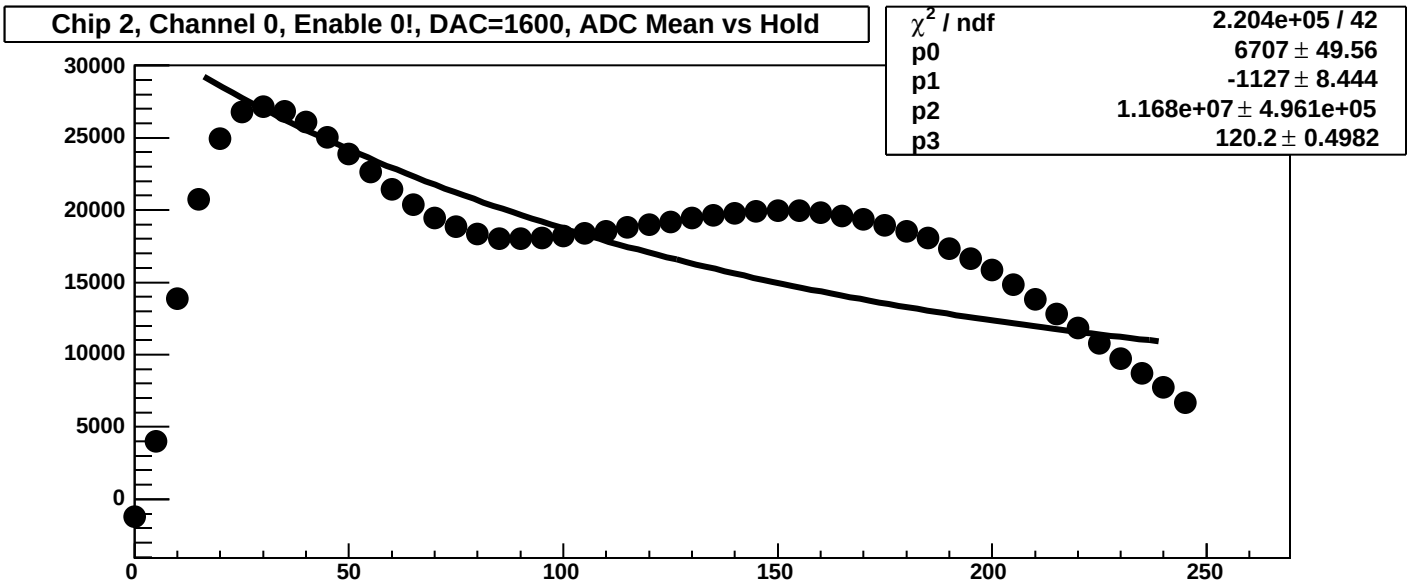



Chip 1, Channel 17, Enable 5!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.073e+07 / 42
p0 -5.608e+09 ± 3.105
p1 -5.078e+04 ± 0.03746
p2 5.608e+09 ± 3.105
p3 5.084e+04 ± 0.0374
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

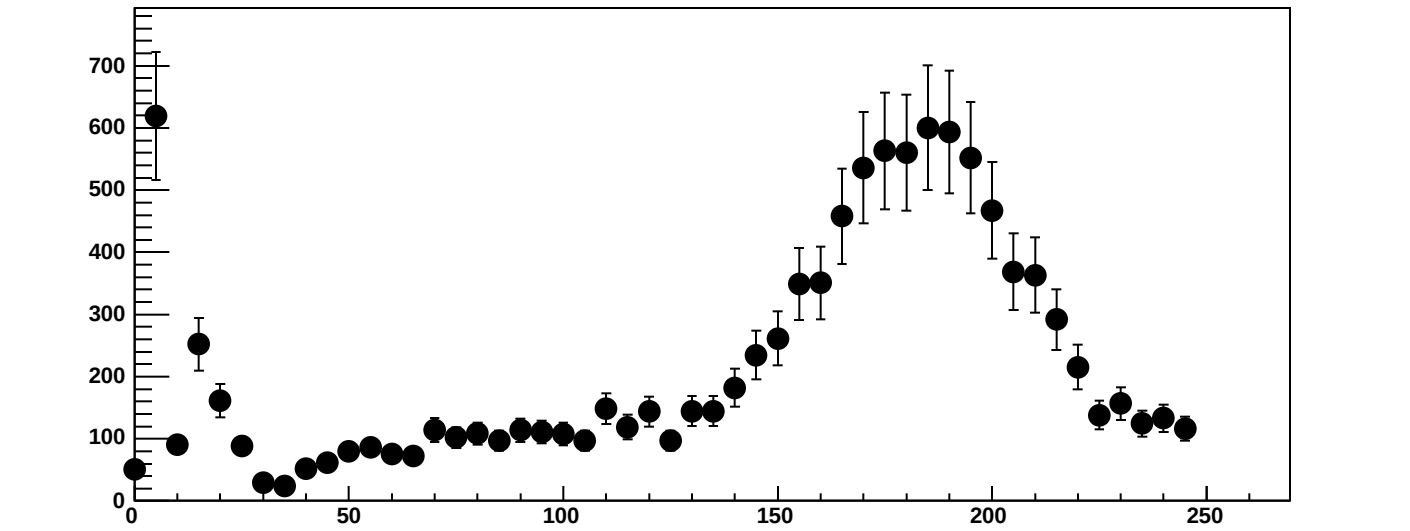

Chip 1, Channel 17, Enable 5!, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 1, Channel 17, Enable 5!, DAC=1600, ADC Residuals vs Hold
10000
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
0
50
100
150
200
250

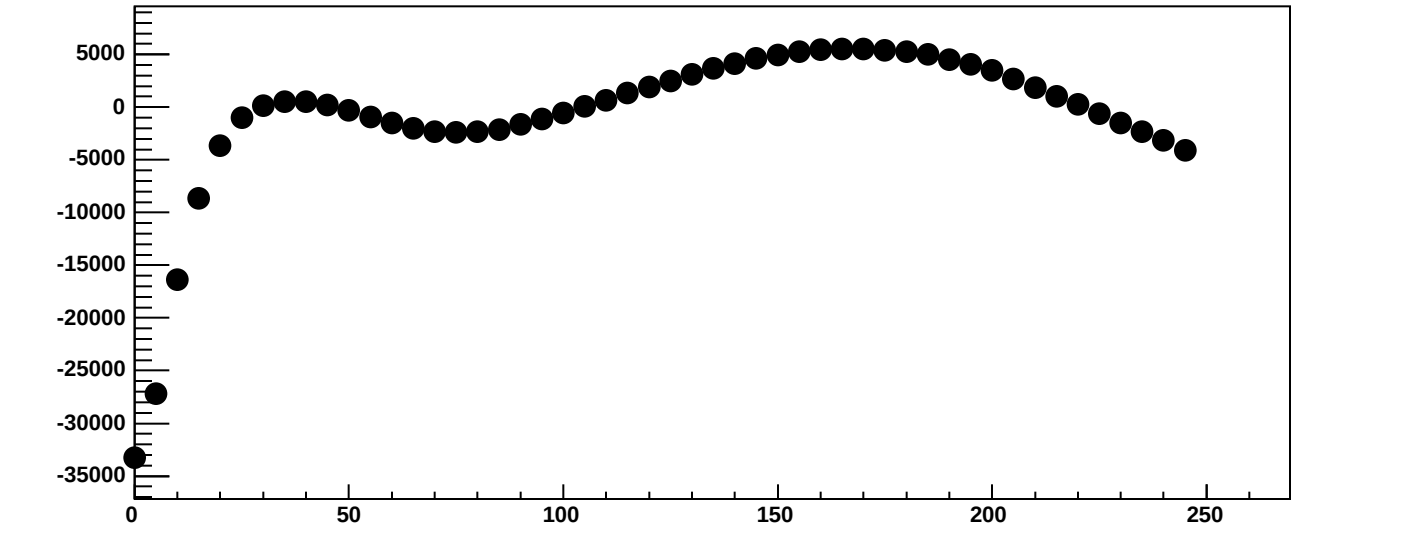
Chip 2, Channel 0, Enable 0!, DAC=1600, ADC Mean vs Hold



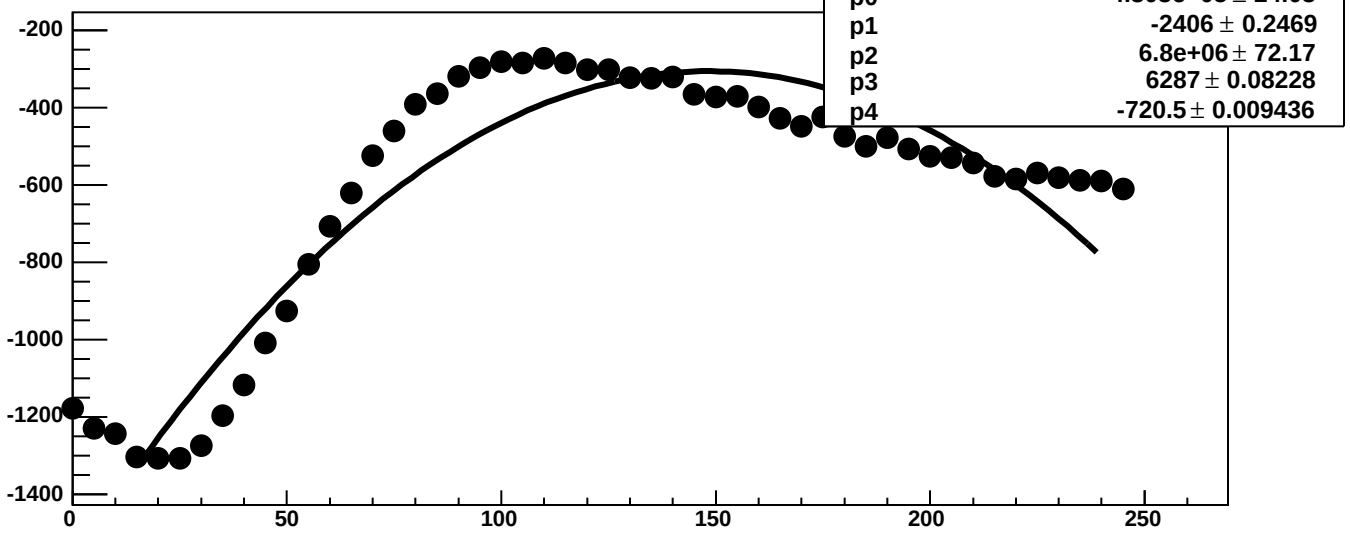
Chip 2, Channel 0, Enable 0!, DAC=1600, ADC Noise vs Hold



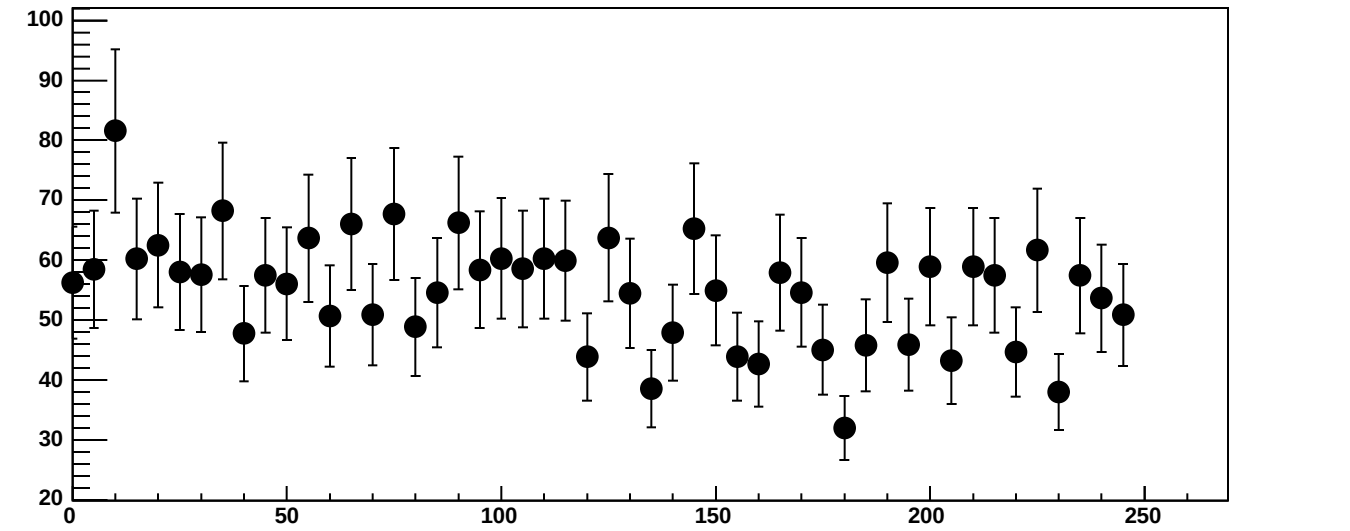
Chip 2, Channel 0, Enable 0!, DAC=1600, ADC Residuals vs Hold



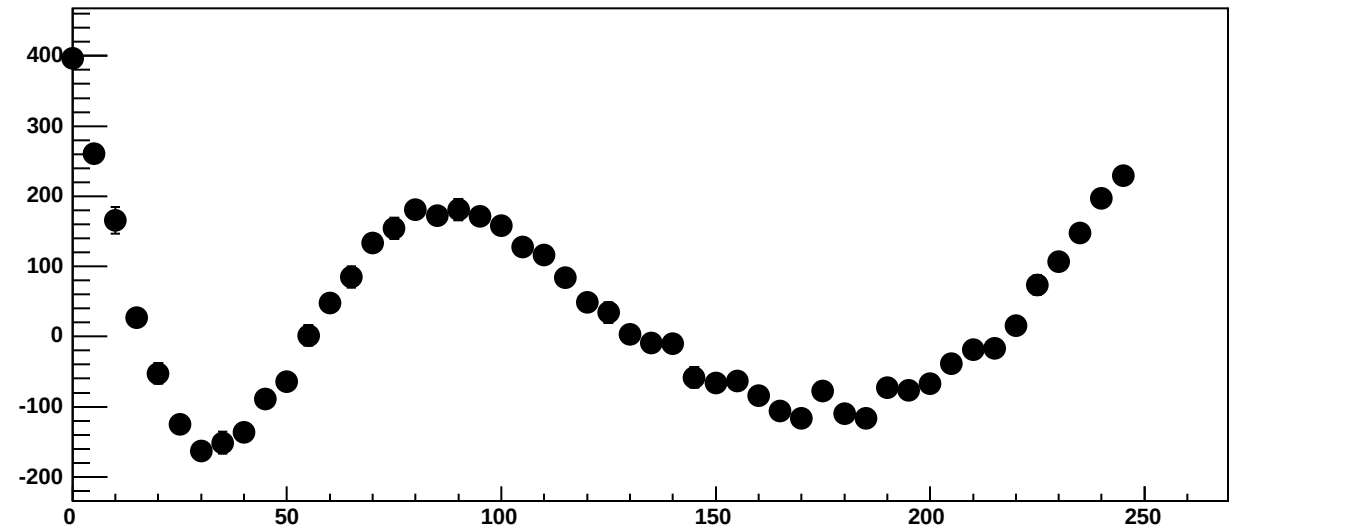
Chip 2, Channel 0, Enable 1, DAC=1600, ADC Mean vs Hold

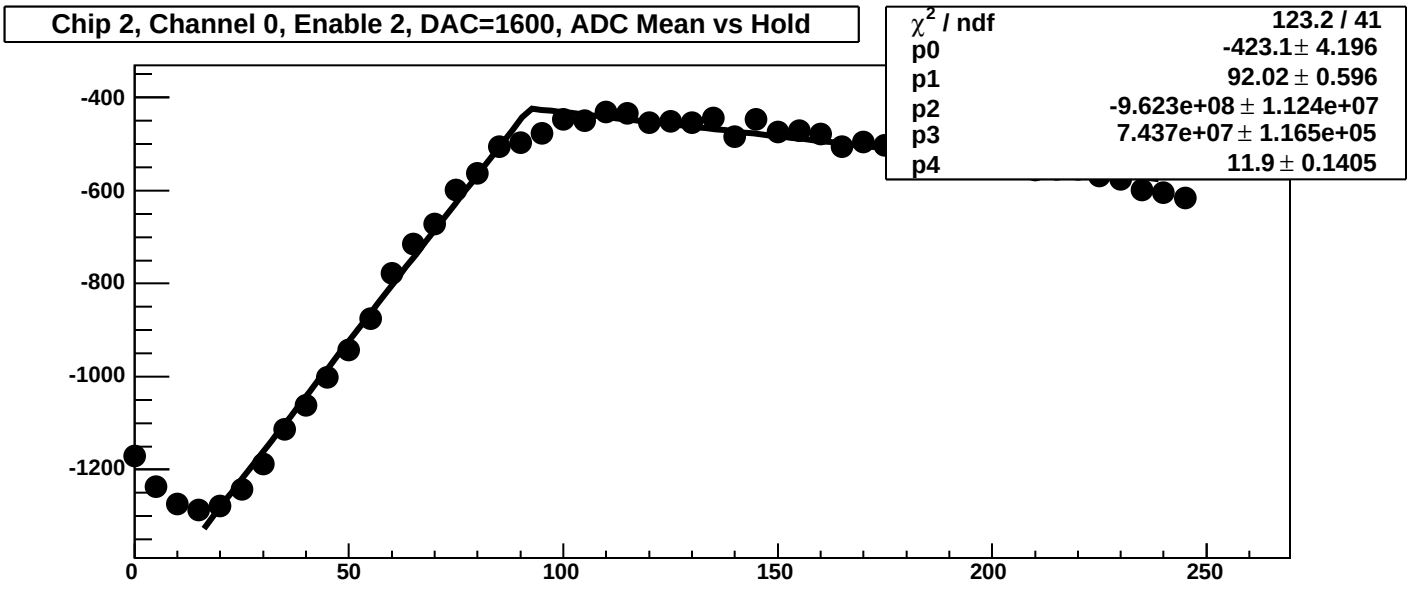


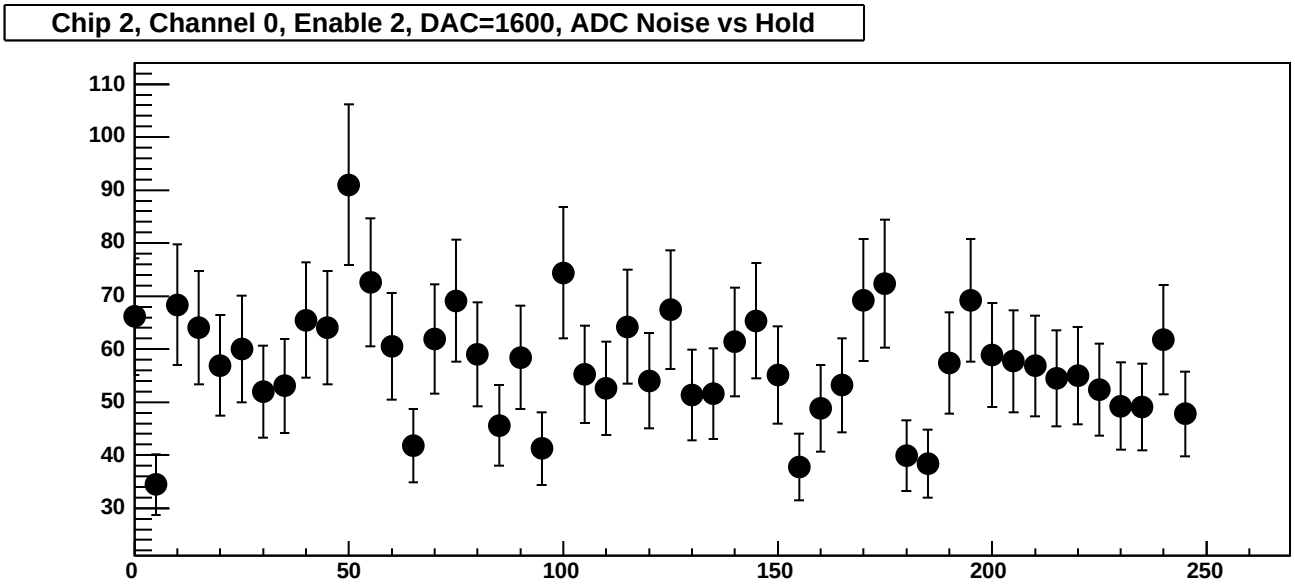
Chip 2, Channel 0, Enable 1, DAC=1600, ADC Noise vs Hold

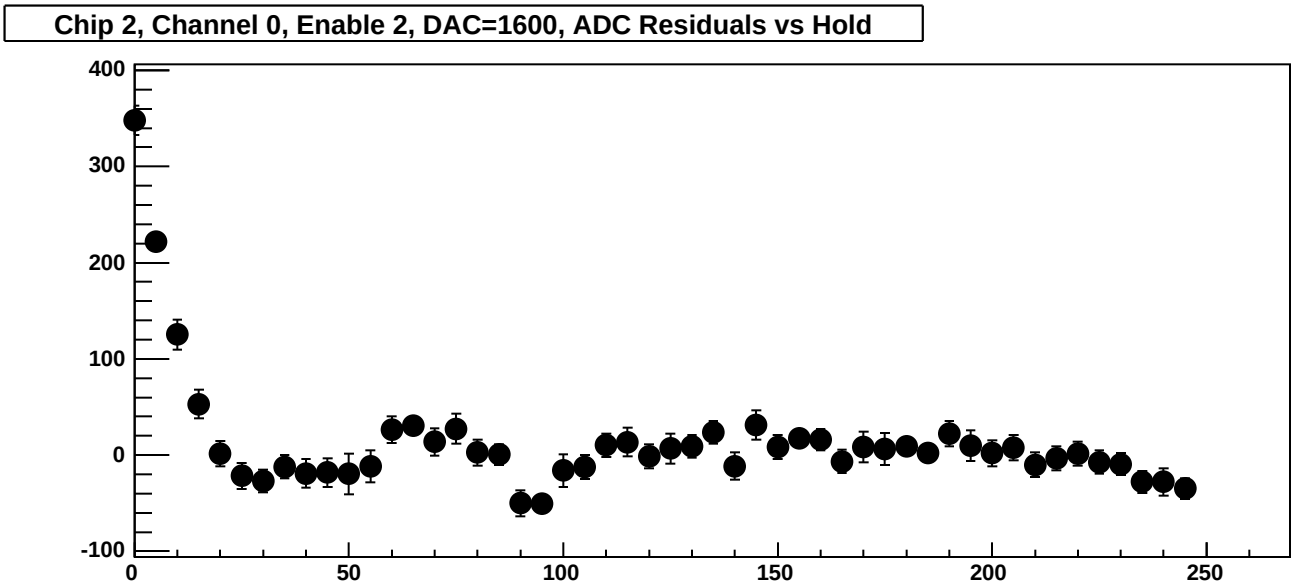


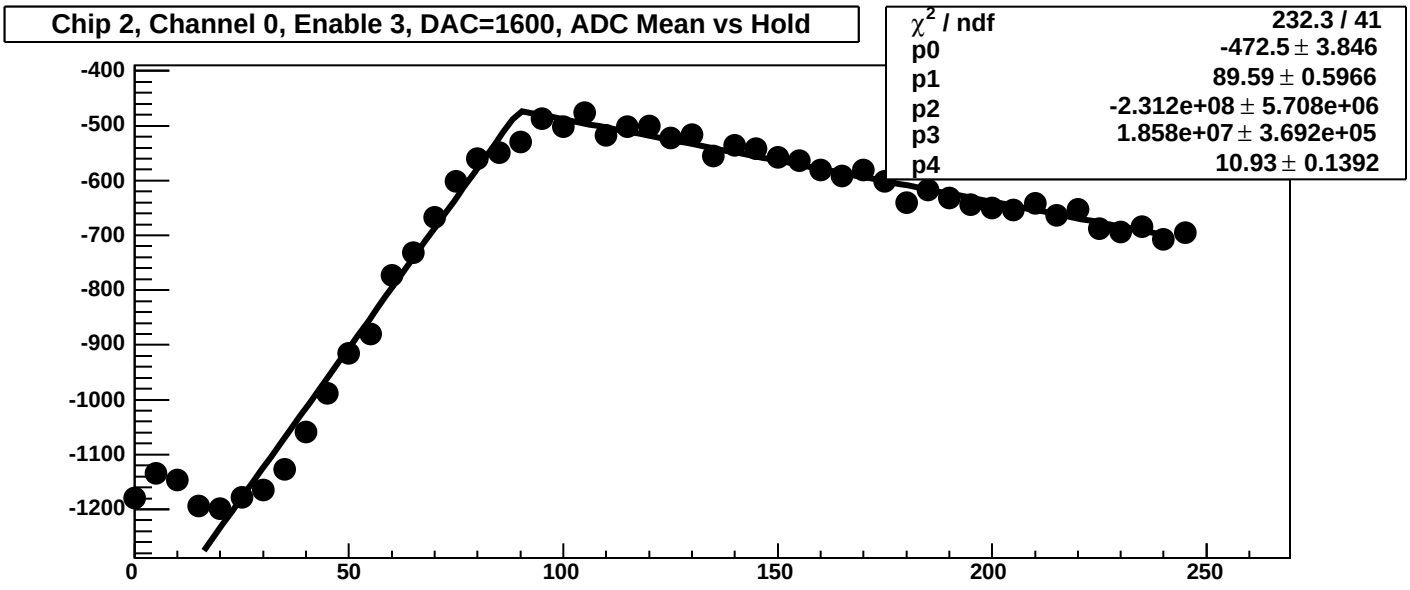
Chip 2, Channel 0, Enable 1, DAC=1600, ADC Residuals vs Hold

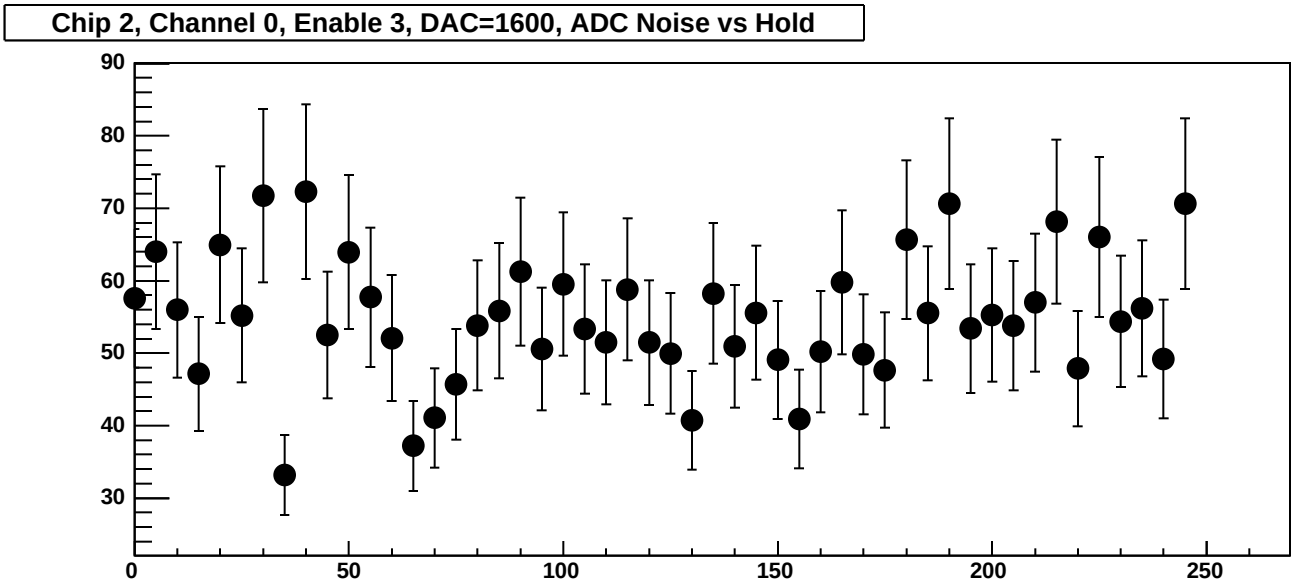


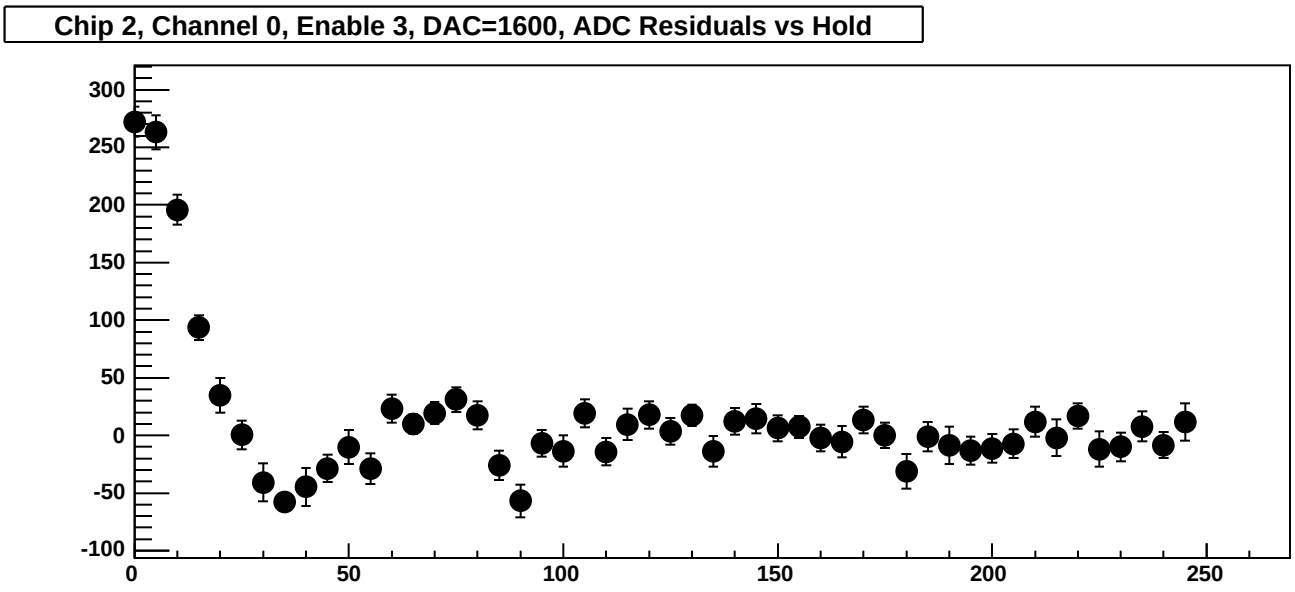

Chip 2, Channel 0, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 123.2 / 41
p0 -423.1 ± 4.196
p1 92.02 ± 0.596
p2 -9.623e+08 ± 1.124e+07
p3 7.437e+07 ± 1.165e+05
p4 11.9 ± 0.1405
-400
-600
-800
-1000
-1200
0
50
100
150
200
250


Chip 2, Channel 0, Enable 2, DAC=1600, ADC Noise vs Hold
110
100
90
80
70
60
50
40
30
0
50
100
150
200
250

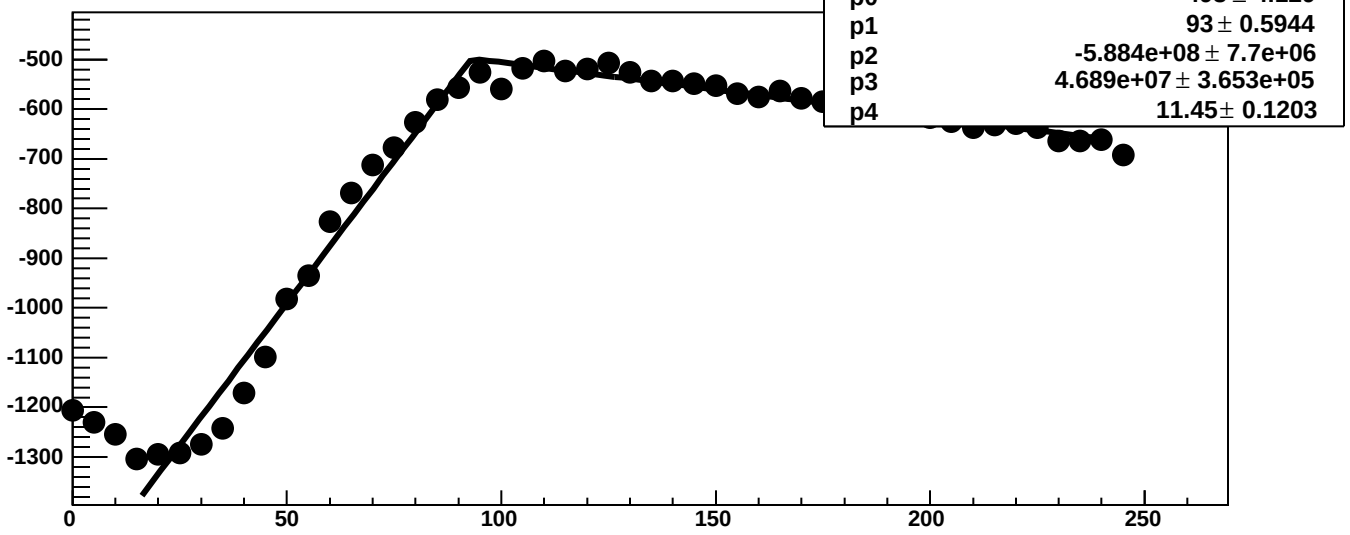

Chip 2, Channel 0, Enable 2, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 2, Channel 0, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 232.3 / 41
p0 -472.5 ± 3.846
p1 89.59 ± 0.5966
p2 -2.312e+08 ± 5.708e+06
p3 1.858e+07 ± 3.692e+05
p4 10.93 ± 0.1392
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
50
100
150
200
250

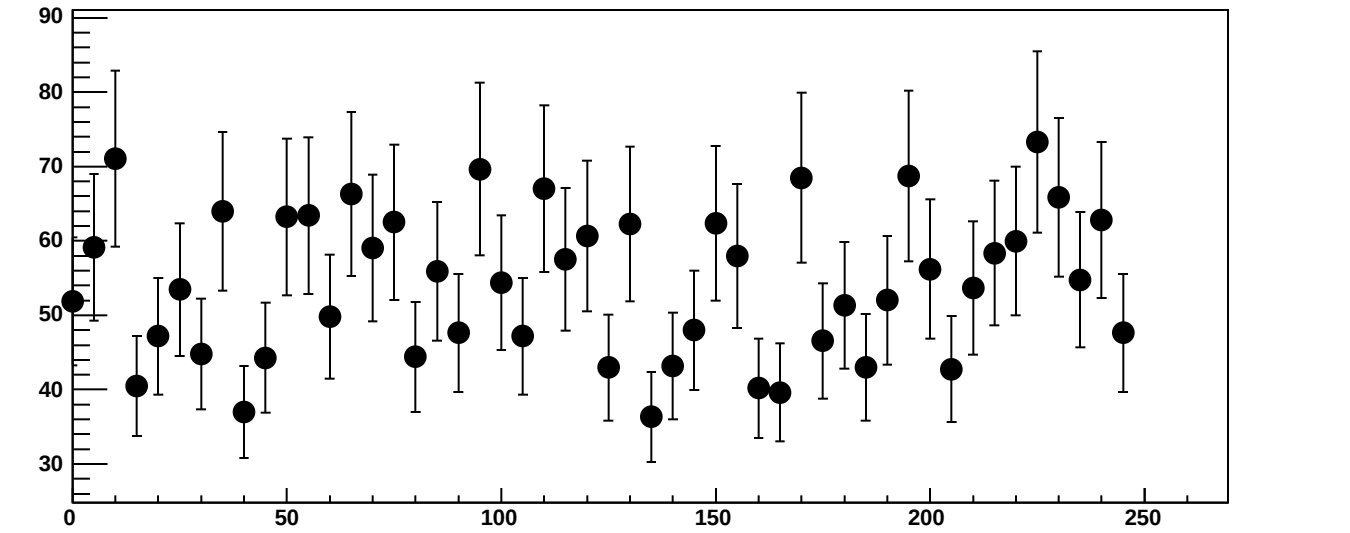

Chip 2, Channel 0, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 2, Channel 0, Enable 3, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

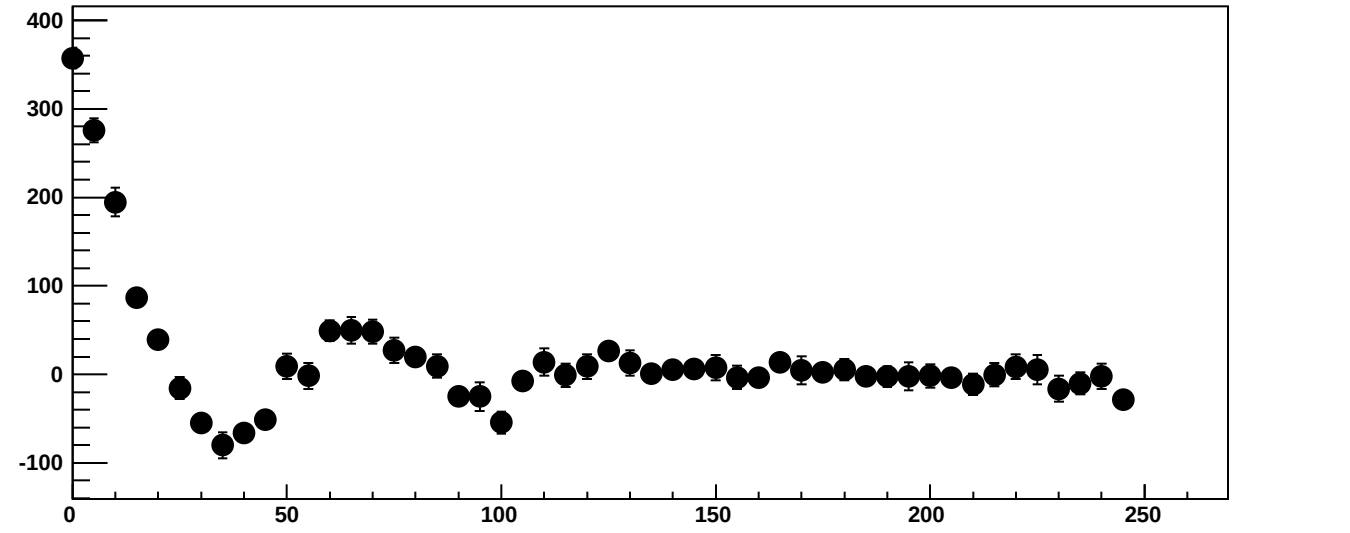
Chip 2, Channel 0, Enable 4, DAC=1600, ADC Mean vs Hold



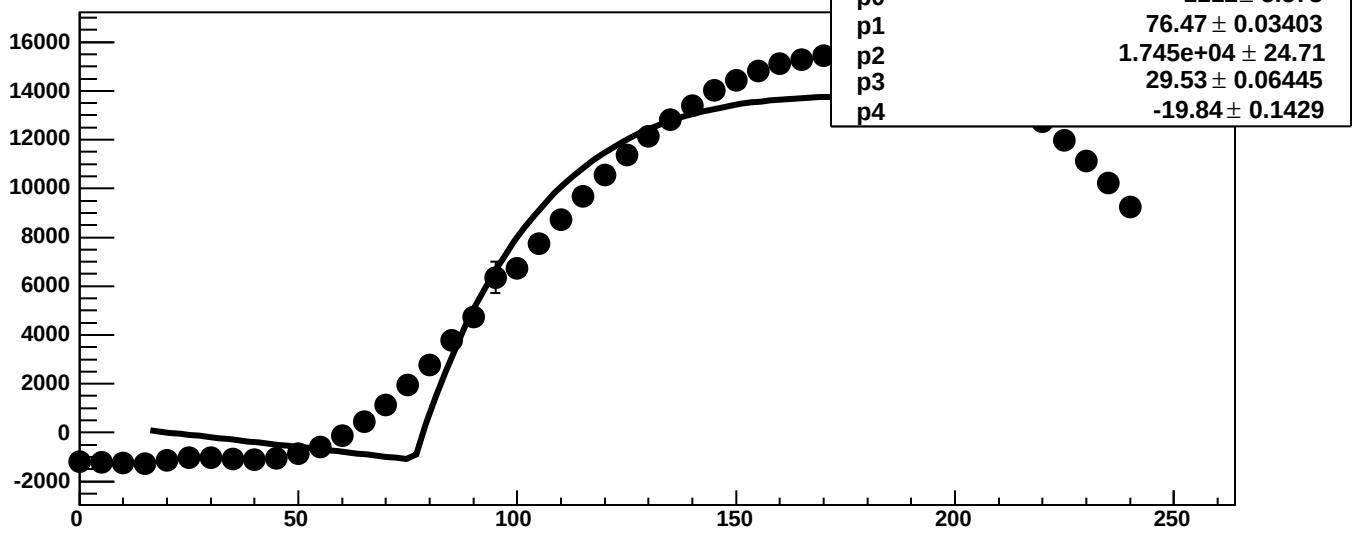
Chip 2, Channel 0, Enable 4, DAC=1600, ADC Noise vs Hold



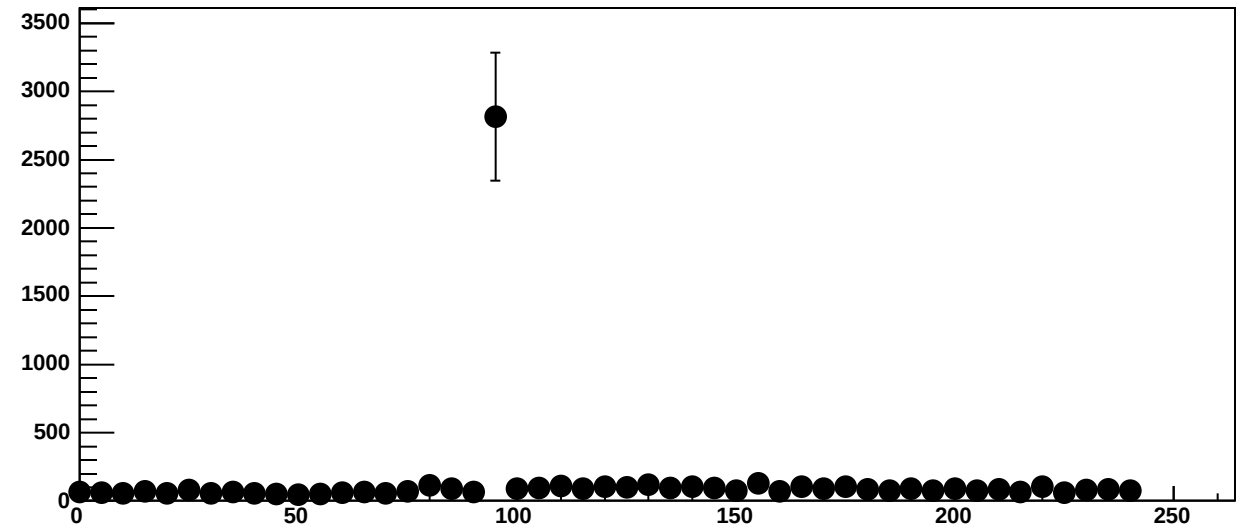
Chip 2, Channel 0, Enable 4, DAC=1600, ADC Residuals vs Hold



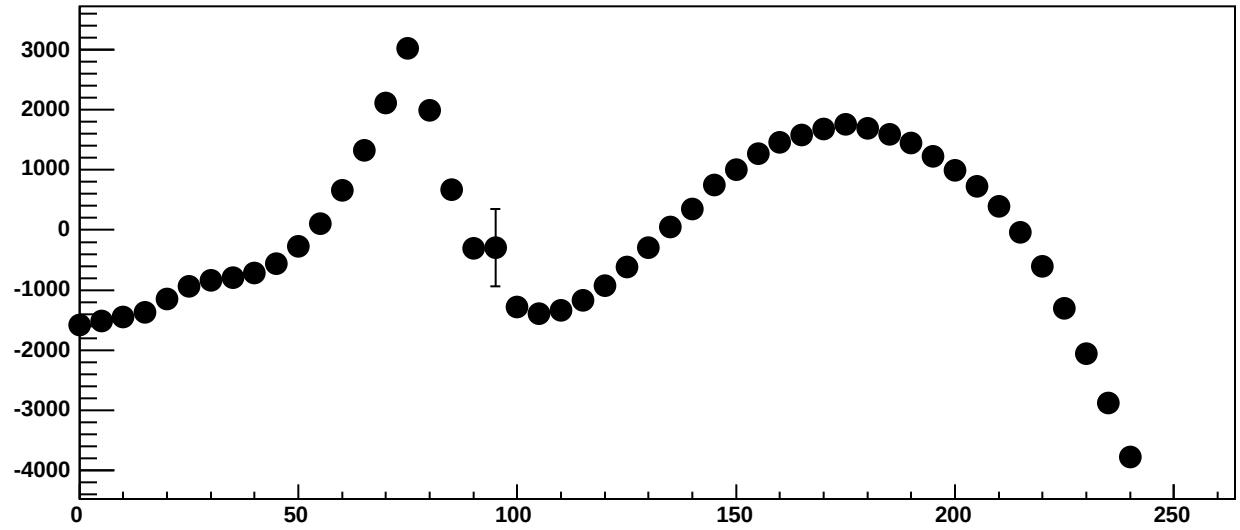
Chip 2, Channel 0, Enable 5, DAC=1600, ADC Mean vs Hold



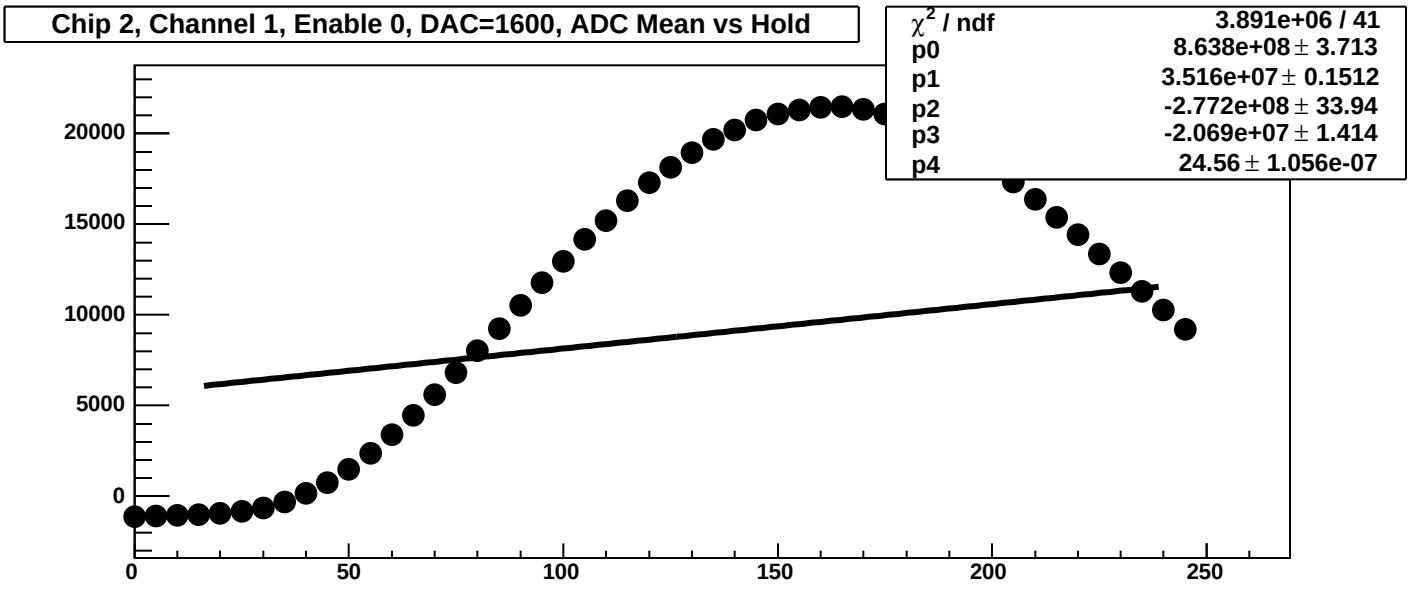
Chip 2, Channel 0, Enable 5, DAC=1600, ADC Noise vs Hold



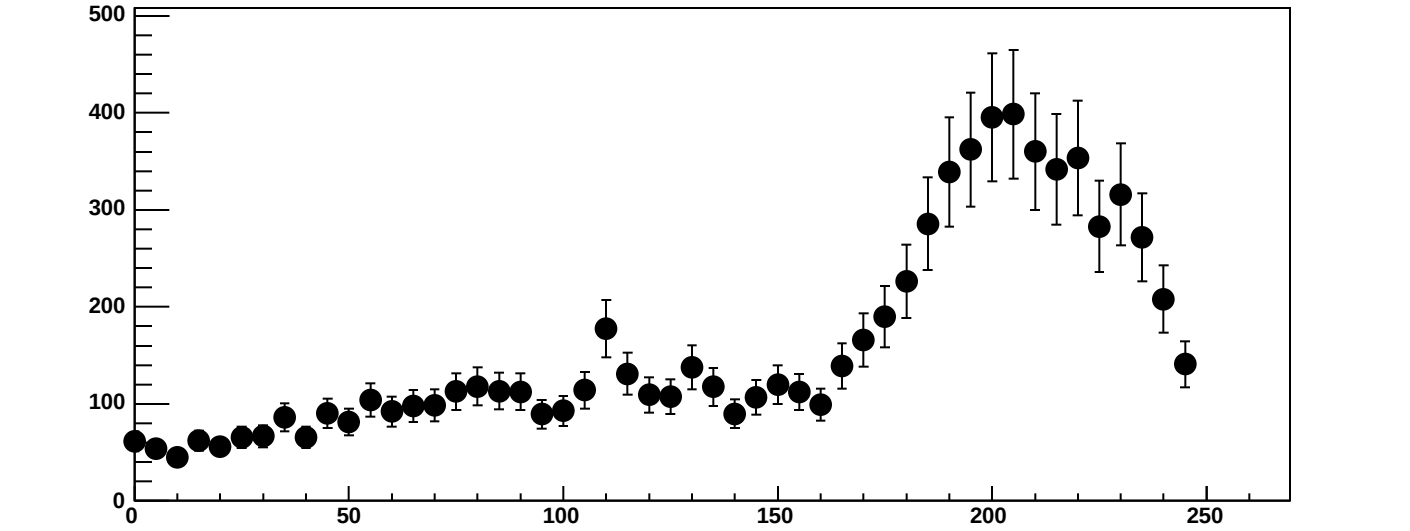
Chip 2, Channel 0, Enable 5, DAC=1600, ADC Residuals vs Hold



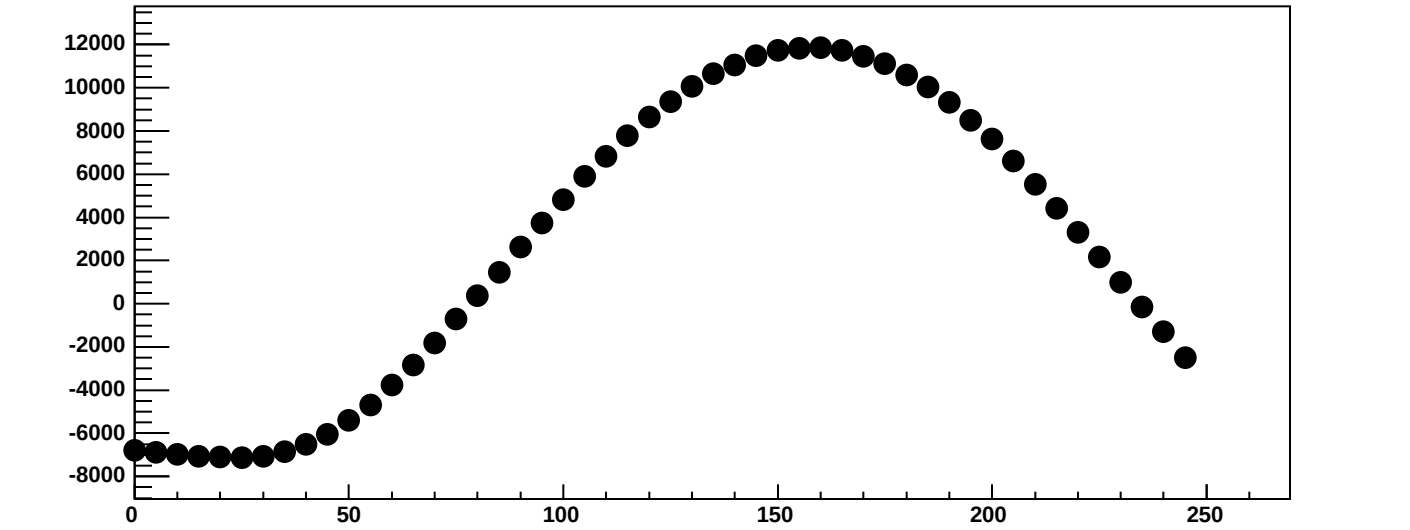
Chip 2, Channel 1, Enable 0, DAC=1600, ADC Mean vs Hold



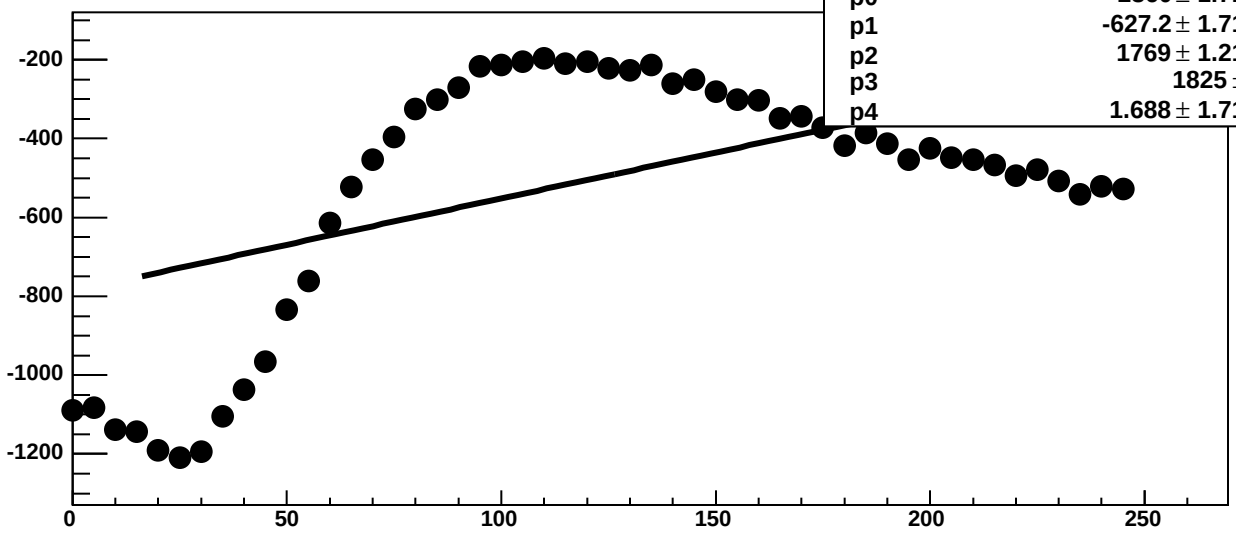
Chip 2, Channel 1, Enable 0, DAC=1600, ADC Noise vs Hold



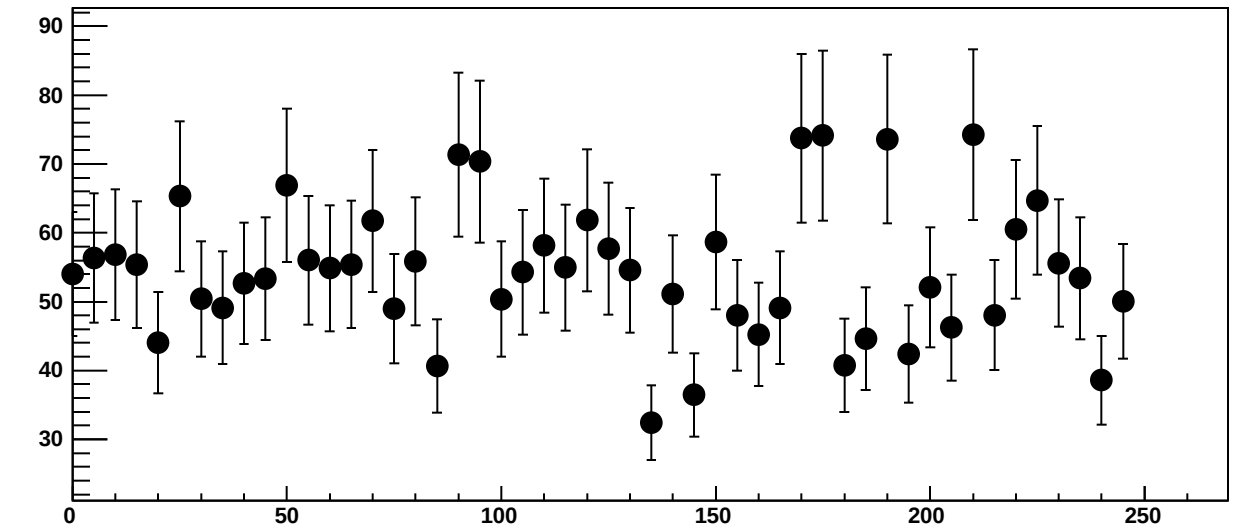
Chip 2, Channel 1, Enable 0, DAC=1600, ADC Residuals vs Hold



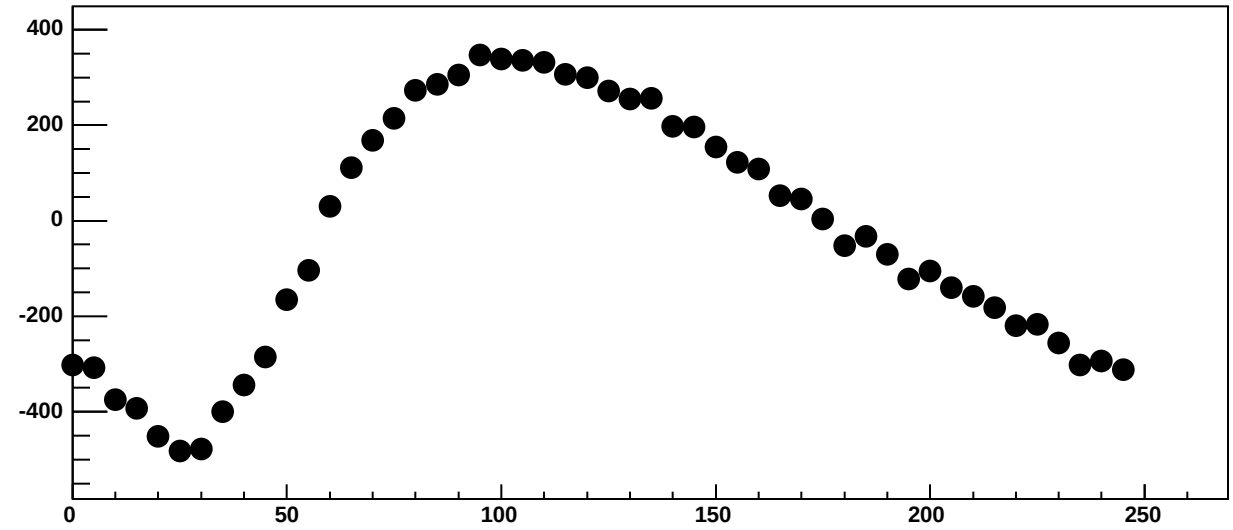
Chip 2, Channel 1, Enable 1, DAC=1600, ADC Mean vs Hold

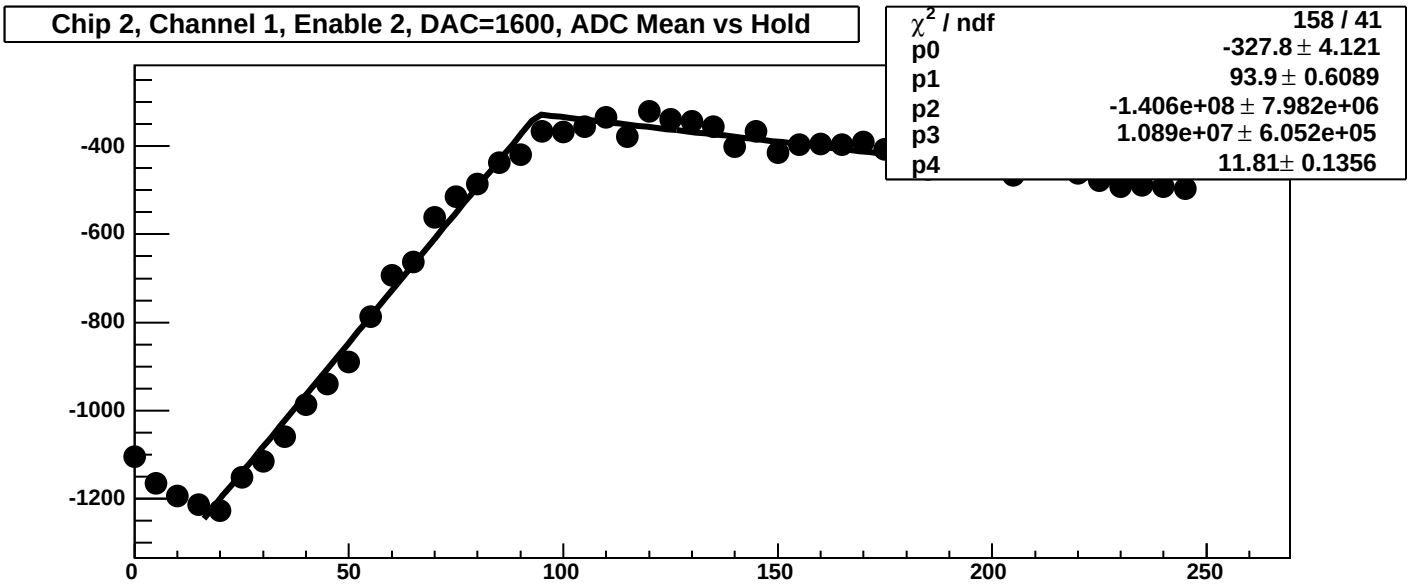


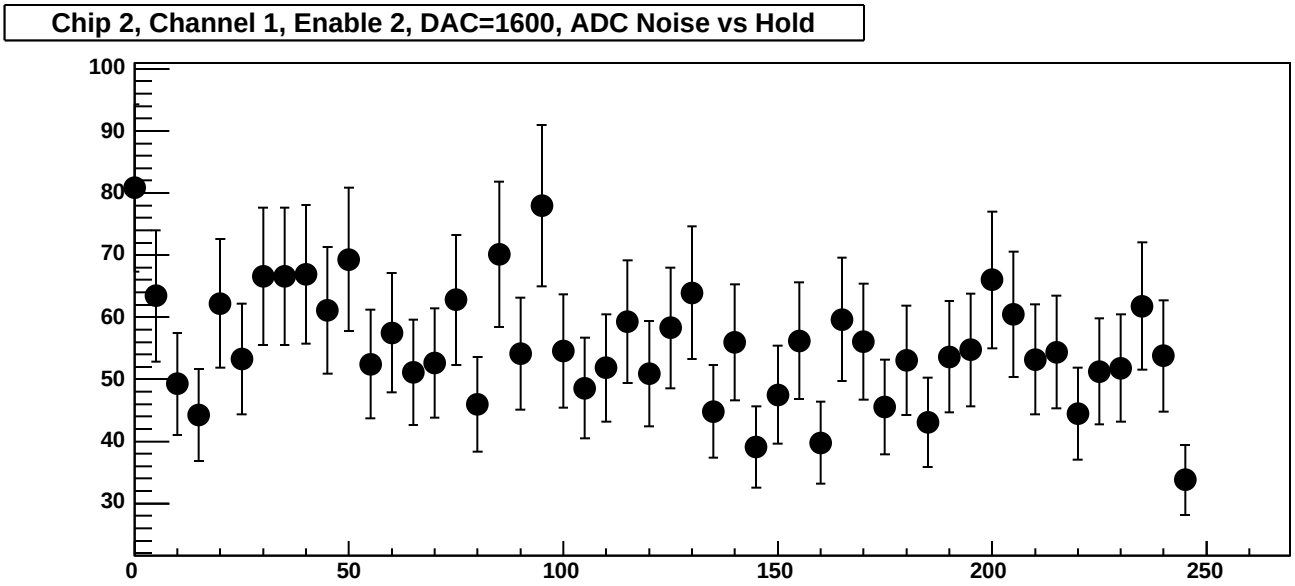
Chip 2, Channel 1, Enable 1, DAC=1600, ADC Noise vs Hold

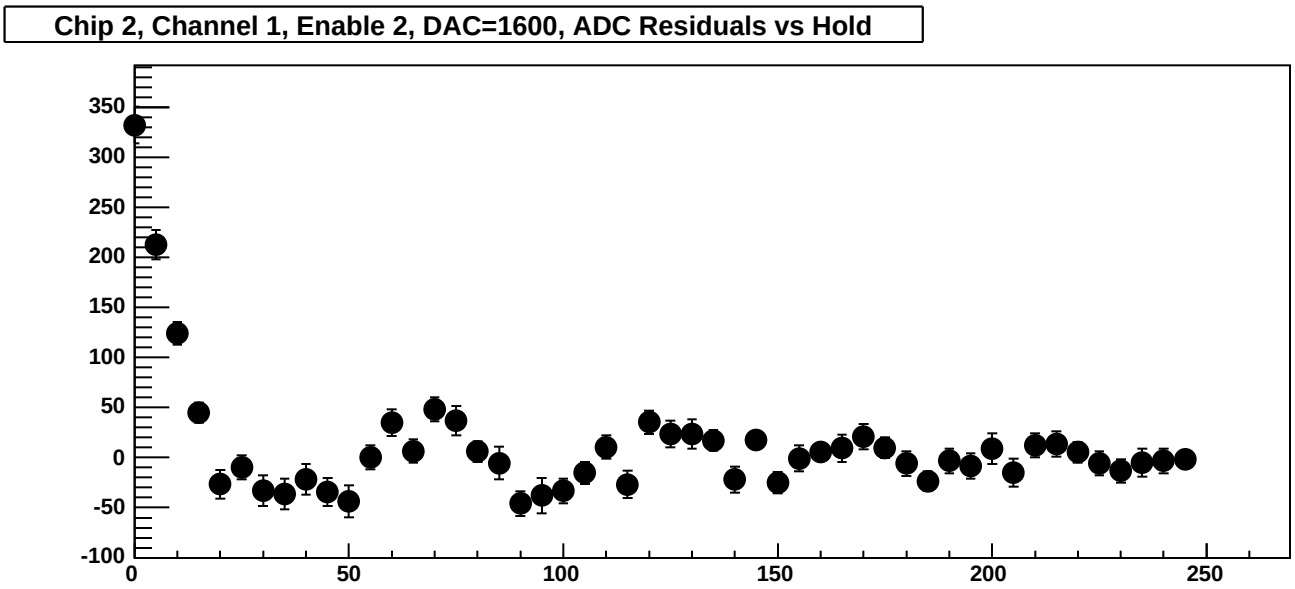


Chip 2, Channel 1, Enable 1, DAC=1600, ADC Residuals vs Hold

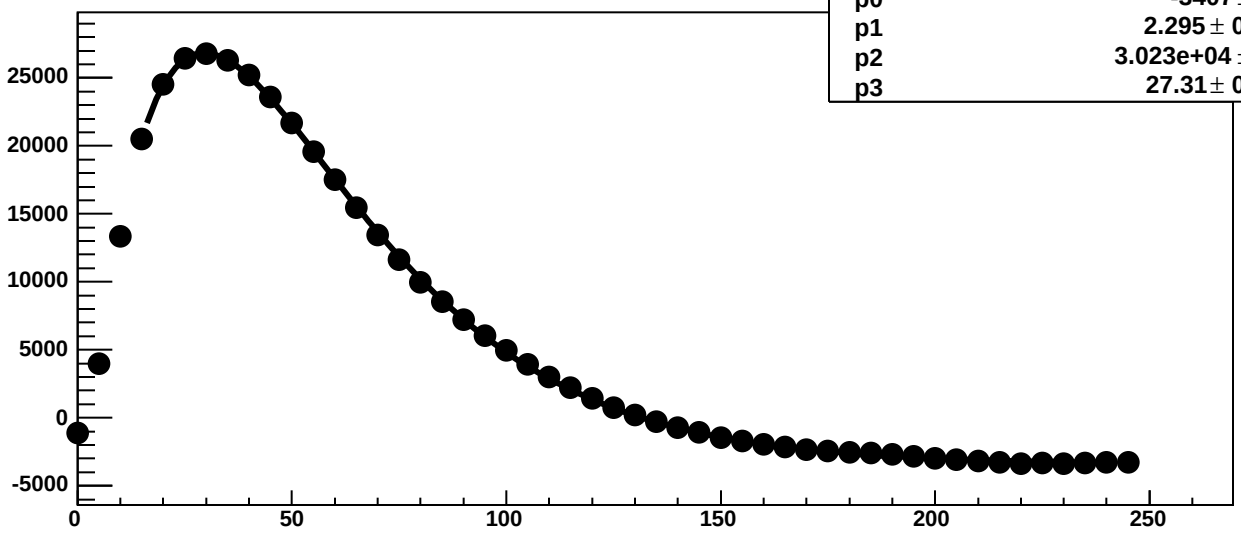



Chip 2, Channel 1, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 158 / 41
p0 -327.8 ± 4.121
p1 93.9 ± 0.6089
p2 -1.406e+08 ± 7.982e+06
p3 1.089e+07 ± 6.052e+05
p4 11.81 ± 0.1356
-400
-600
-800
-1000
-1200
0
50
100
150
200
250

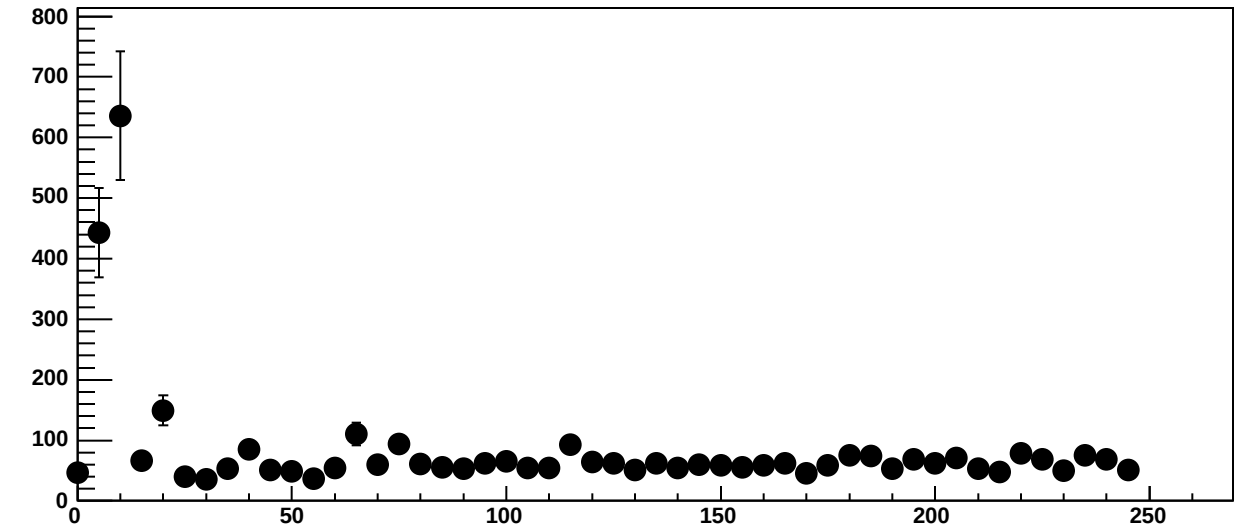

Chip 2, Channel 1, Enable 2, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 2, Channel 1, Enable 2, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

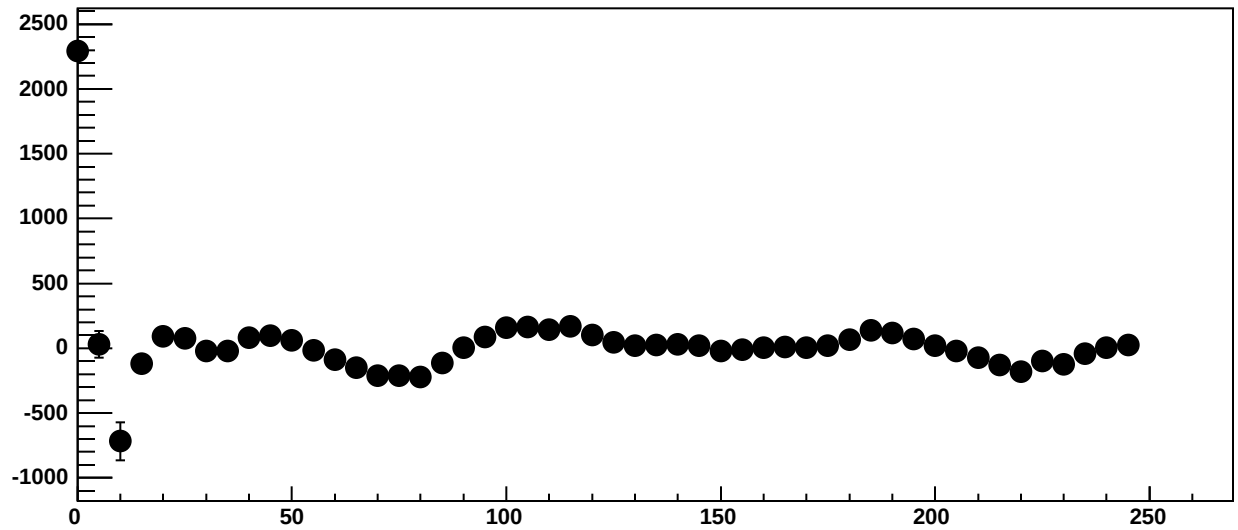
Chip 2, Channel 1, Enable 3!, DAC=1600, ADC Mean vs Hold



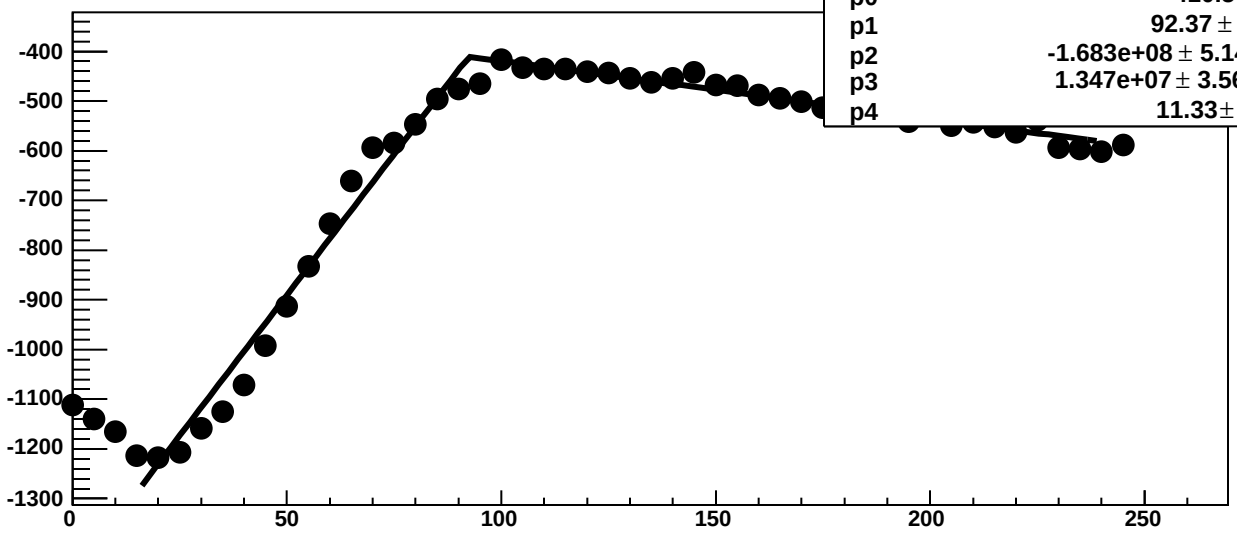
Chip 2, Channel 1, Enable 3!, DAC=1600, ADC Noise vs Hold



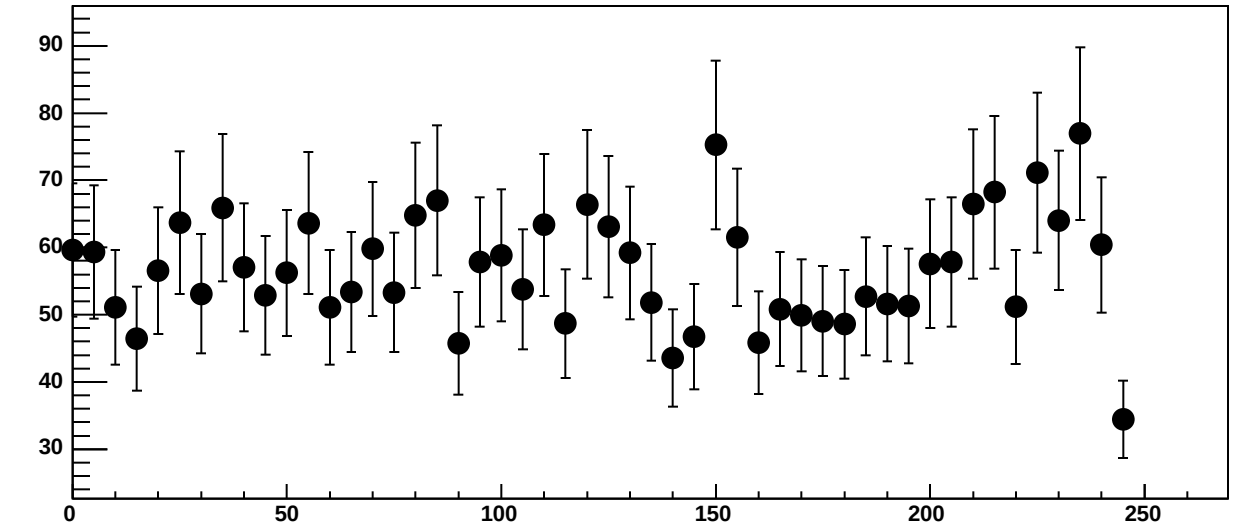
Chip 2, Channel 1, Enable 3!, DAC=1600, ADC Residuals vs Hold



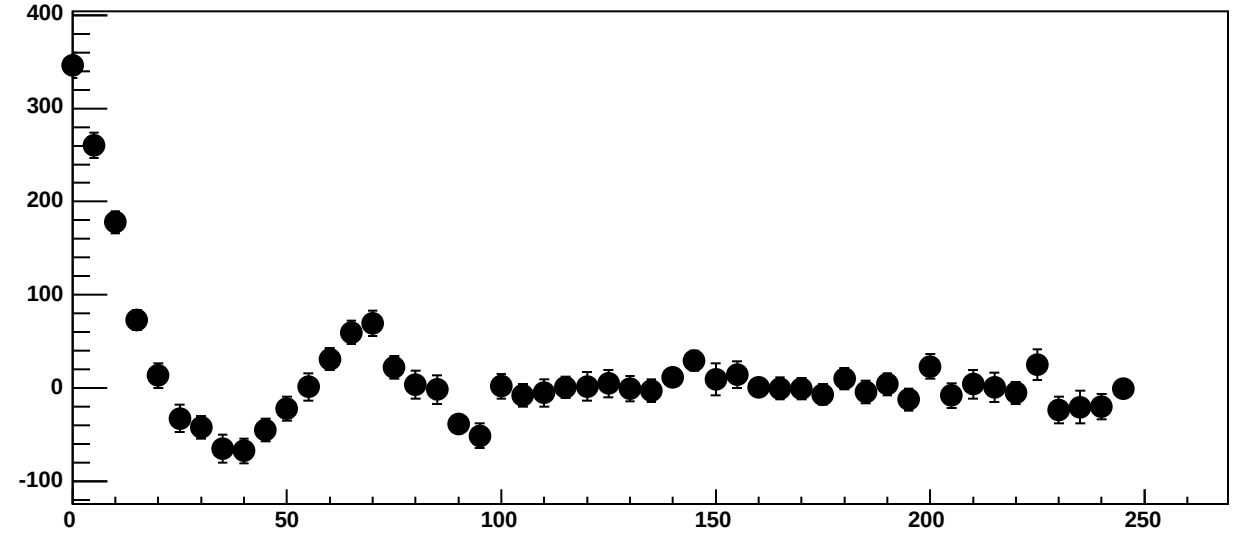
Chip 2, Channel 1, Enable 4, DAC=1600, ADC Mean vs Hold

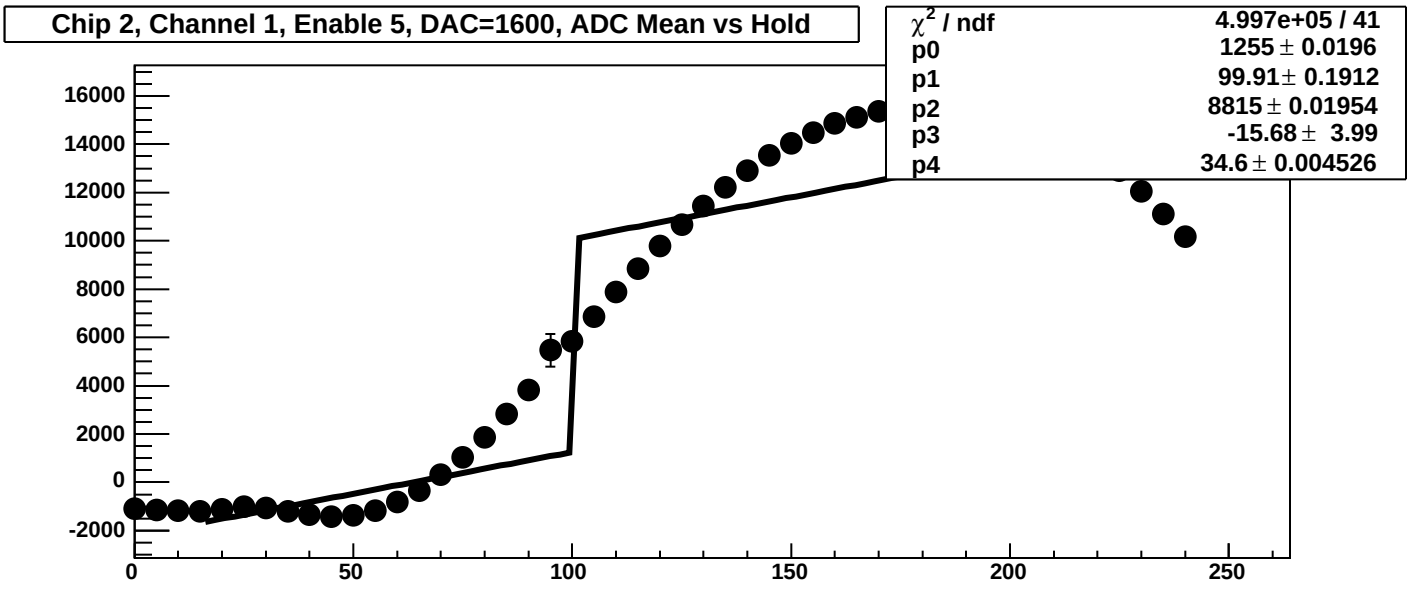


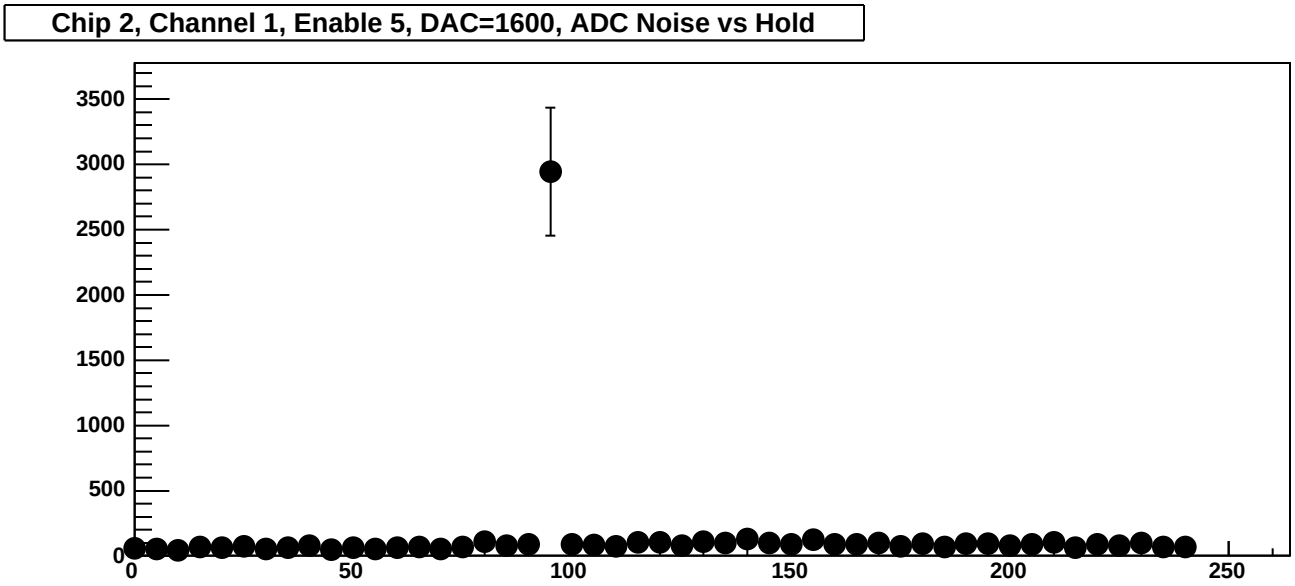
Chip 2, Channel 1, Enable 4, DAC=1600, ADC Noise vs Hold

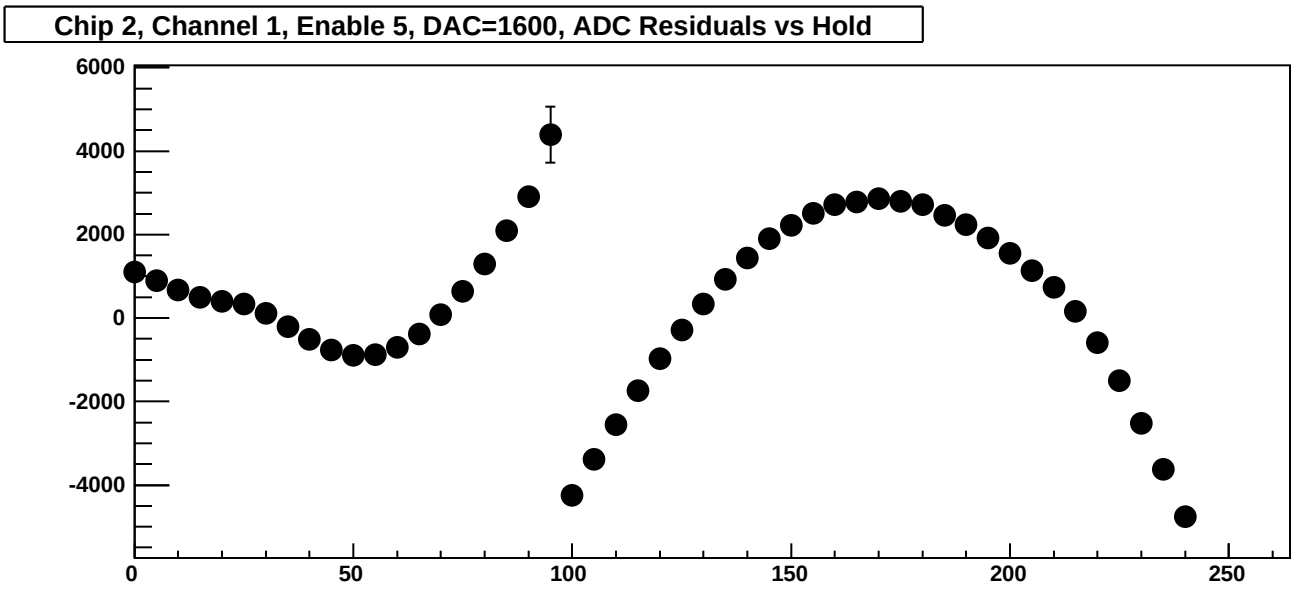


Chip 2, Channel 1, Enable 4, DAC=1600, ADC Residuals vs Hold

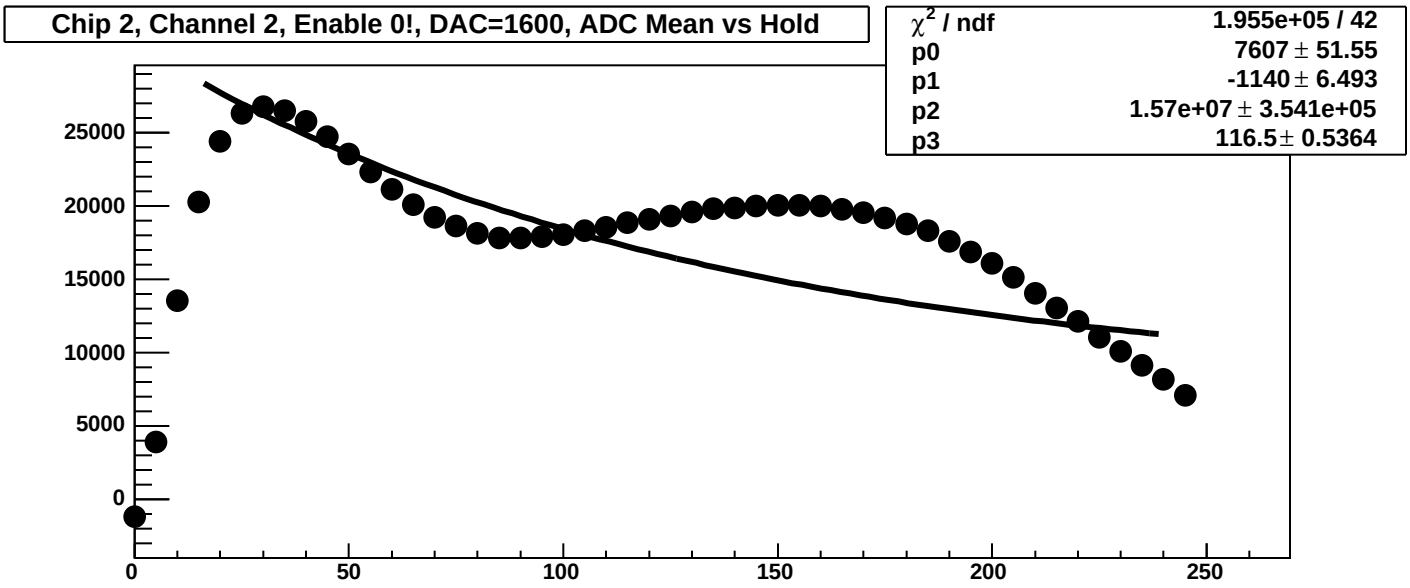



Chip 2, Channel 1, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.997e+05 / 41
p0 1255 ± 0.0196
p1 99.91 ± 0.1912
p2 8815 ± 0.01954
p3 -15.68 ± 3.99
p4 34.6 ± 0.004526
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

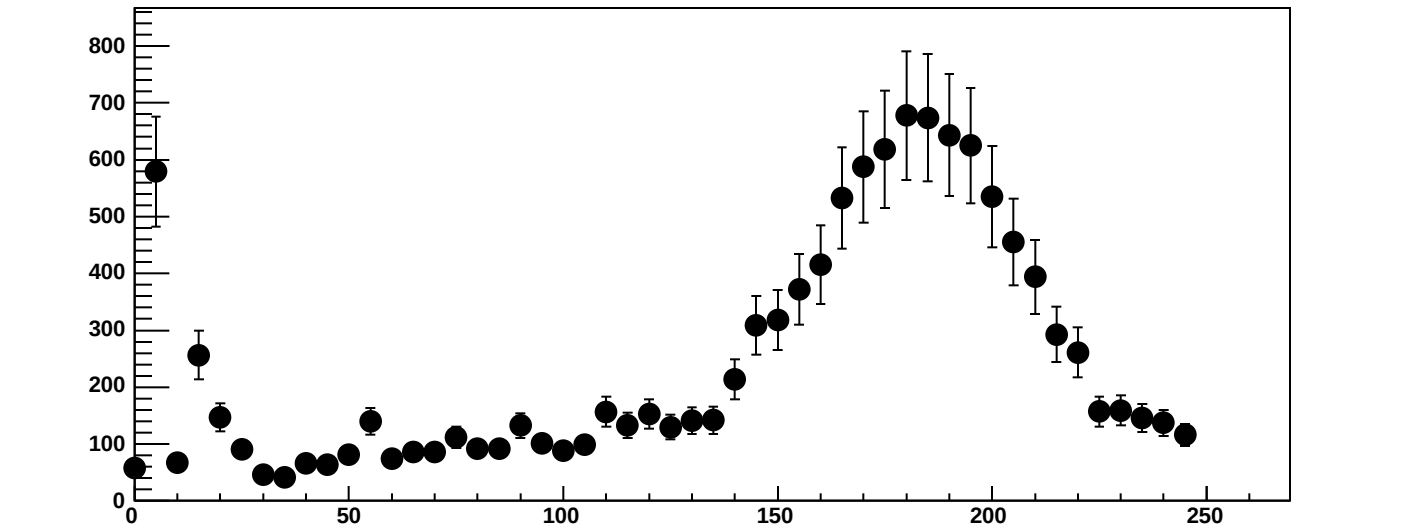

Chip 2, Channel 1, Enable 5, DAC=1600, ADC Noise vs Hold
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 2, Channel 1, Enable 5, DAC=1600, ADC Residuals vs Hold
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

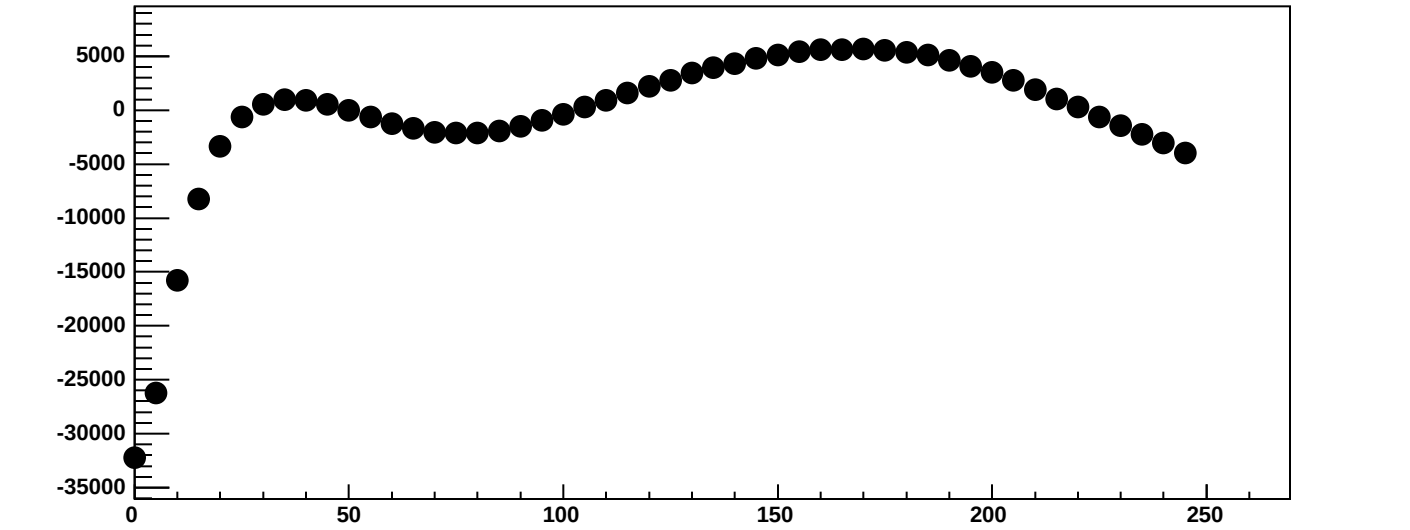
Chip 2, Channel 2, Enable 0!, DAC=1600, ADC Mean vs Hold



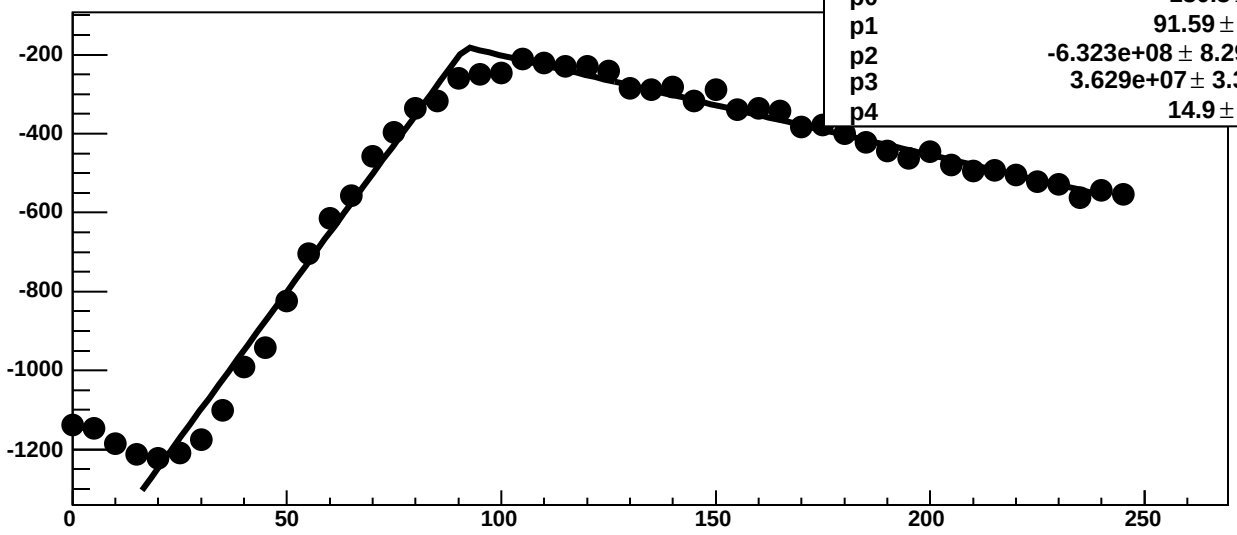
Chip 2, Channel 2, Enable 0!, DAC=1600, ADC Noise vs Hold



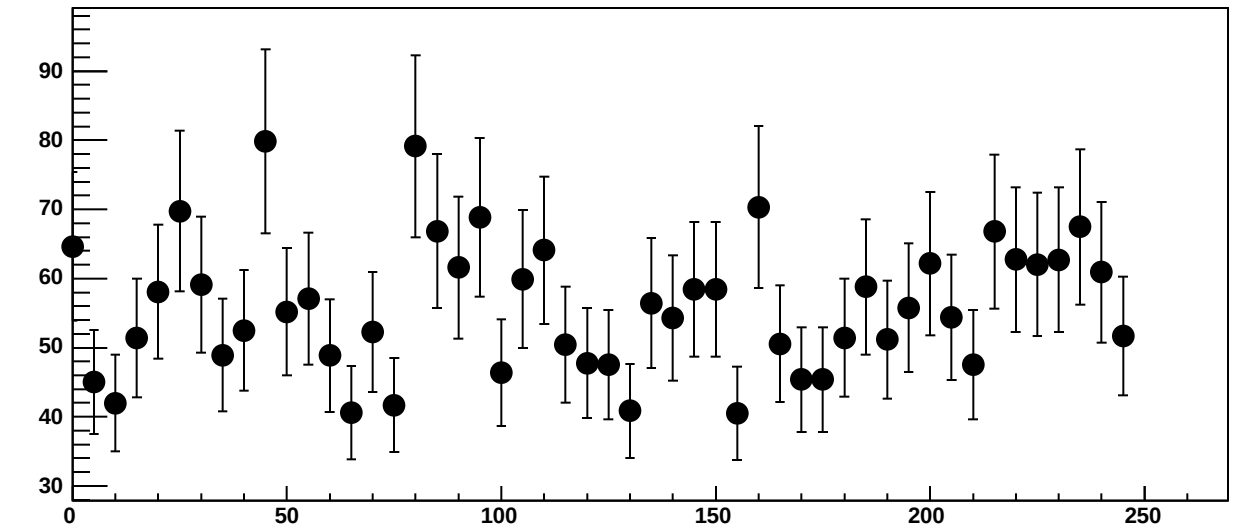
Chip 2, Channel 2, Enable 0!, DAC=1600, ADC Residuals vs Hold



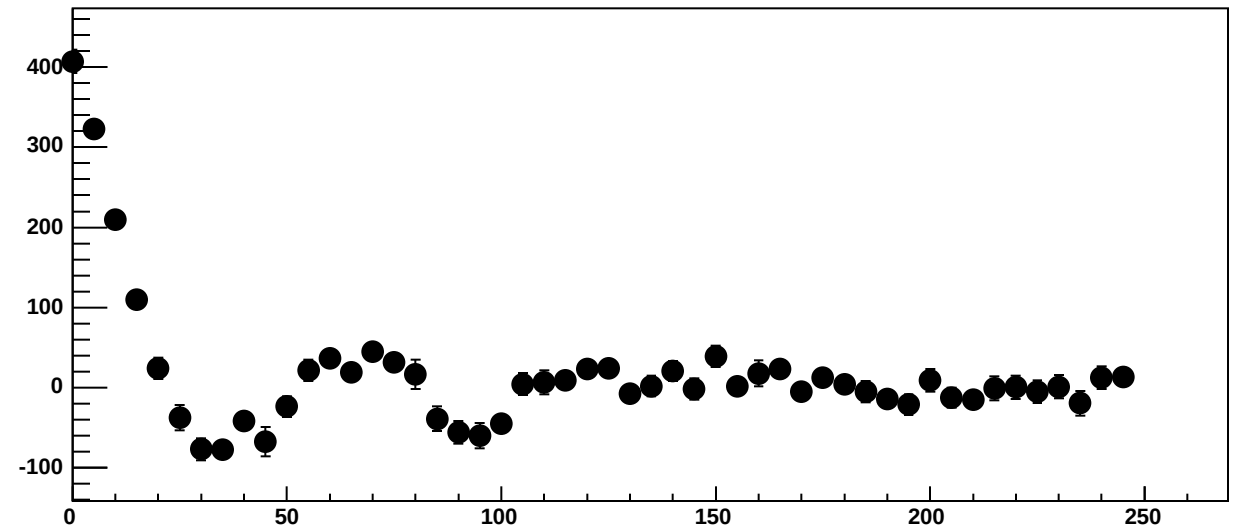
Chip 2, Channel 2, Enable 1, DAC=1600, ADC Mean vs Hold



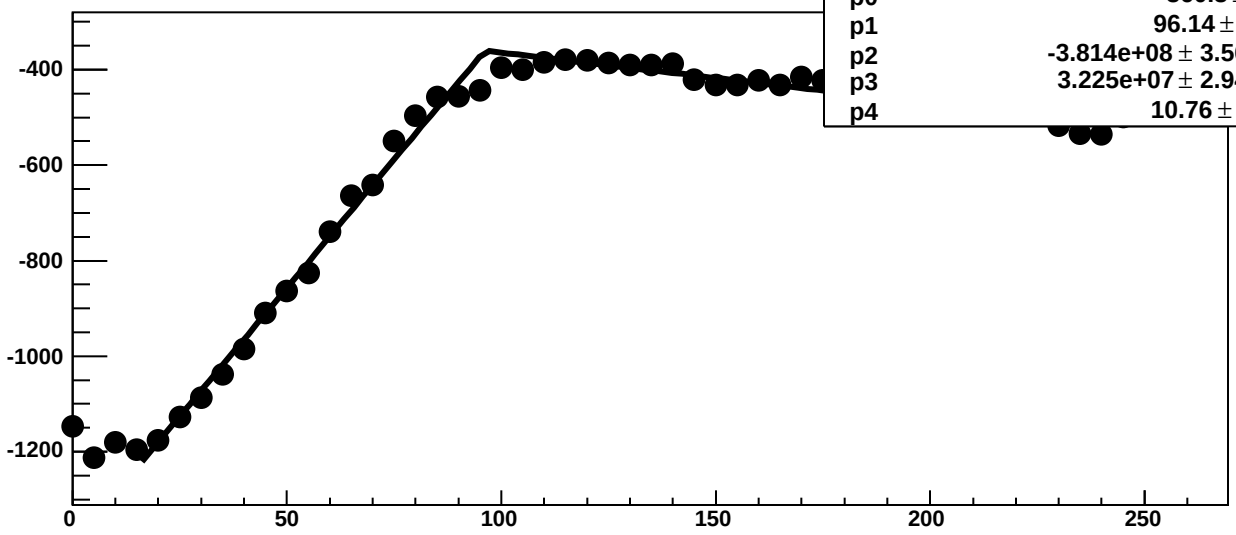
Chip 2, Channel 2, Enable 1, DAC=1600, ADC Noise vs Hold



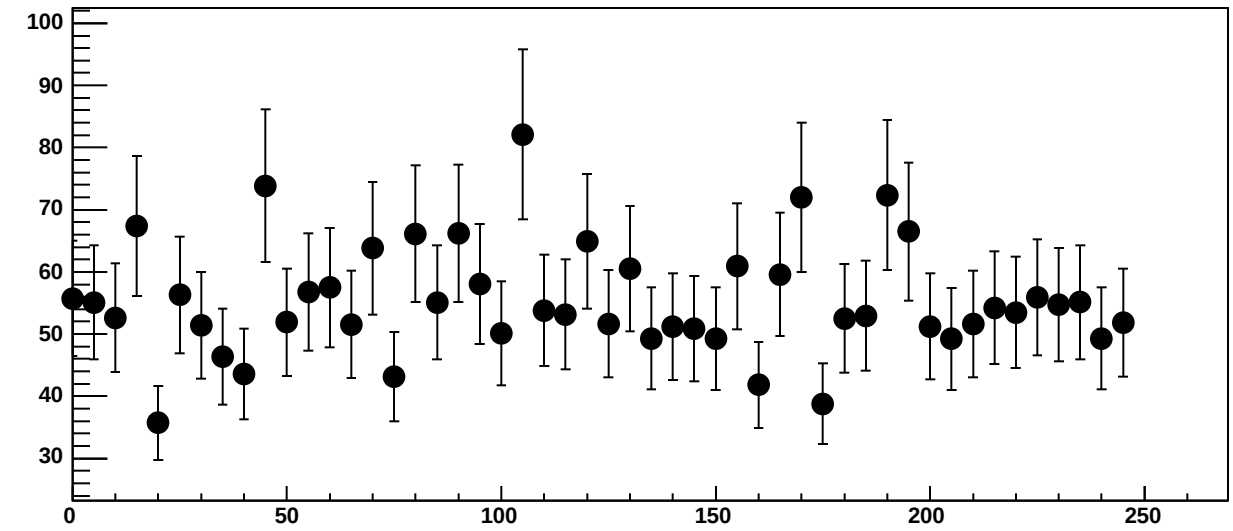
Chip 2, Channel 2, Enable 1, DAC=1600, ADC Residuals vs Hold



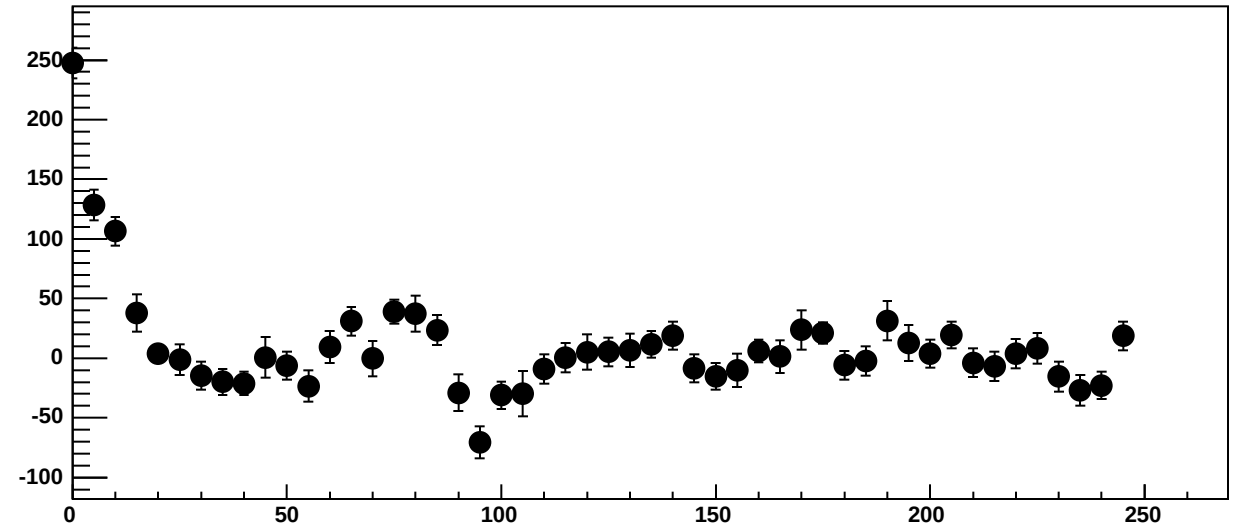
Chip 2, Channel 2, Enable 2, DAC=1600, ADC Mean vs Hold

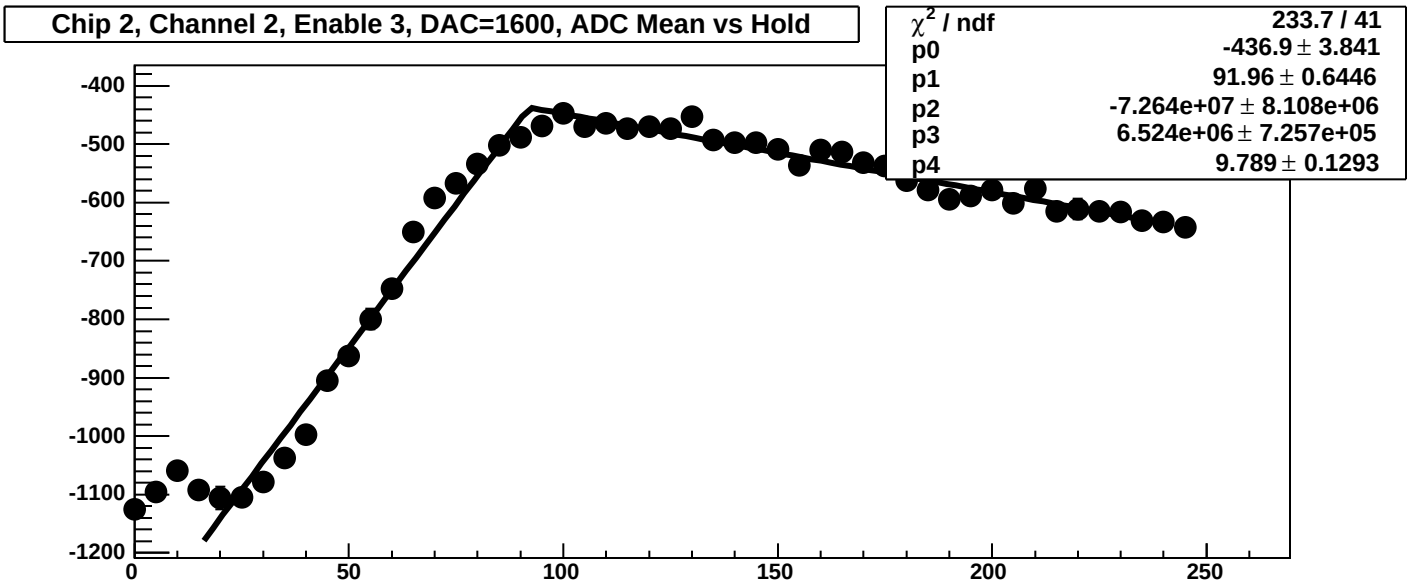


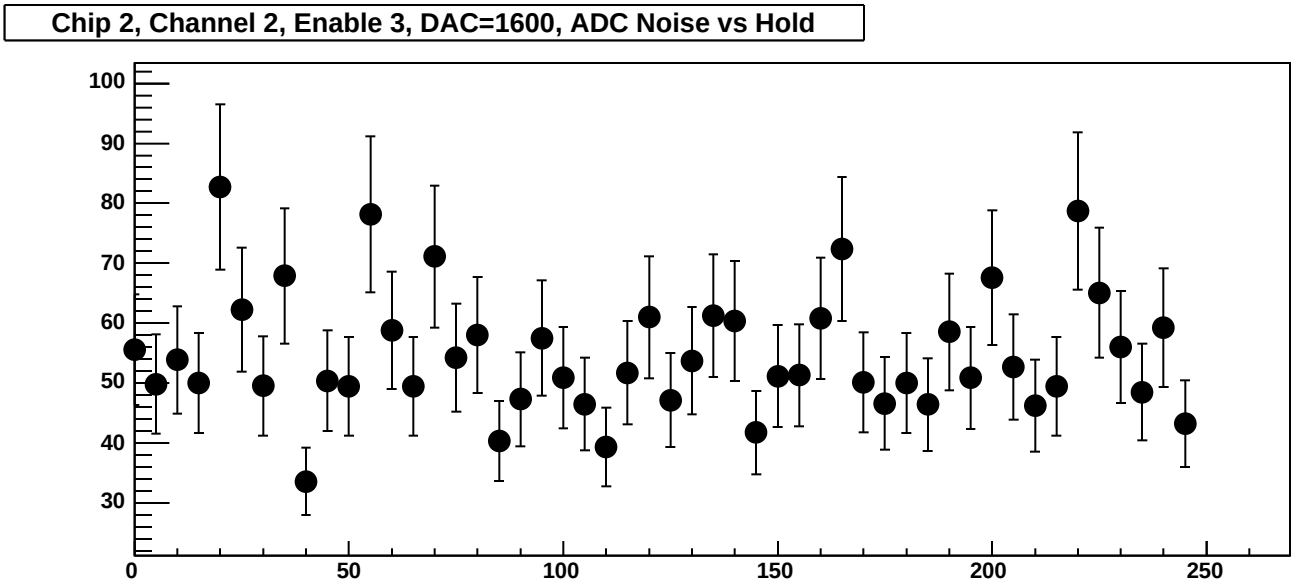
Chip 2, Channel 2, Enable 2, DAC=1600, ADC Noise vs Hold

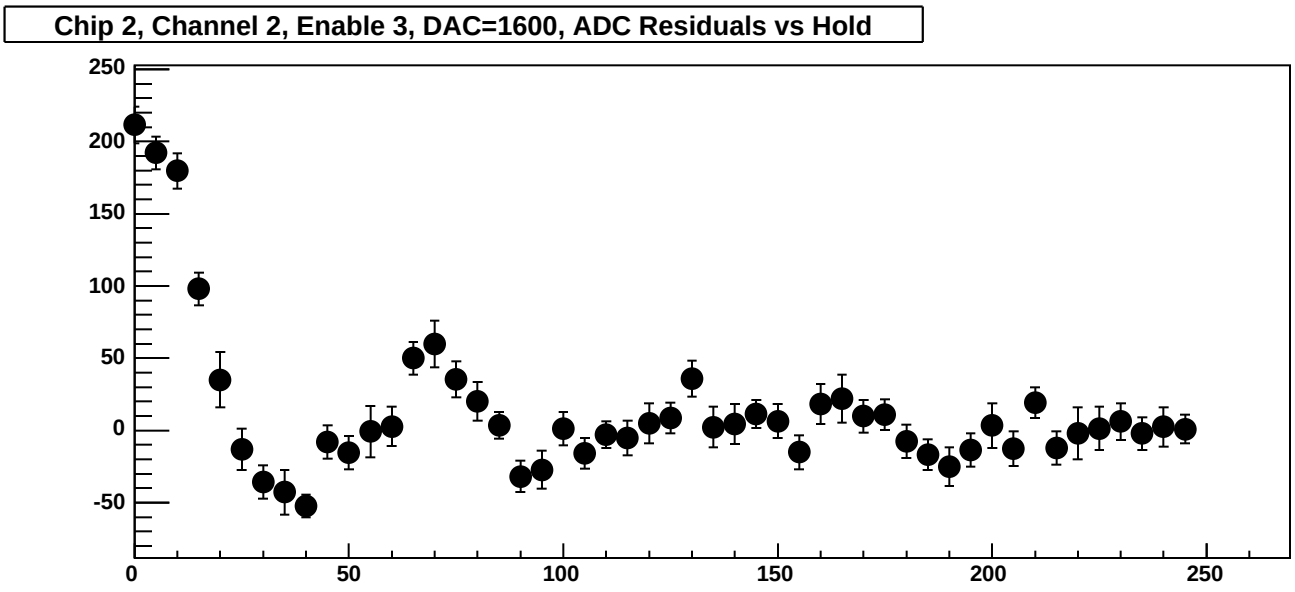


Chip 2, Channel 2, Enable 2, DAC=1600, ADC Residuals vs Hold

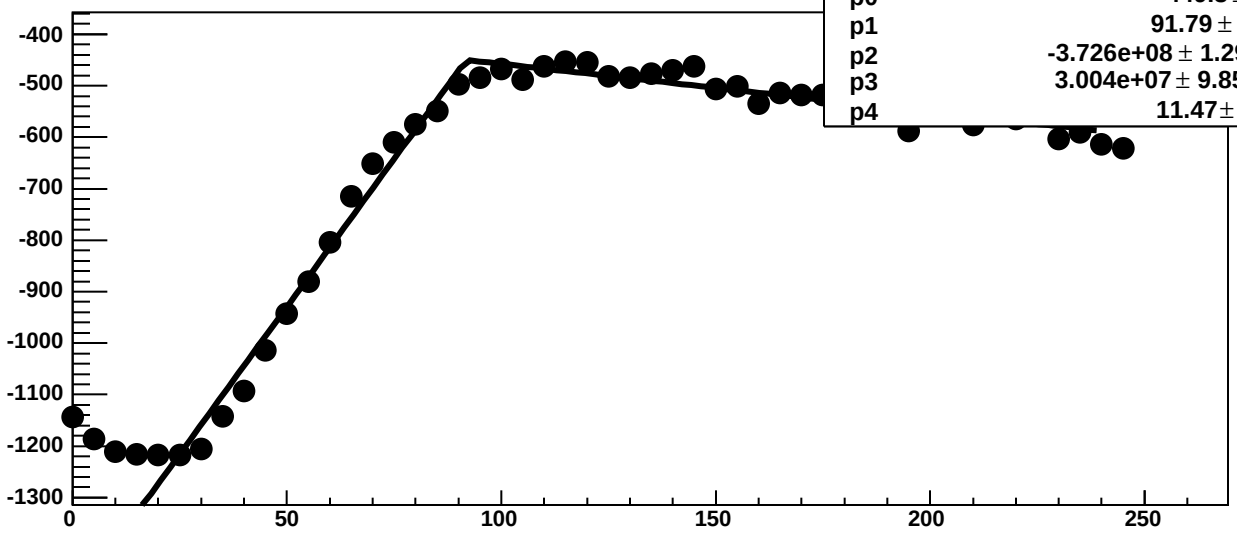



Chip 2, Channel 2, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 233.7 / 41
p0 -436.9 ± 3.841
p1 91.96 ± 0.6446
p2 -7.264e+07 ± 8.108e+06
p3 6.524e+06 ± 7.257e+05
p4 9.789 ± 0.1293
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
50
100
150
200
250

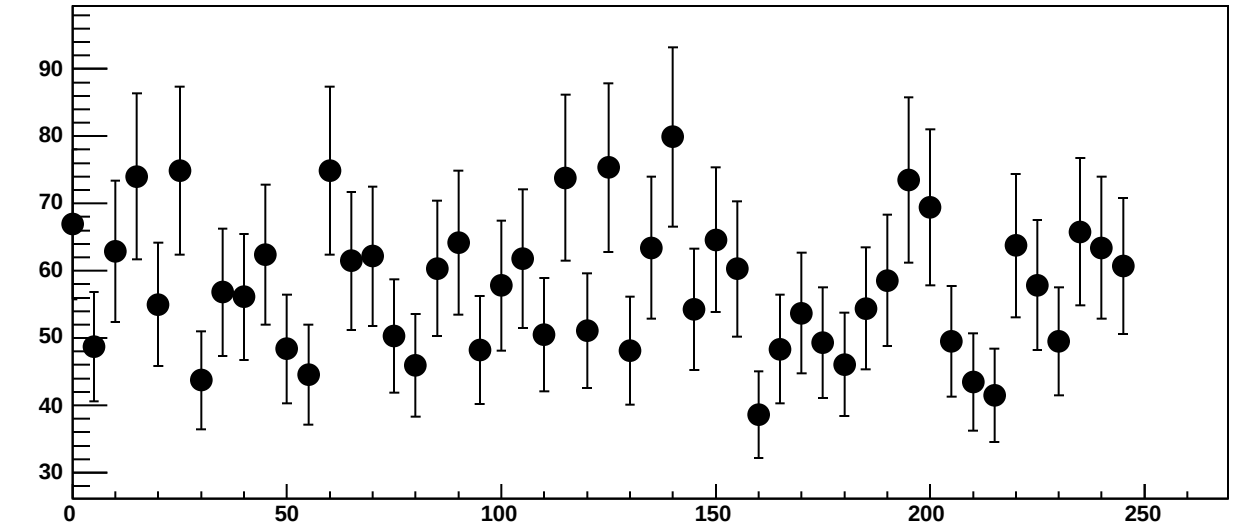

Chip 2, Channel 2, Enable 3, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 2, Channel 2, Enable 3, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
0
50
100
150
200
250

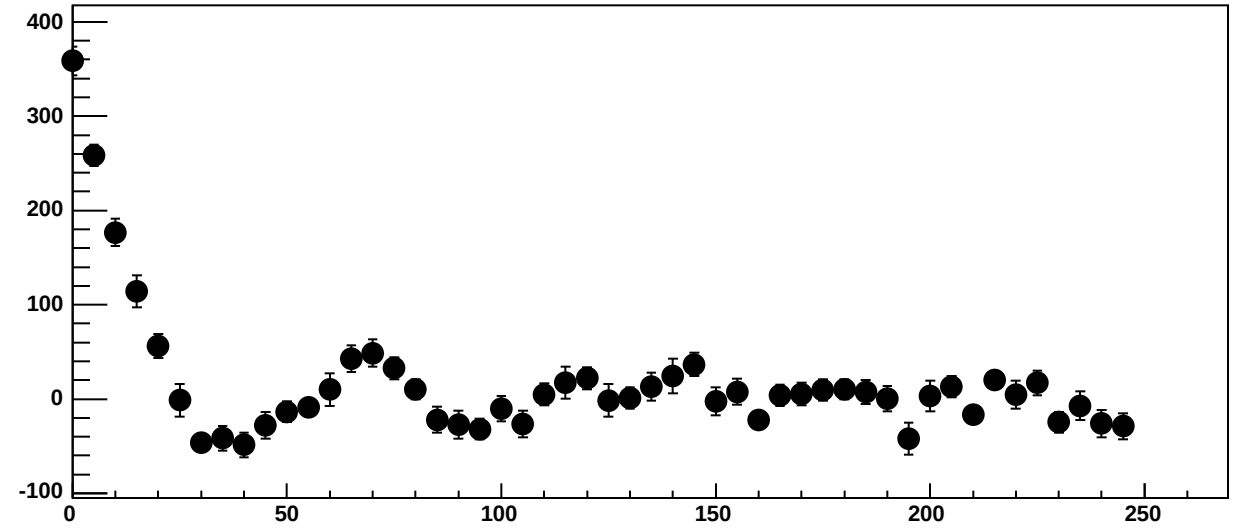
Chip 2, Channel 2, Enable 4, DAC=1600, ADC Mean vs Hold



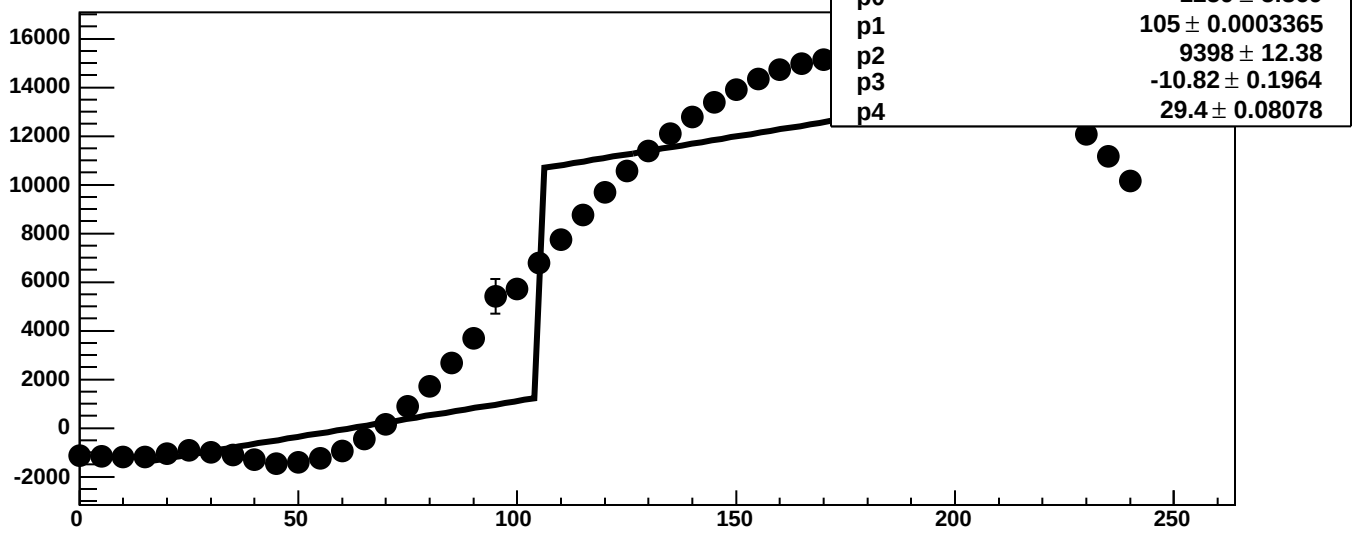
Chip 2, Channel 2, Enable 4, DAC=1600, ADC Noise vs Hold



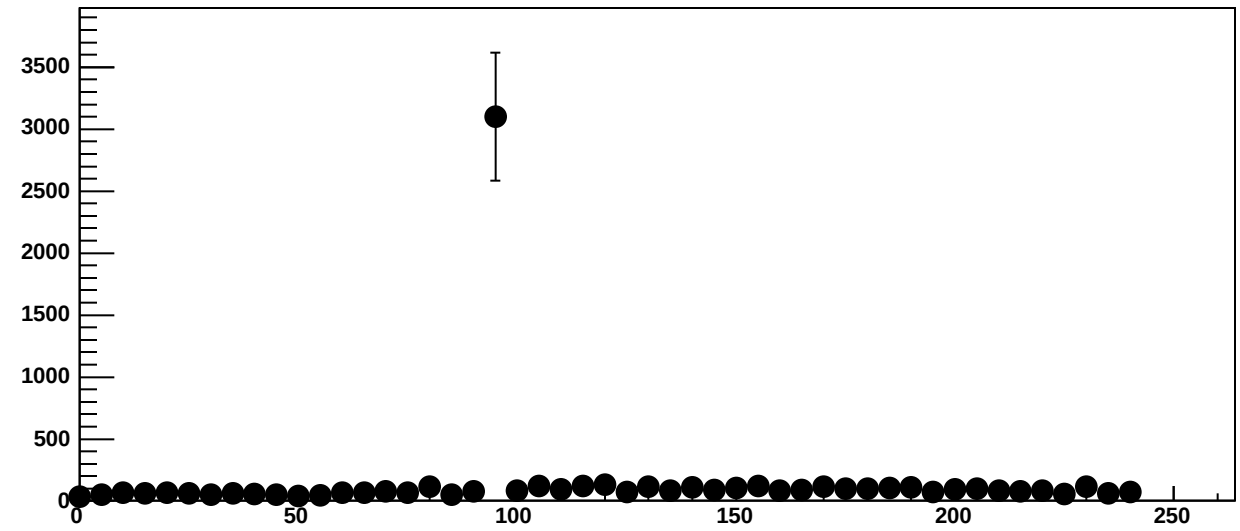
Chip 2, Channel 2, Enable 4, DAC=1600, ADC Residuals vs Hold



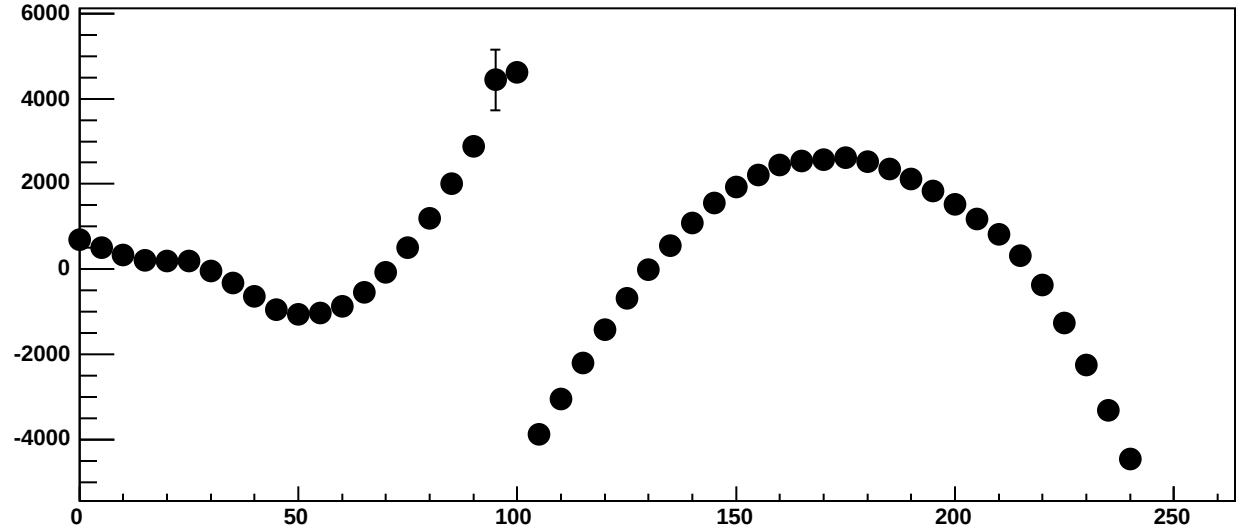
Chip 2, Channel 2, Enable 5, DAC=1600, ADC Mean vs Hold

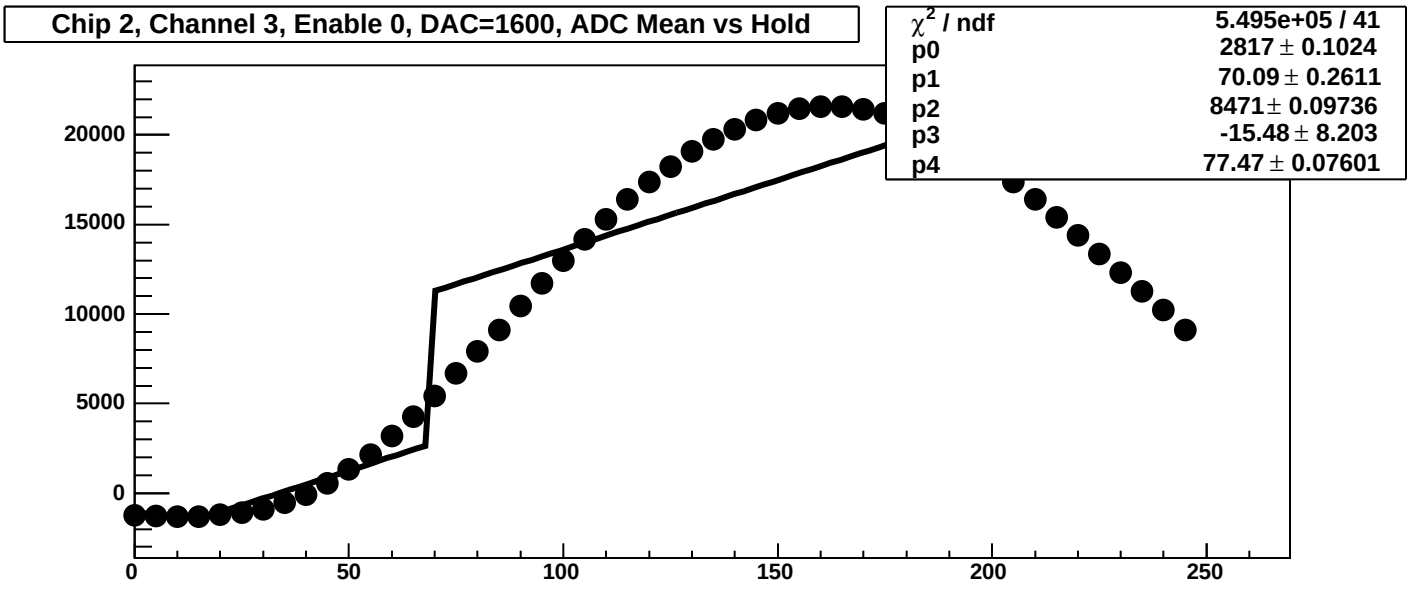


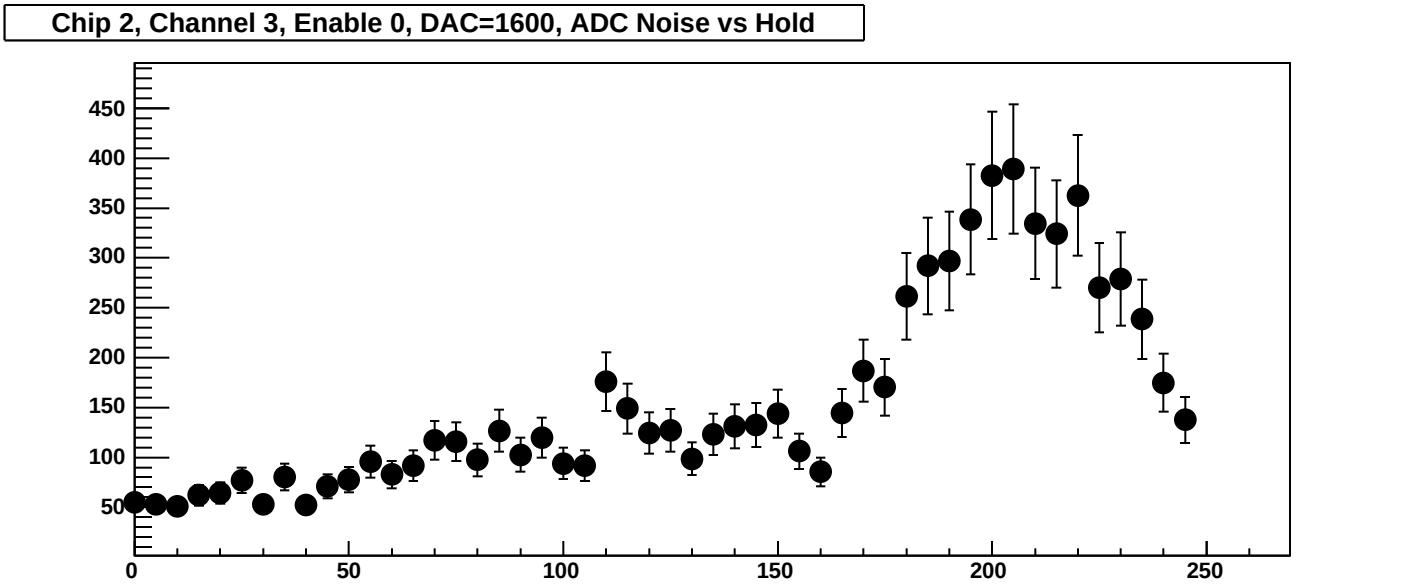
Chip 2, Channel 2, Enable 5, DAC=1600, ADC Noise vs Hold

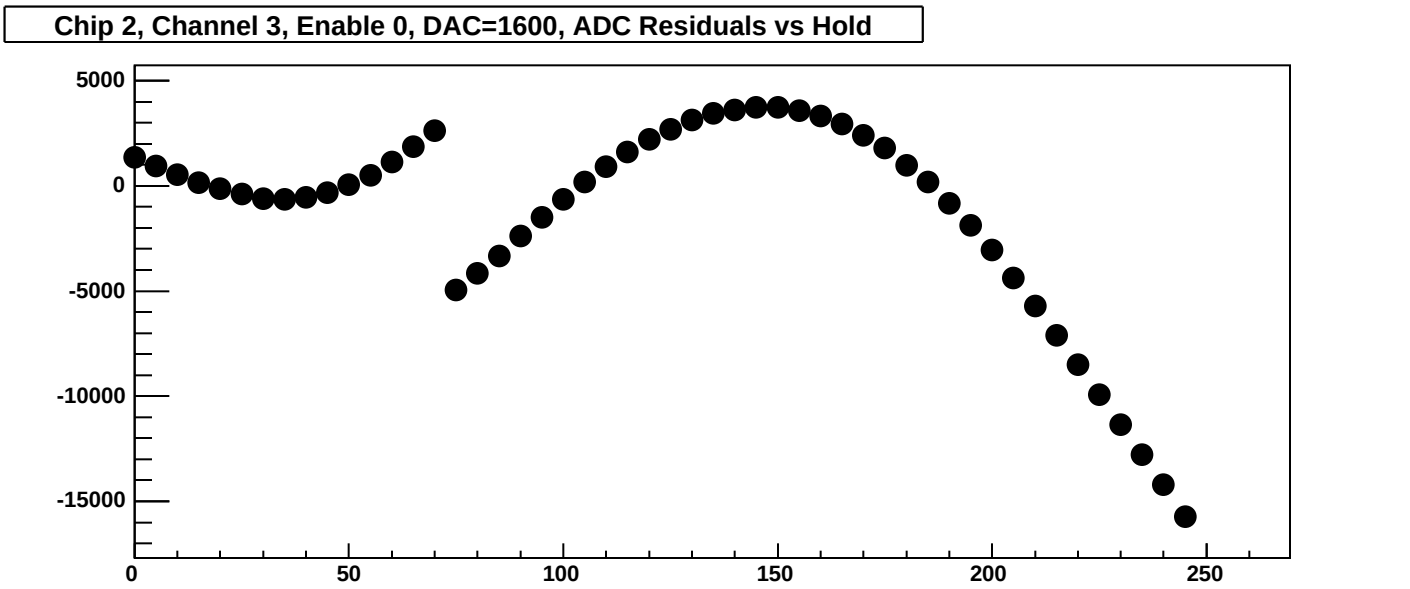


Chip 2, Channel 2, Enable 5, DAC=1600, ADC Residuals vs Hold

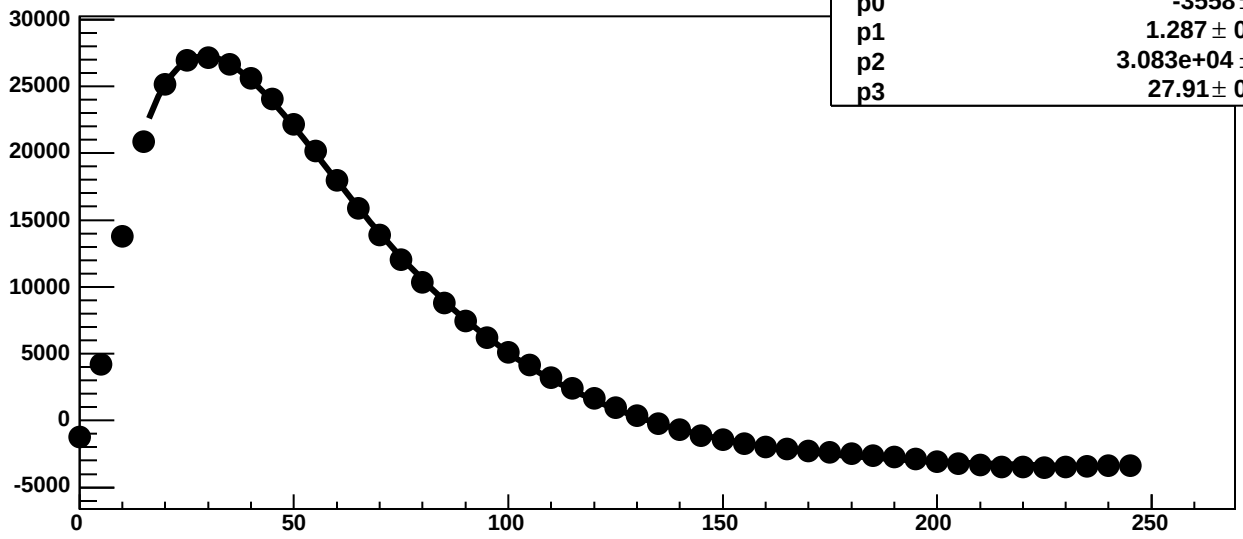



Chip 2, Channel 3, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.495e+05 / 41
p0 2817 ± 0.1024
p1 70.09 ± 0.2611
p2 8471 ± 0.09736
p3 -15.48 ± 8.203
p4 77.47 ± 0.07601
20000
15000
10000
5000
0
0
50
100
150
200
250

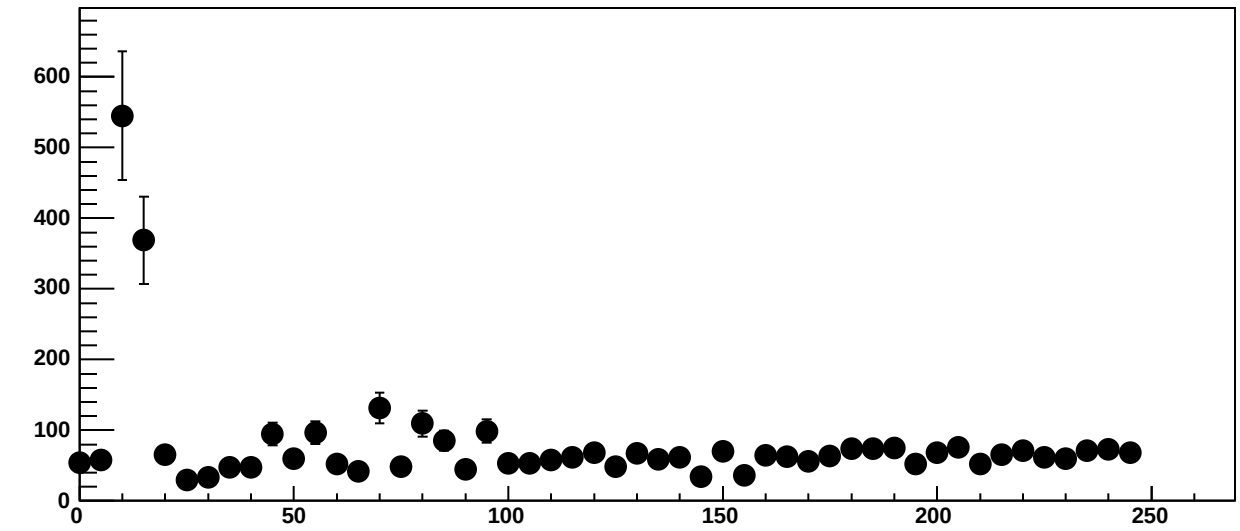

Chip 2, Channel 3, Enable 0, DAC=1600, ADC Noise vs Hold
450
400
350
300
250
200
150
100
50
0
50
100
150
200
250


Chip 2, Channel 3, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
0
50
100
150
200
250

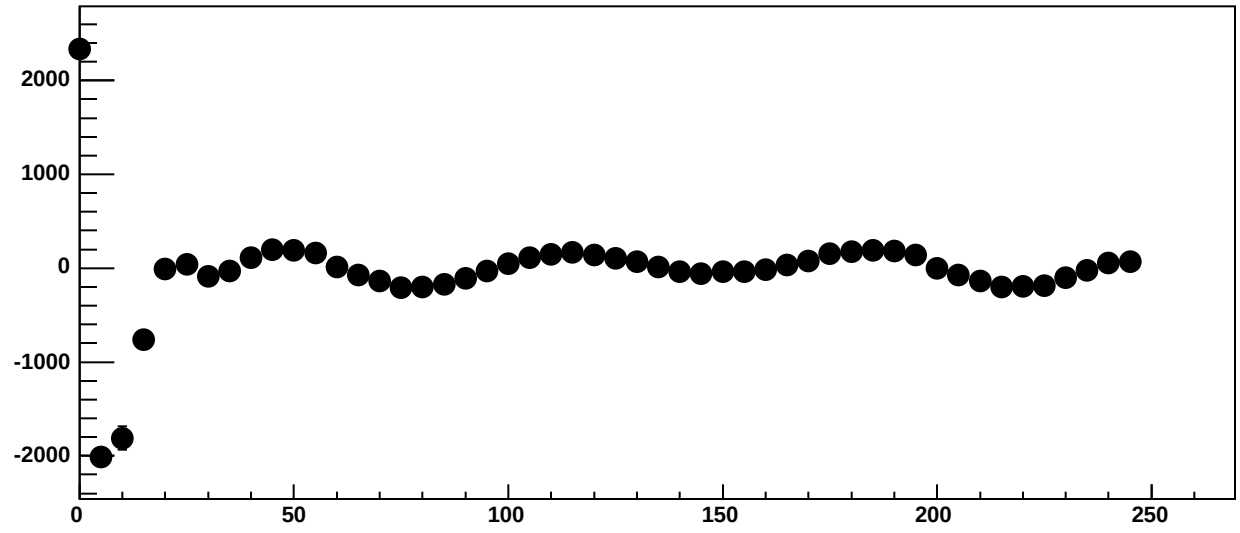
Chip 2, Channel 3, Enable 1!, DAC=1600, ADC Mean vs Hold



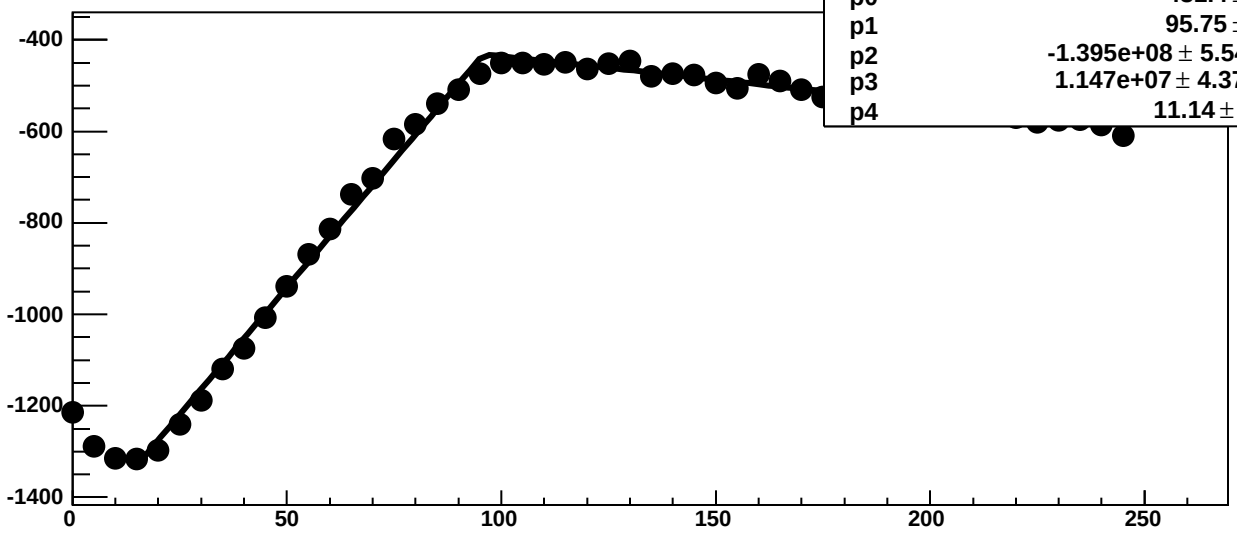
Chip 2, Channel 3, Enable 1!, DAC=1600, ADC Noise vs Hold



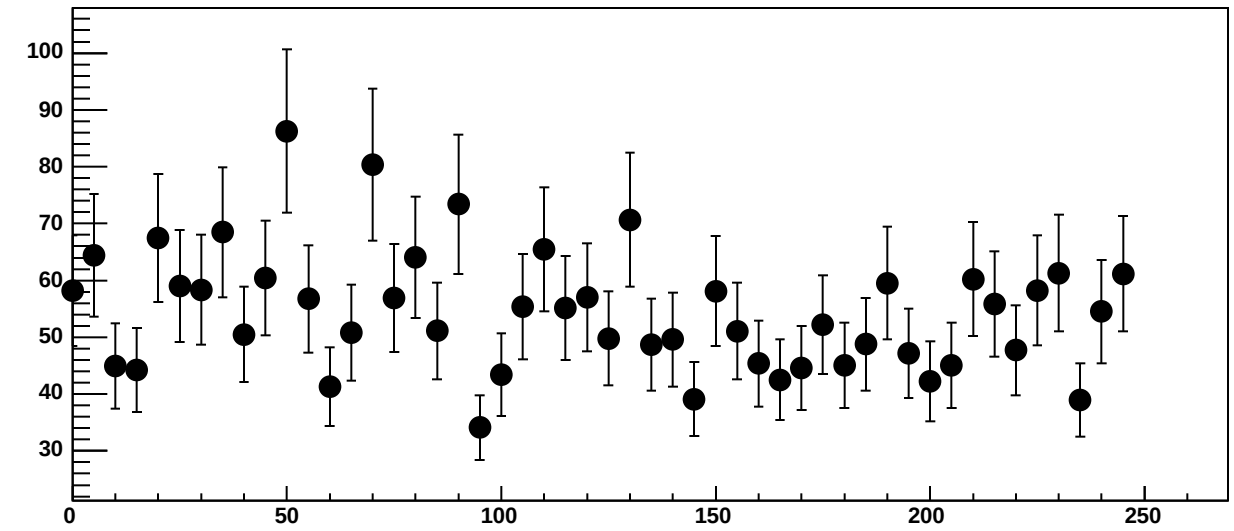
Chip 2, Channel 3, Enable 1!, DAC=1600, ADC Residuals vs Hold



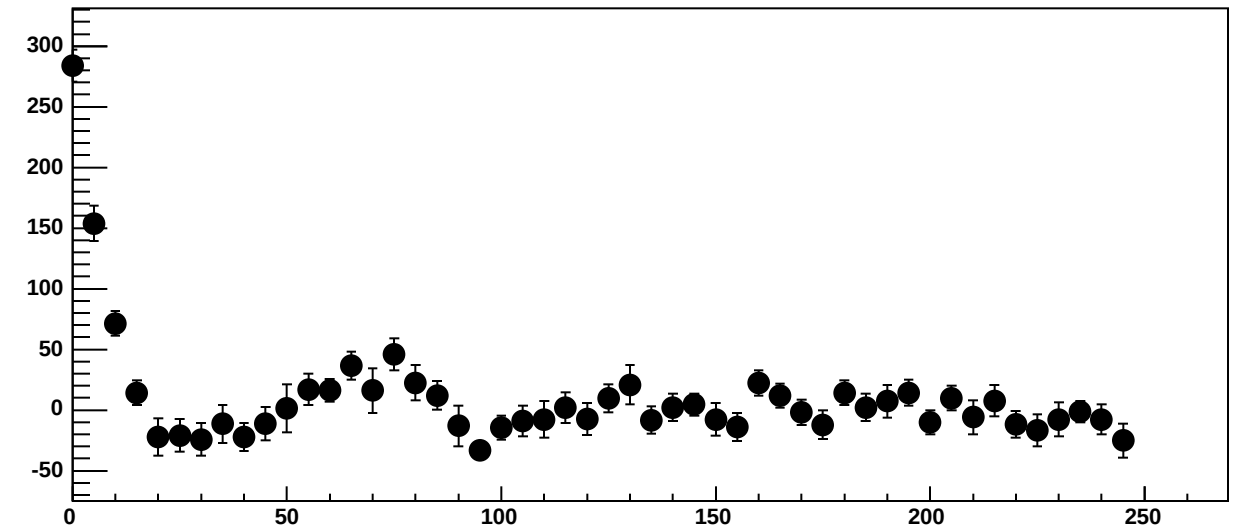
Chip 2, Channel 3, Enable 2, DAC=1600, ADC Mean vs Hold



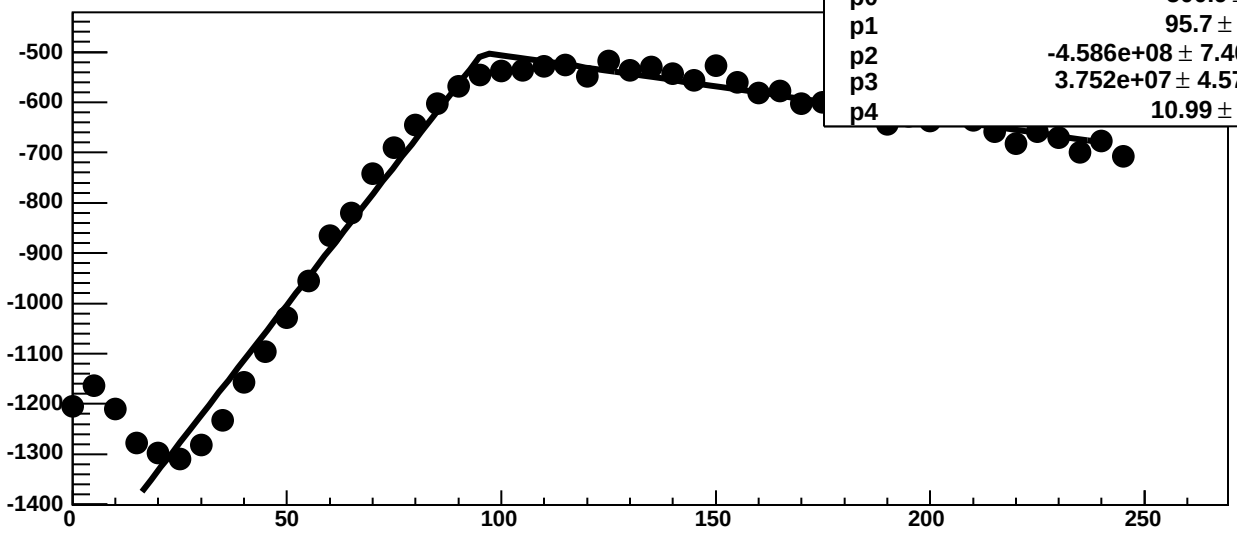
Chip 2, Channel 3, Enable 2, DAC=1600, ADC Noise vs Hold



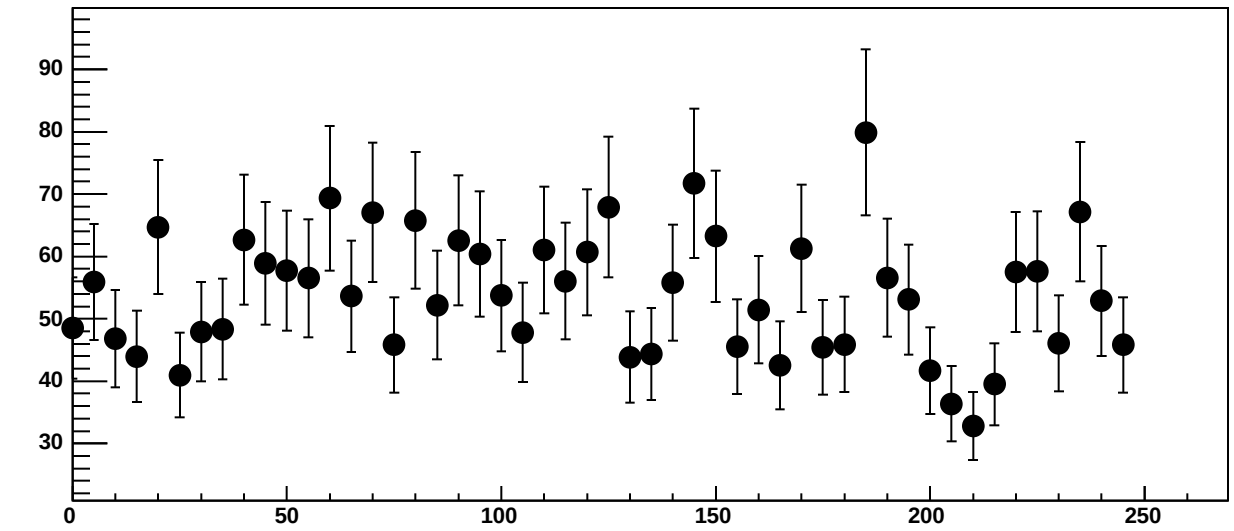
Chip 2, Channel 3, Enable 2, DAC=1600, ADC Residuals vs Hold



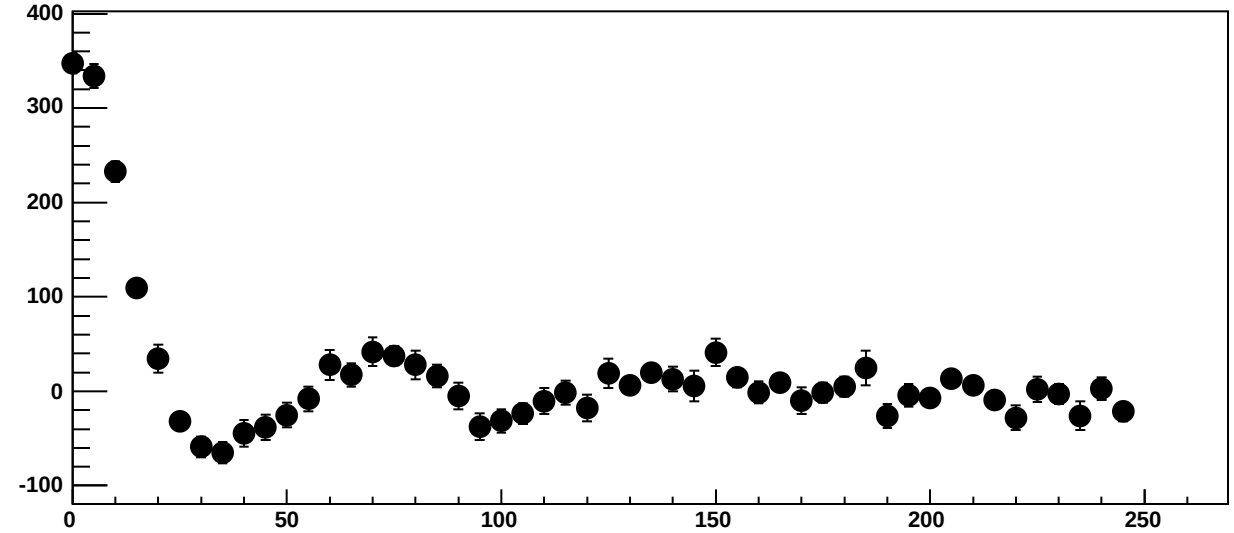
Chip 2, Channel 3, Enable 3, DAC=1600, ADC Mean vs Hold



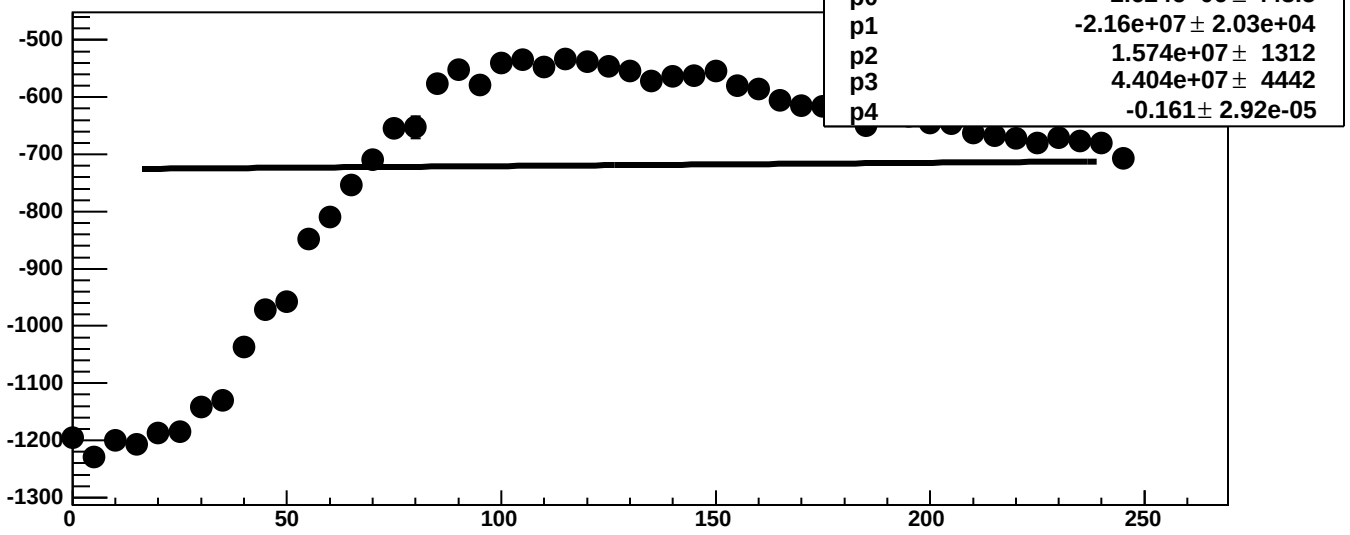
Chip 2, Channel 3, Enable 3, DAC=1600, ADC Noise vs Hold



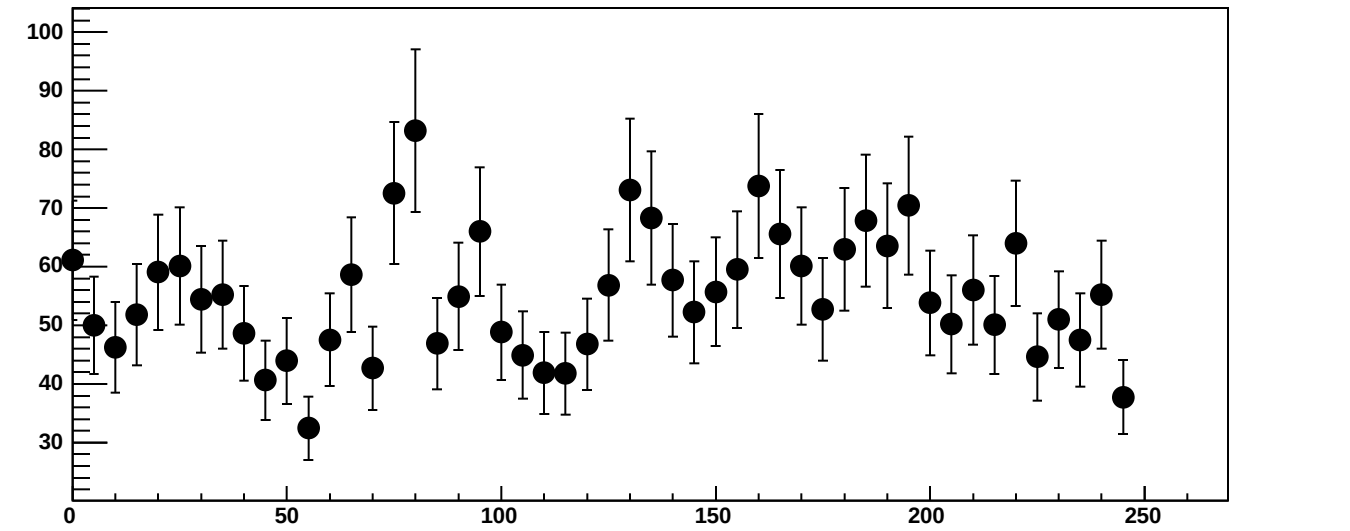
Chip 2, Channel 3, Enable 3, DAC=1600, ADC Residuals vs Hold



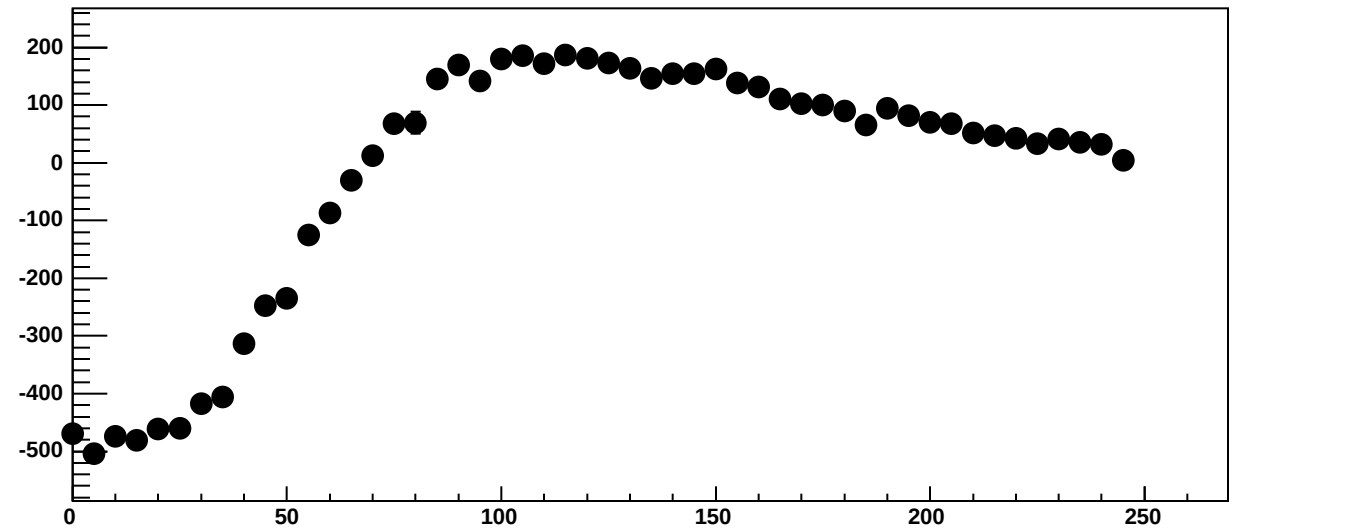
Chip 2, Channel 3, Enable 4, DAC=1600, ADC Mean vs Hold



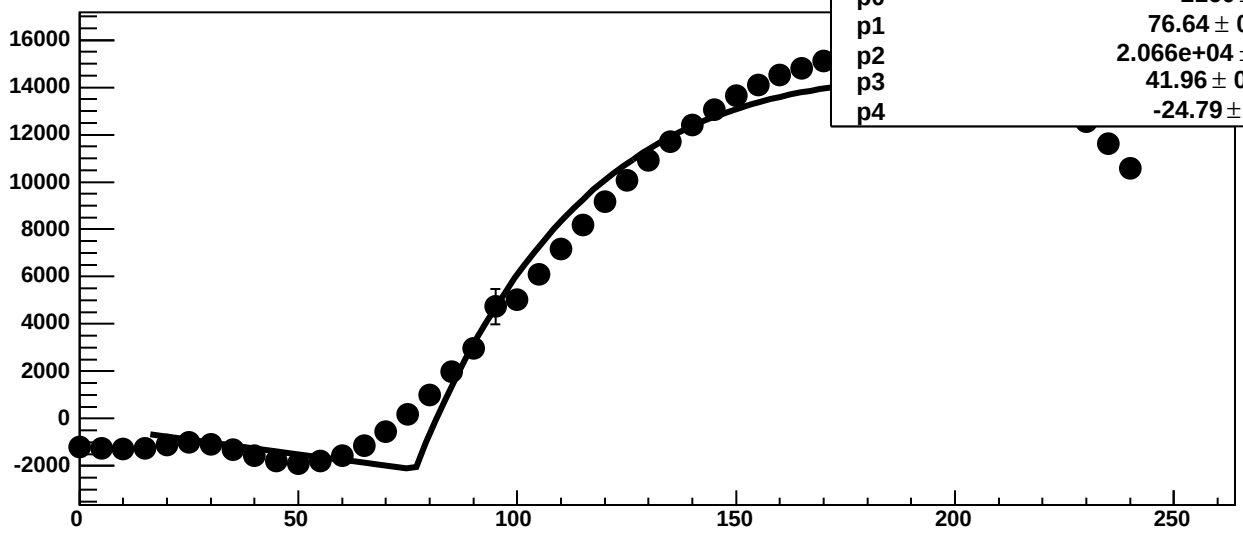
Chip 2, Channel 3, Enable 4, DAC=1600, ADC Noise vs Hold



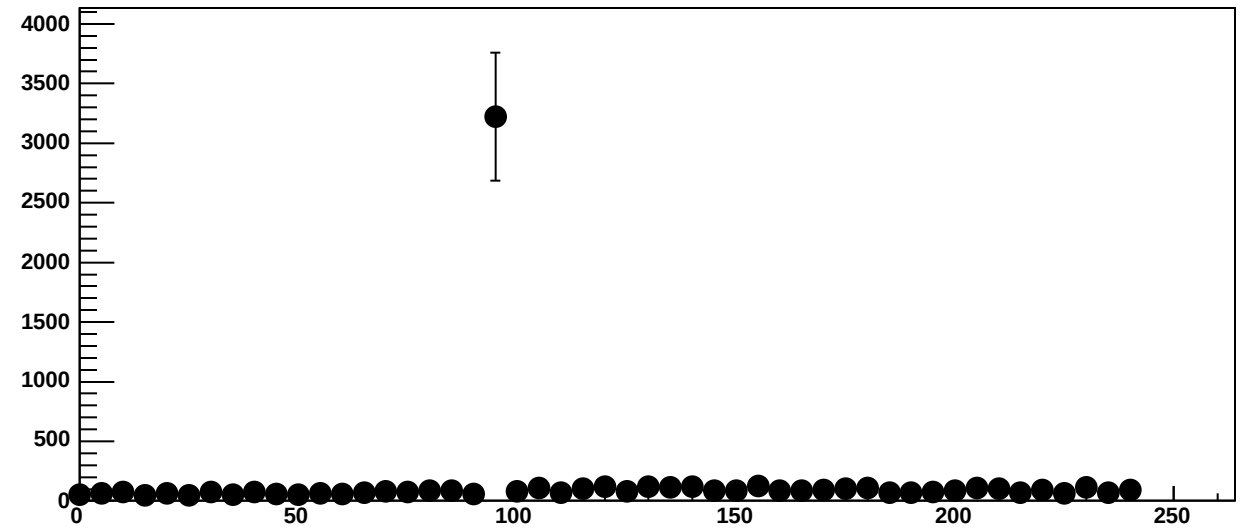
Chip 2, Channel 3, Enable 4, DAC=1600, ADC Residuals vs Hold



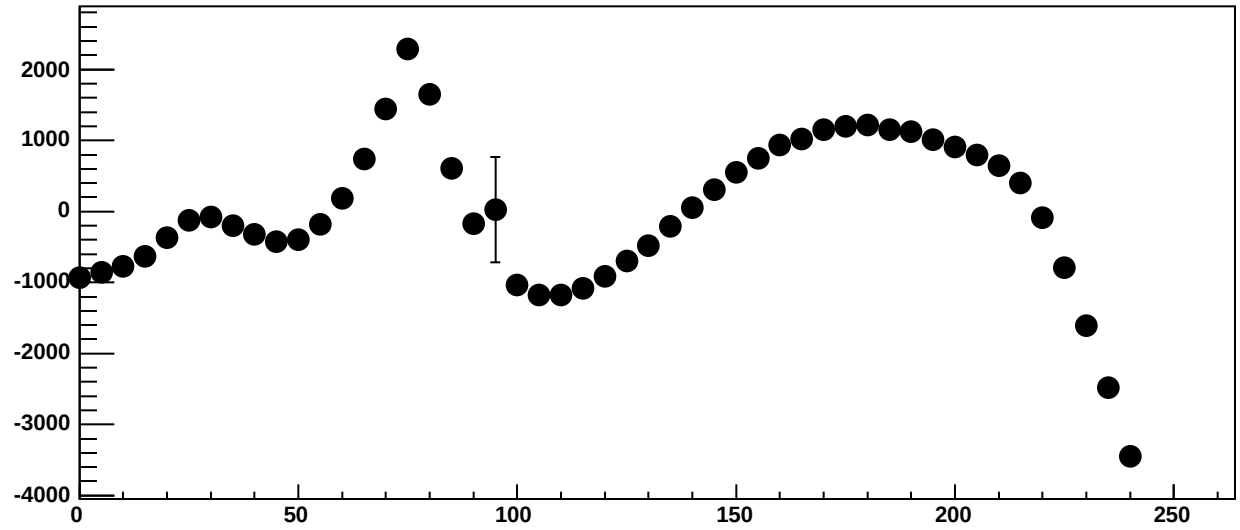
Chip 2, Channel 3, Enable 5, DAC=1600, ADC Mean vs Hold



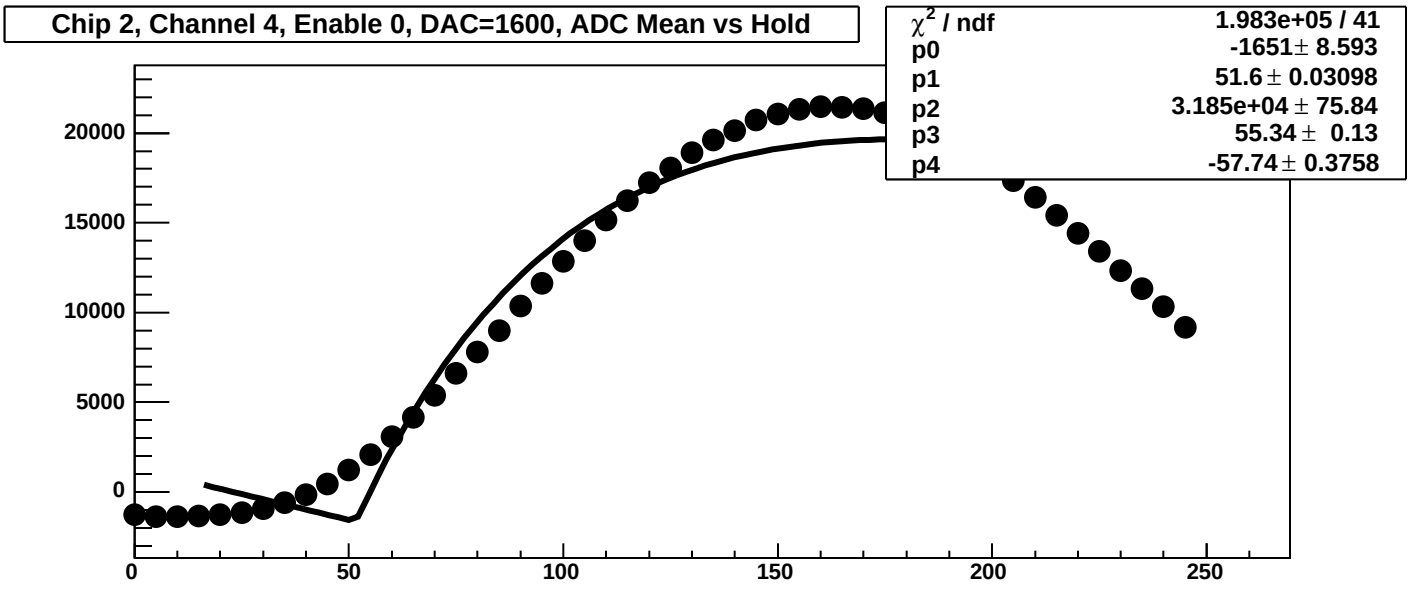
Chip 2, Channel 3, Enable 5, DAC=1600, ADC Noise vs Hold



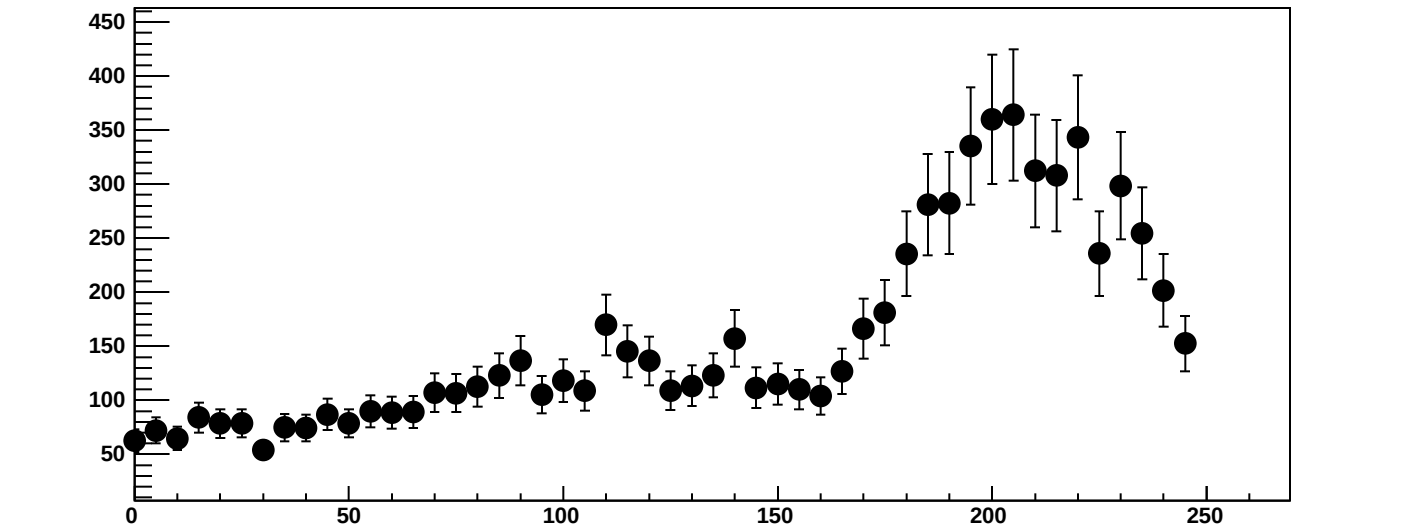
Chip 2, Channel 3, Enable 5, DAC=1600, ADC Residuals vs Hold



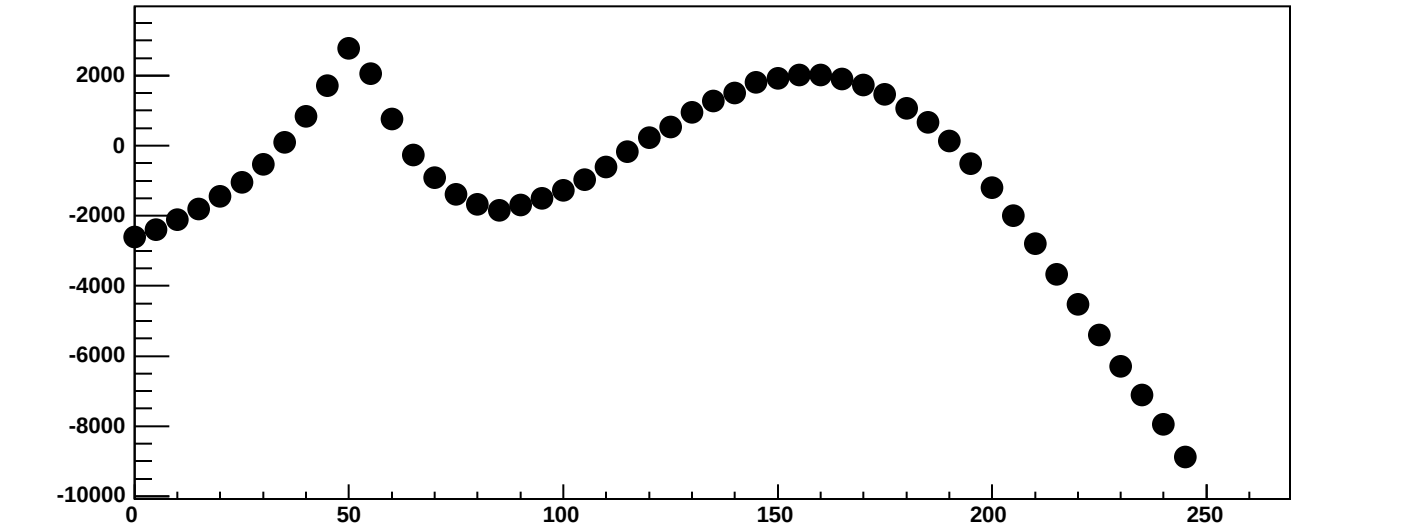
Chip 2, Channel 4, Enable 0, DAC=1600, ADC Mean vs Hold



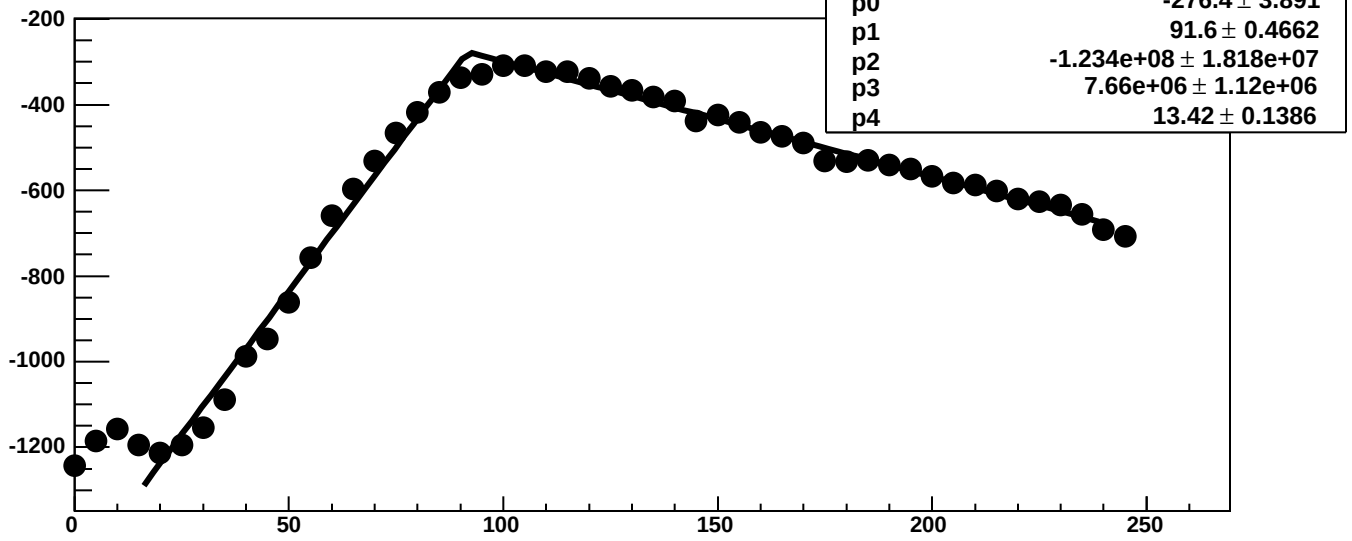
Chip 2, Channel 4, Enable 0, DAC=1600, ADC Noise vs Hold



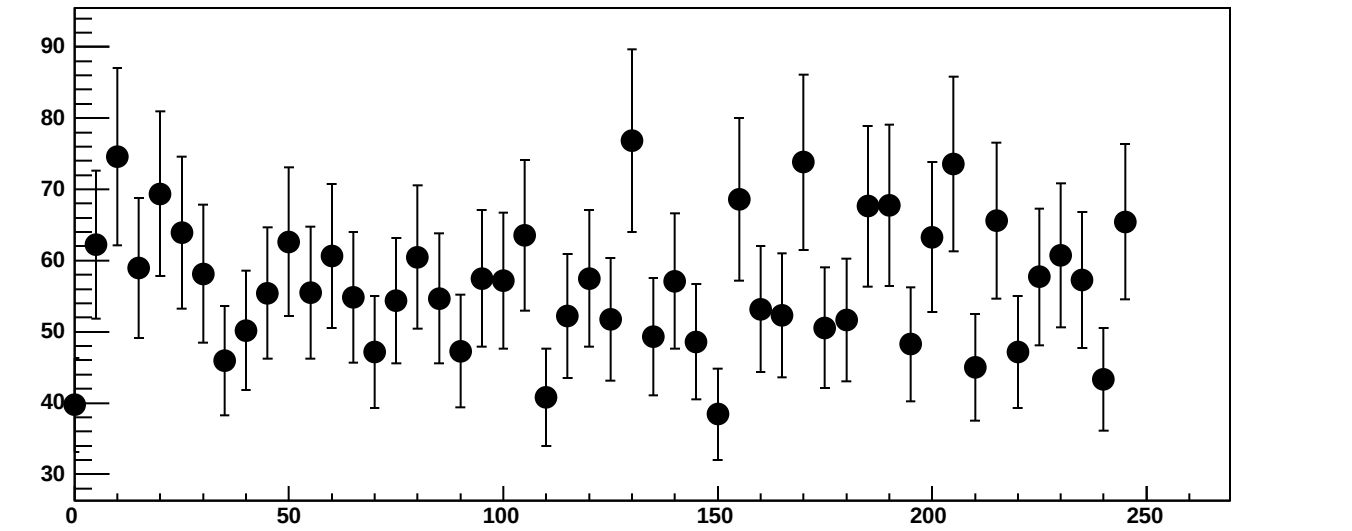
Chip 2, Channel 4, Enable 0, DAC=1600, ADC Residuals vs Hold



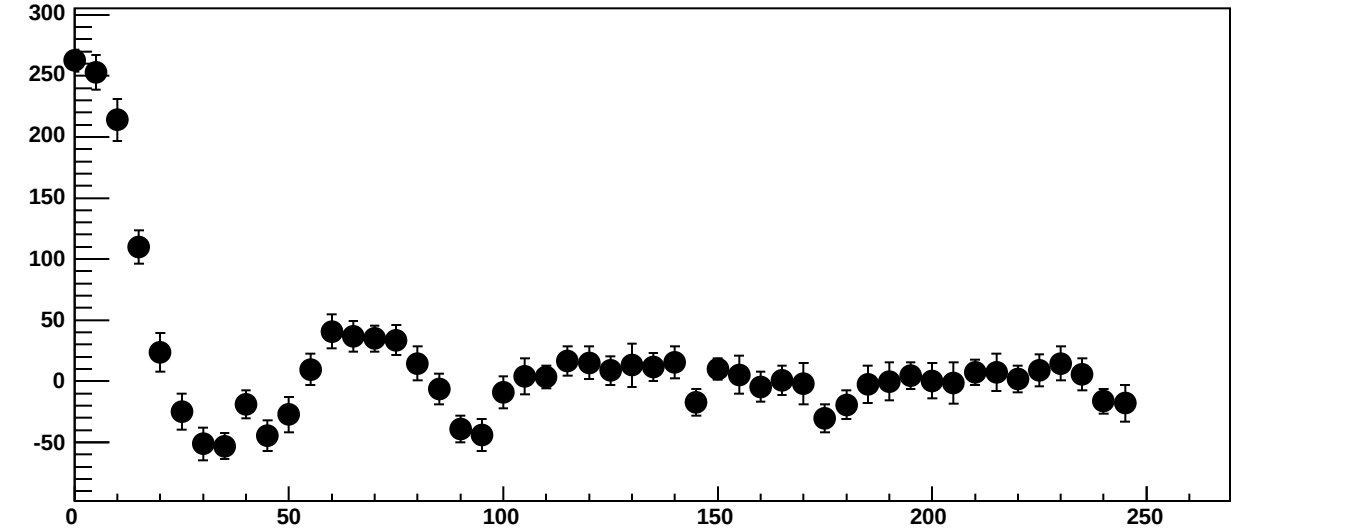
Chip 2, Channel 4, Enable 1, DAC=1600, ADC Mean vs Hold

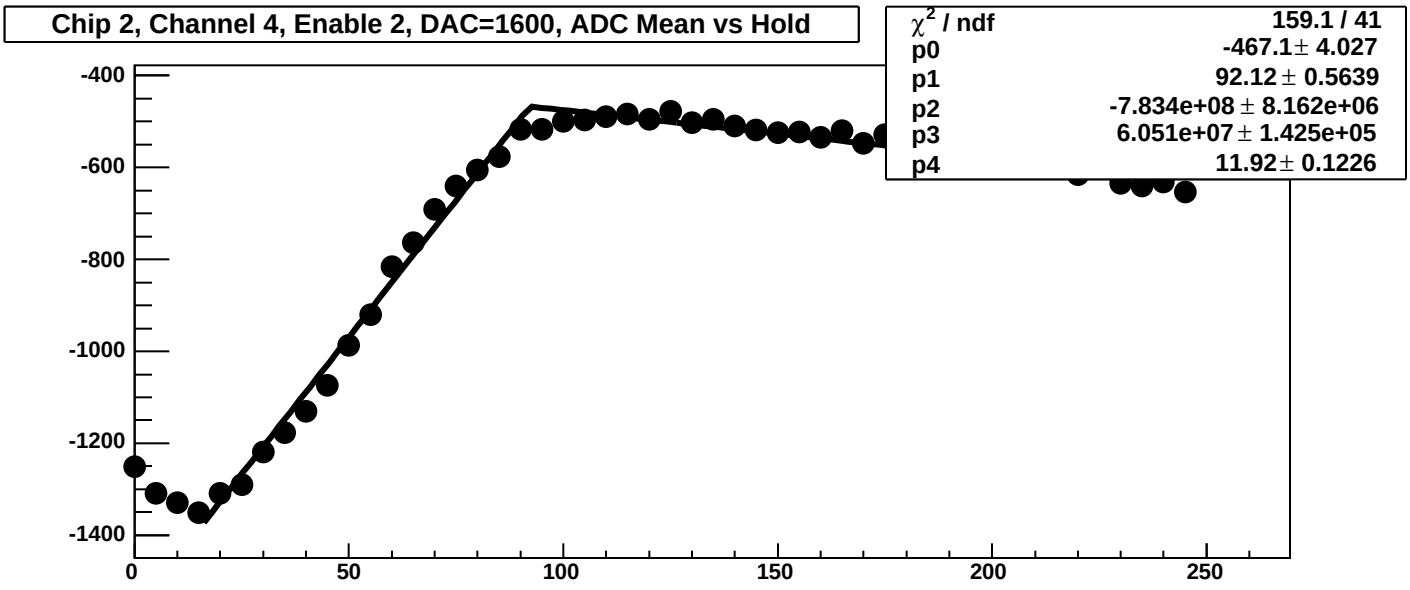


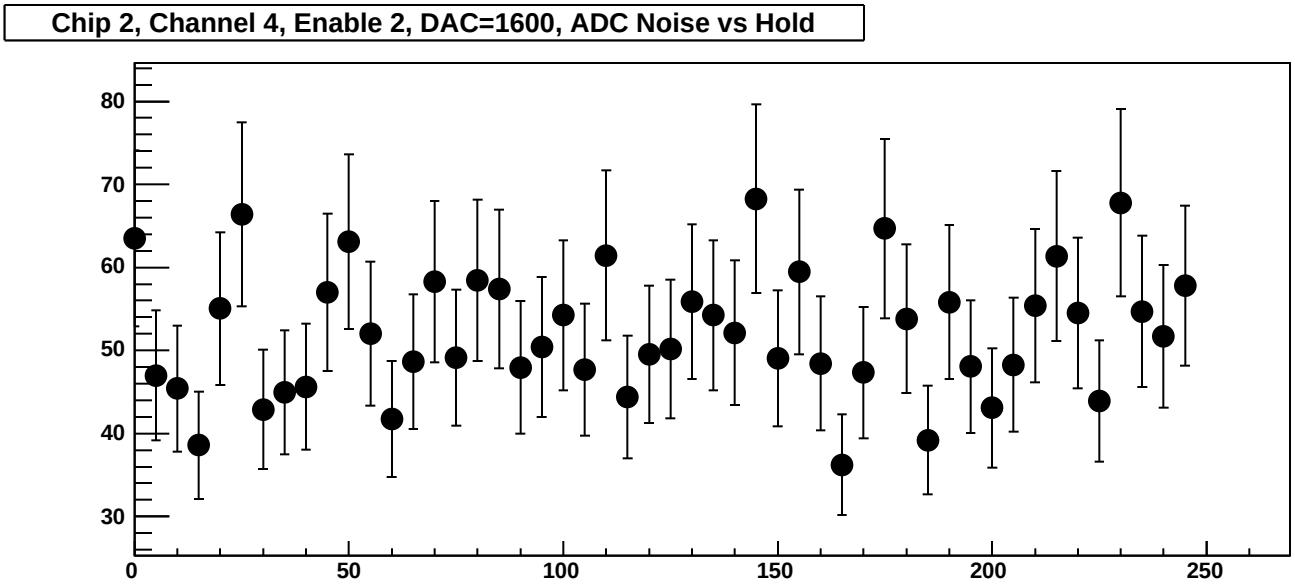
Chip 2, Channel 4, Enable 1, DAC=1600, ADC Noise vs Hold

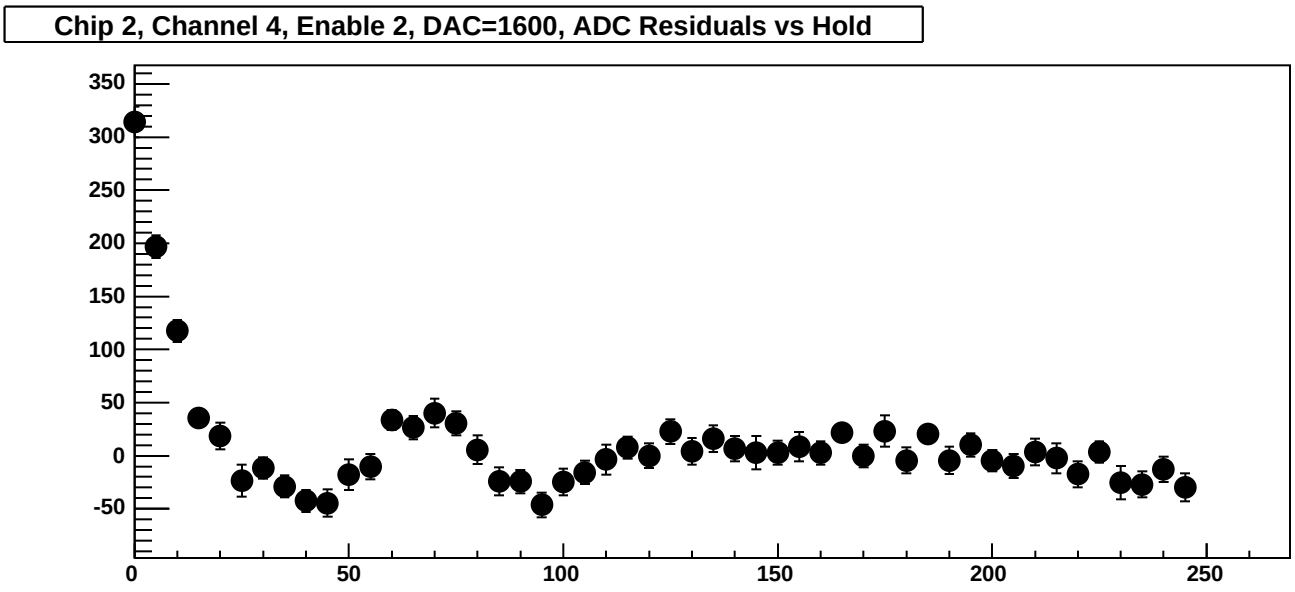


Chip 2, Channel 4, Enable 1, DAC=1600, ADC Residuals vs Hold

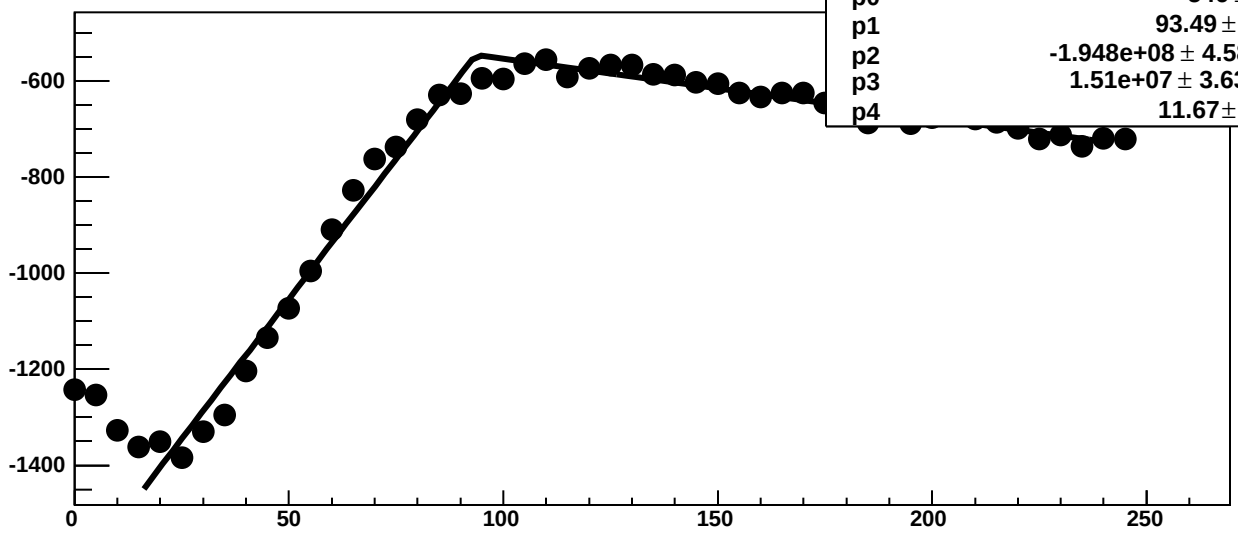



Chip 2, Channel 4, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 159.1 / 41
p0 -467.1 ± 4.027
p1 92.12 ± 0.5639
p2 -7.834e+08 ± 8.162e+06
p3 6.051e+07 ± 1.425e+05
p4 11.92 ± 0.1226
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

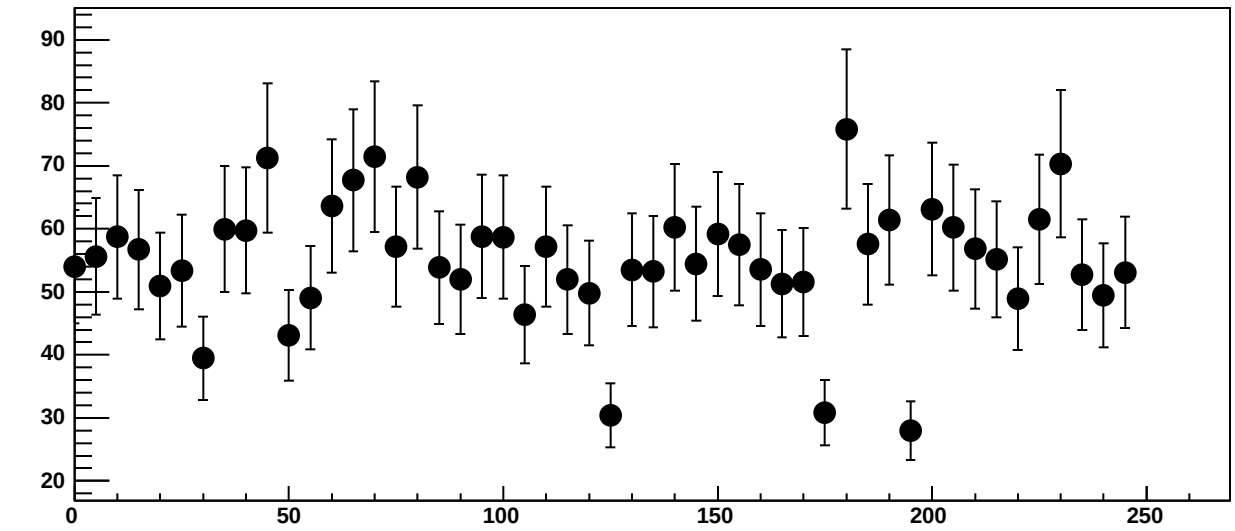

Chip 2, Channel 4, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 2, Channel 4, Enable 2, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
0
50
100
150
200
250

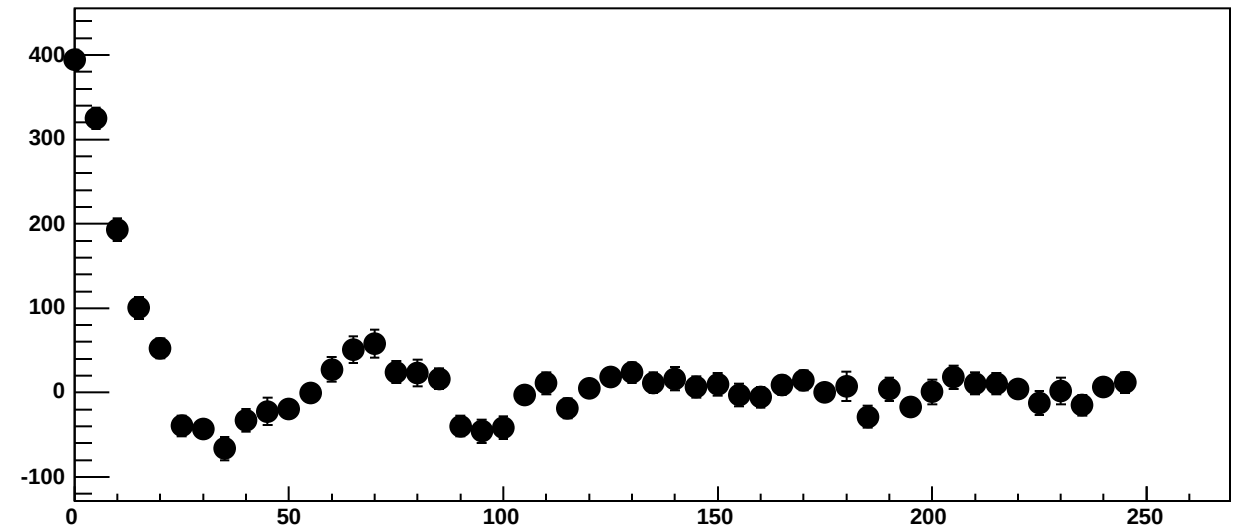
Chip 2, Channel 4, Enable 3, DAC=1600, ADC Mean vs Hold



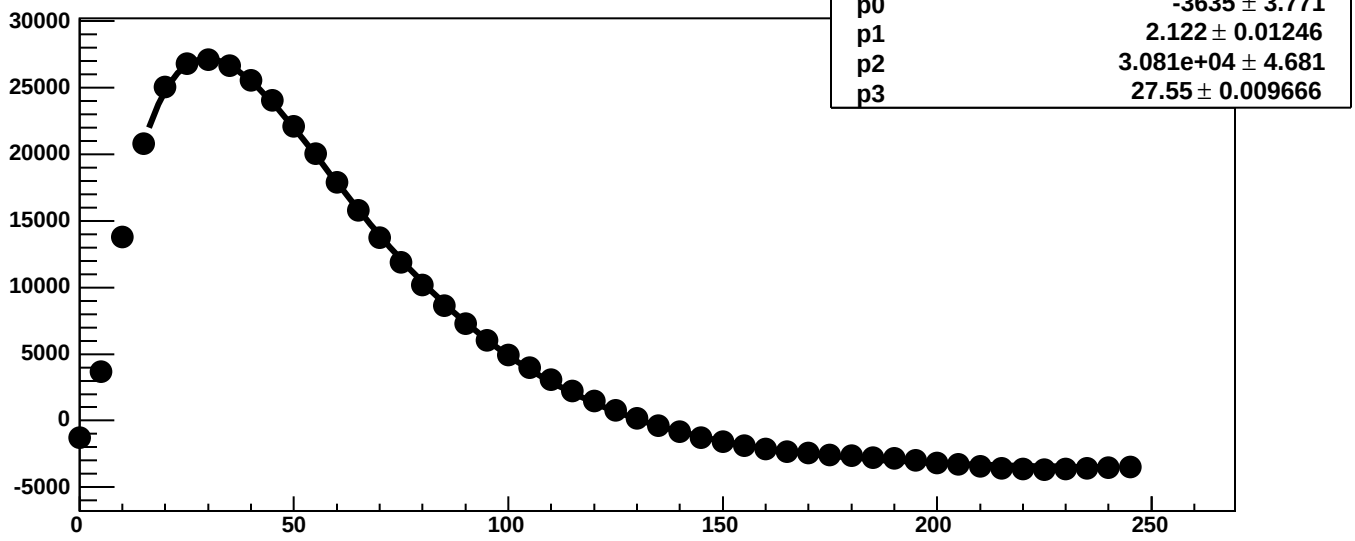
Chip 2, Channel 4, Enable 3, DAC=1600, ADC Noise vs Hold



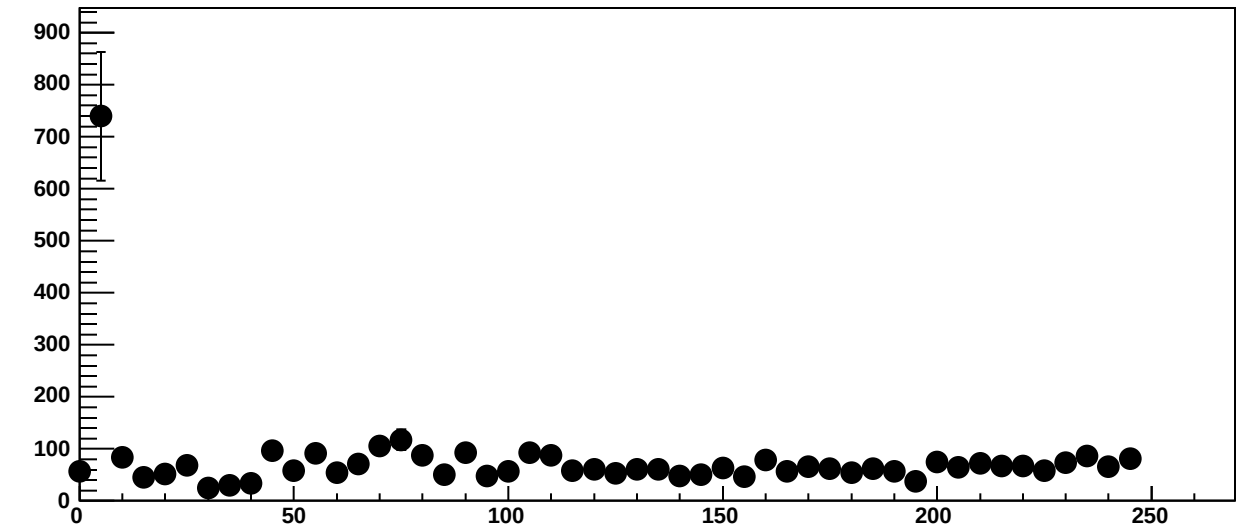
Chip 2, Channel 4, Enable 3, DAC=1600, ADC Residuals vs Hold



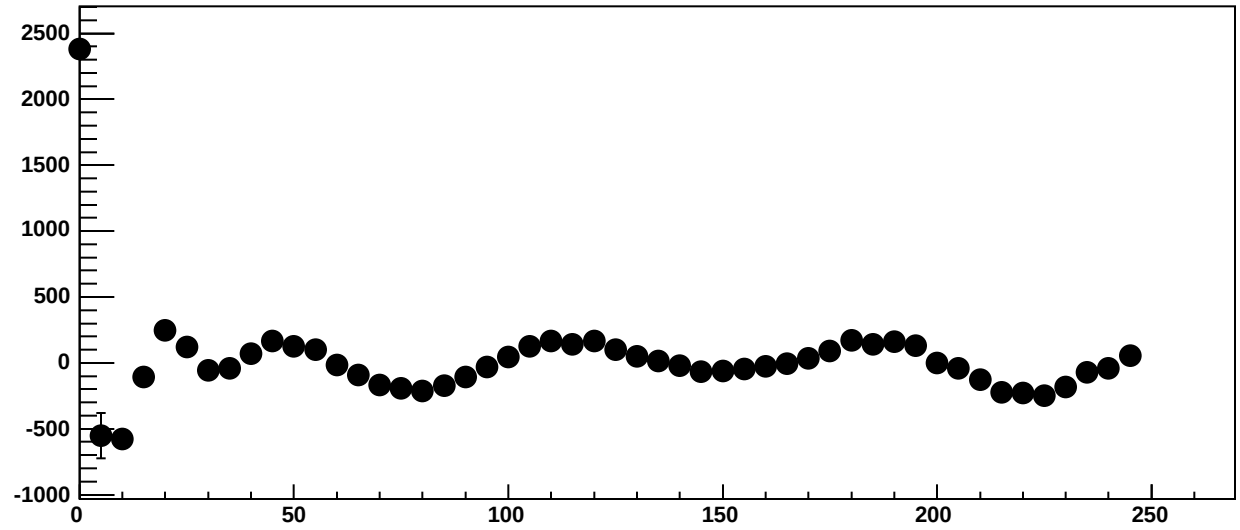
Chip 2, Channel 4, Enable 4!, DAC=1600, ADC Mean vs Hold

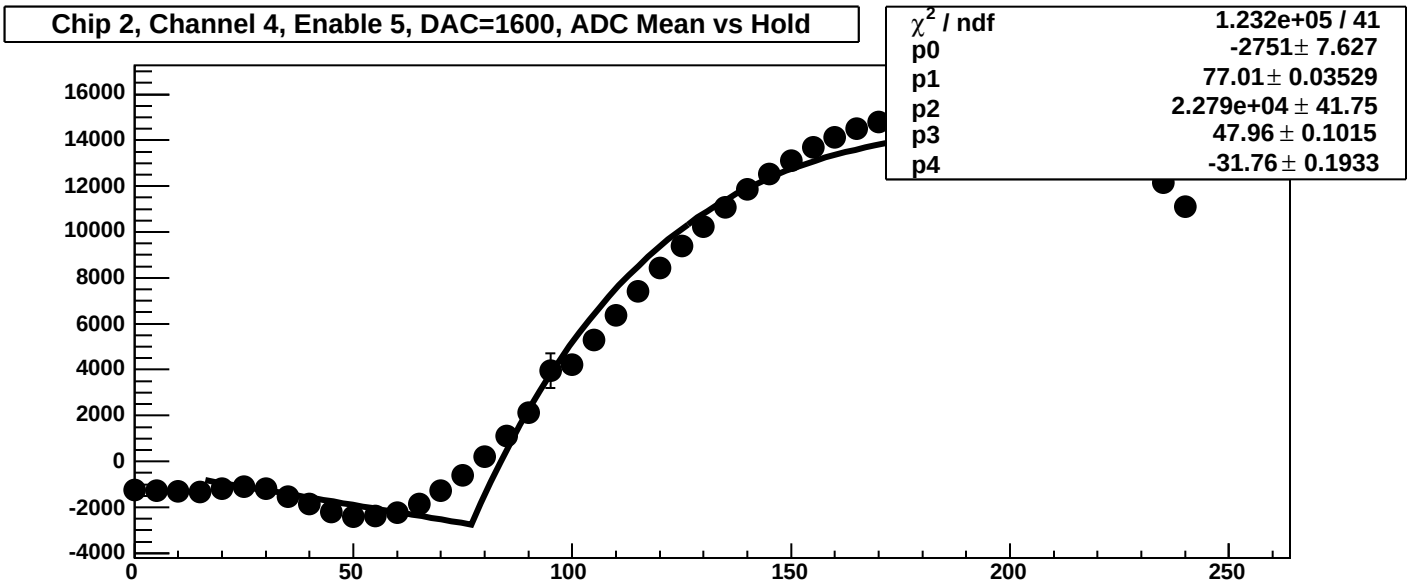


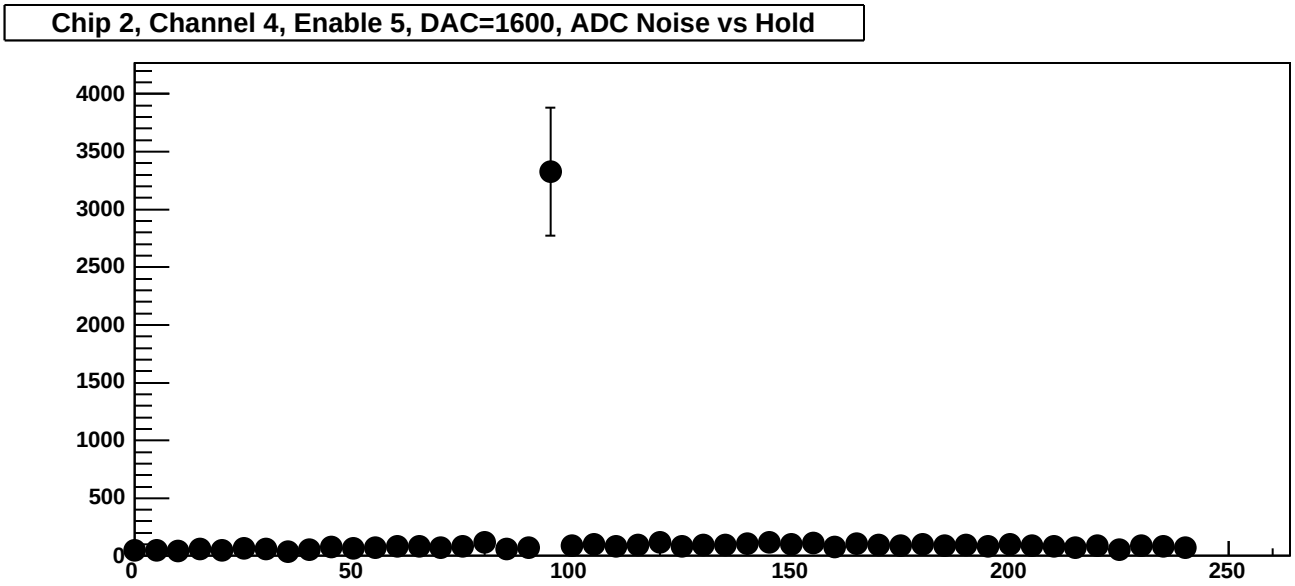
Chip 2, Channel 4, Enable 4!, DAC=1600, ADC Noise vs Hold

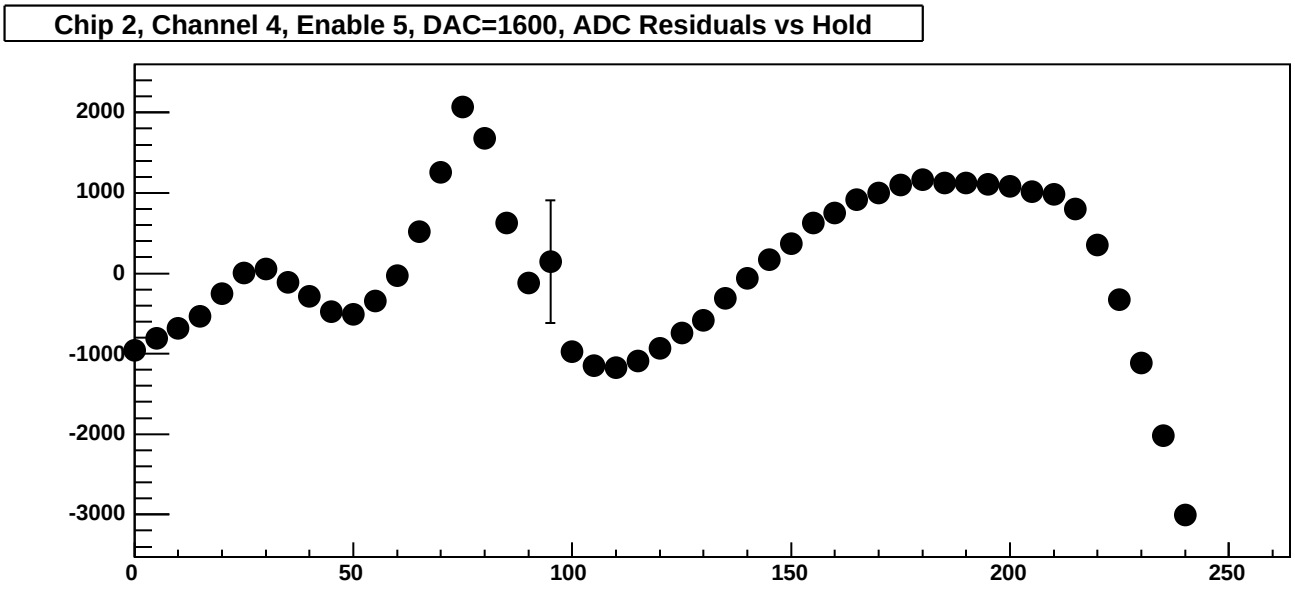


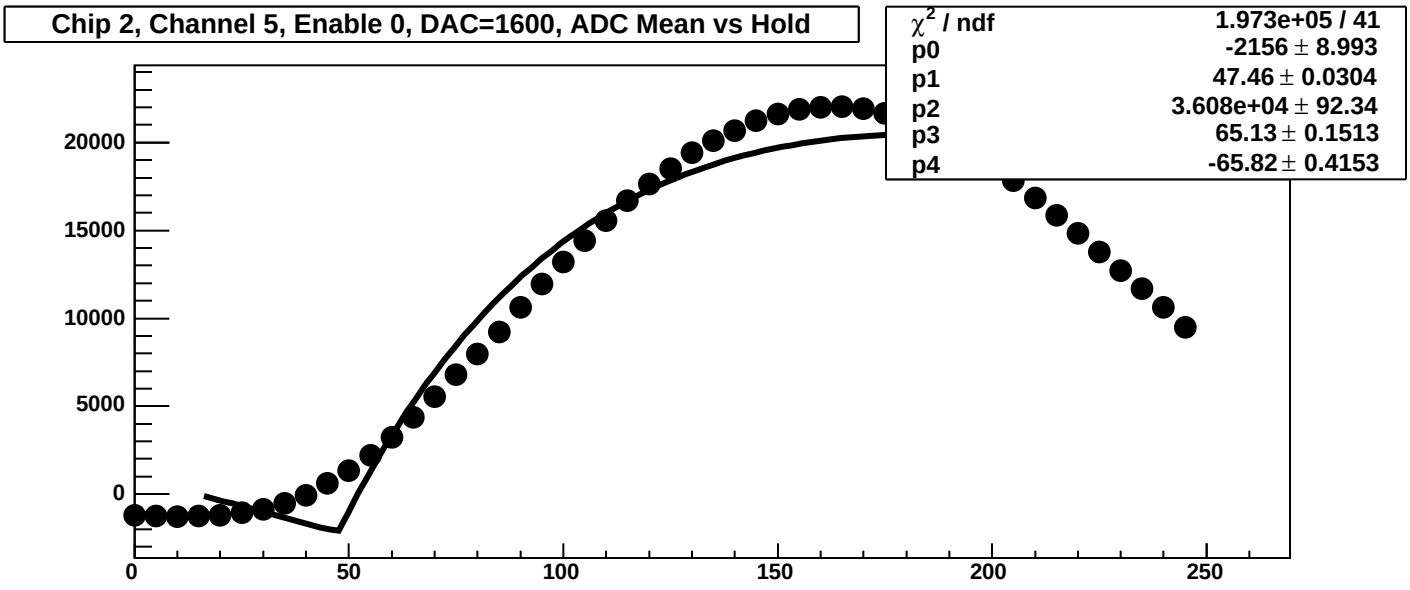
Chip 2, Channel 4, Enable 4!, DAC=1600, ADC Residuals vs Hold

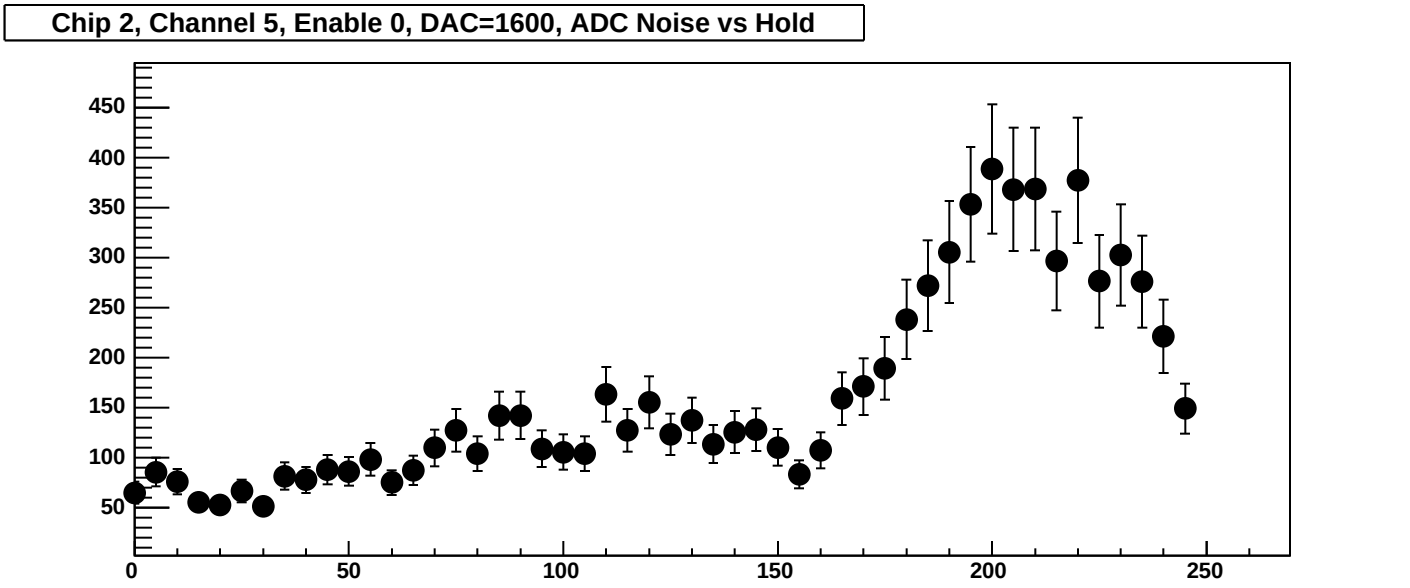


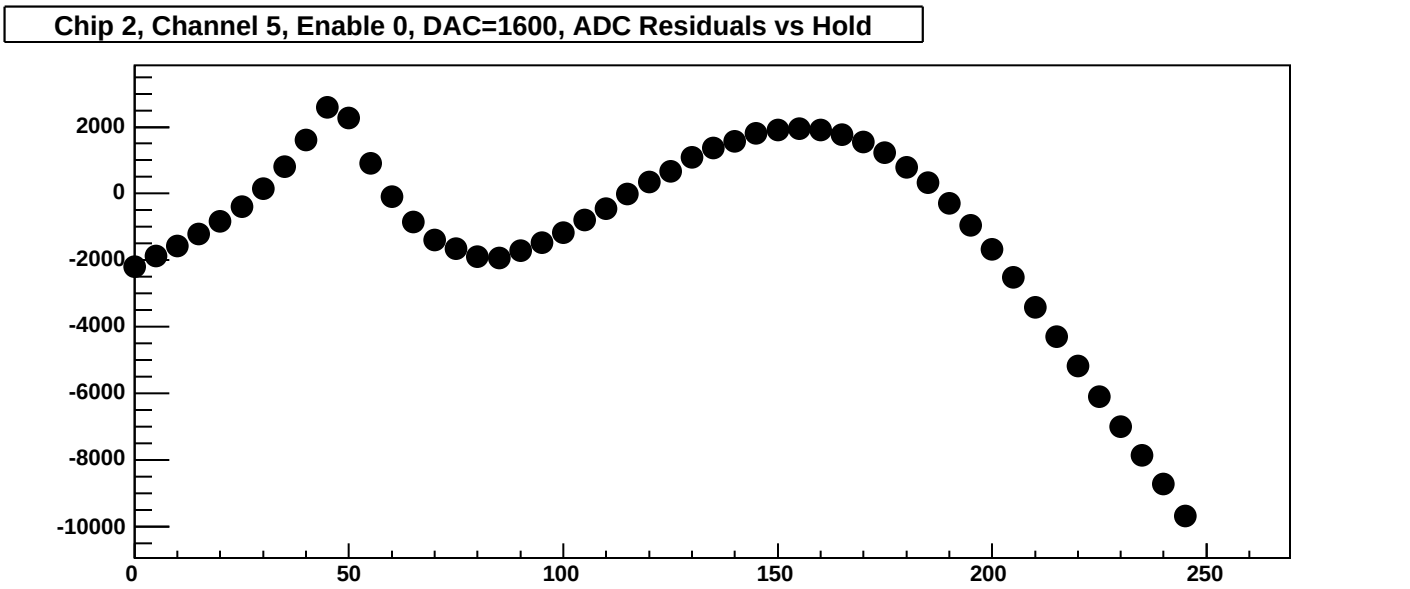

Chip 2, Channel 4, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.232e+05 / 41
p0 -2751 ± 7.627
p1 77.01 ± 0.03529
p2 2.279e+04 ± 41.75
p3 47.96 ± 0.1015
p4 -31.76 ± 0.1933
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250


Chip 2, Channel 4, Enable 5, DAC=1600, ADC Noise vs Hold
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250

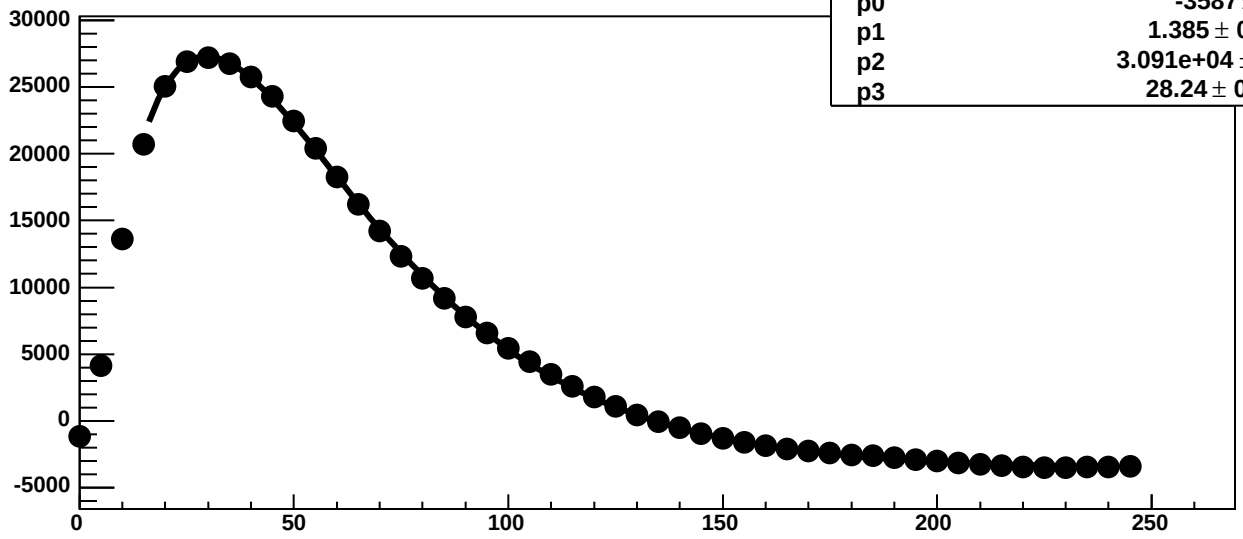

Chip 2, Channel 4, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250


Chip 2, Channel 5, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.973e+05 / 41
p0 -2156 ± 8.993
p1 47.46 ± 0.0304
p2 3.608e+04 ± 92.34
p3 65.13 ± 0.1513
p4 -65.82 ± 0.4153
20000
15000
10000
5000
0
0
50
100
150
200
250

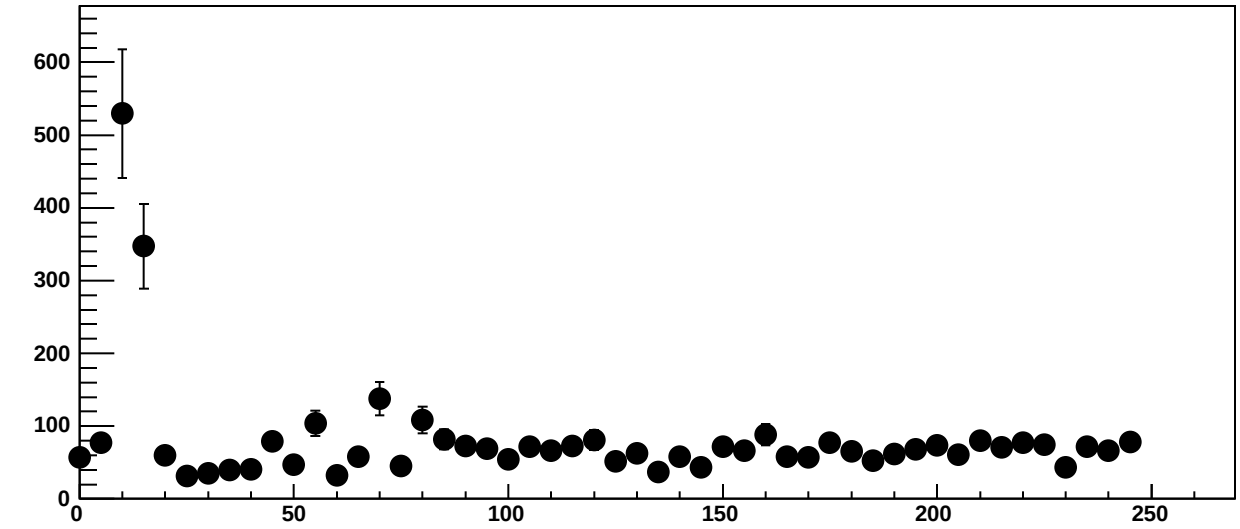

Chip 2, Channel 5, Enable 0, DAC=1600, ADC Noise vs Hold
450
400
350
300
250
200
150
100
50
0
50
100
150
200
250


Chip 2, Channel 5, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

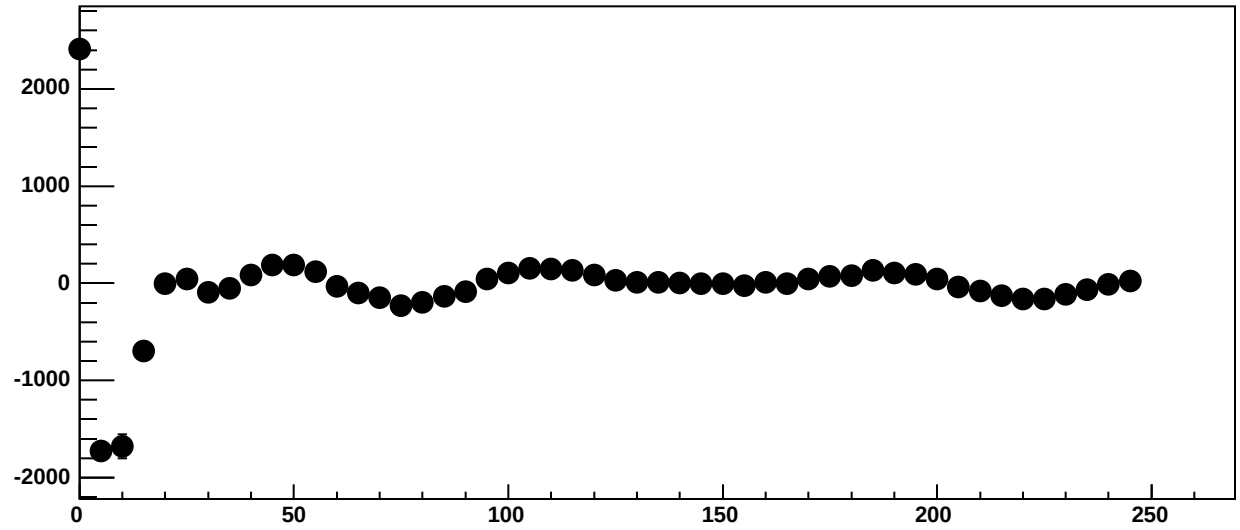
Chip 2, Channel 5, Enable 1!, DAC=1600, ADC Mean vs Hold



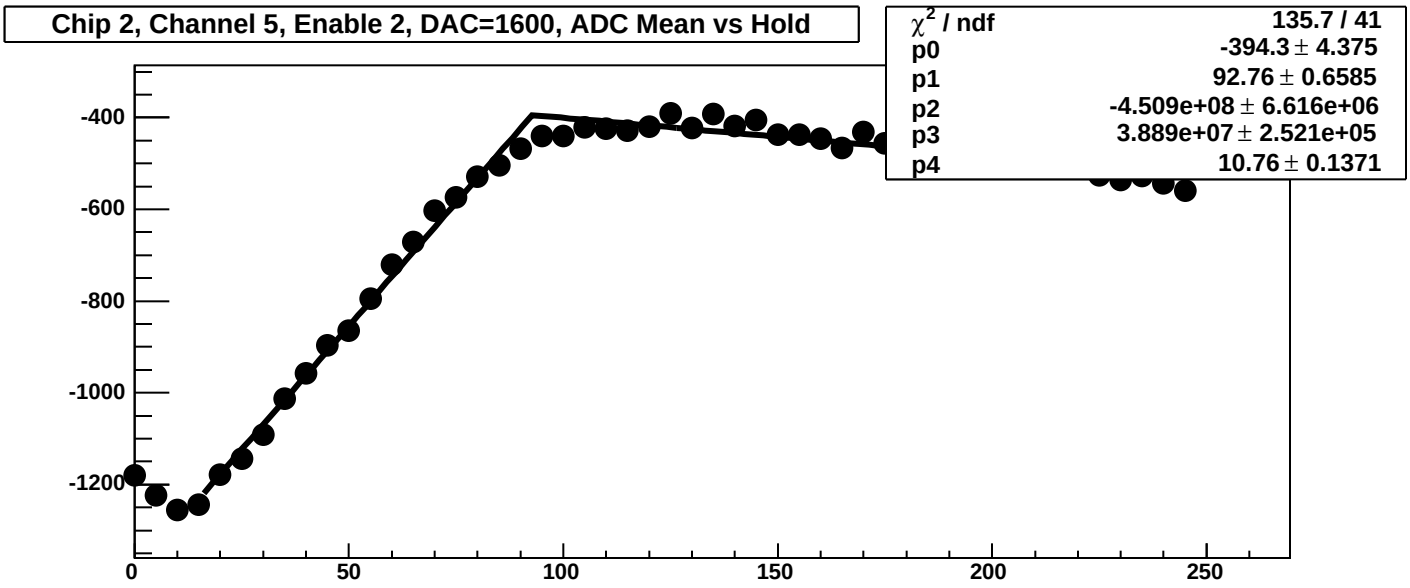
Chip 2, Channel 5, Enable 1!, DAC=1600, ADC Noise vs Hold



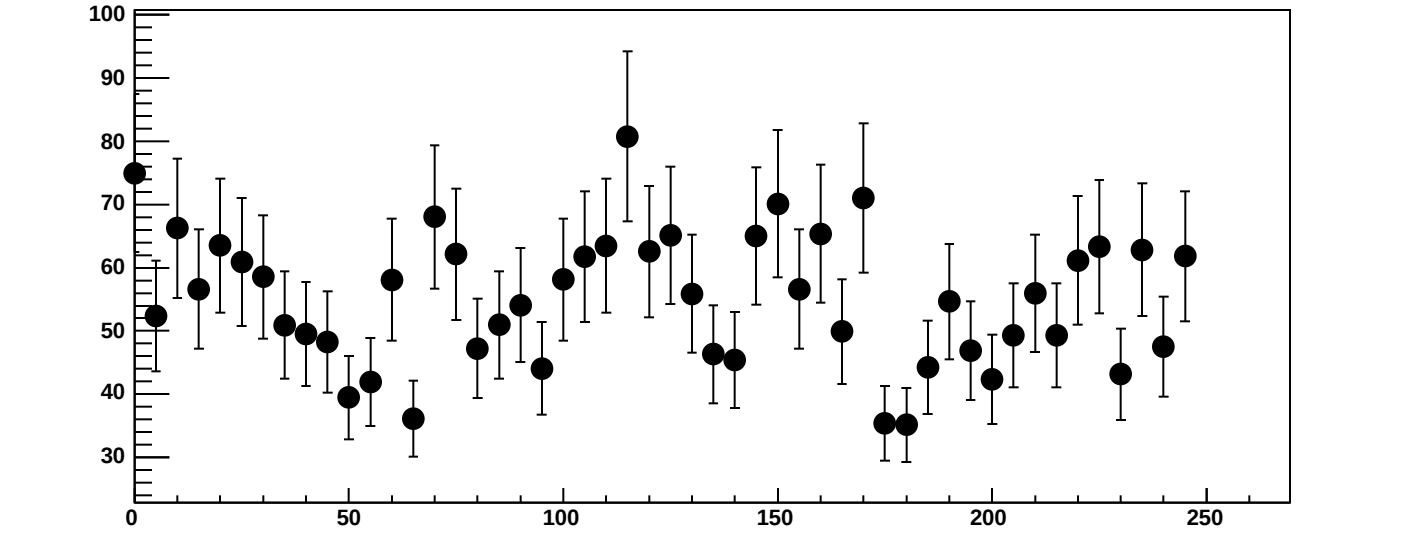
Chip 2, Channel 5, Enable 1!, DAC=1600, ADC Residuals vs Hold



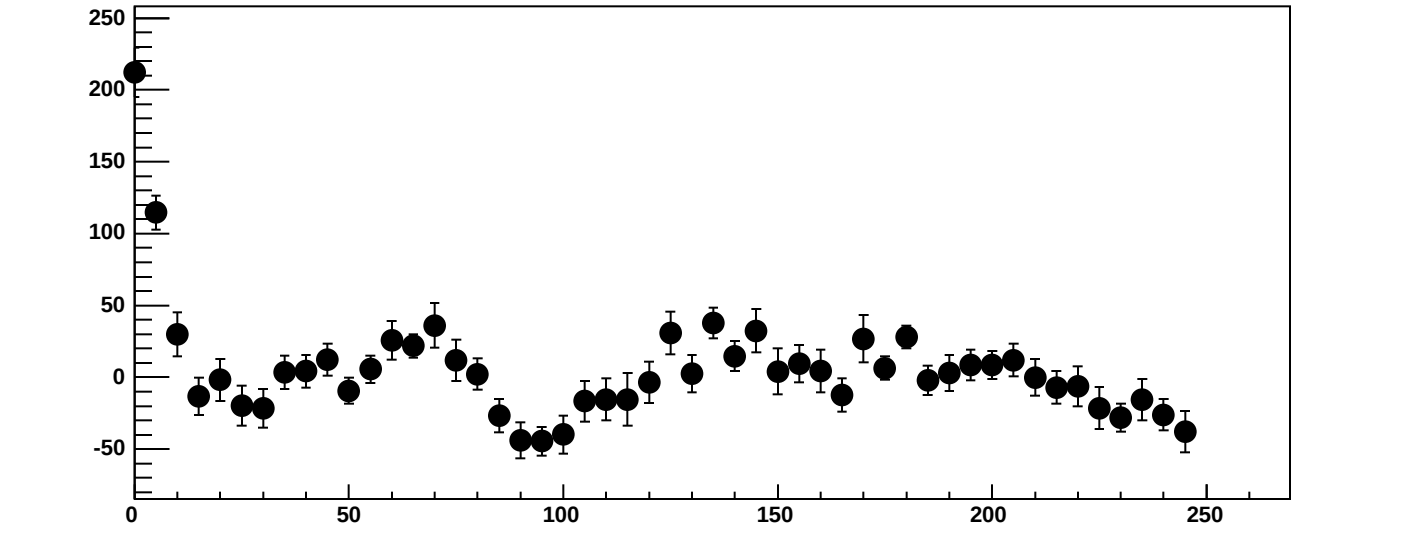
Chip 2, Channel 5, Enable 2, DAC=1600, ADC Mean vs Hold



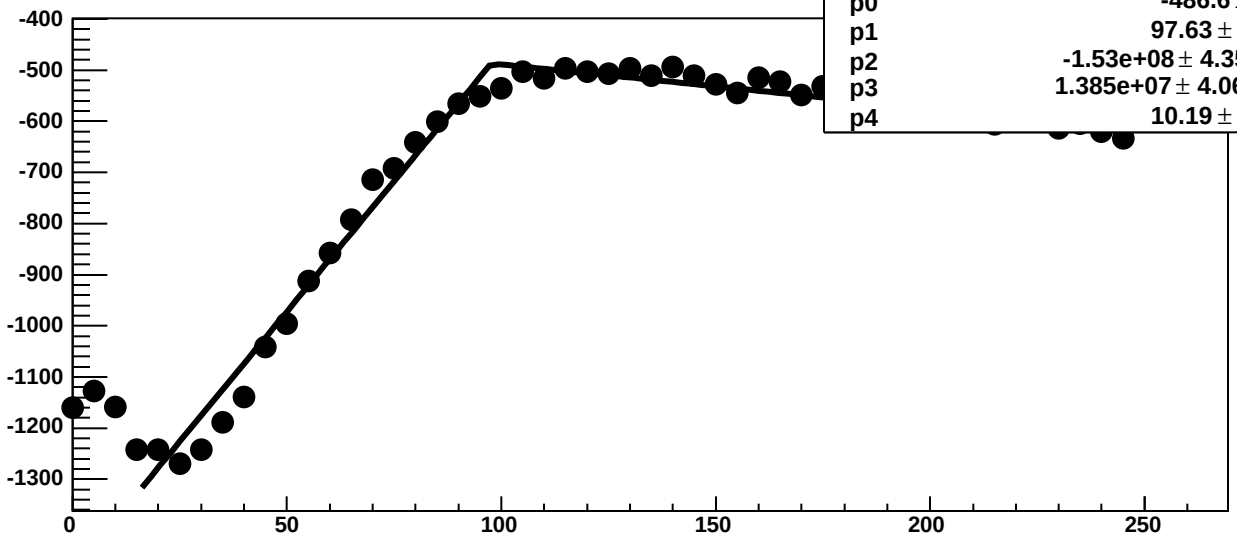
Chip 2, Channel 5, Enable 2, DAC=1600, ADC Noise vs Hold



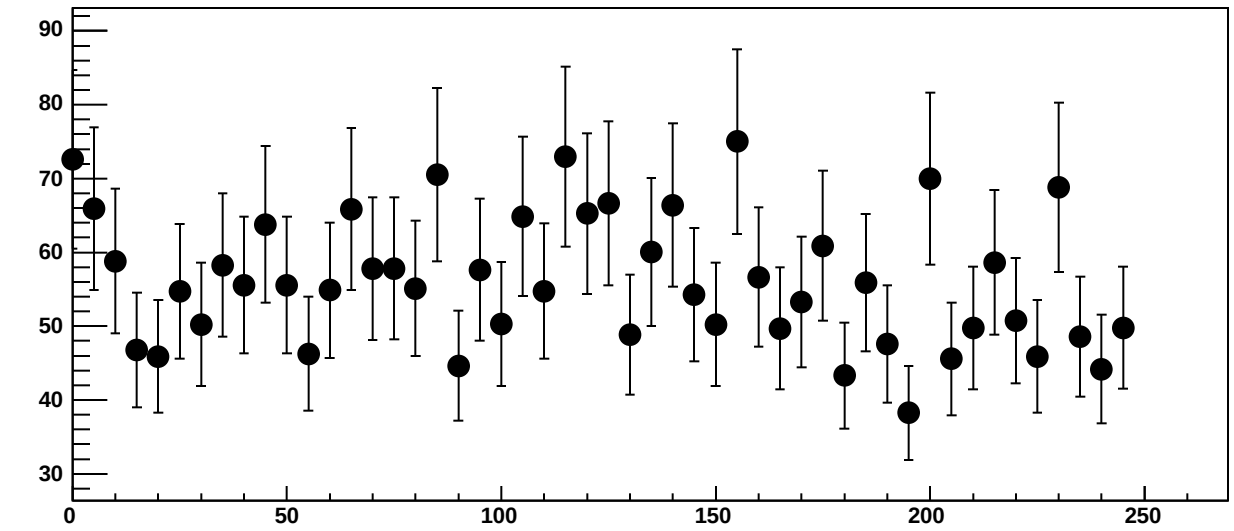
Chip 2, Channel 5, Enable 2, DAC=1600, ADC Residuals vs Hold



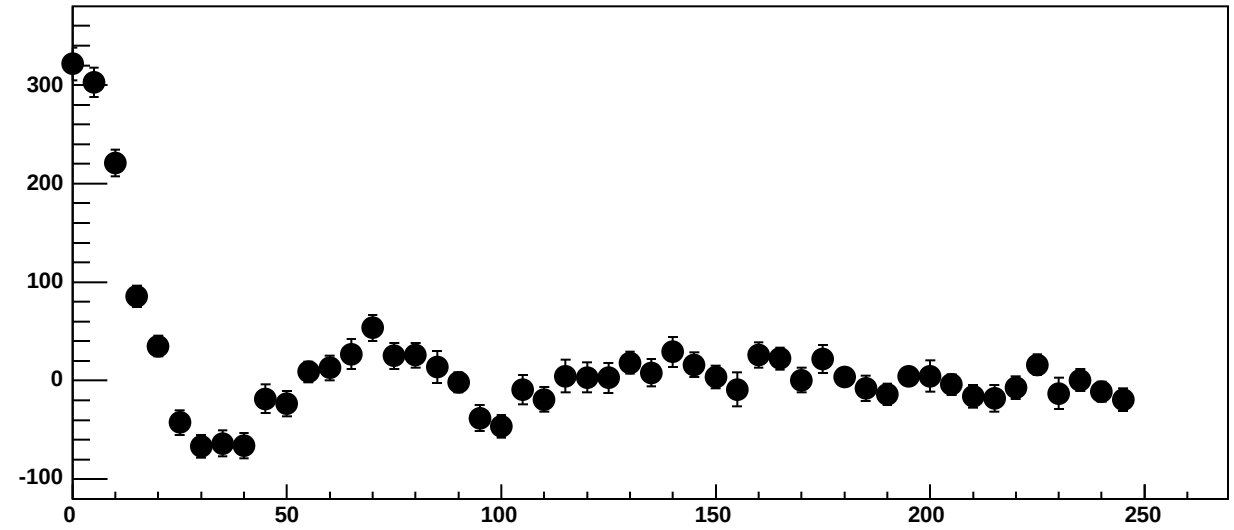
Chip 2, Channel 5, Enable 3, DAC=1600, ADC Mean vs Hold



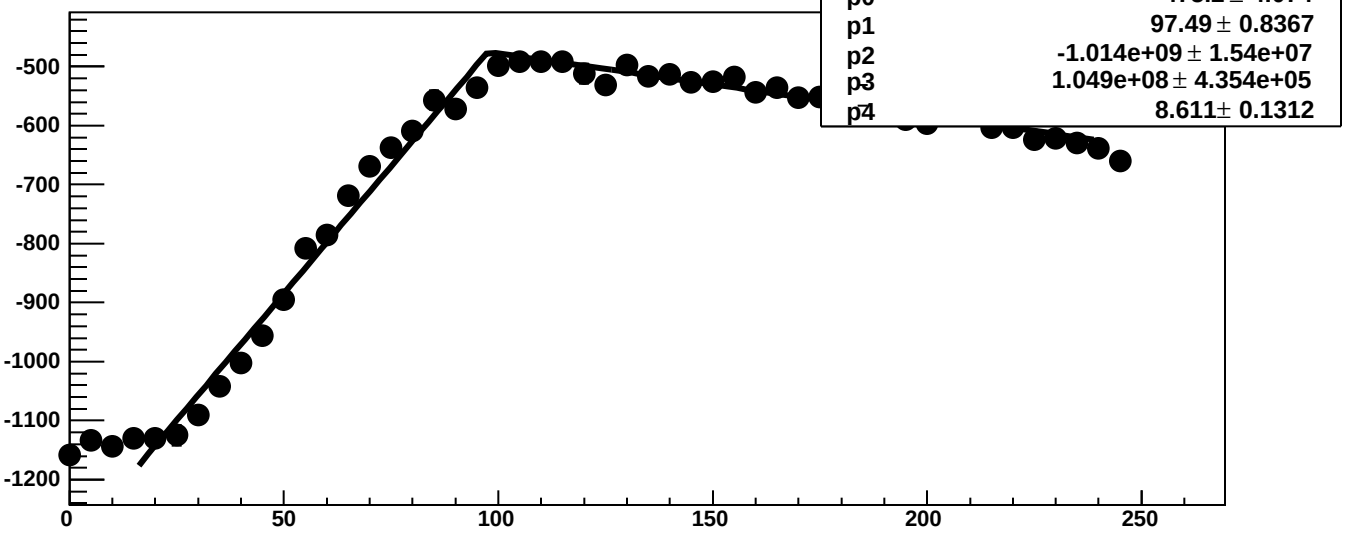
Chip 2, Channel 5, Enable 3, DAC=1600, ADC Noise vs Hold



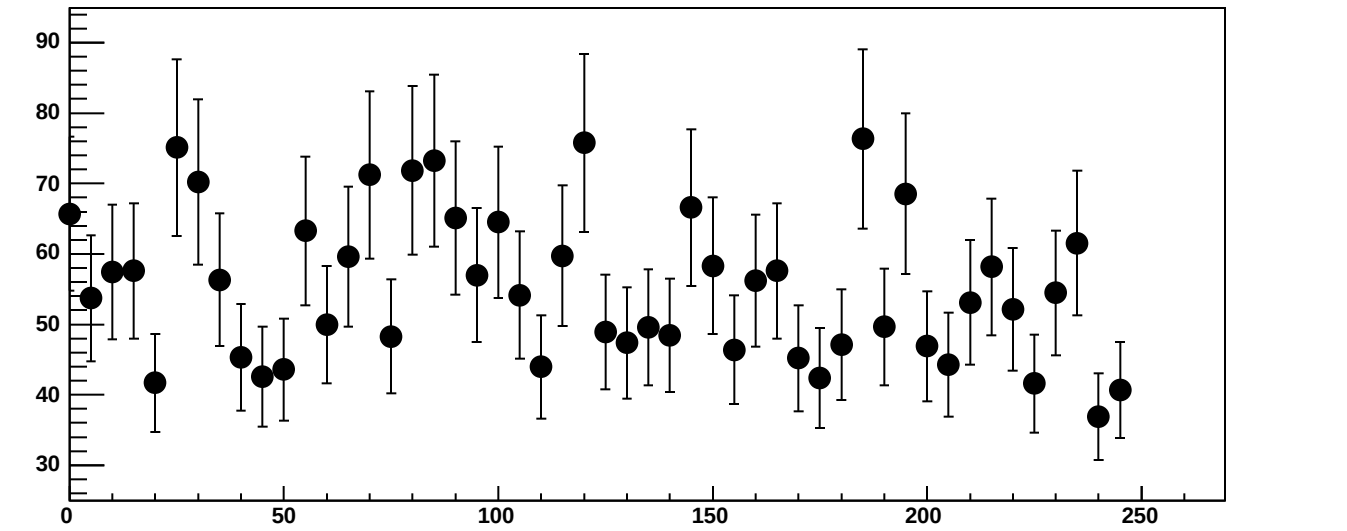
Chip 2, Channel 5, Enable 3, DAC=1600, ADC Residuals vs Hold



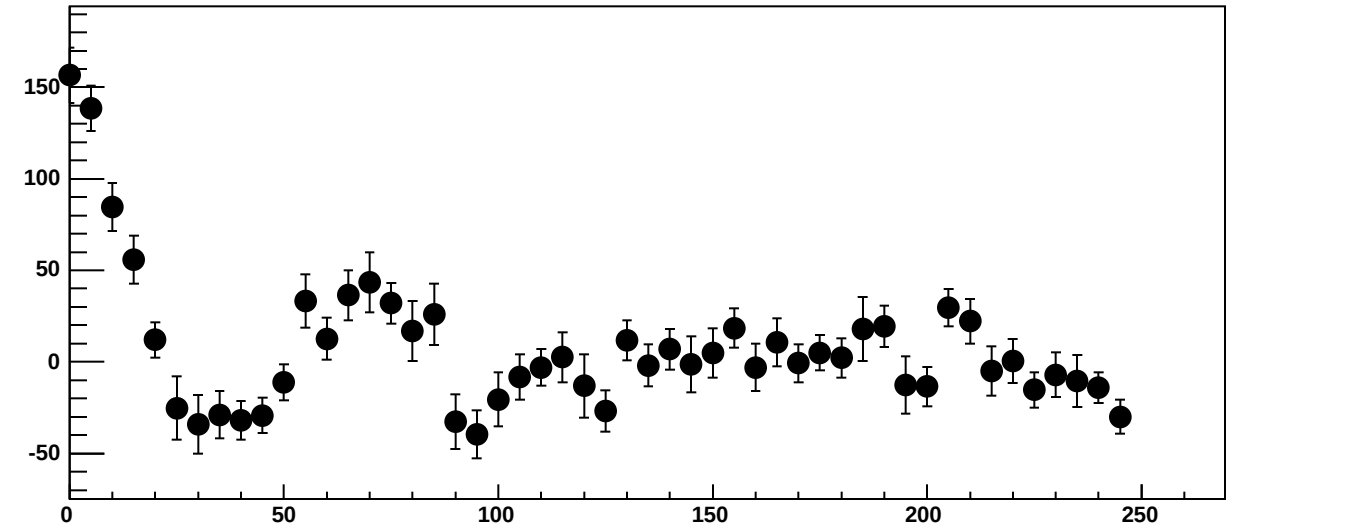
Chip 2, Channel 5, Enable 4, DAC=1600, ADC Mean vs Hold

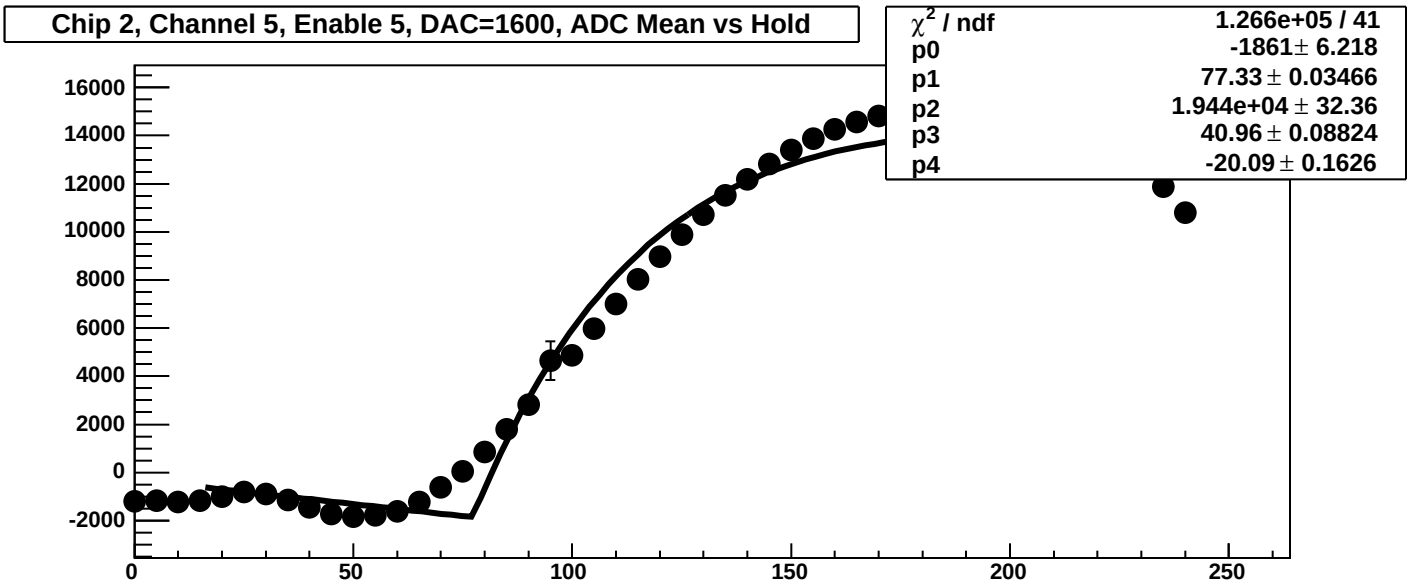


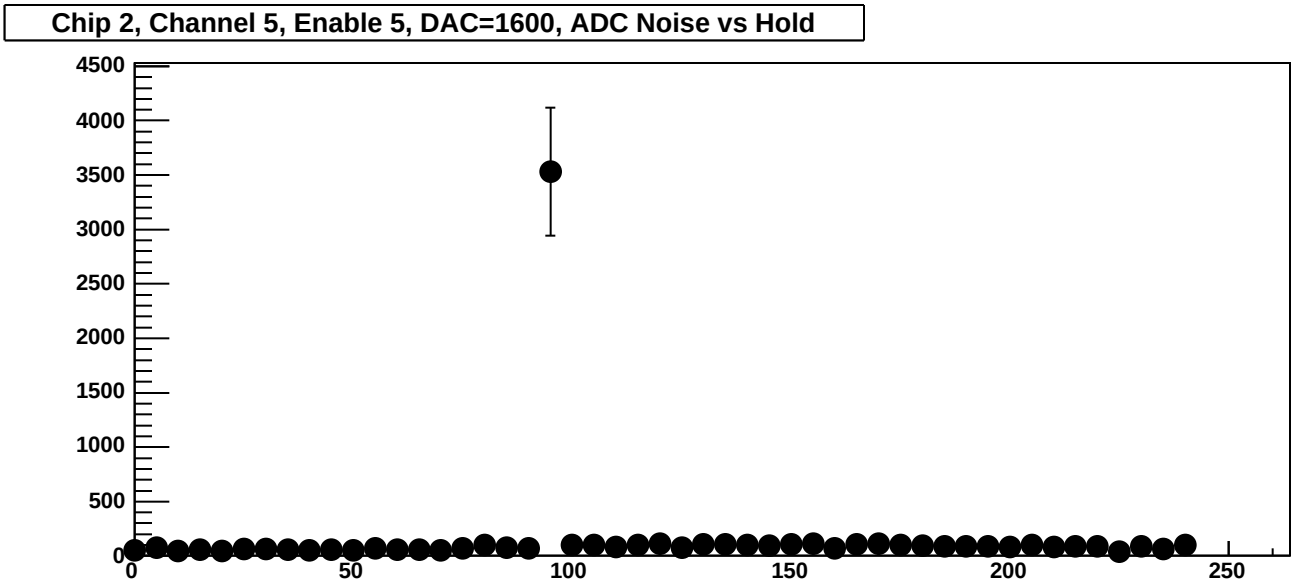
Chip 2, Channel 5, Enable 4, DAC=1600, ADC Noise vs Hold

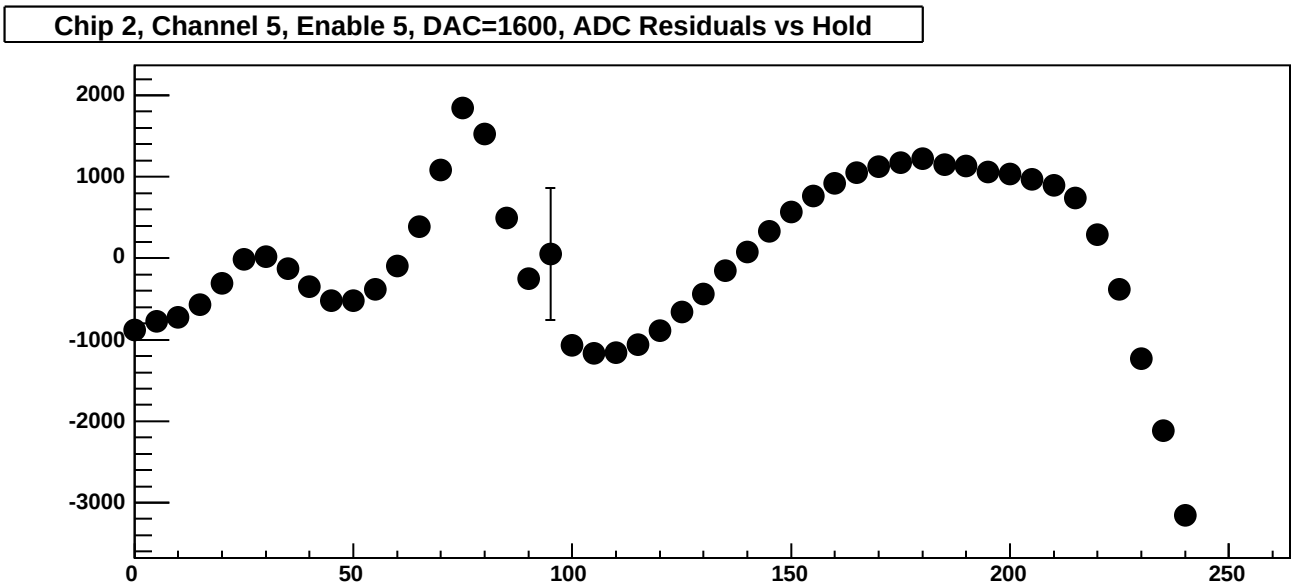


Chip 2, Channel 5, Enable 4, DAC=1600, ADC Residuals vs Hold

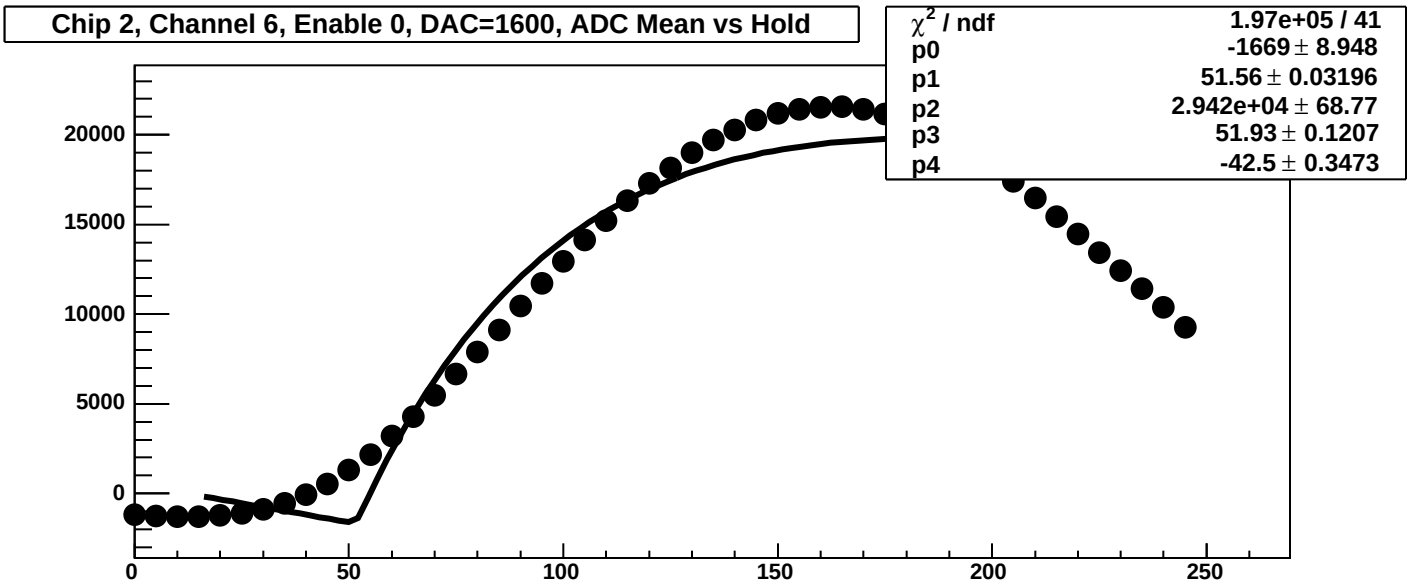



Chip 2, Channel 5, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.266e+05 / 41
p0 -1861 ± 6.218
p1 77.33 ± 0.03466
p2 1.944e+04 ± 32.36
p3 40.96 ± 0.08824
p4 -20.09 ± 0.1626
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

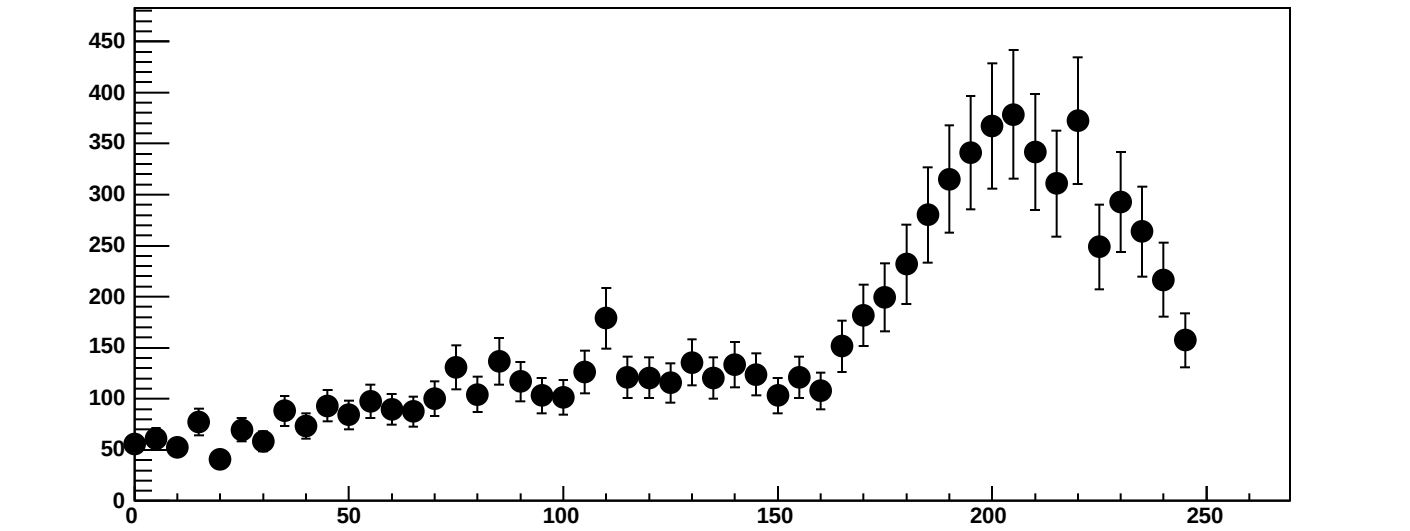

Chip 2, Channel 5, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 2, Channel 5, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

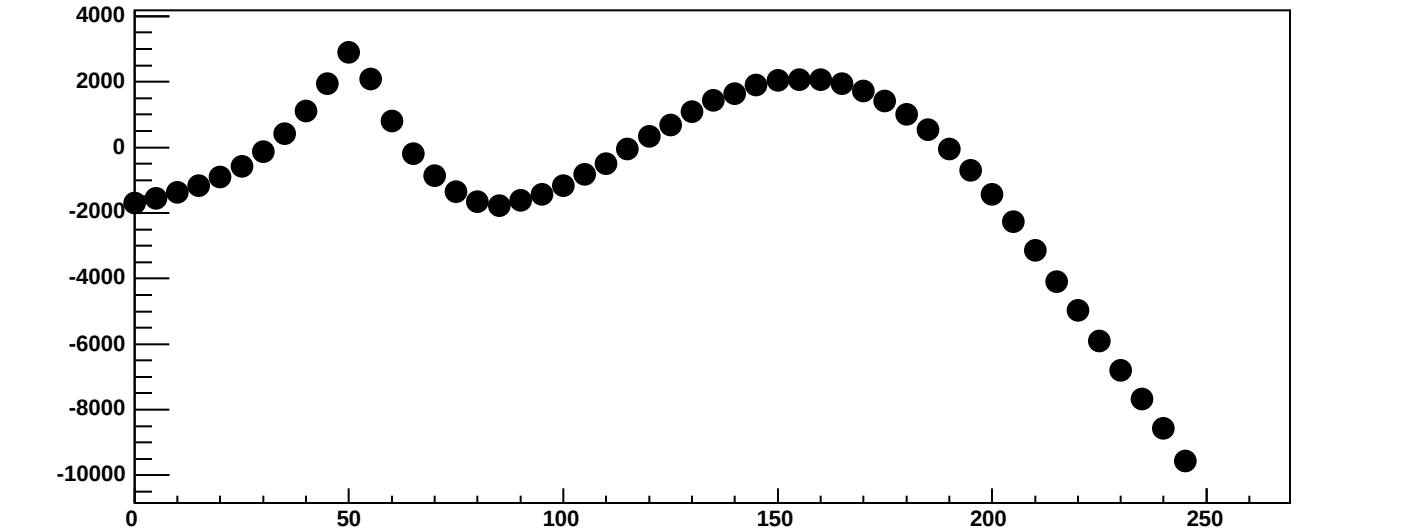
Chip 2, Channel 6, Enable 0, DAC=1600, ADC Mean vs Hold



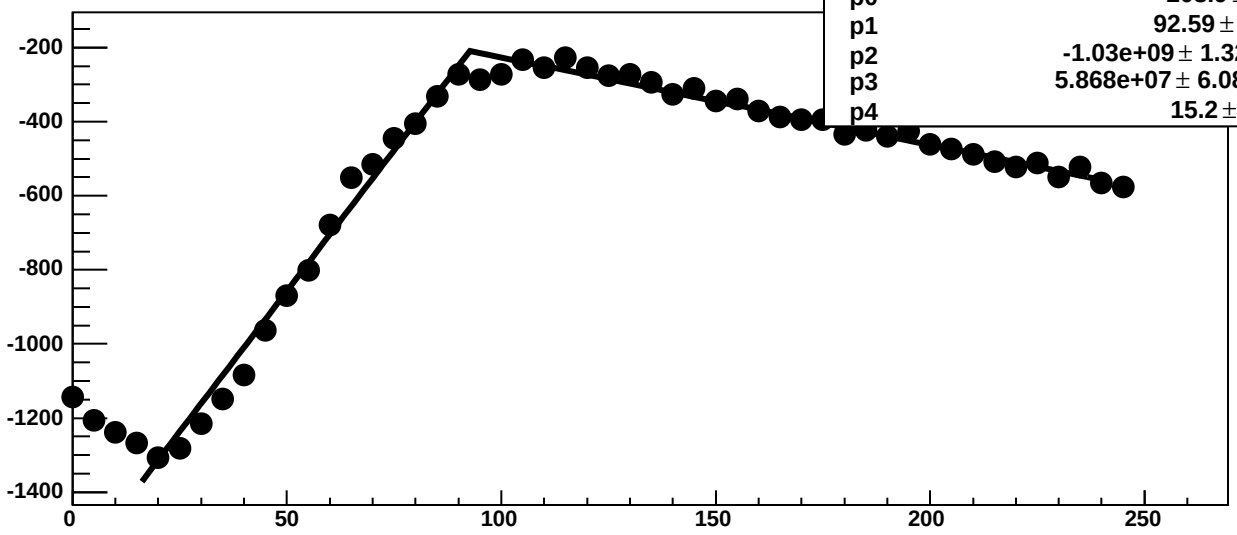
Chip 2, Channel 6, Enable 0, DAC=1600, ADC Noise vs Hold



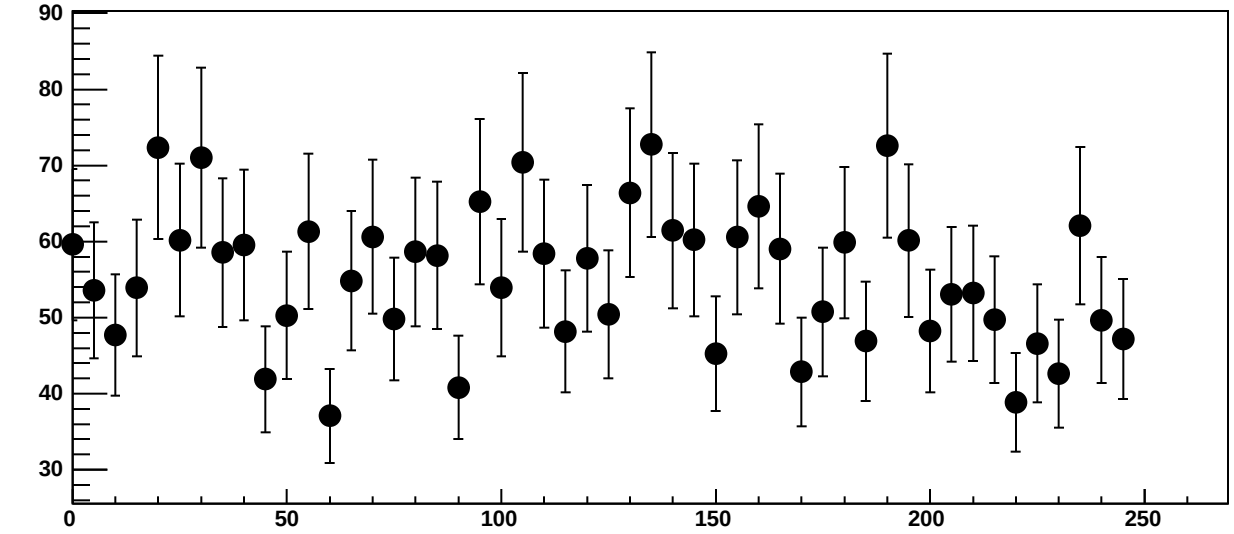
Chip 2, Channel 6, Enable 0, DAC=1600, ADC Residuals vs Hold



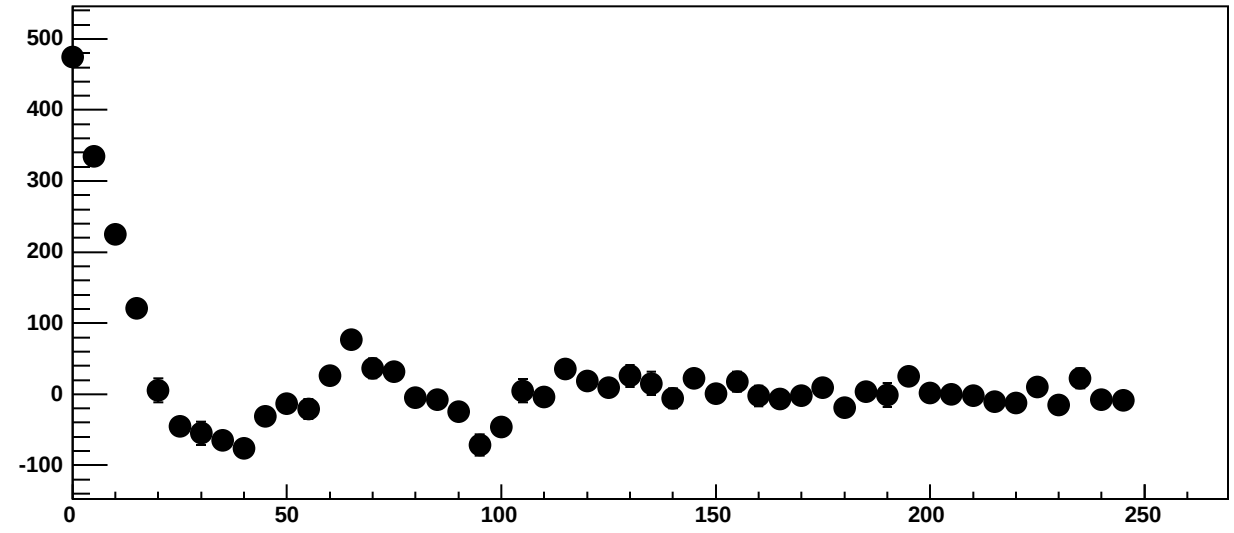
Chip 2, Channel 6, Enable 1, DAC=1600, ADC Mean vs Hold



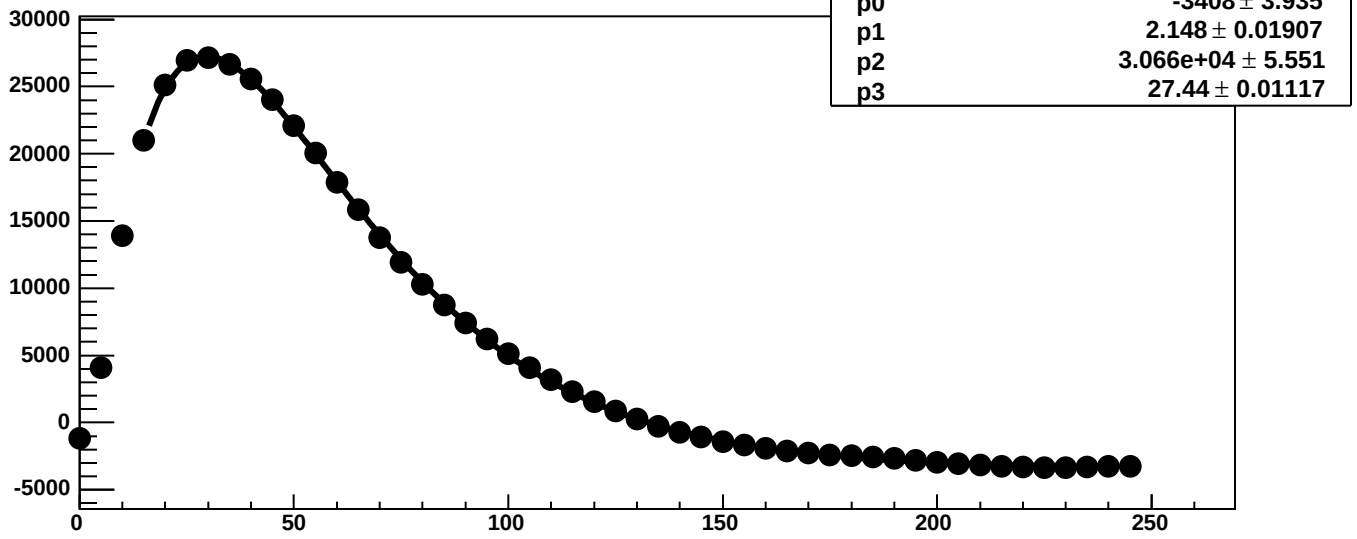
Chip 2, Channel 6, Enable 1, DAC=1600, ADC Noise vs Hold



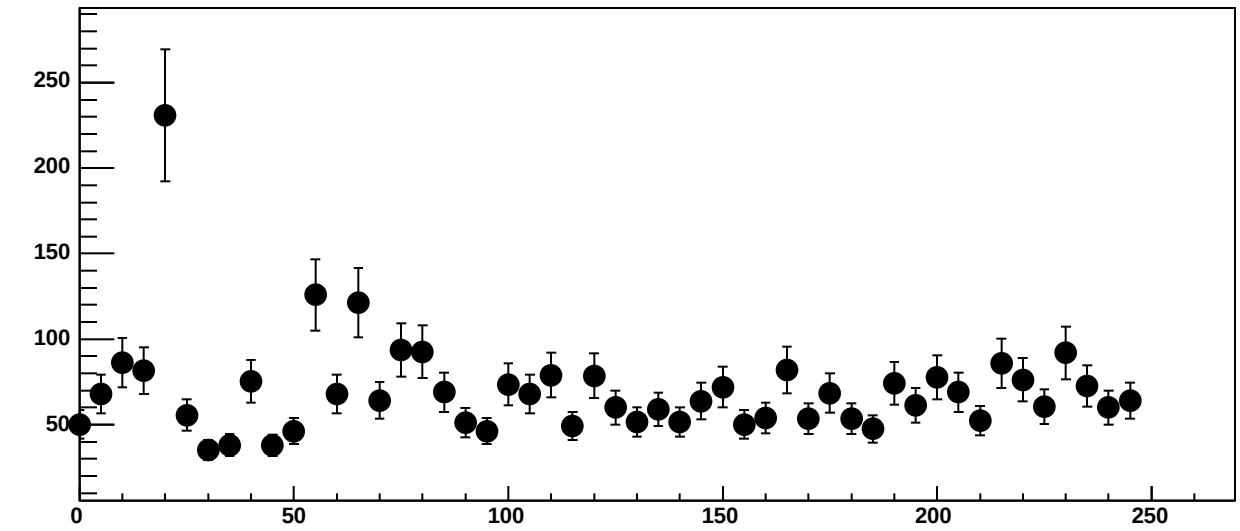
Chip 2, Channel 6, Enable 1, DAC=1600, ADC Residuals vs Hold



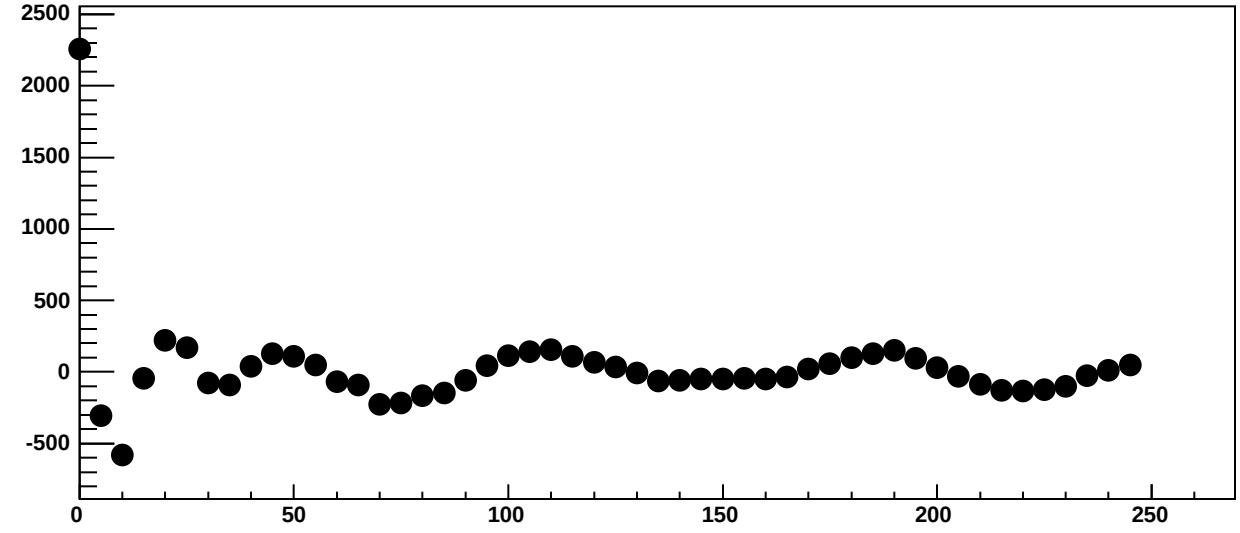
Chip 2, Channel 6, Enable 2!, DAC=1600, ADC Mean vs Hold



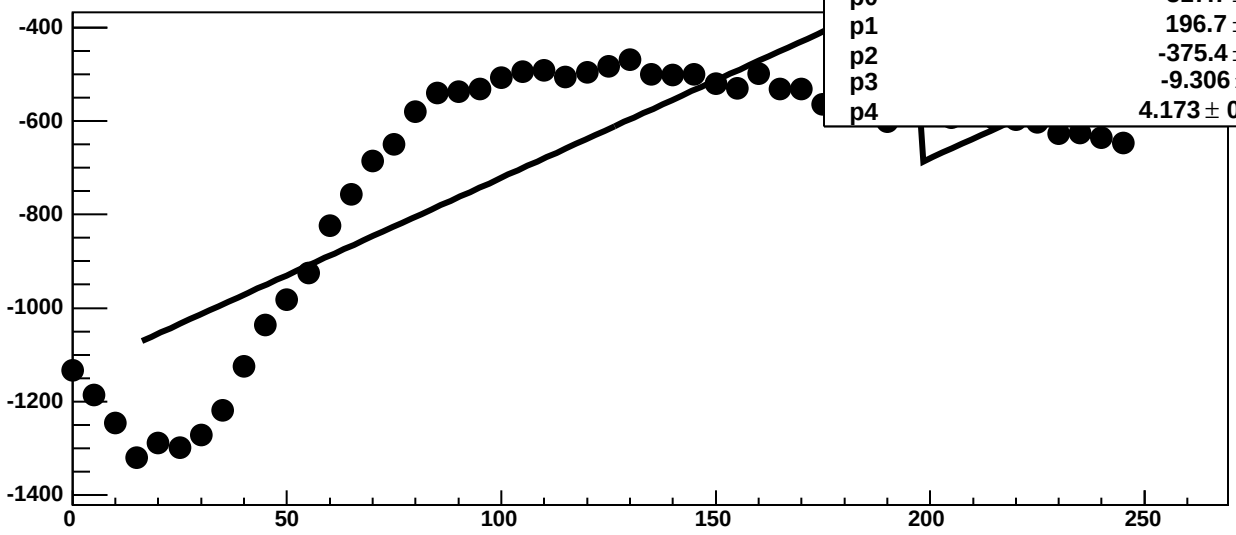
Chip 2, Channel 6, Enable 2!, DAC=1600, ADC Noise vs Hold



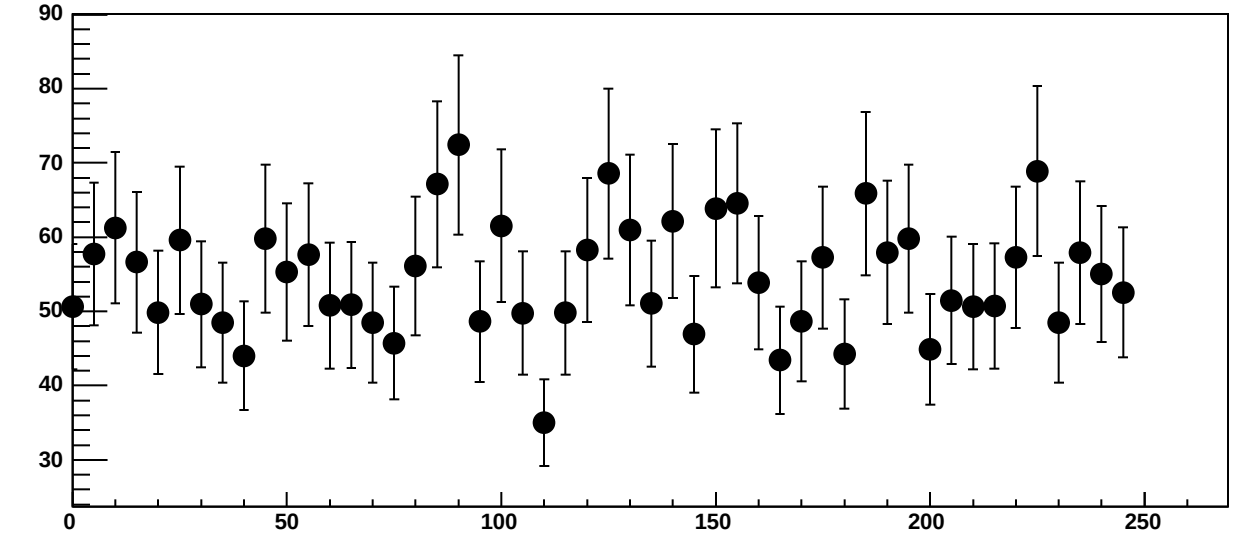
Chip 2, Channel 6, Enable 2!, DAC=1600, ADC Residuals vs Hold



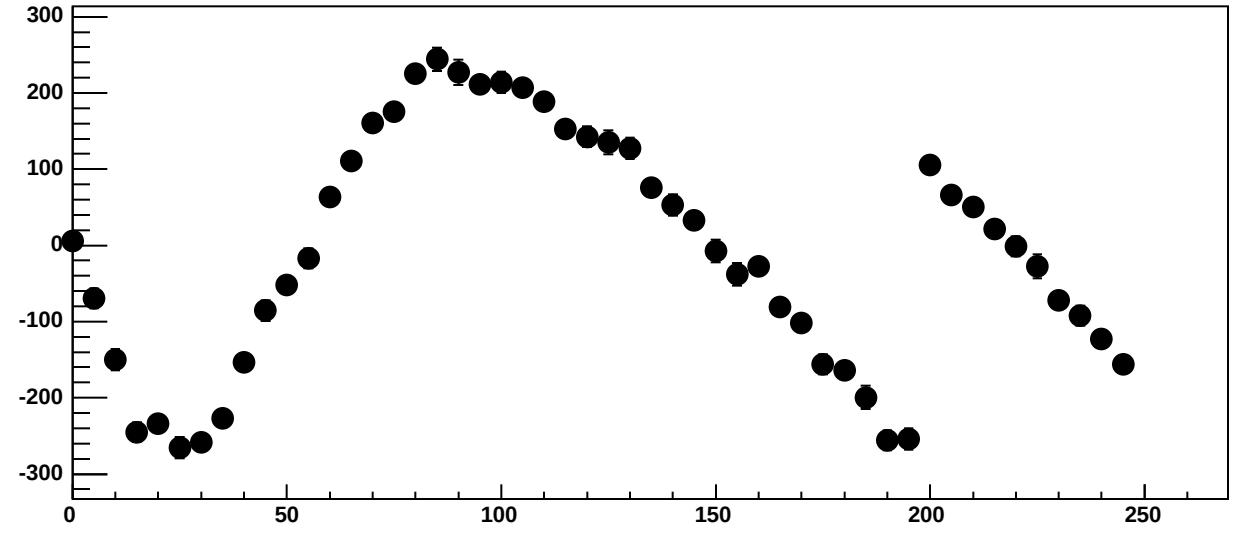
Chip 2, Channel 6, Enable 3, DAC=1600, ADC Mean vs Hold



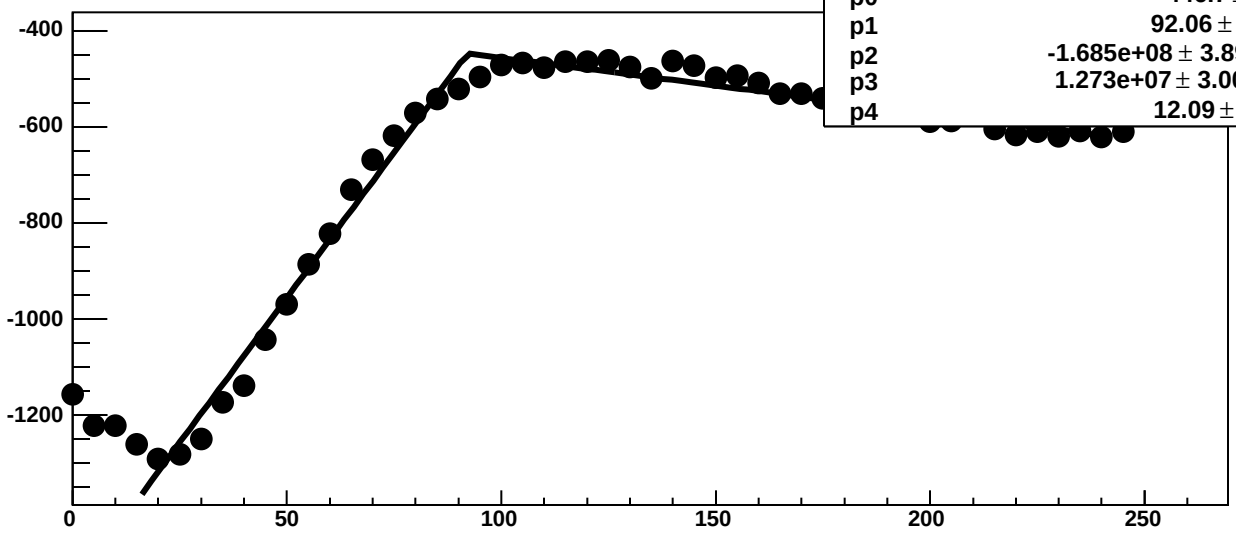
Chip 2, Channel 6, Enable 3, DAC=1600, ADC Noise vs Hold



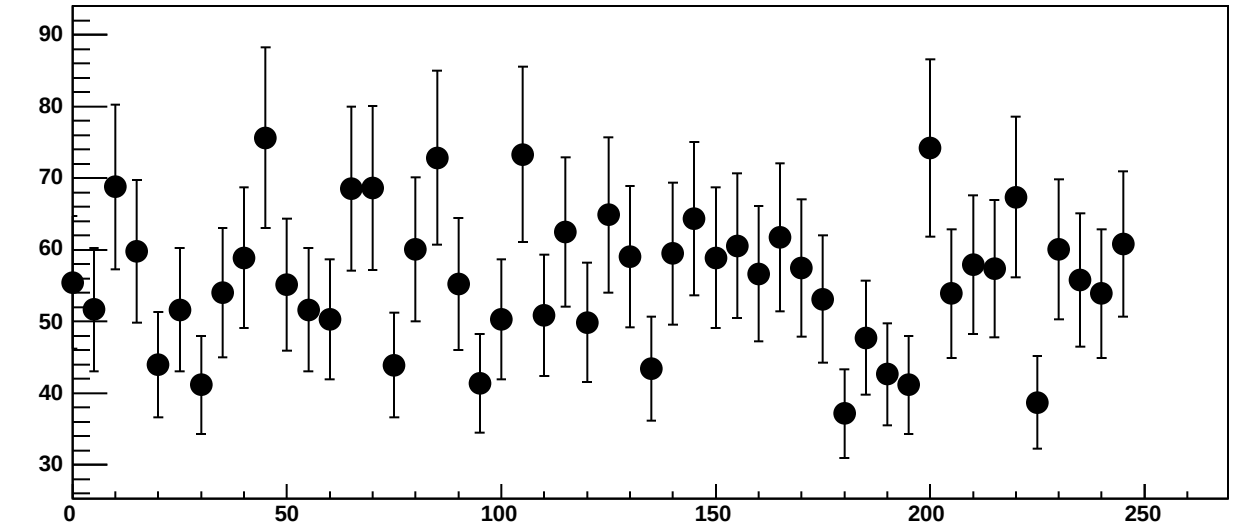
Chip 2, Channel 6, Enable 3, DAC=1600, ADC Residuals vs Hold



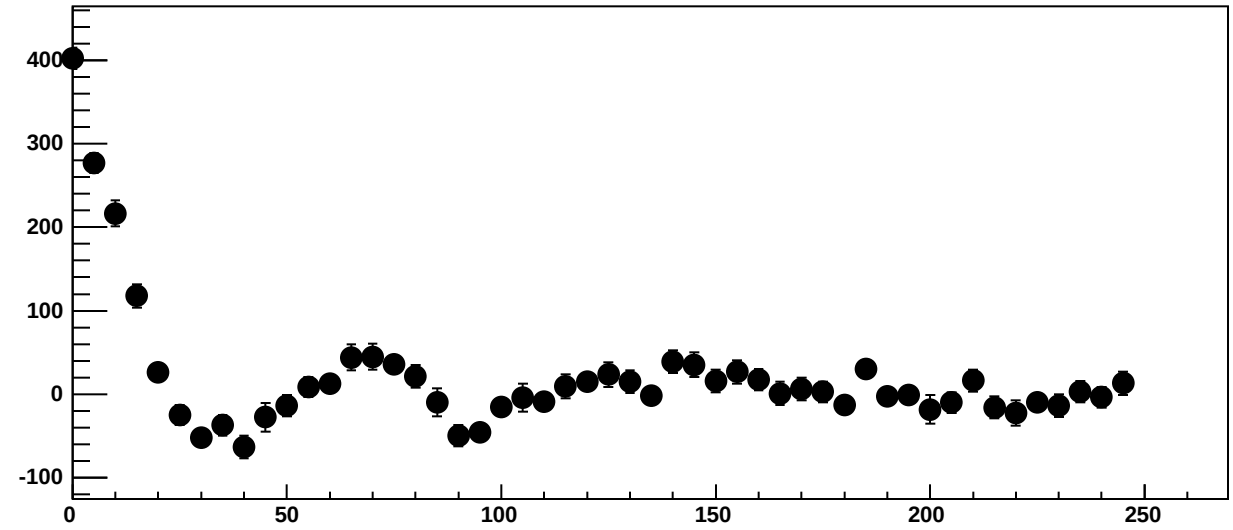
Chip 2, Channel 6, Enable 4, DAC=1600, ADC Mean vs Hold



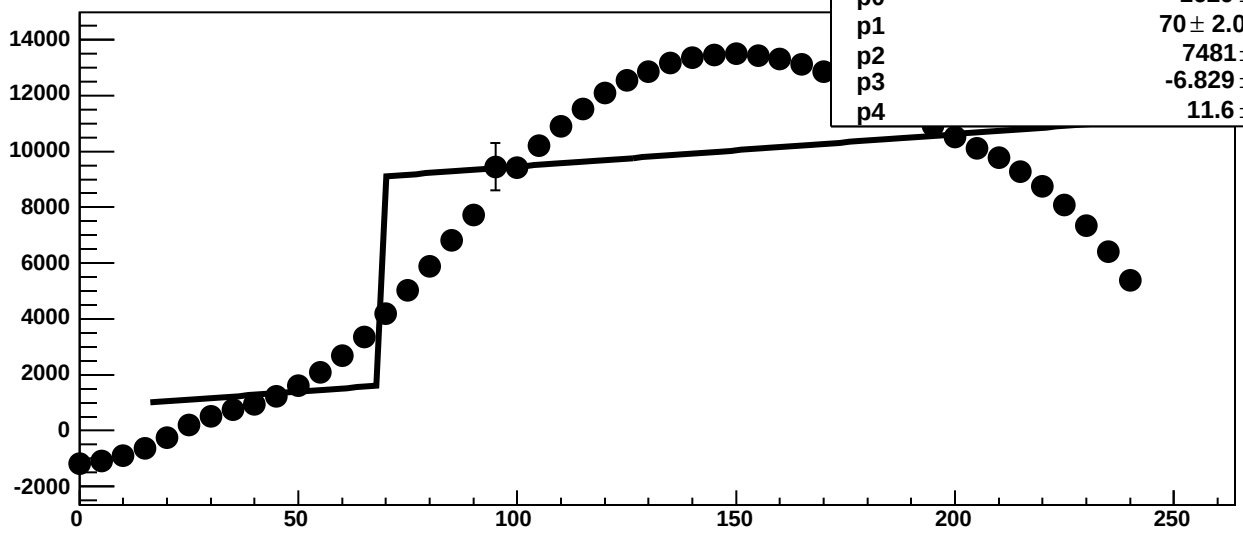
Chip 2, Channel 6, Enable 4, DAC=1600, ADC Noise vs Hold



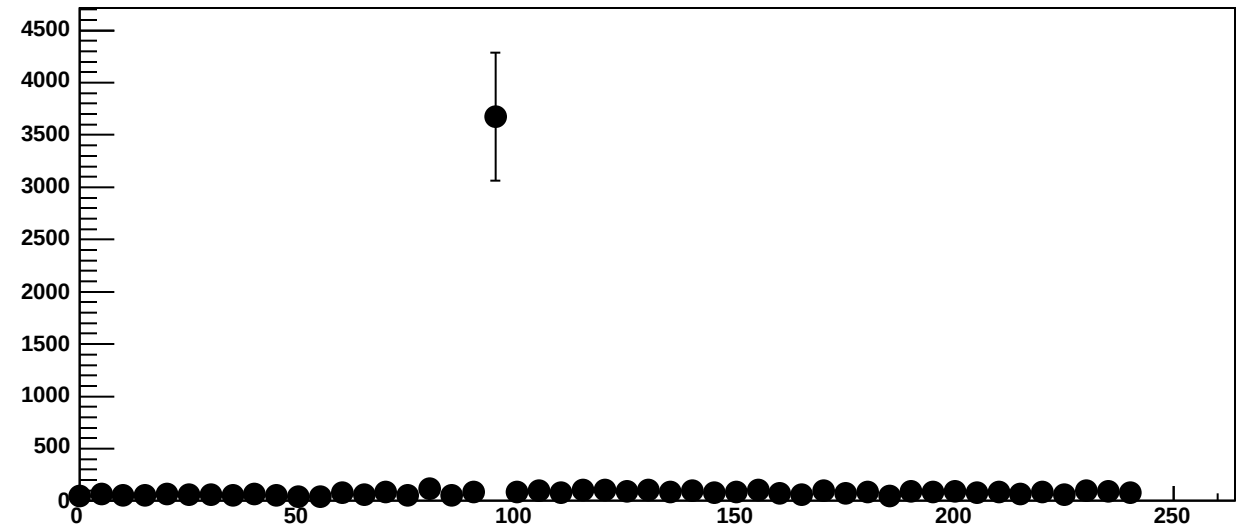
Chip 2, Channel 6, Enable 4, DAC=1600, ADC Residuals vs Hold



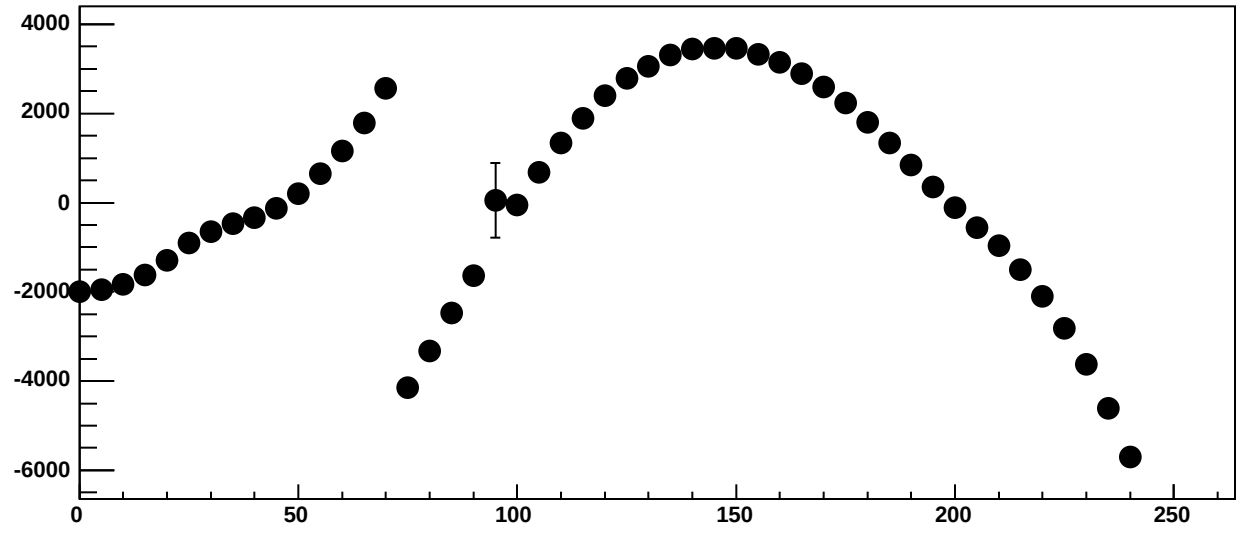
Chip 2, Channel 6, Enable 5, DAC=1600, ADC Mean vs Hold

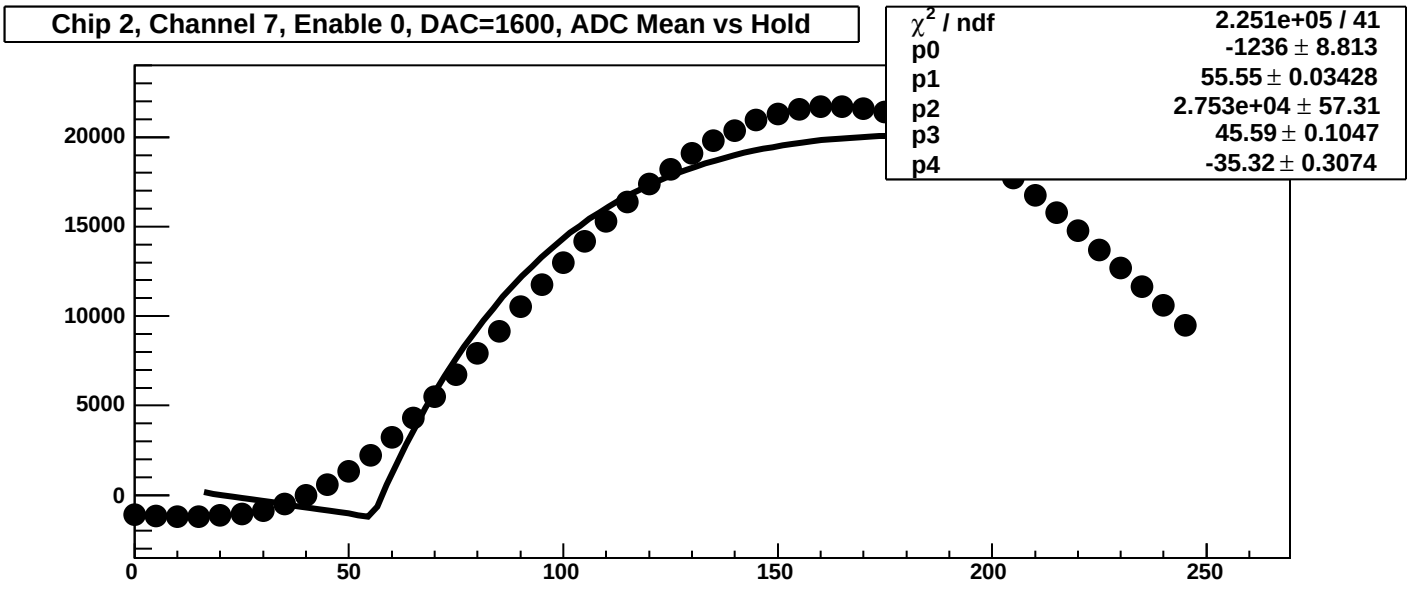


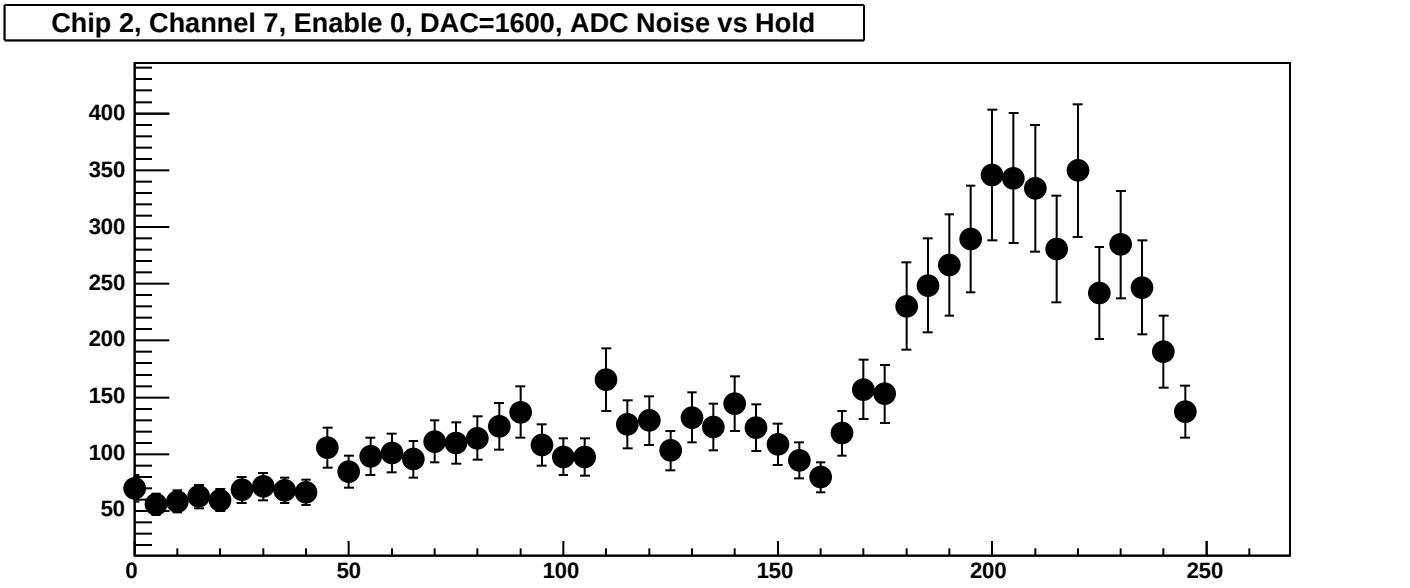
Chip 2, Channel 6, Enable 5, DAC=1600, ADC Noise vs Hold

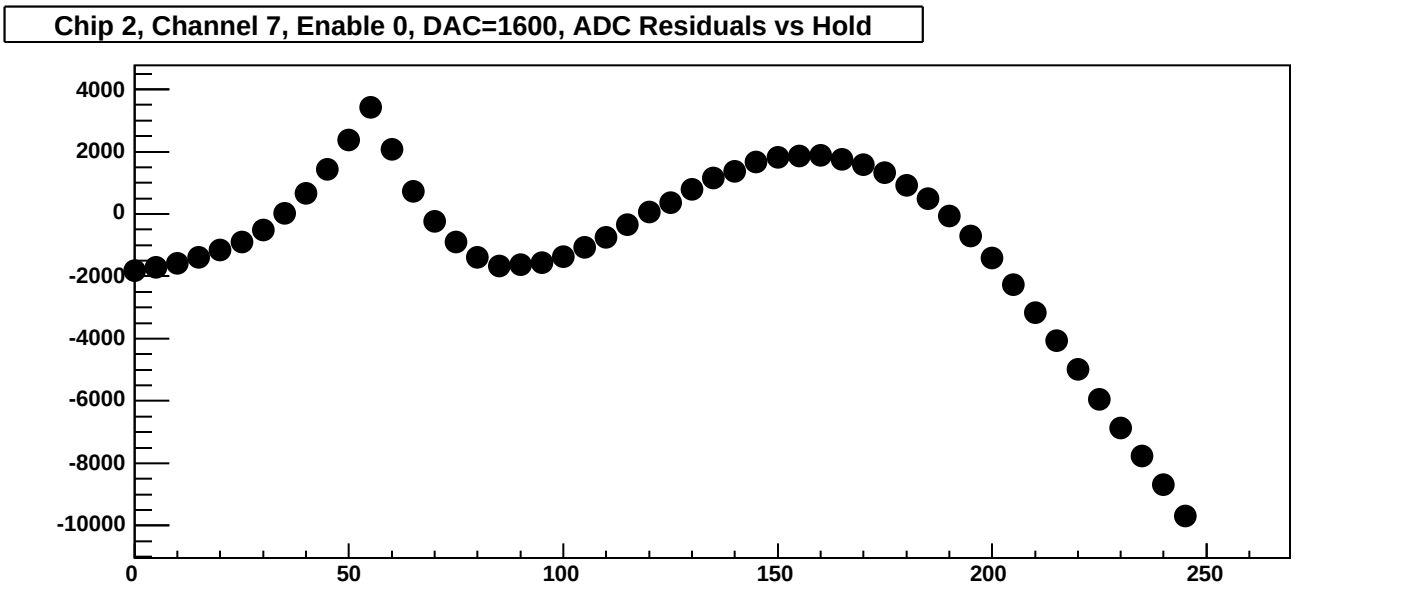


Chip 2, Channel 6, Enable 5, DAC=1600, ADC Residuals vs Hold

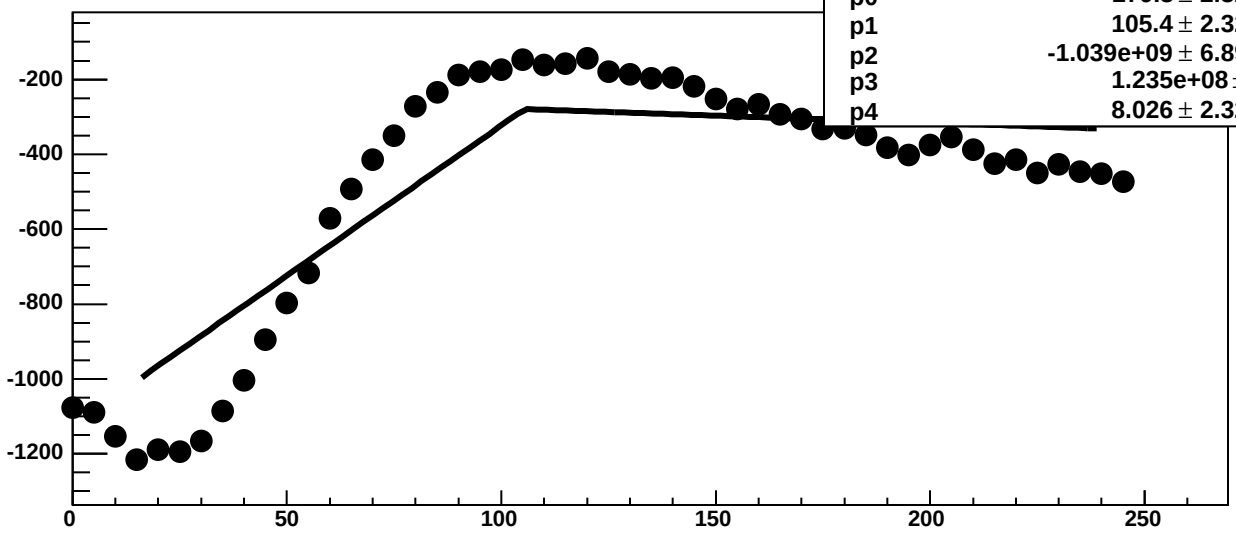



Chip 2, Channel 7, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.251e+05 / 41
p0 -1236 ± 8.813
p1 55.55 ± 0.03428
p2 2.753e+04 ± 57.31
p3 45.59 ± 0.1047
p4 -35.32 ± 0.3074
20000
15000
10000
5000
0
0
50
100
150
200
250

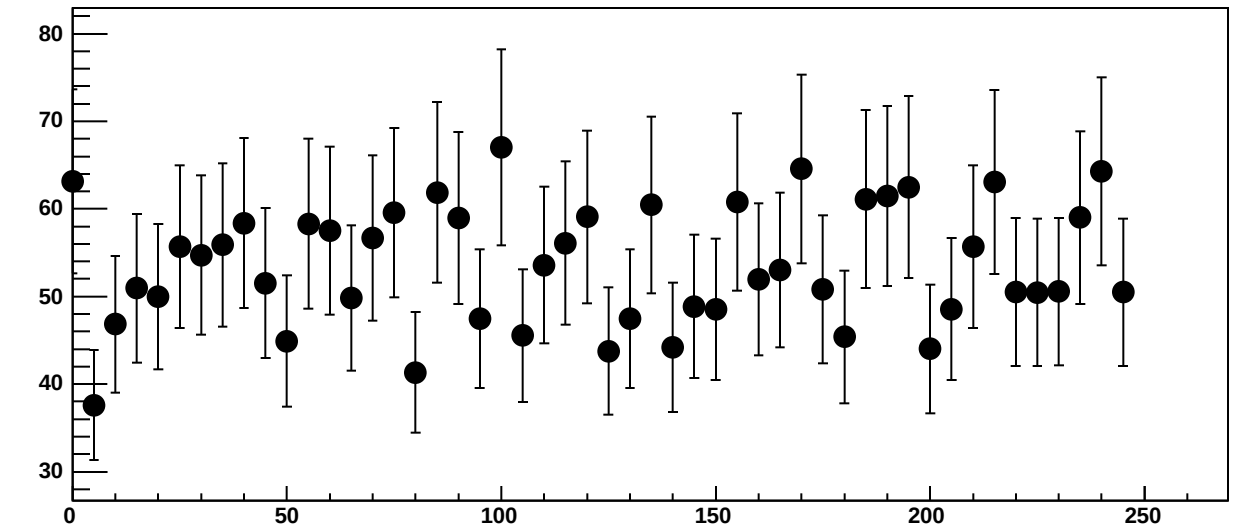

Chip 2, Channel 7, Enable 0, DAC=1600, ADC Noise vs Hold
400
350
300
250
200
150
100
50
0
50
100
150
200
250


Chip 2, Channel 7, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

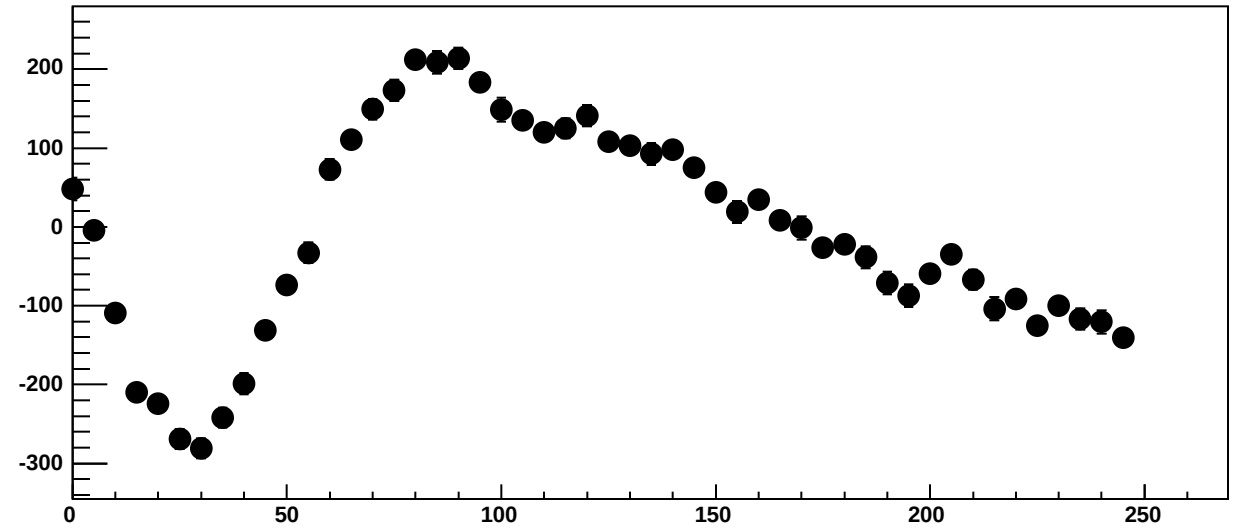
Chip 2, Channel 7, Enable 1, DAC=1600, ADC Mean vs Hold



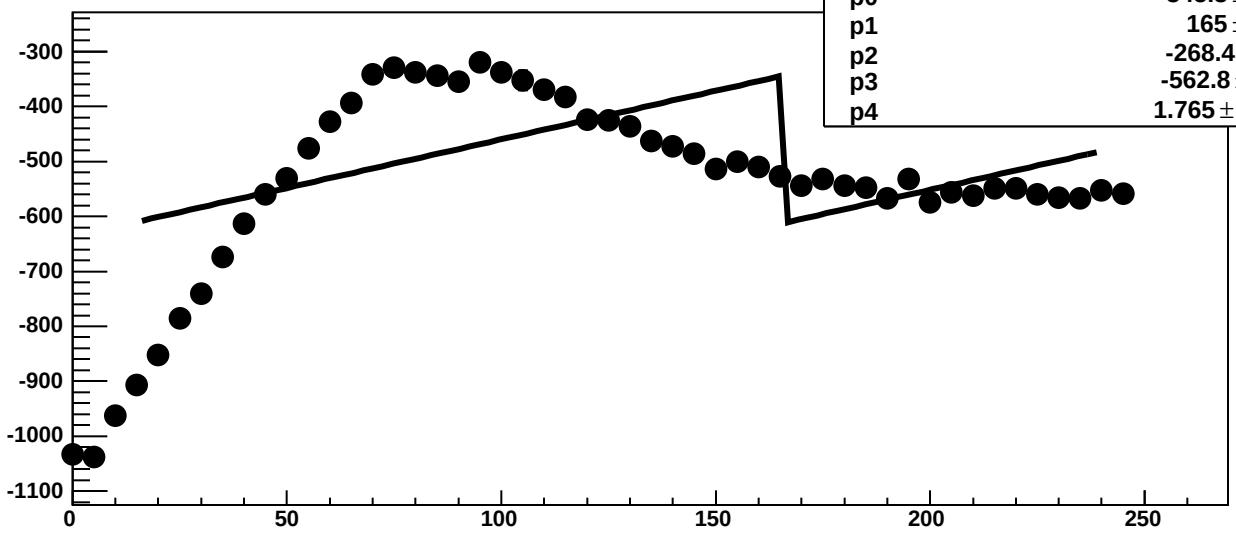
Chip 2, Channel 7, Enable 1, DAC=1600, ADC Noise vs Hold



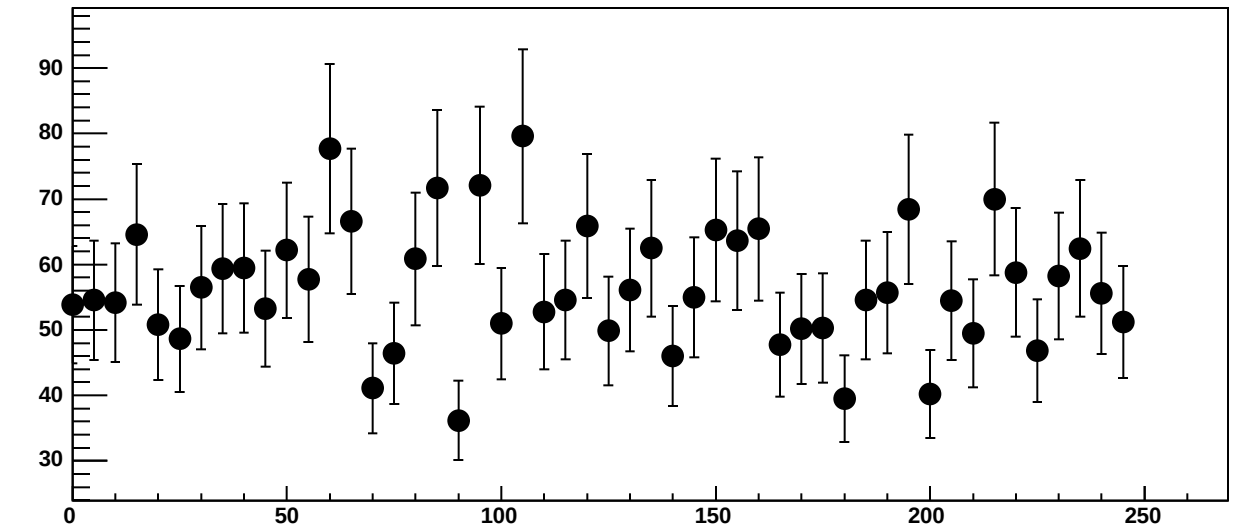
Chip 2, Channel 7, Enable 1, DAC=1600, ADC Residuals vs Hold



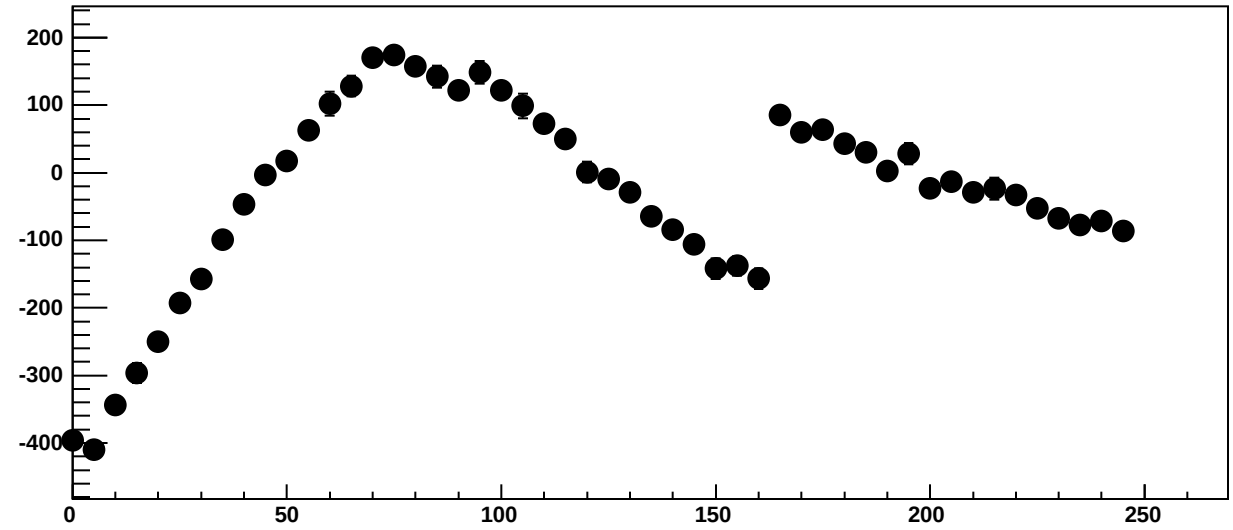
Chip 2, Channel 7, Enable 2, DAC=1600, ADC Mean vs Hold



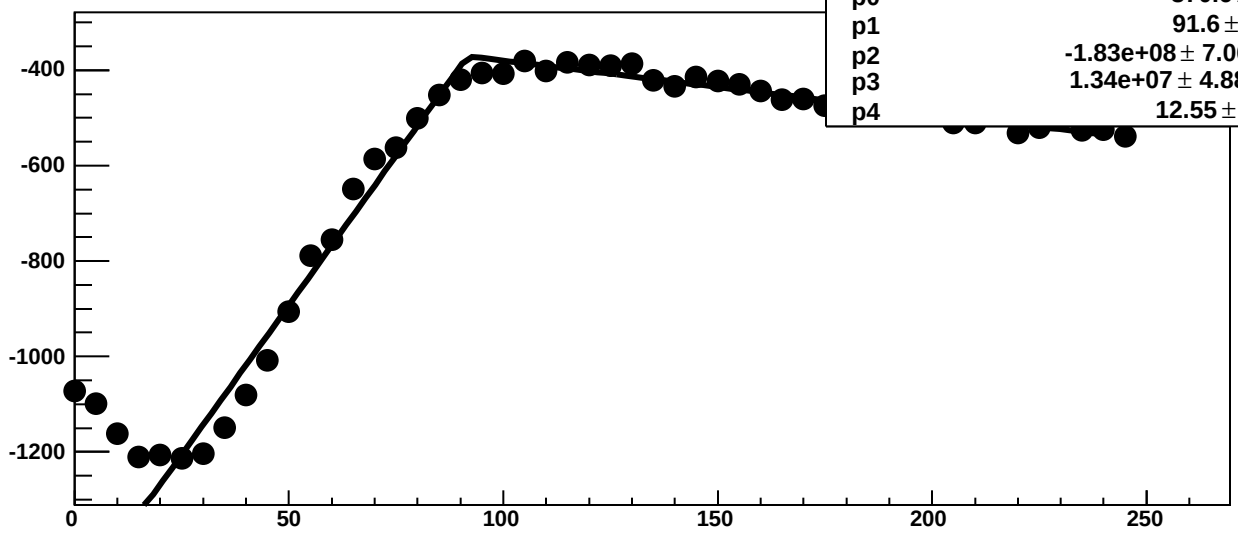
Chip 2, Channel 7, Enable 2, DAC=1600, ADC Noise vs Hold



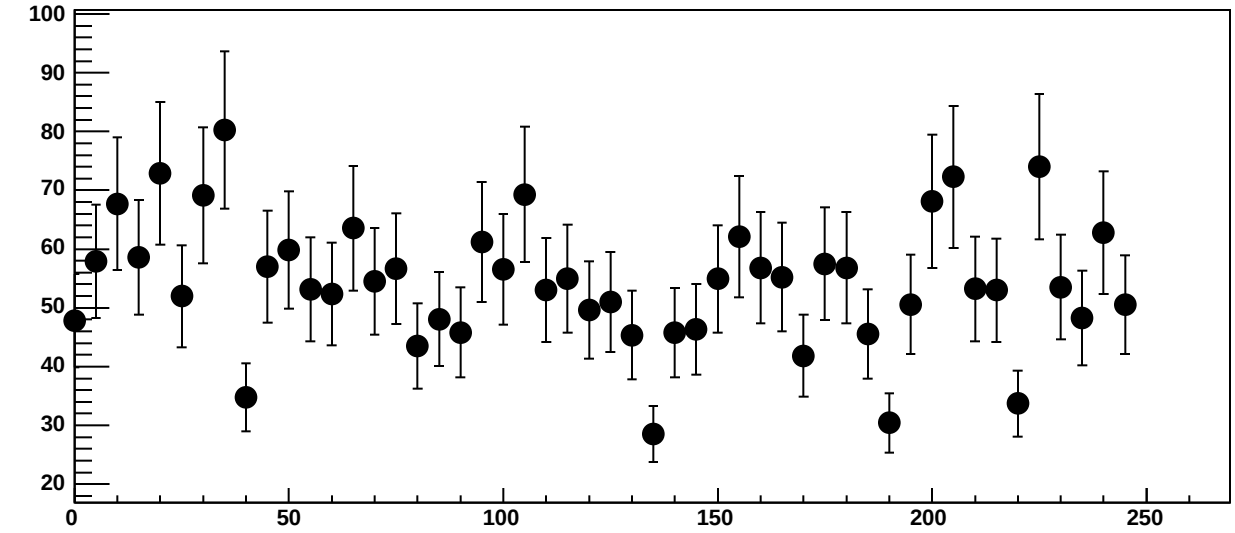
Chip 2, Channel 7, Enable 2, DAC=1600, ADC Residuals vs Hold



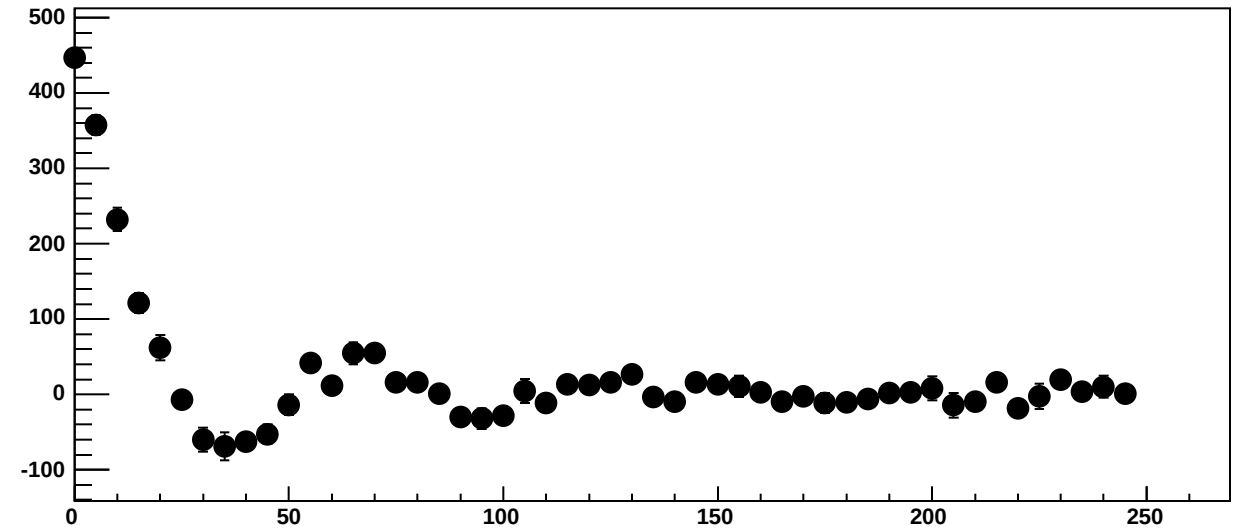
Chip 2, Channel 7, Enable 3, DAC=1600, ADC Mean vs Hold



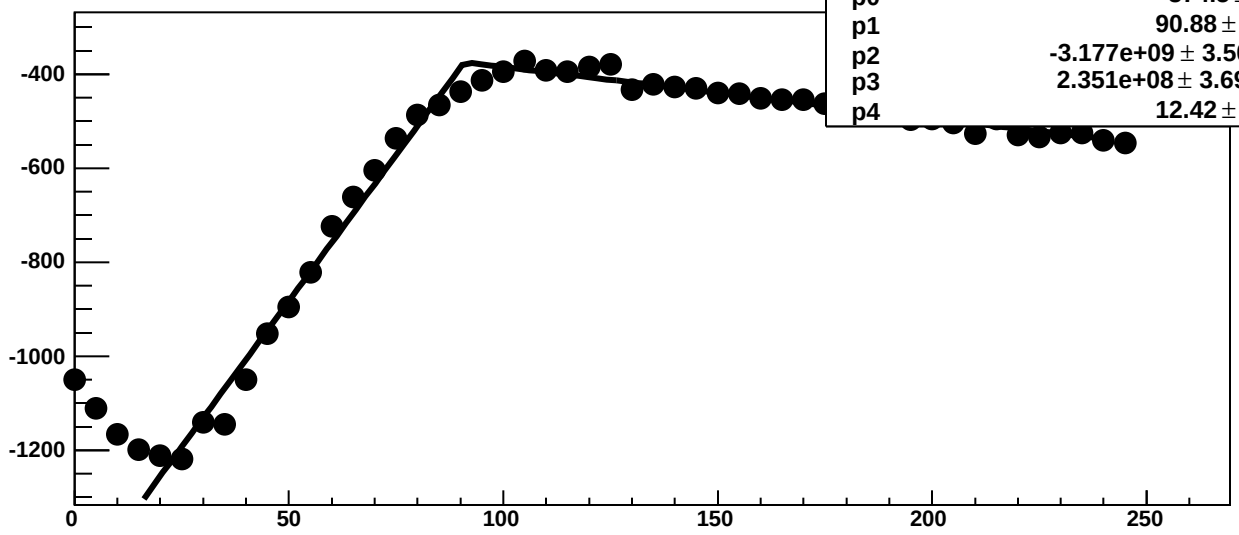
Chip 2, Channel 7, Enable 3, DAC=1600, ADC Noise vs Hold



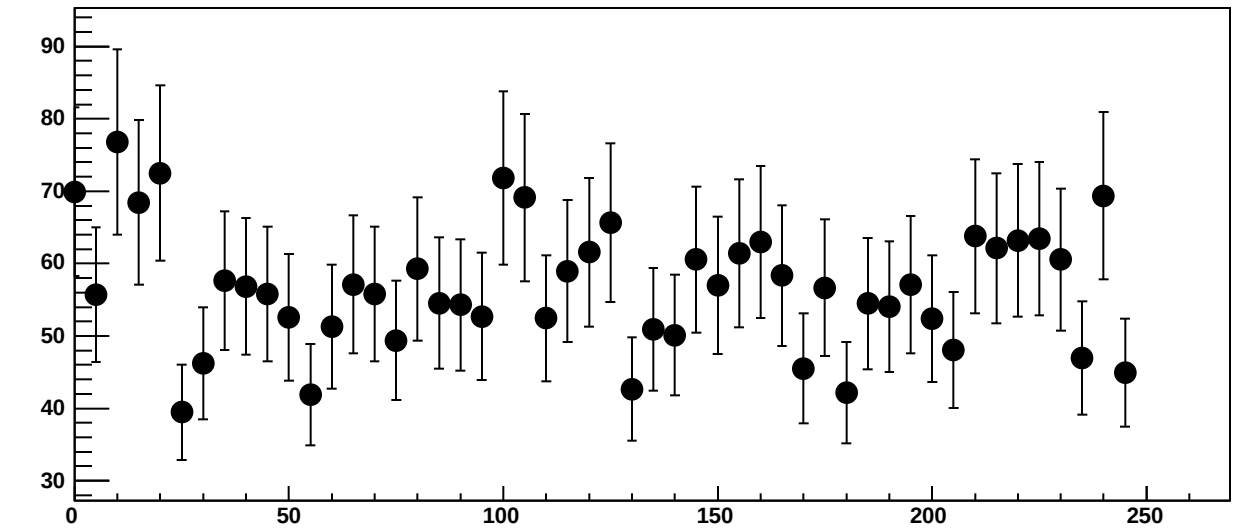
Chip 2, Channel 7, Enable 3, DAC=1600, ADC Residuals vs Hold



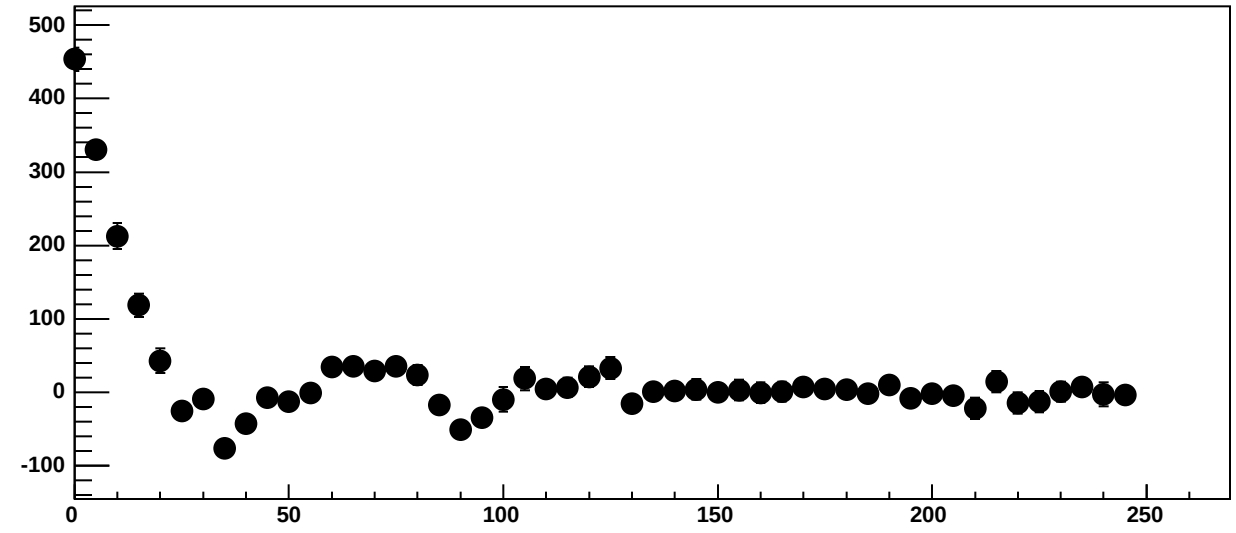
Chip 2, Channel 7, Enable 4, DAC=1600, ADC Mean vs Hold



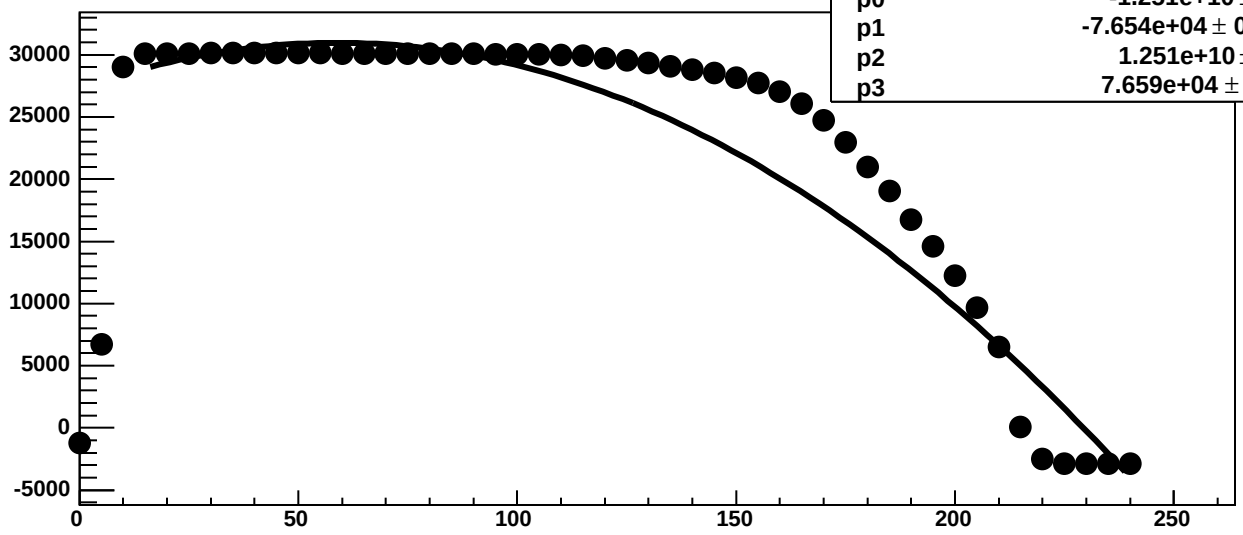
Chip 2, Channel 7, Enable 4, DAC=1600, ADC Noise vs Hold



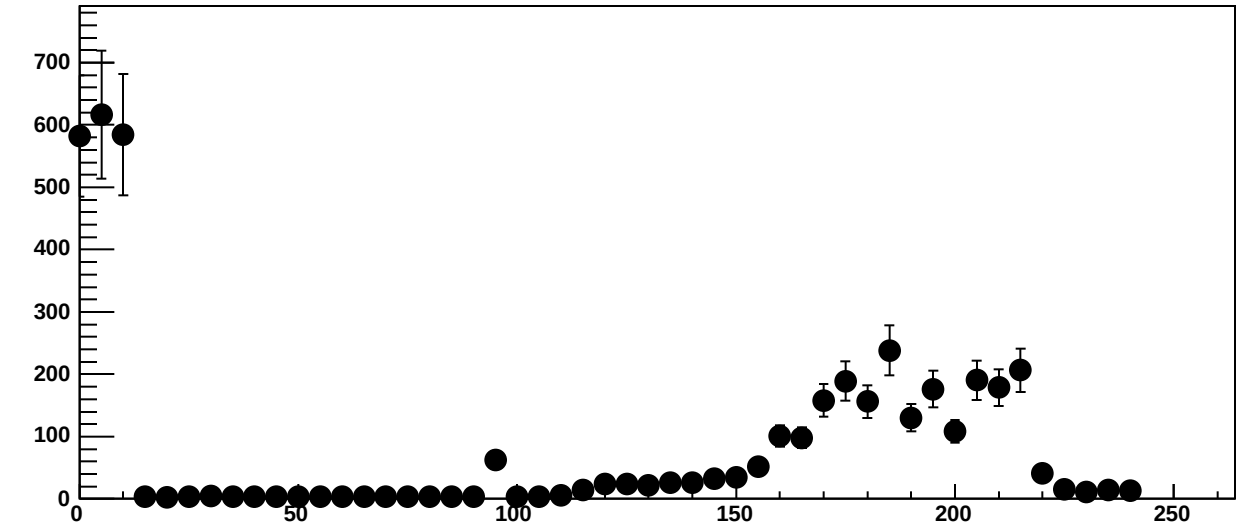
Chip 2, Channel 7, Enable 4, DAC=1600, ADC Residuals vs Hold



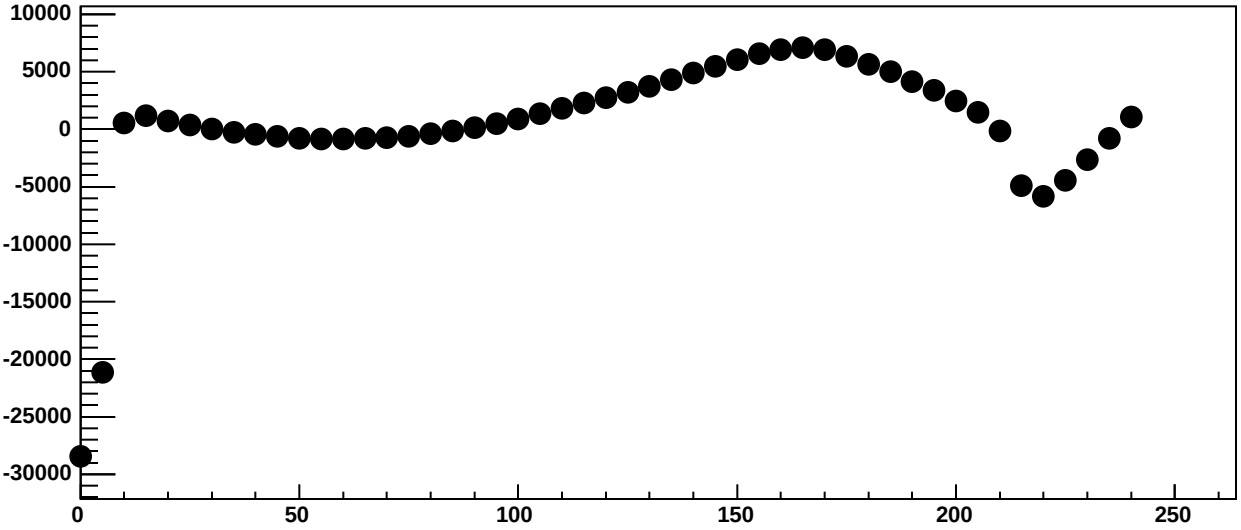
Chip 2, Channel 7, Enable 5!, DAC=1600, ADC Mean vs Hold



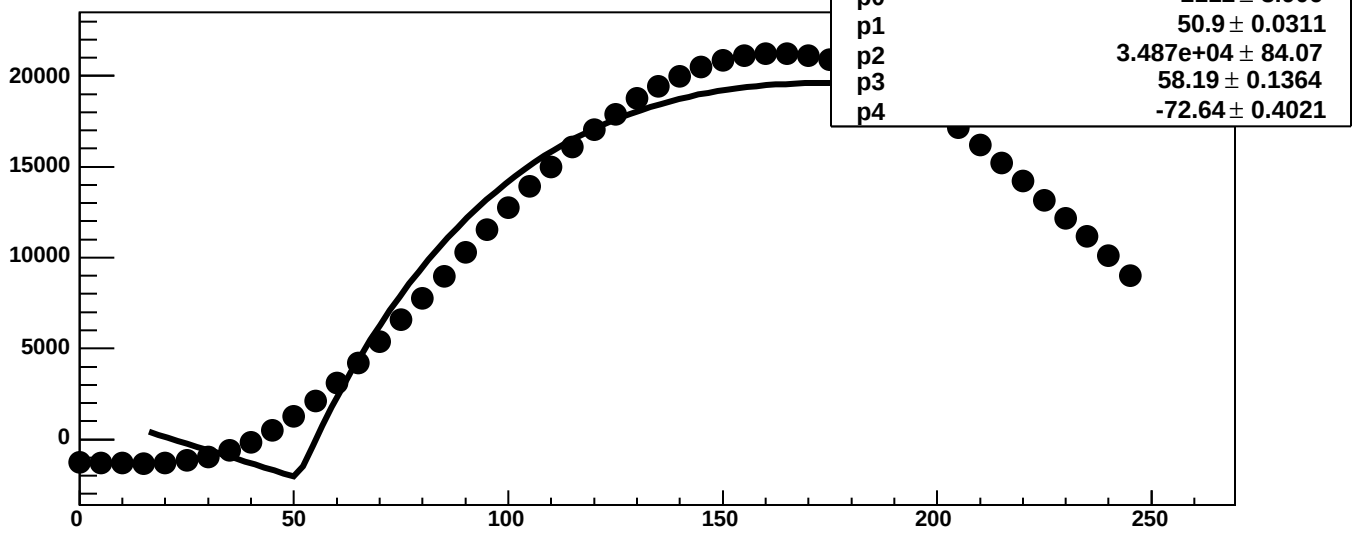
Chip 2, Channel 7, Enable 5!, DAC=1600, ADC Noise vs Hold



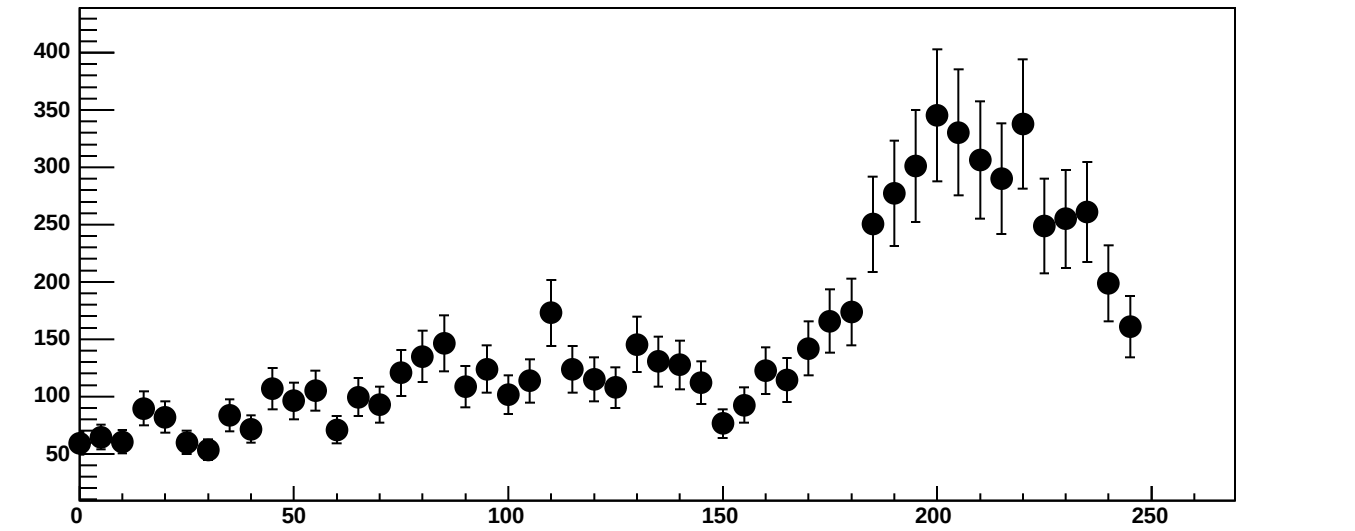
Chip 2, Channel 7, Enable 5!, DAC=1600, ADC Residuals vs Hold



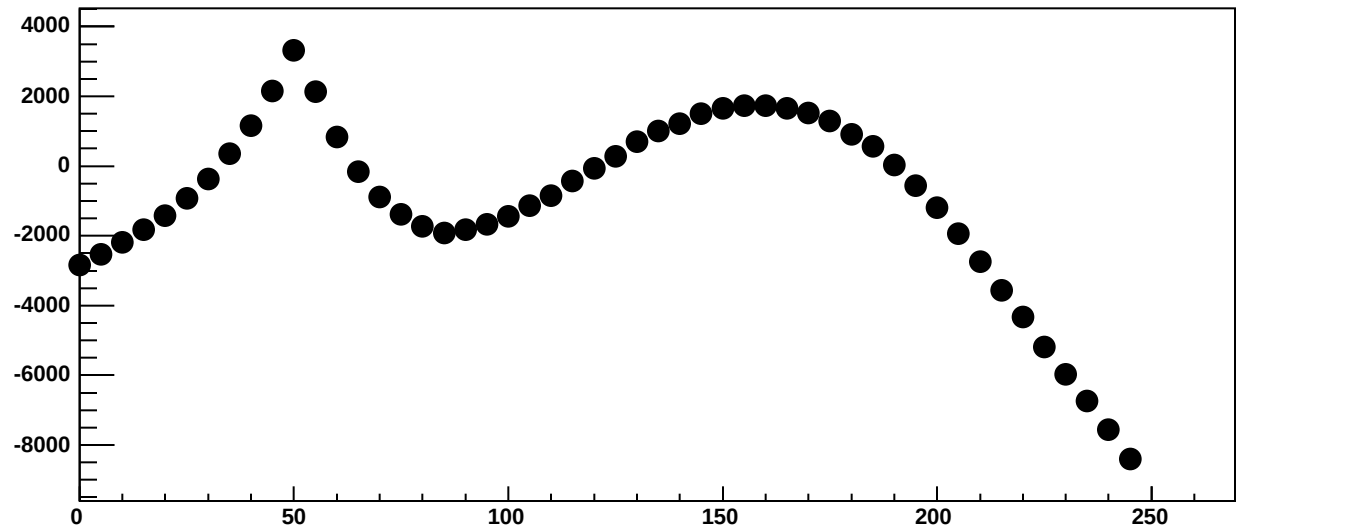
Chip 2, Channel 8, Enable 0, DAC=1600, ADC Mean vs Hold

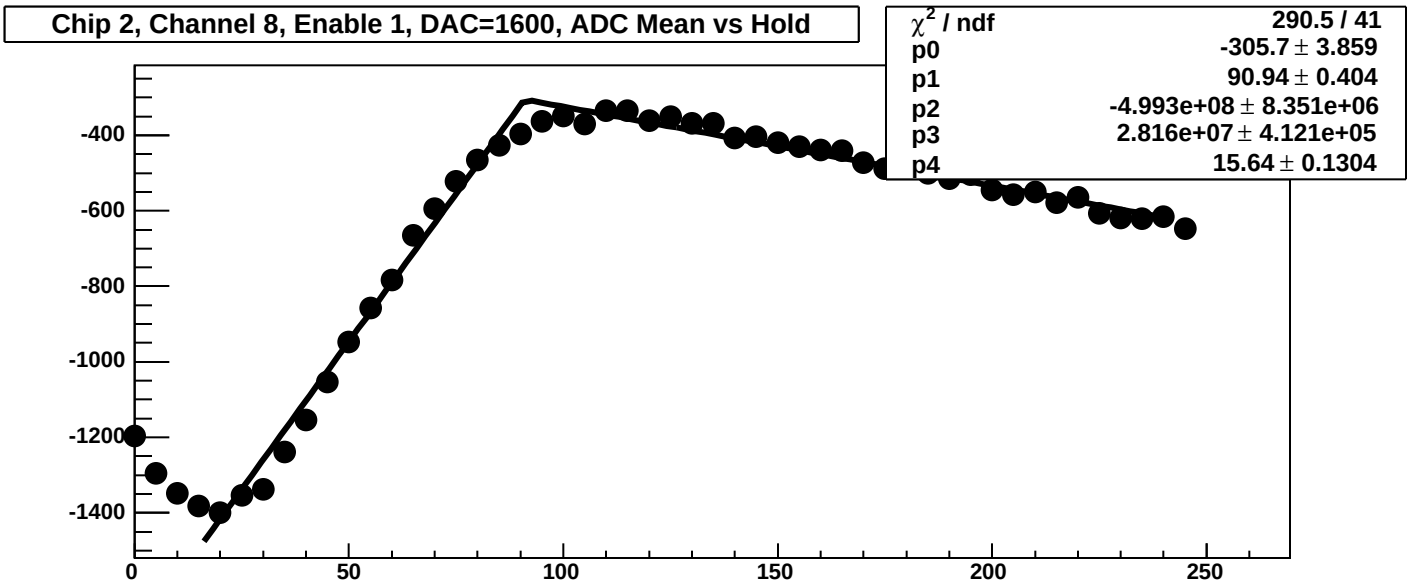


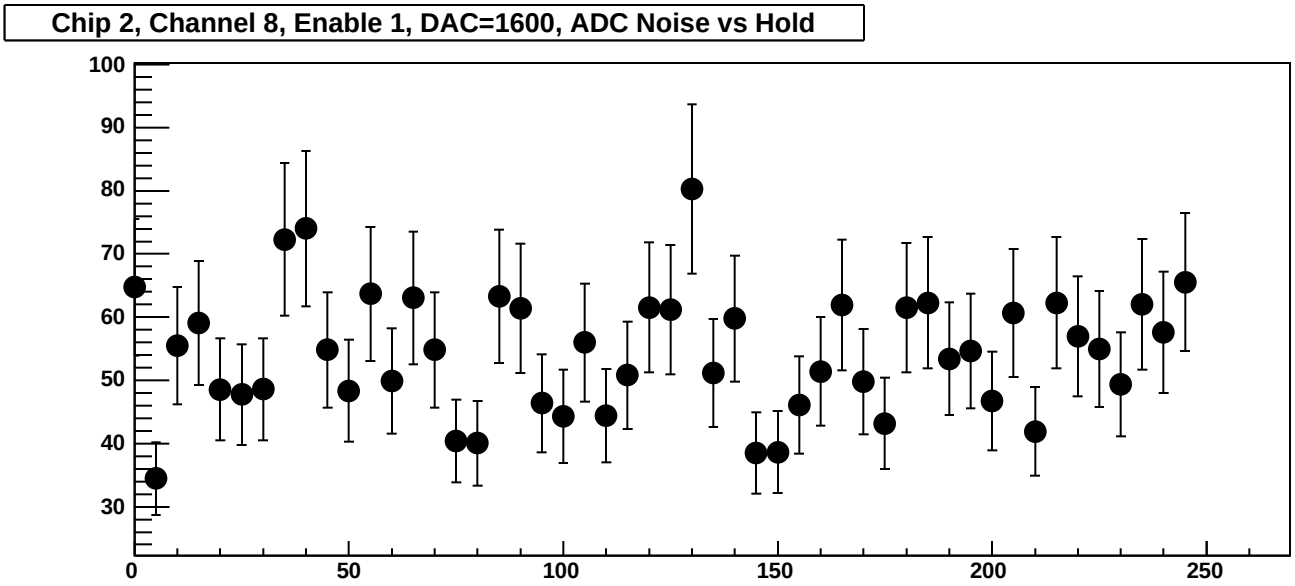
Chip 2, Channel 8, Enable 0, DAC=1600, ADC Noise vs Hold

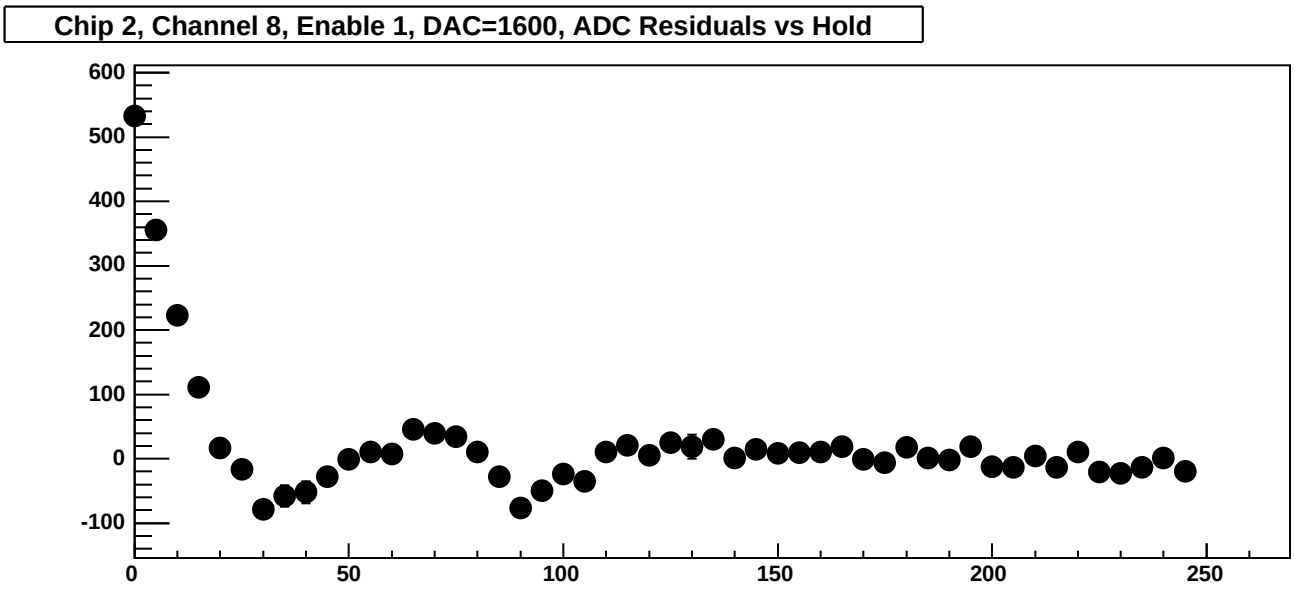


Chip 2, Channel 8, Enable 0, DAC=1600, ADC Residuals vs Hold

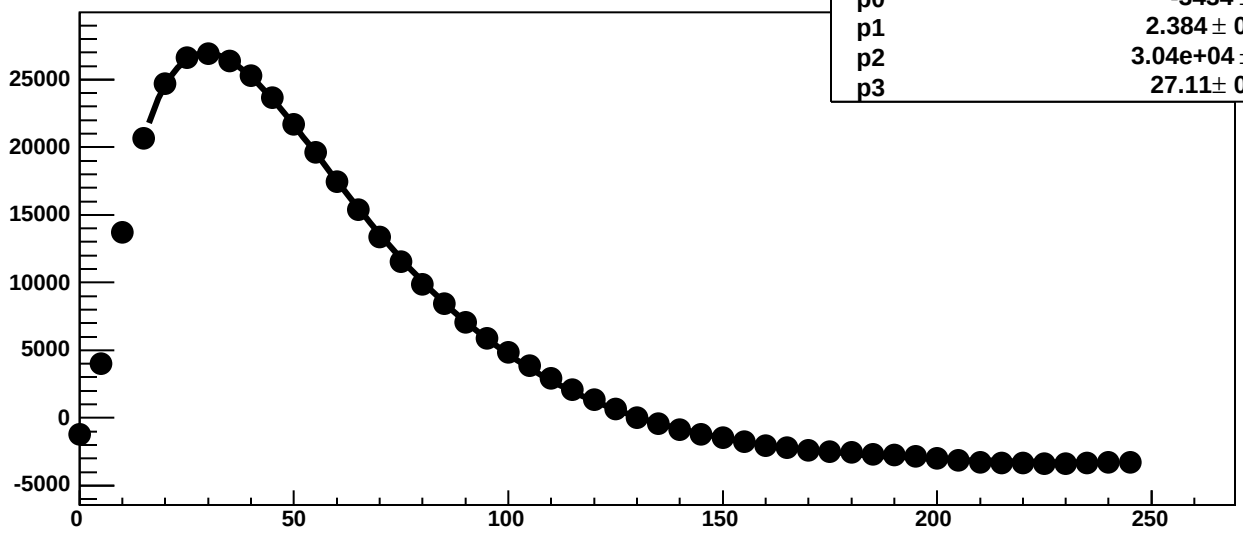



Chip 2, Channel 8, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 290.5 / 41
p0 -305.7 ± 3.859
p1 90.94 ± 0.404
p2 -4.993e+08 ± 8.351e+06
p3 2.816e+07 ± 4.121e+05
p4 15.64 ± 0.1304
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

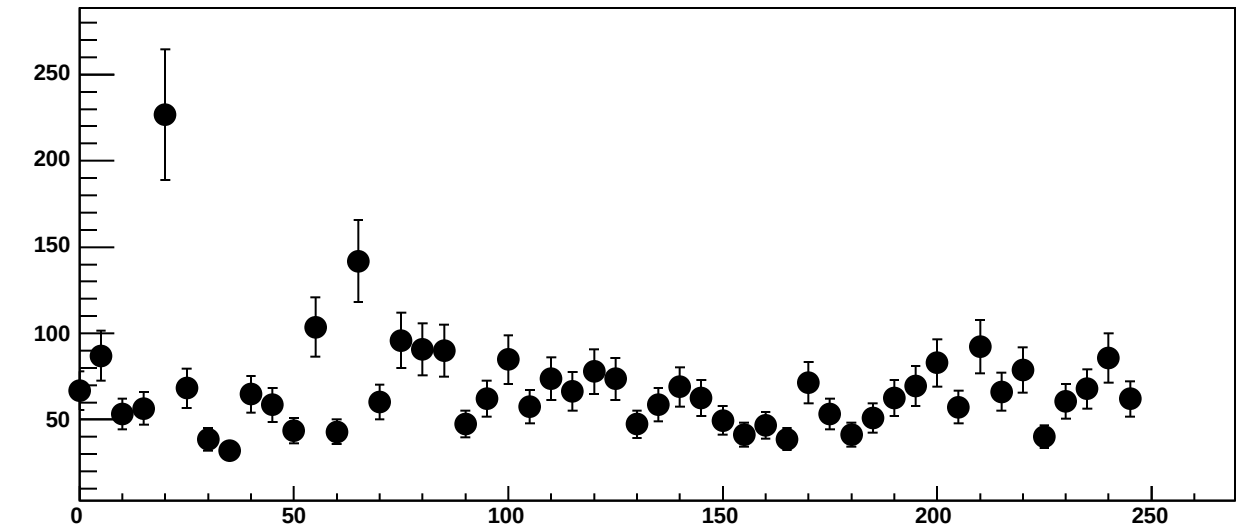

Chip 2, Channel 8, Enable 1, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 2, Channel 8, Enable 1, DAC=1600, ADC Residuals vs Hold
600
500
400
300
200
100
0
-100
0
50
100
150
200
250

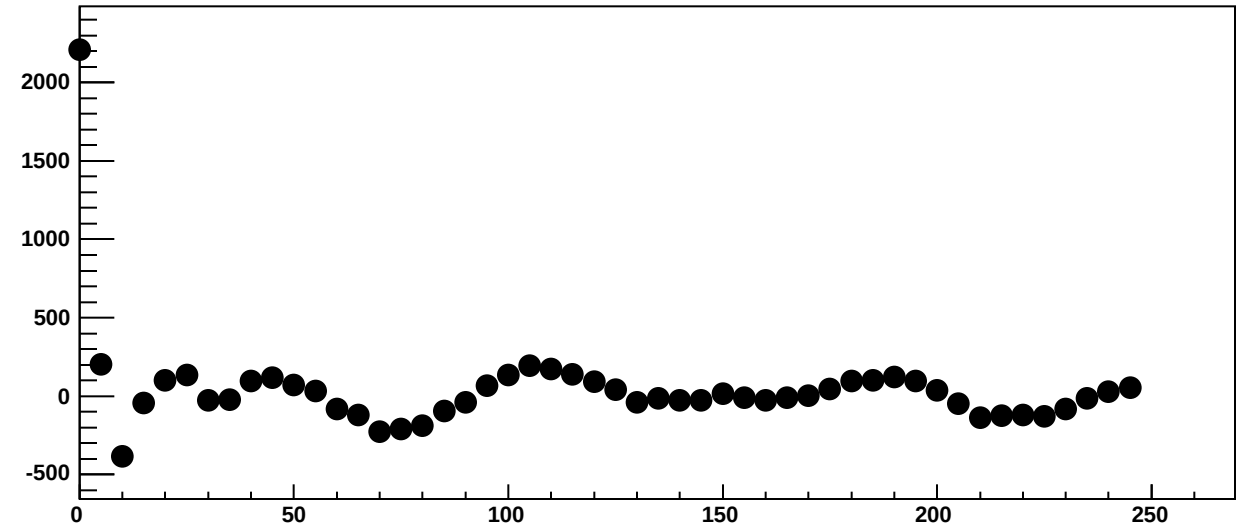
Chip 2, Channel 8, Enable 2!, DAC=1600, ADC Mean vs Hold



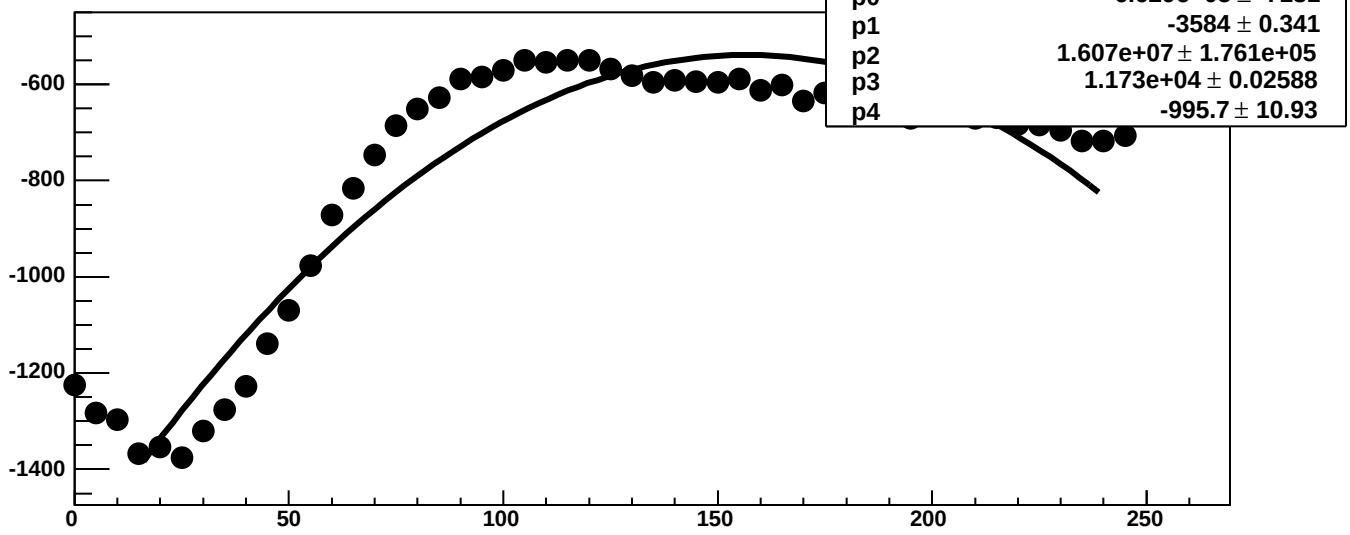
Chip 2, Channel 8, Enable 2!, DAC=1600, ADC Noise vs Hold



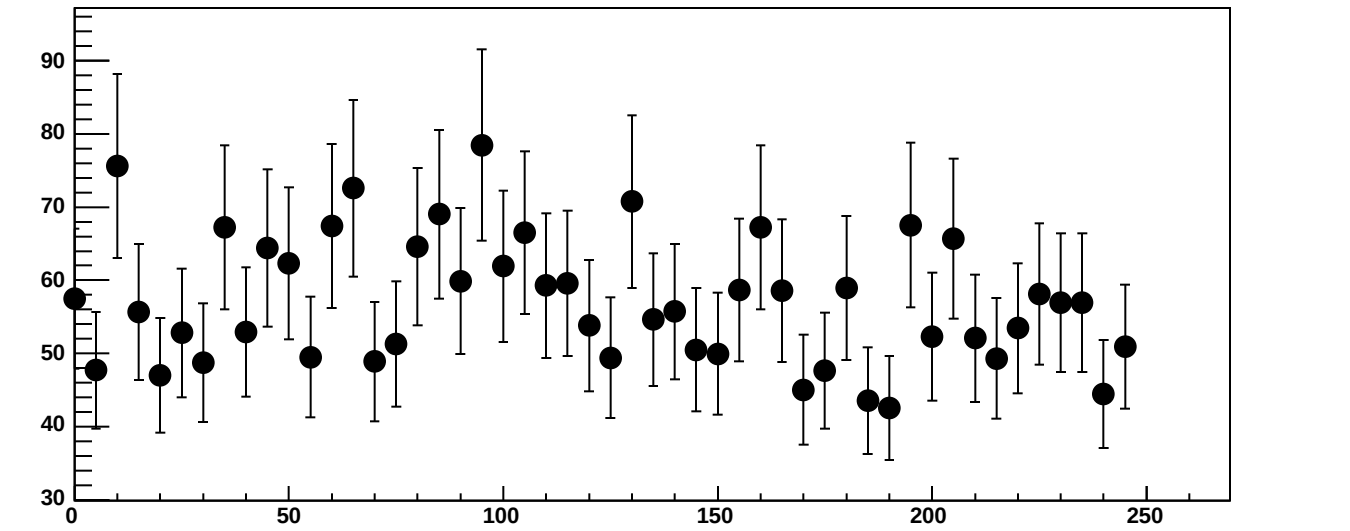
Chip 2, Channel 8, Enable 2!, DAC=1600, ADC Residuals vs Hold



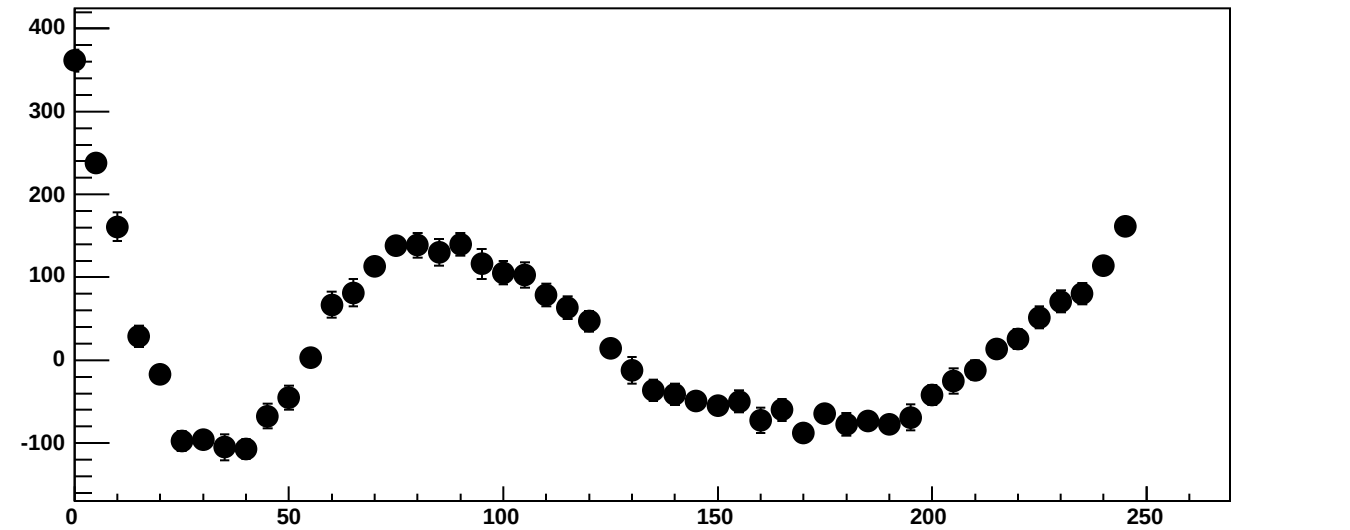
Chip 2, Channel 8, Enable 3, DAC=1600, ADC Mean vs Hold



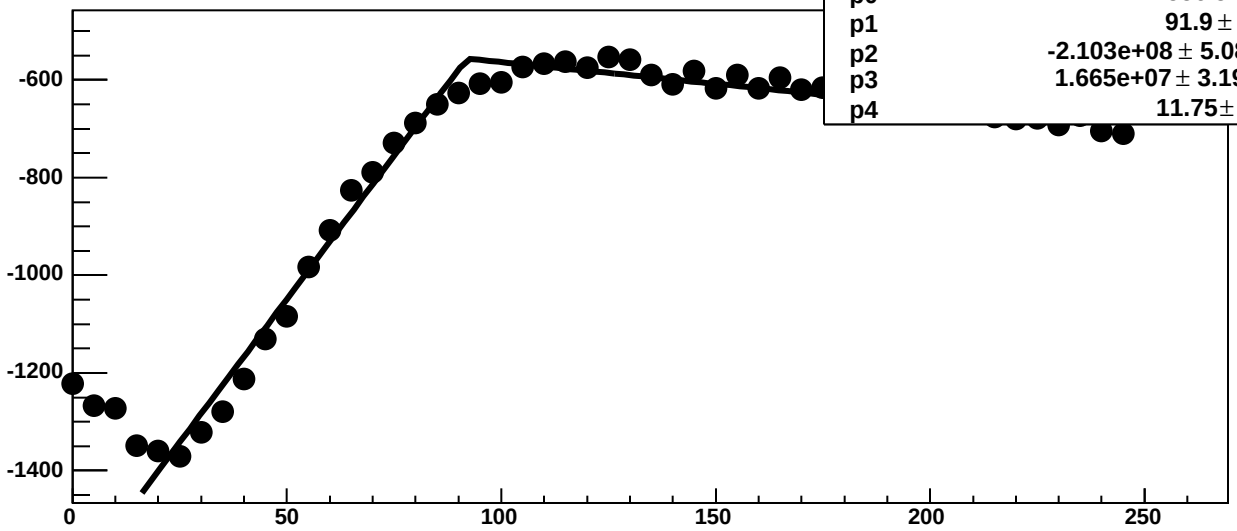
Chip 2, Channel 8, Enable 3, DAC=1600, ADC Noise vs Hold



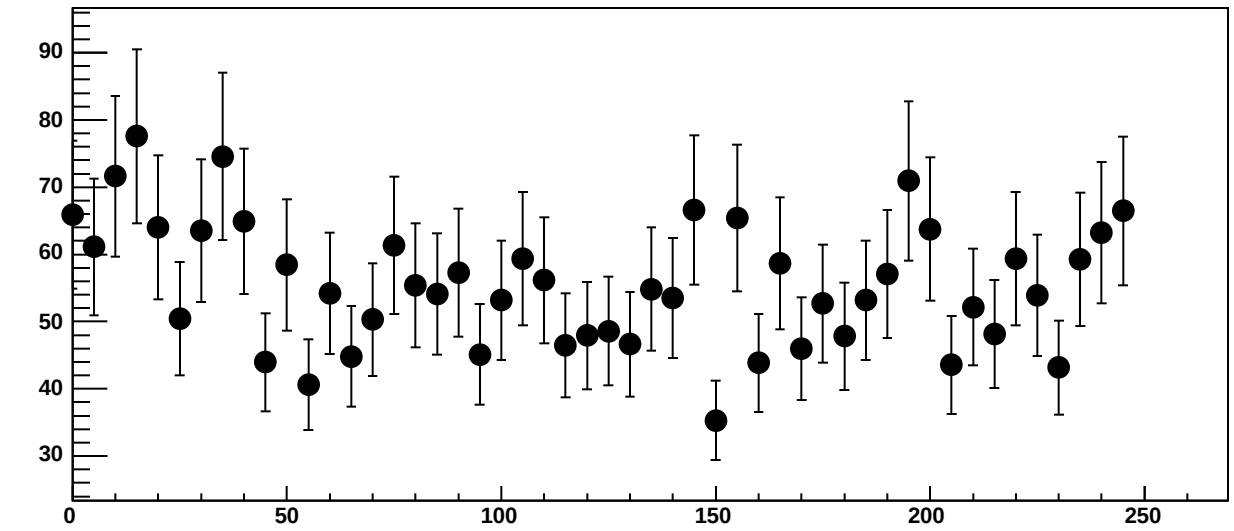
Chip 2, Channel 8, Enable 3, DAC=1600, ADC Residuals vs Hold



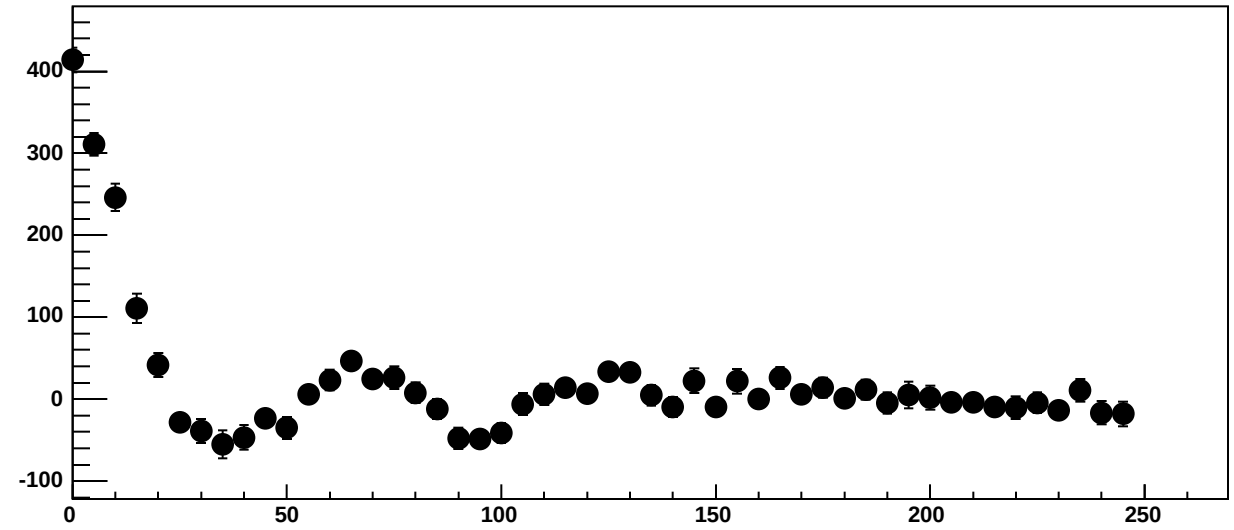
Chip 2, Channel 8, Enable 4, DAC=1600, ADC Mean vs Hold

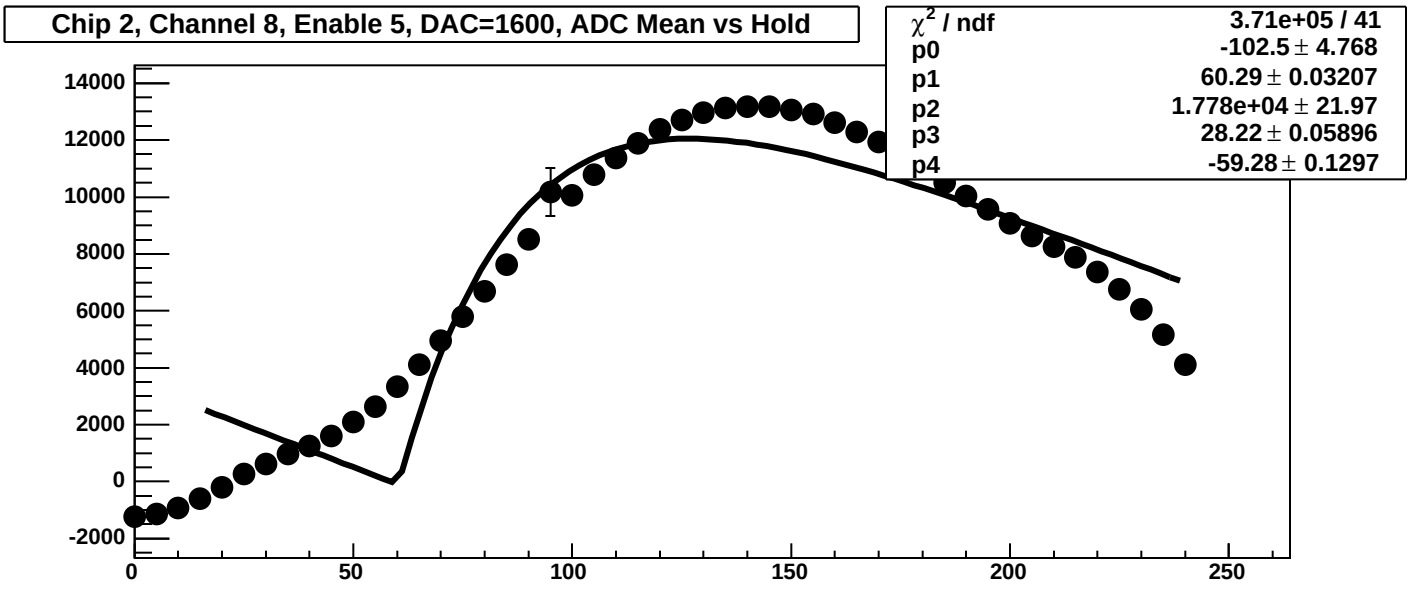


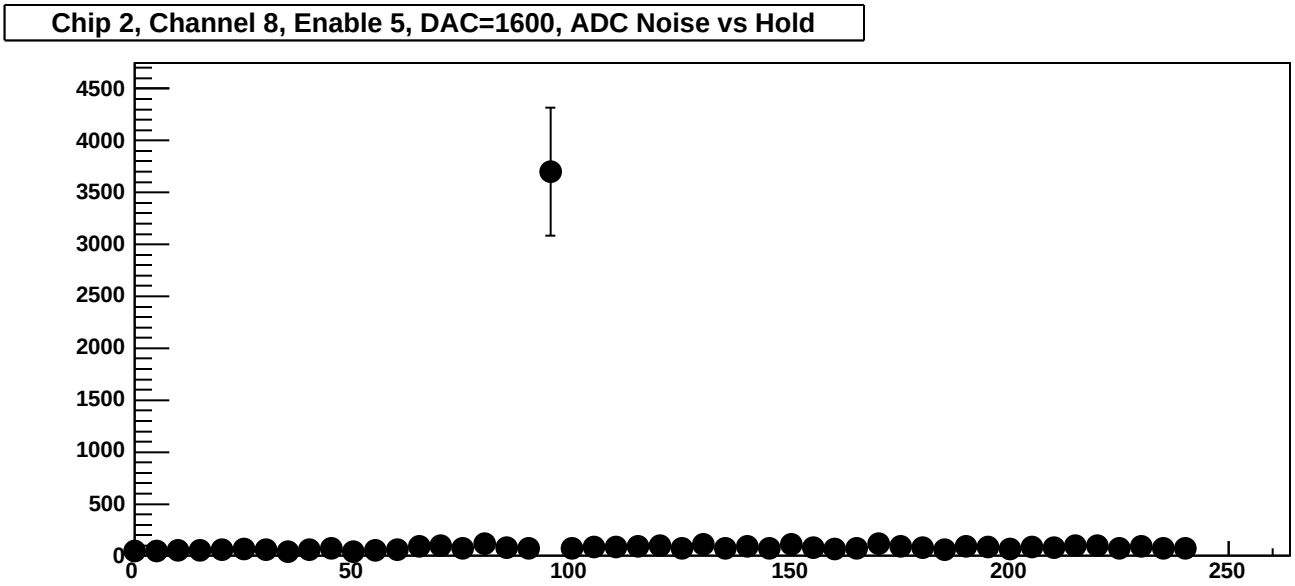
Chip 2, Channel 8, Enable 4, DAC=1600, ADC Noise vs Hold

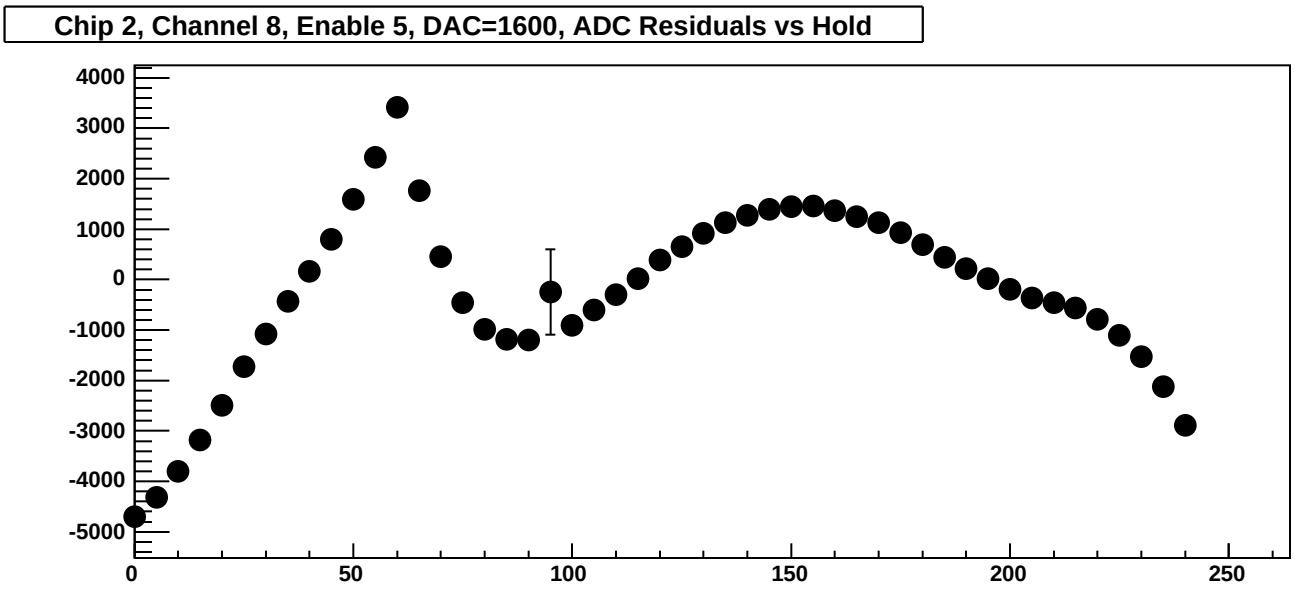


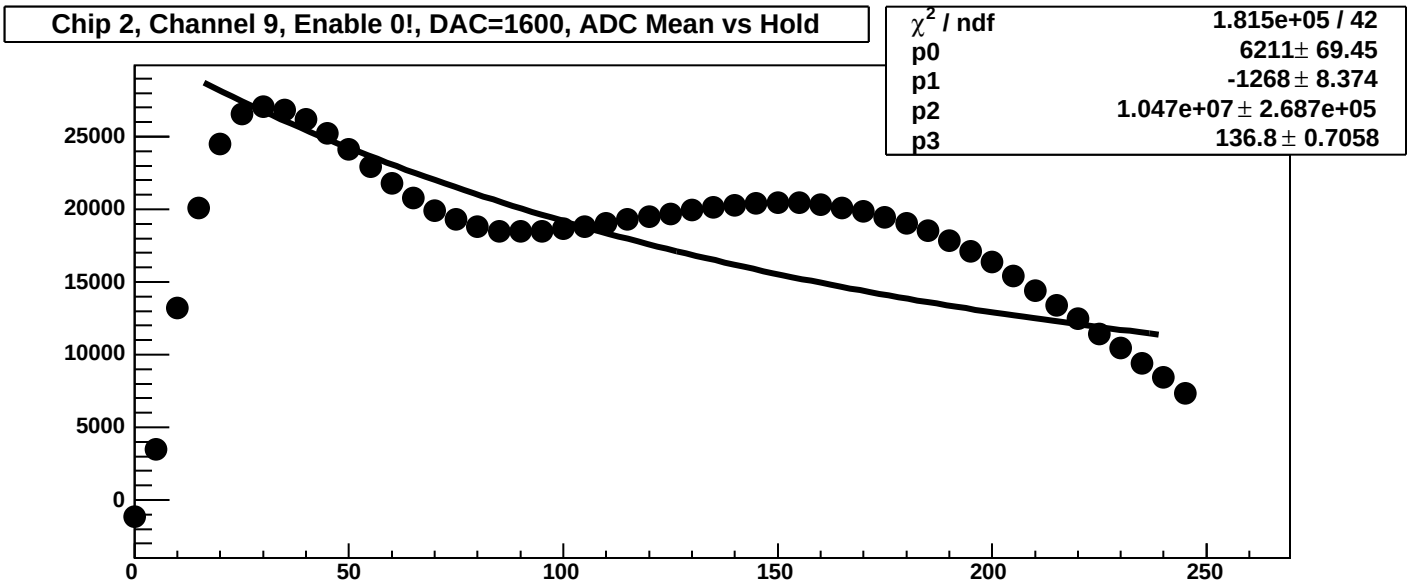
Chip 2, Channel 8, Enable 4, DAC=1600, ADC Residuals vs Hold

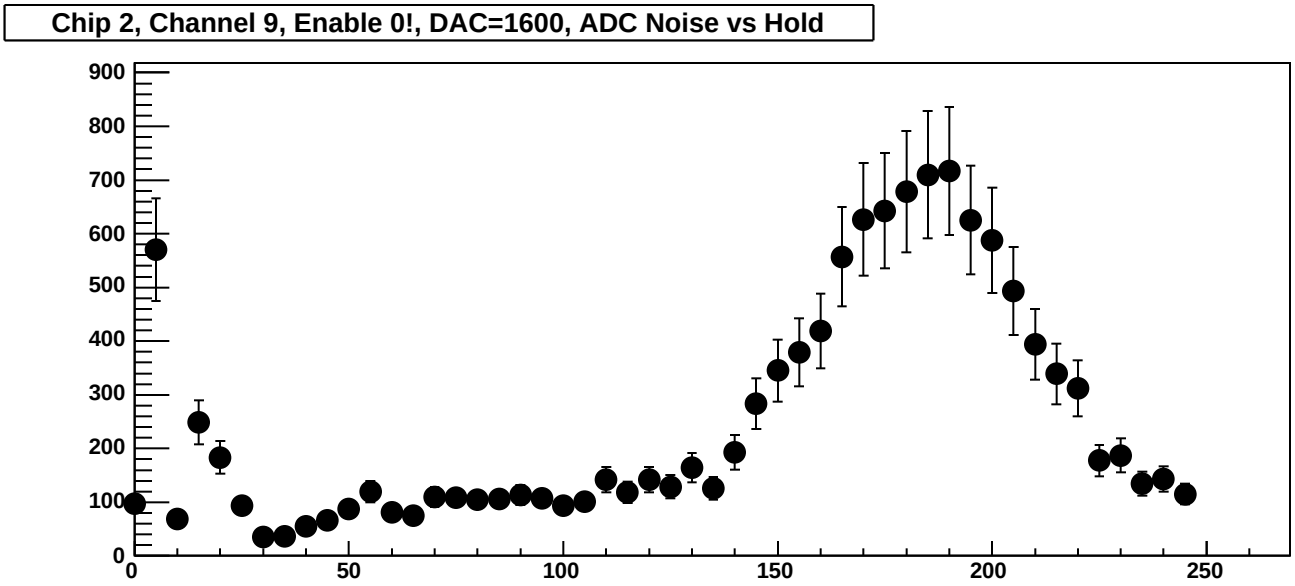


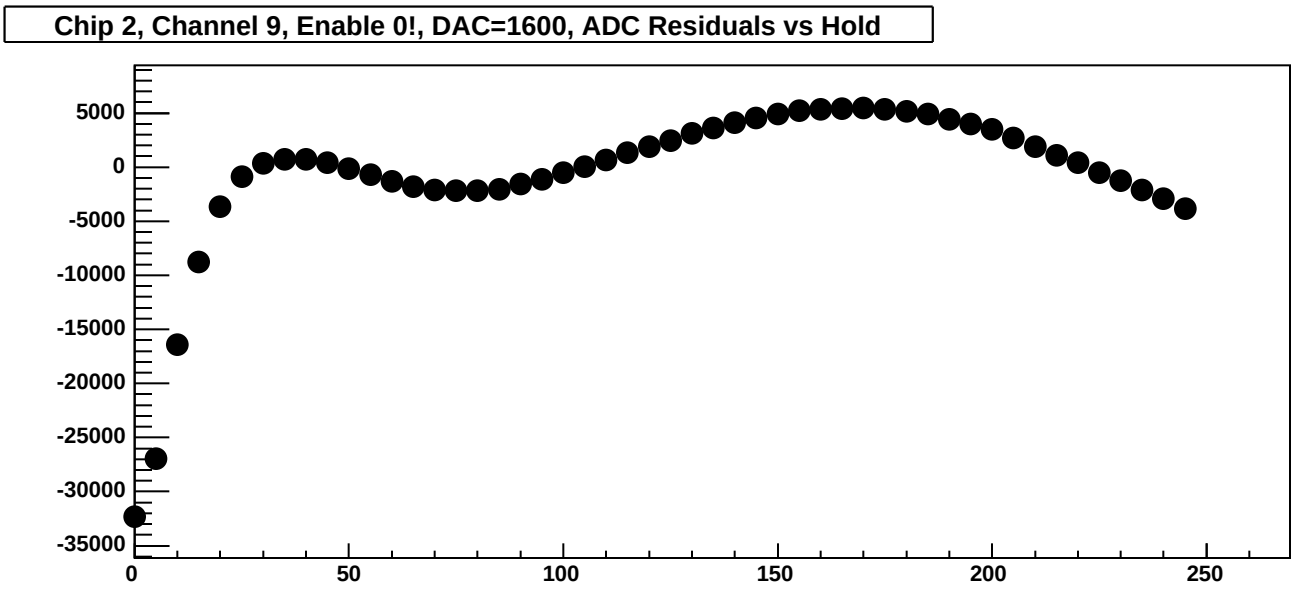

Chip 2, Channel 8, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.71e+05 / 41
p0 -102.5 ± 4.768
p1 60.29 ± 0.03207
p2 1.778e+04 ± 21.97
p3 28.22 ± 0.05896
p4 -59.28 ± 0.1297
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250


Chip 2, Channel 8, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250

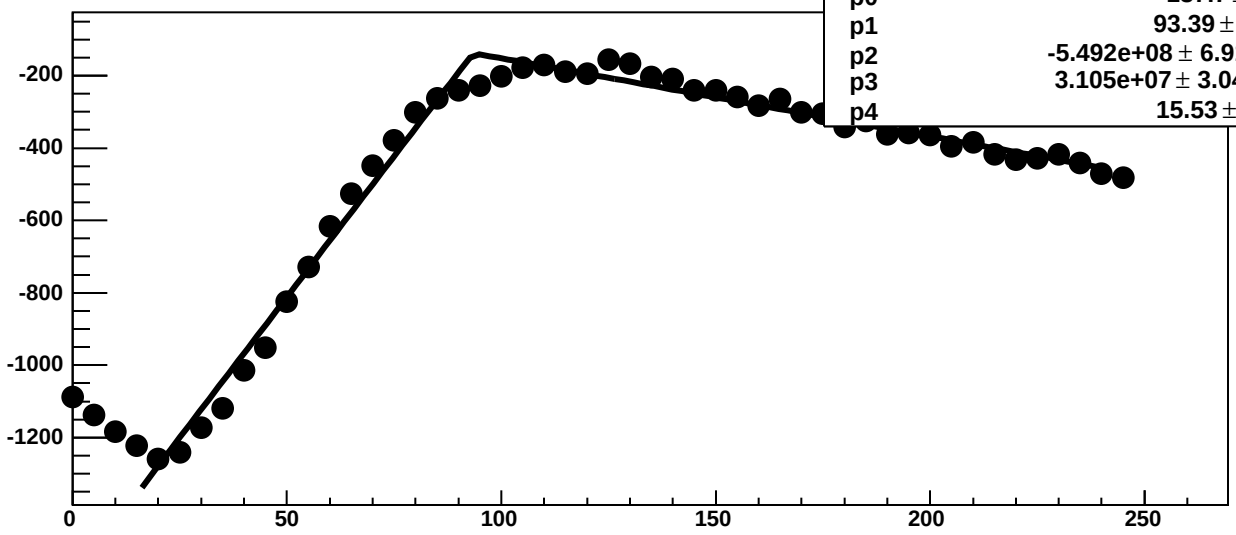

Chip 2, Channel 8, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250


Chip 2, Channel 9, Enable 0!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.815e+05 / 42
p0 6211 ± 69.45
p1 -1268 ± 8.374
p2 1.047e+07 ± 2.687e+05
p3 136.8 ± 0.7058
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

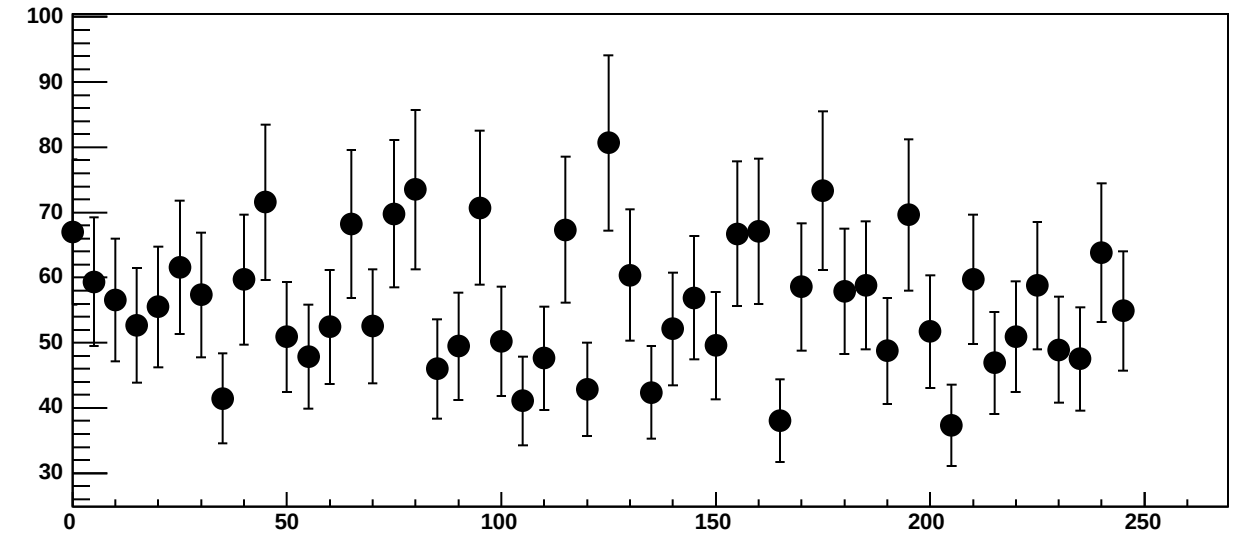

Chip 2, Channel 9, Enable 0!, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 2, Channel 9, Enable 0!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
-35000
0
50
100
150
200
250

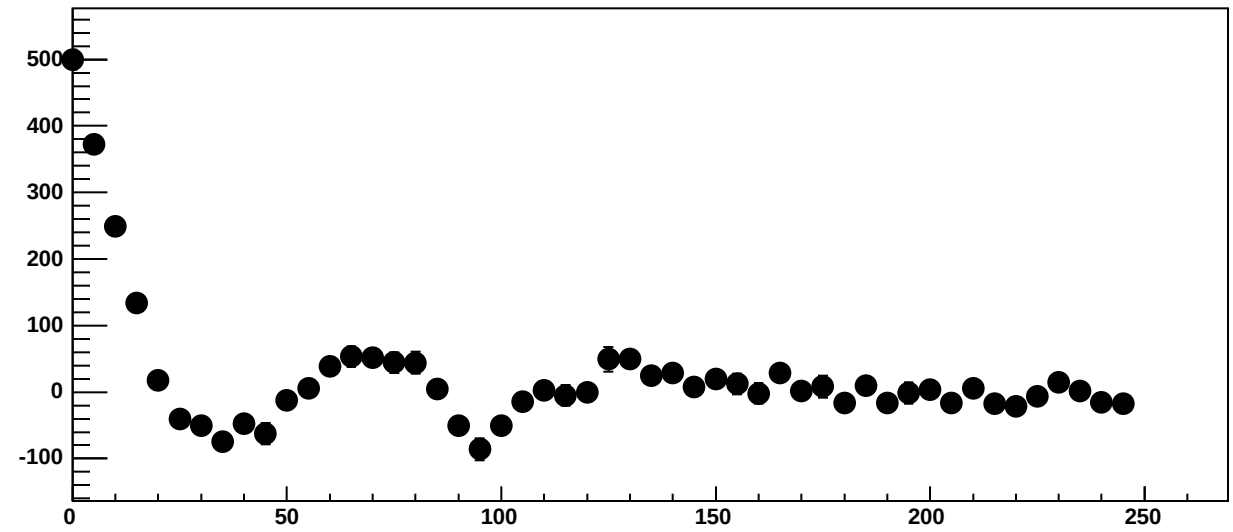
Chip 2, Channel 9, Enable 1, DAC=1600, ADC Mean vs Hold



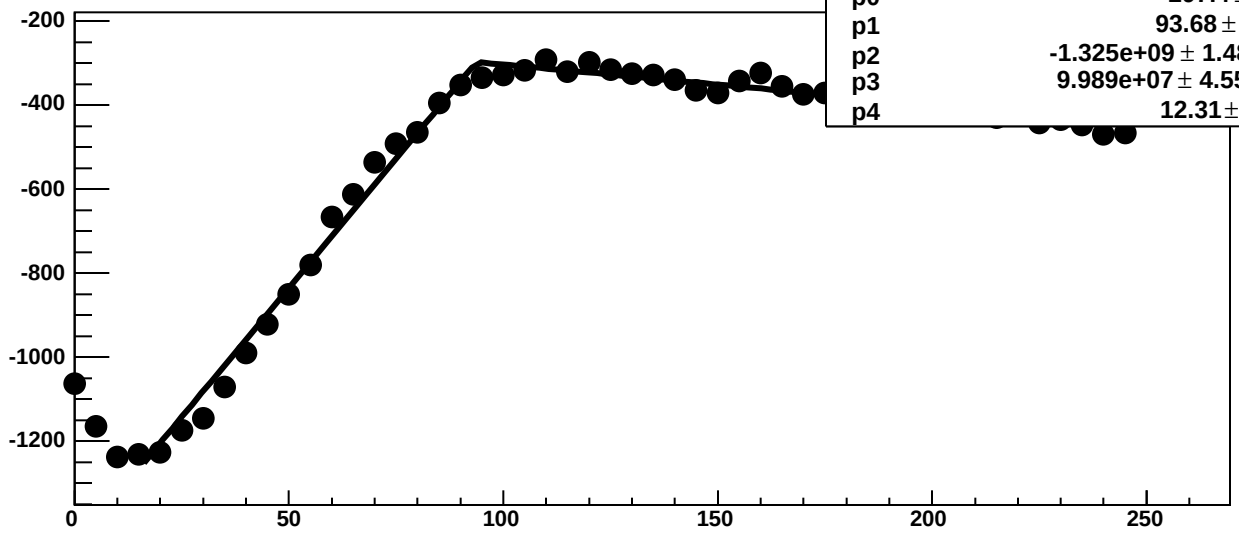
Chip 2, Channel 9, Enable 1, DAC=1600, ADC Noise vs Hold



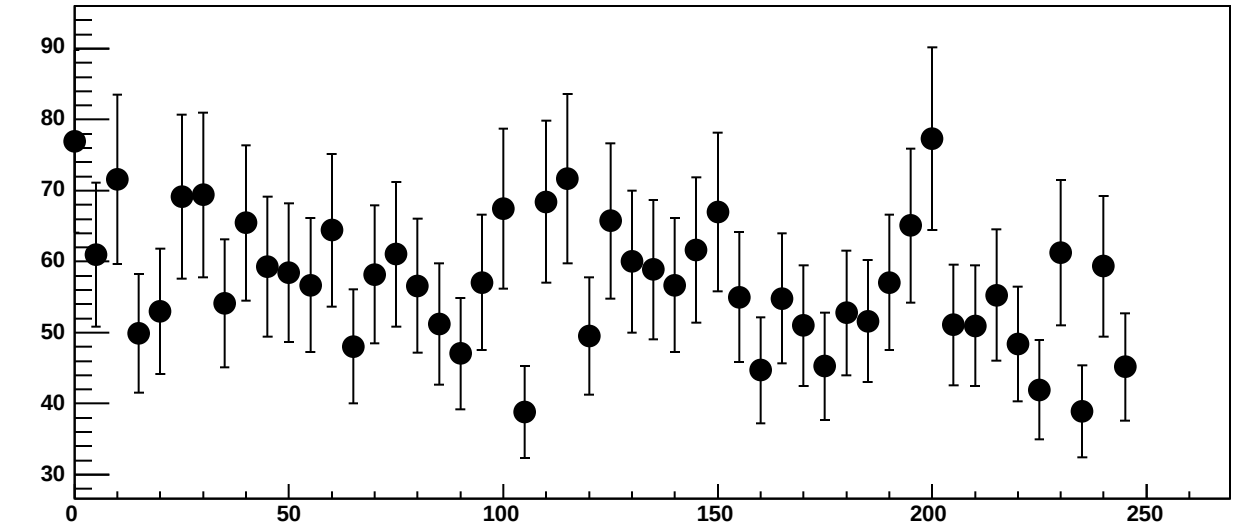
Chip 2, Channel 9, Enable 1, DAC=1600, ADC Residuals vs Hold



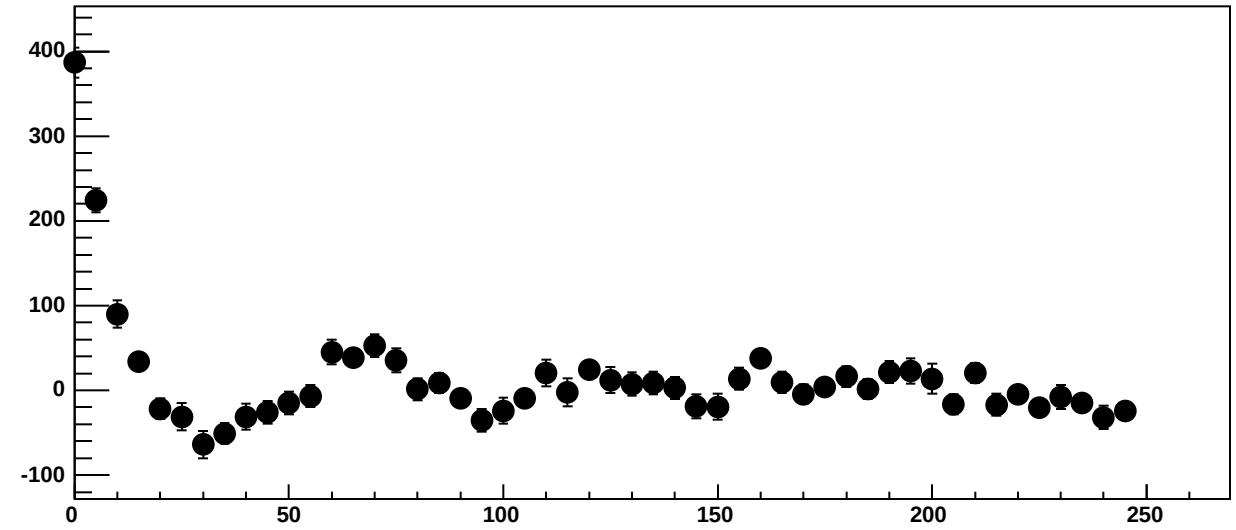
Chip 2, Channel 9, Enable 2, DAC=1600, ADC Mean vs Hold



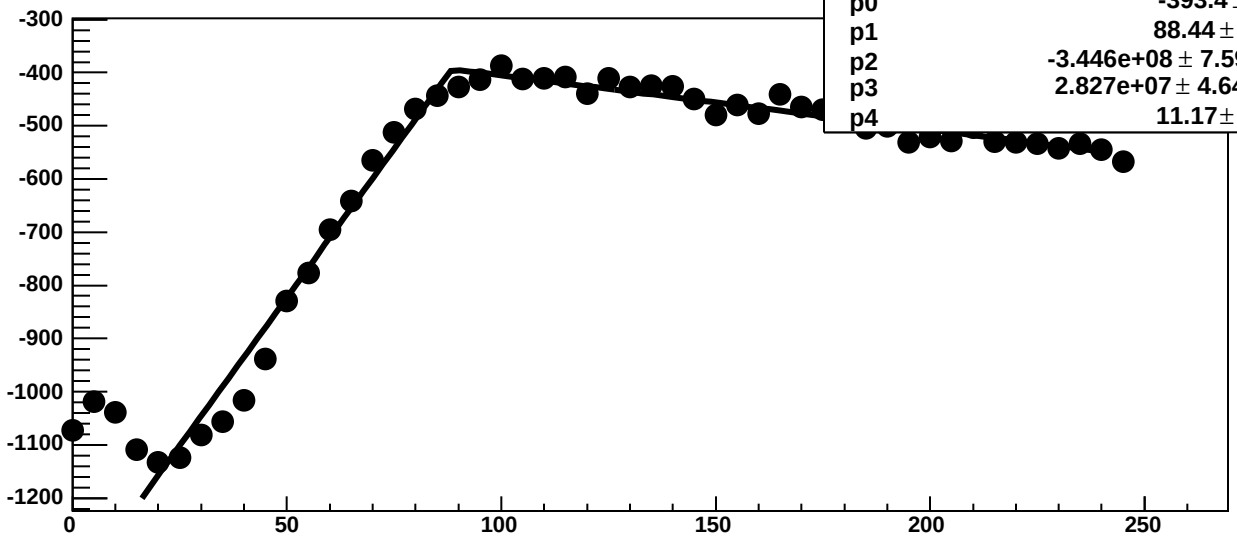
Chip 2, Channel 9, Enable 2, DAC=1600, ADC Noise vs Hold



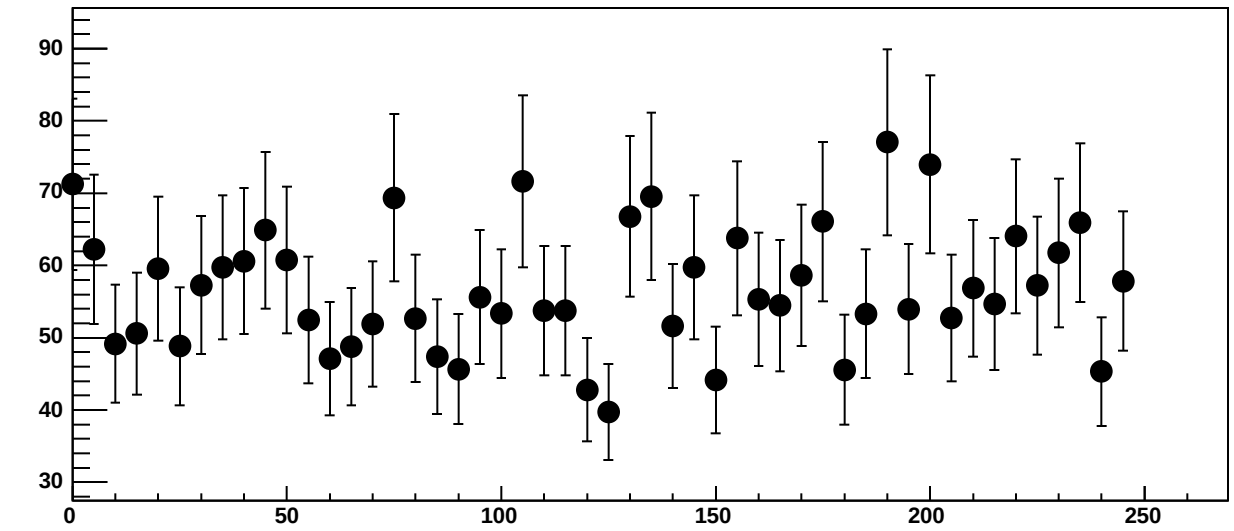
Chip 2, Channel 9, Enable 2, DAC=1600, ADC Residuals vs Hold



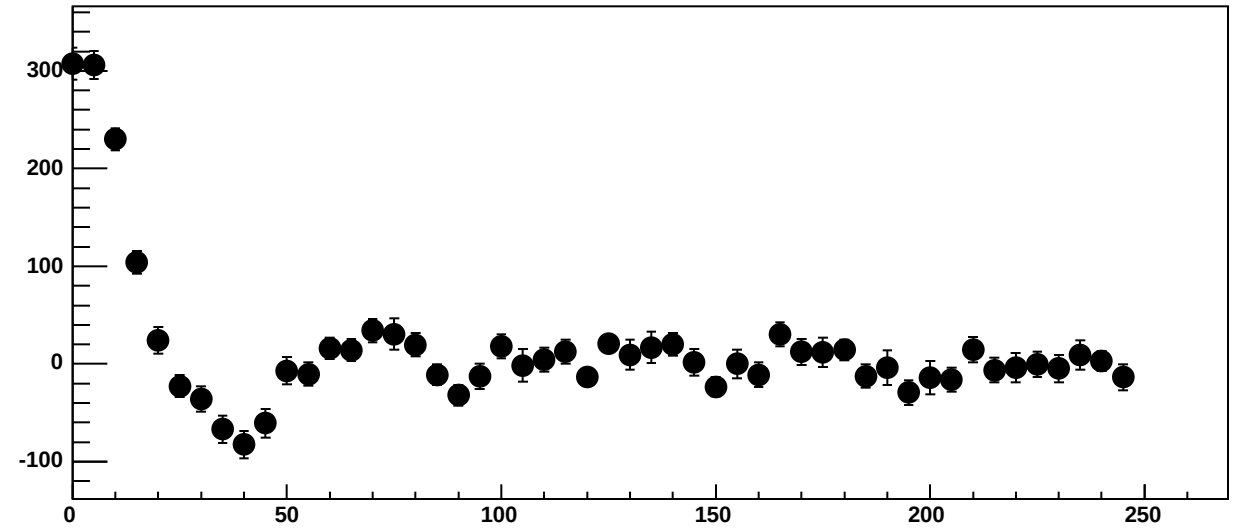
Chip 2, Channel 9, Enable 3, DAC=1600, ADC Mean vs Hold



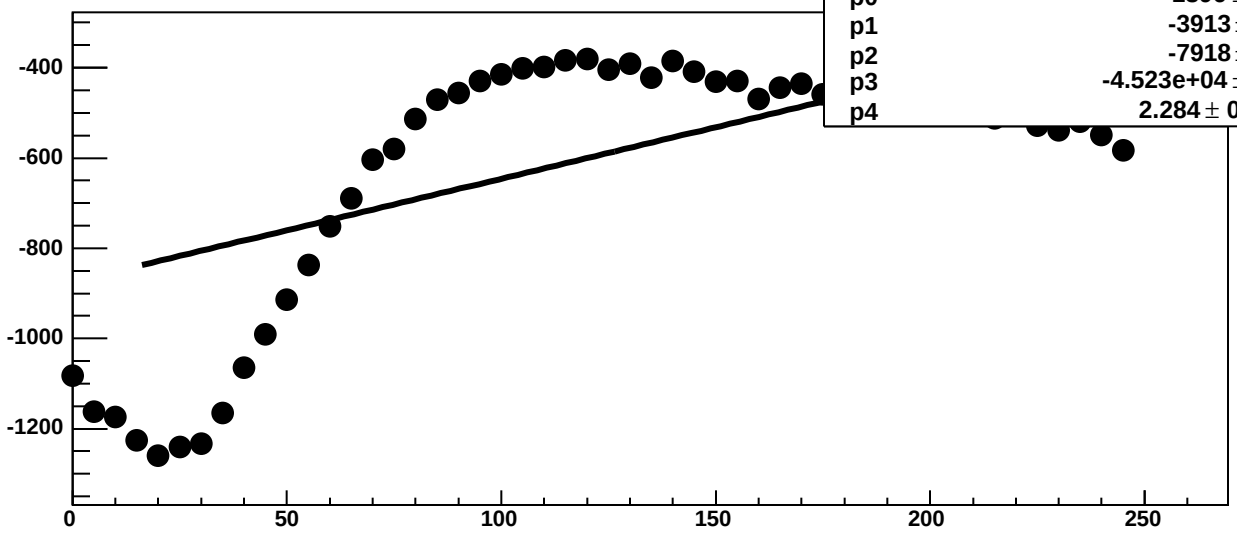
Chip 2, Channel 9, Enable 3, DAC=1600, ADC Noise vs Hold



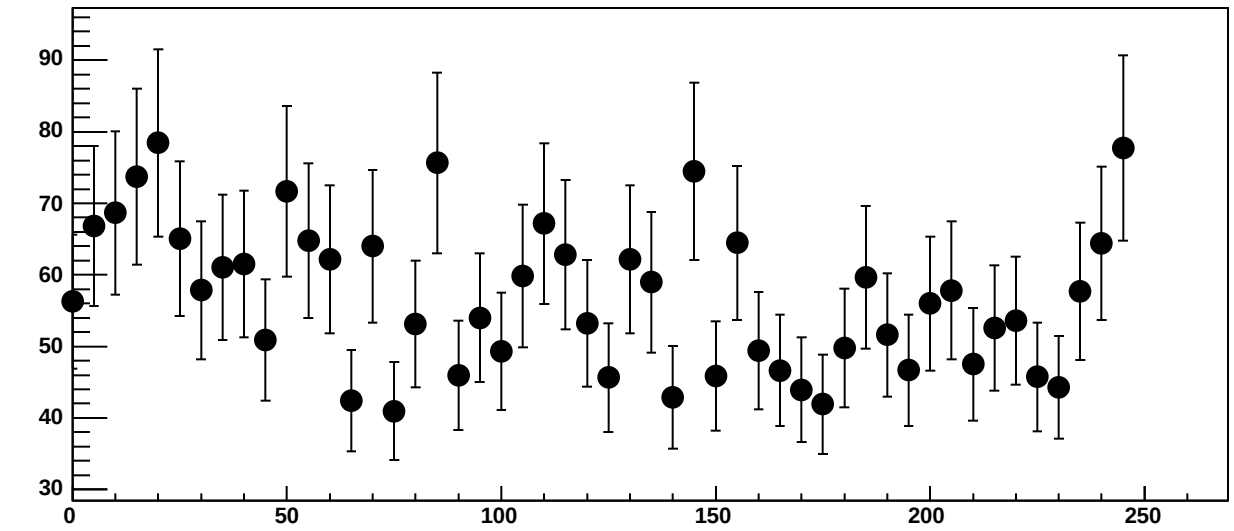
Chip 2, Channel 9, Enable 3, DAC=1600, ADC Residuals vs Hold



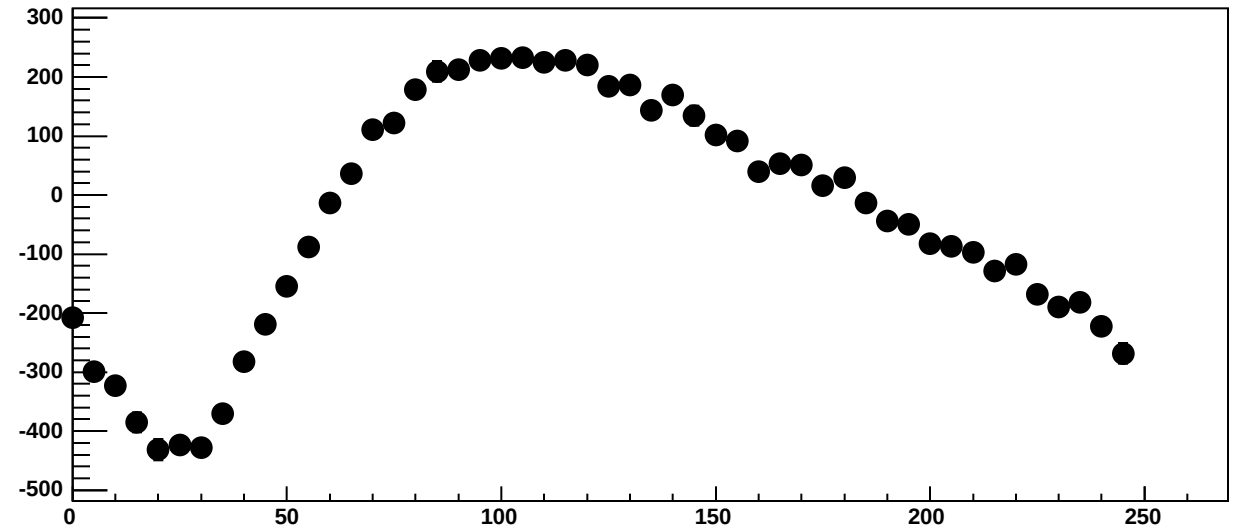
Chip 2, Channel 9, Enable 4, DAC=1600, ADC Mean vs Hold

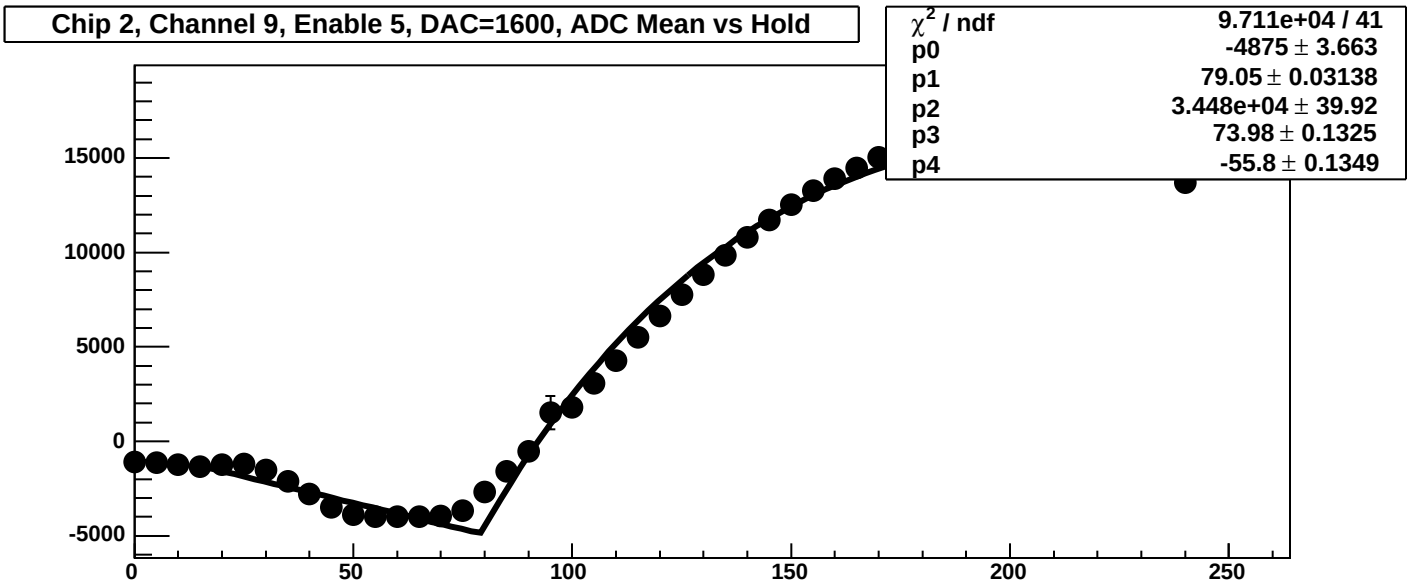


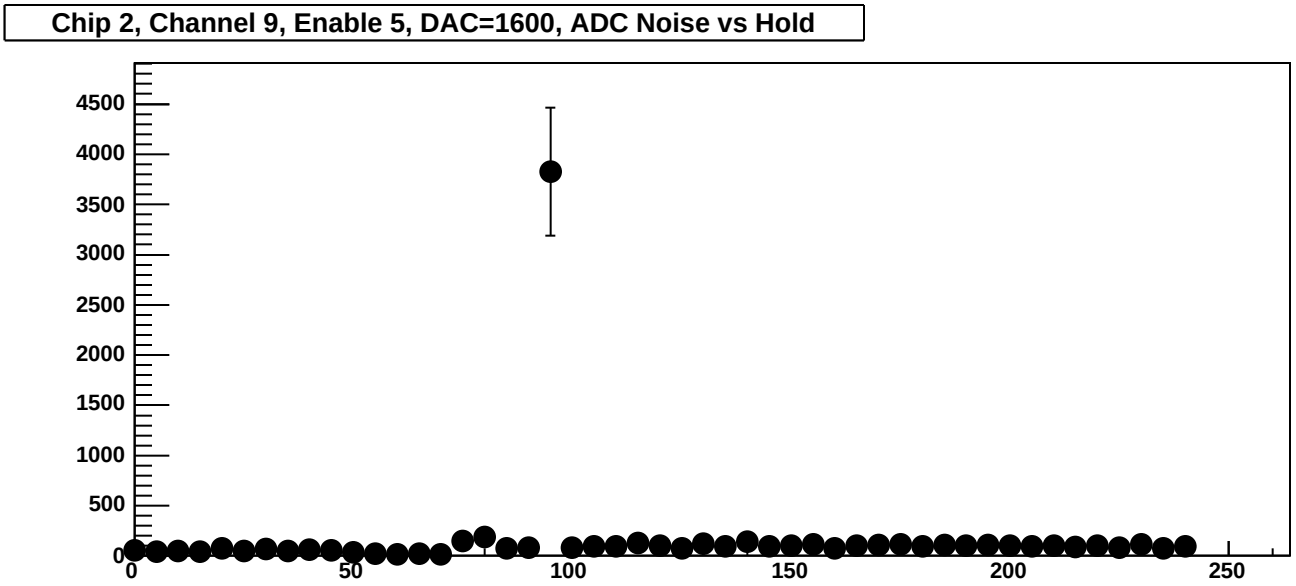
Chip 2, Channel 9, Enable 4, DAC=1600, ADC Noise vs Hold

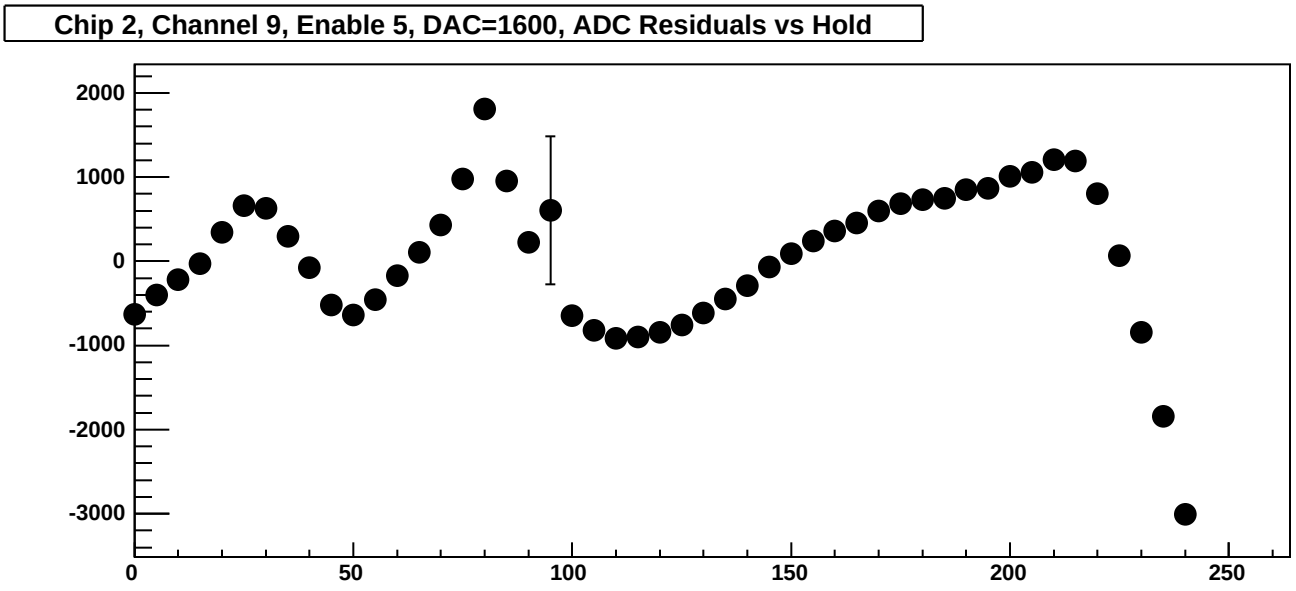


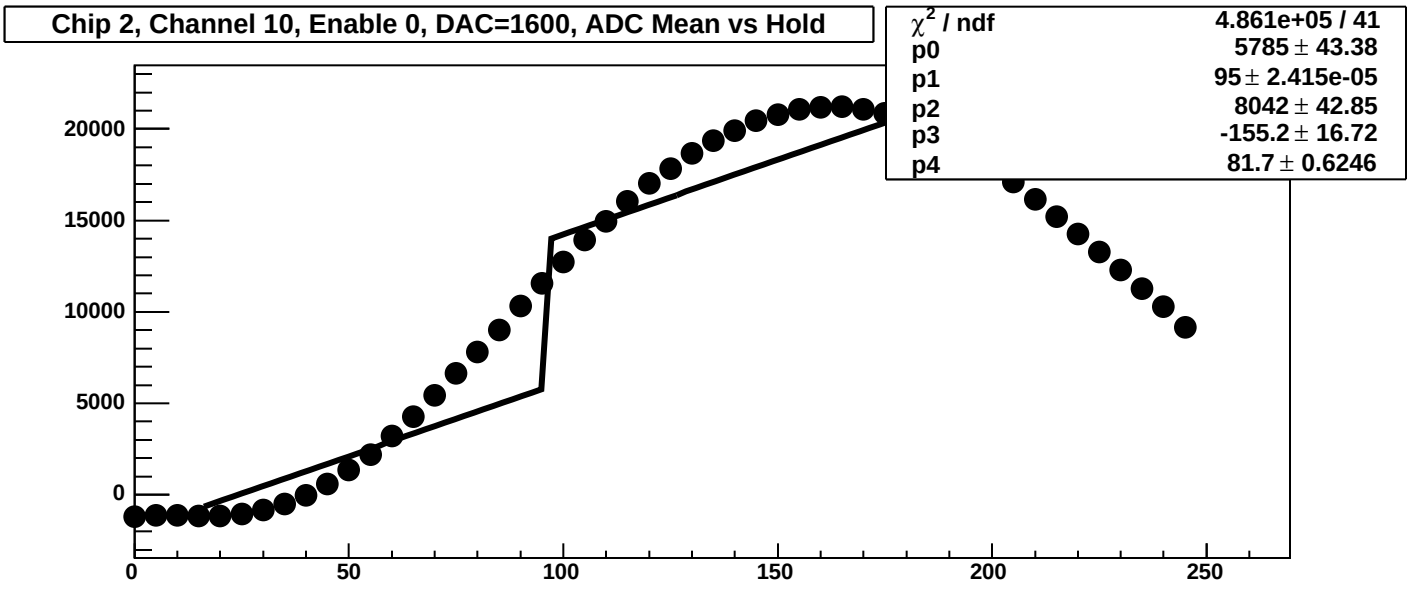
Chip 2, Channel 9, Enable 4, DAC=1600, ADC Residuals vs Hold

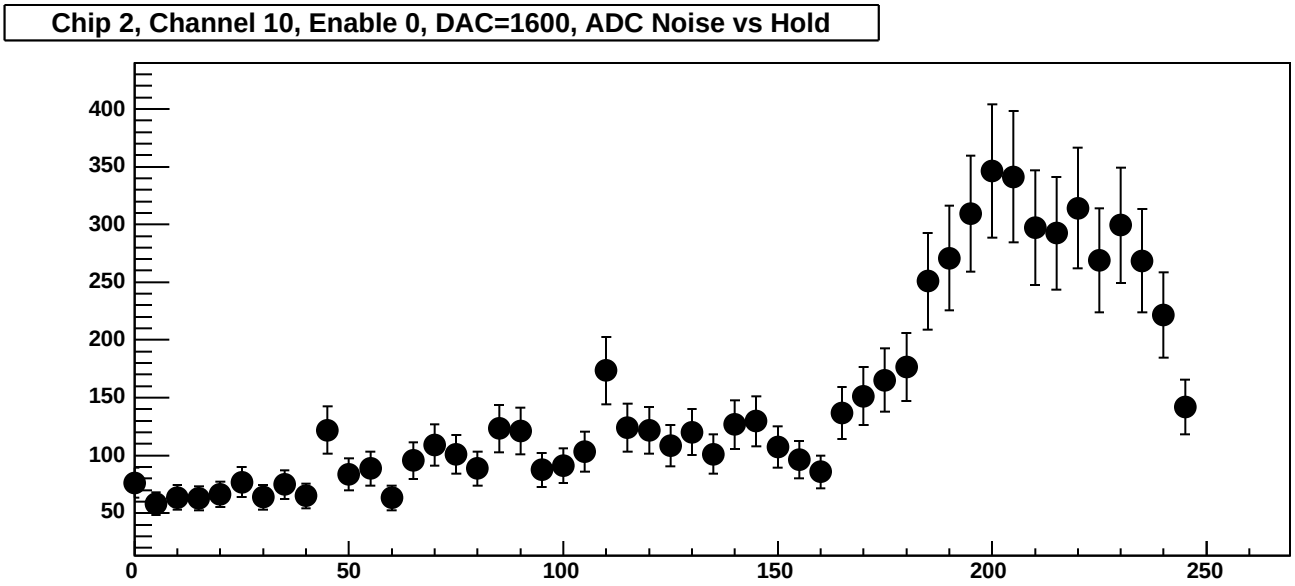


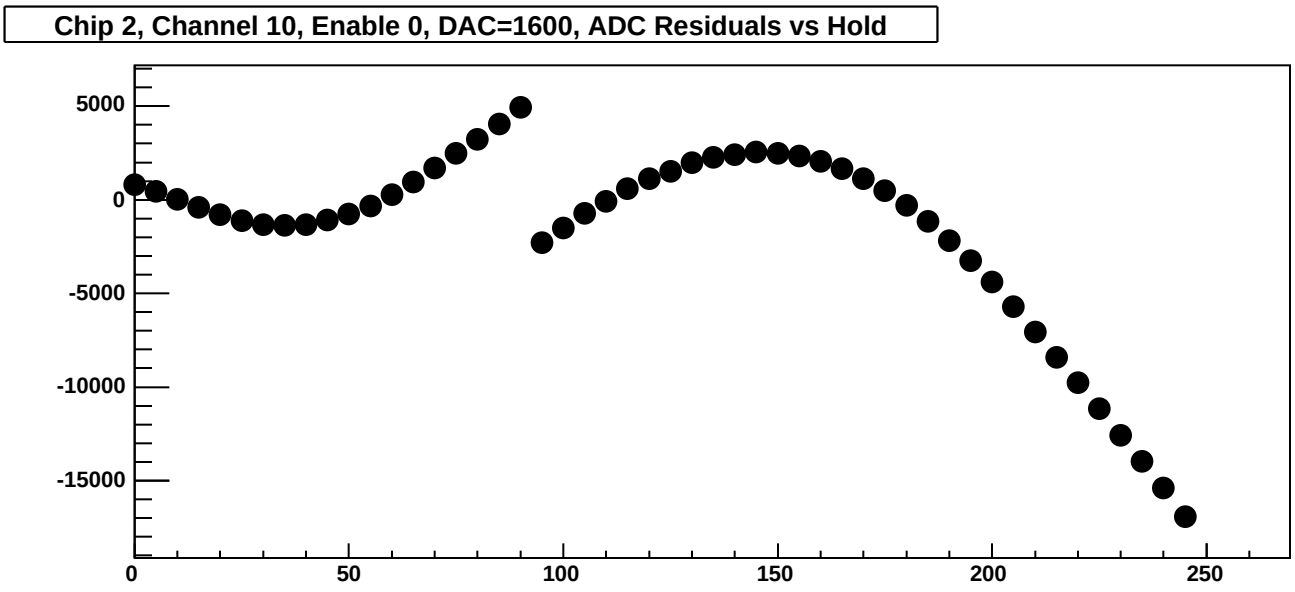

Chip 2, Channel 9, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 9.711e+04 / 41
p0 -4875 ± 3.663
p1 79.05 ± 0.03138
p2 3.448e+04 ± 39.92
p3 73.98 ± 0.1325
p4 -55.8 ± 0.1349
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 2, Channel 9, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250

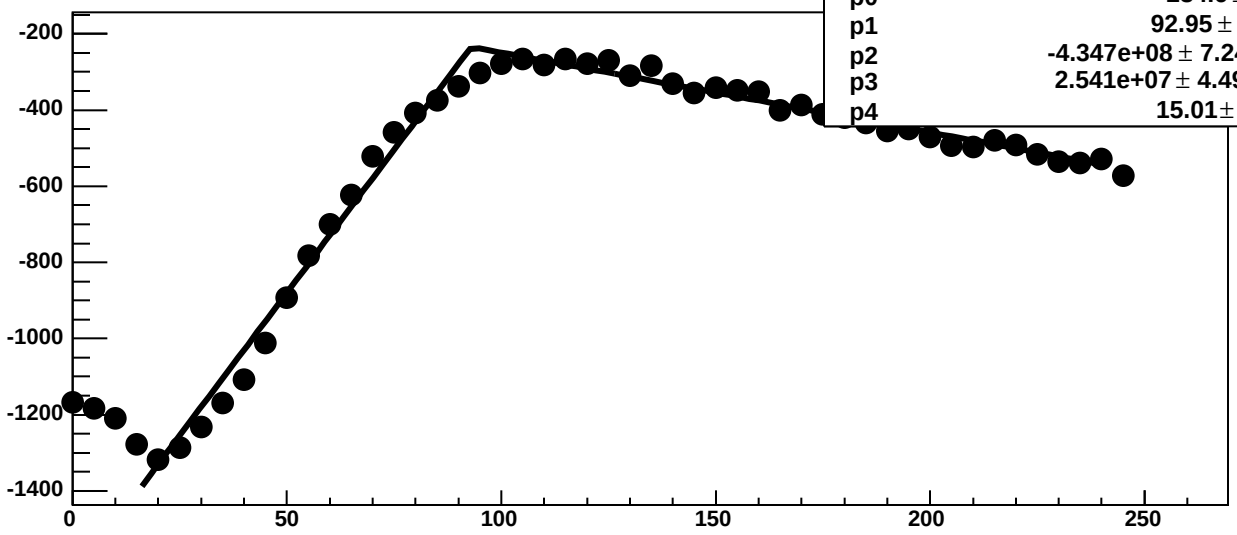

Chip 2, Channel 9, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250


Chip 2, Channel 10, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.861e+05 / 41
p0 5785 ± 43.38
p1 95 ± 2.415e-05
p2 8042 ± 42.85
p3 -155.2 ± 16.72
p4 81.7 ± 0.6246
20000
15000
10000
5000
0
0
50
100
150
200
250

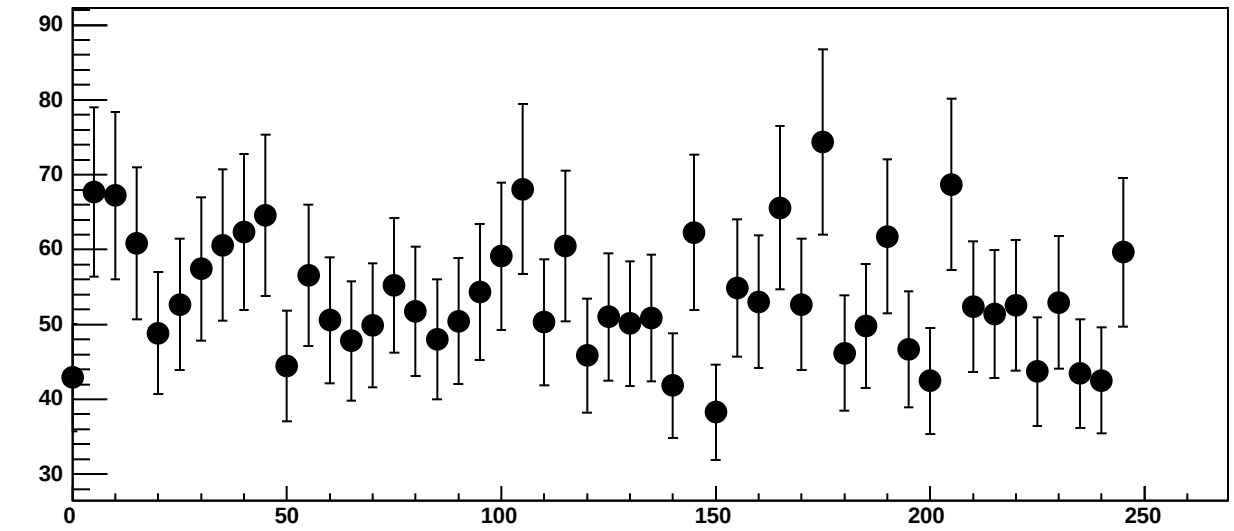

Chip 2, Channel 10, Enable 0, DAC=1600, ADC Noise vs Hold
400
350
300
250
200
150
100
50
0
50
100
150
200
250


Chip 2, Channel 10, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
0
50
100
150
200
250

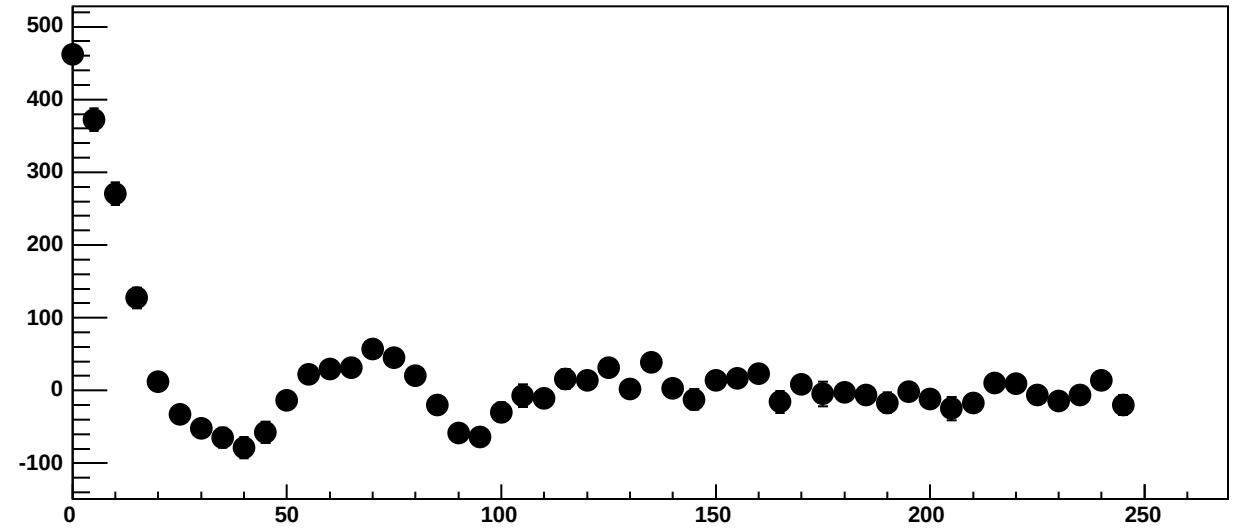
Chip 2, Channel 10, Enable 1, DAC=1600, ADC Mean vs Hold

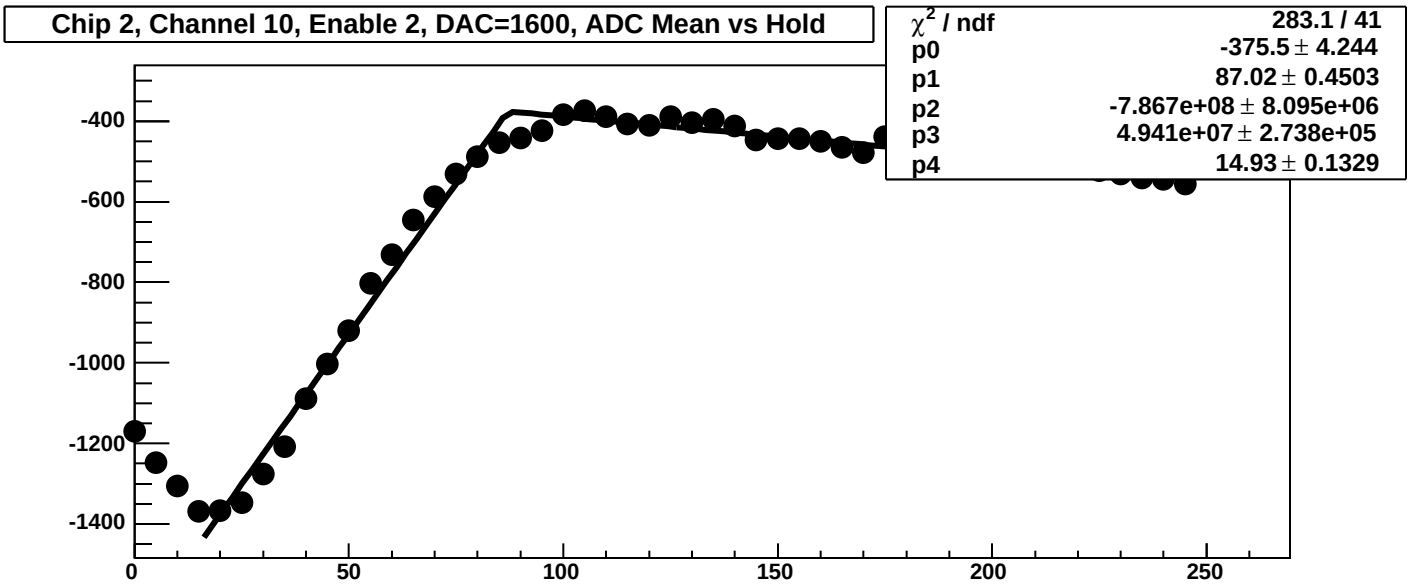


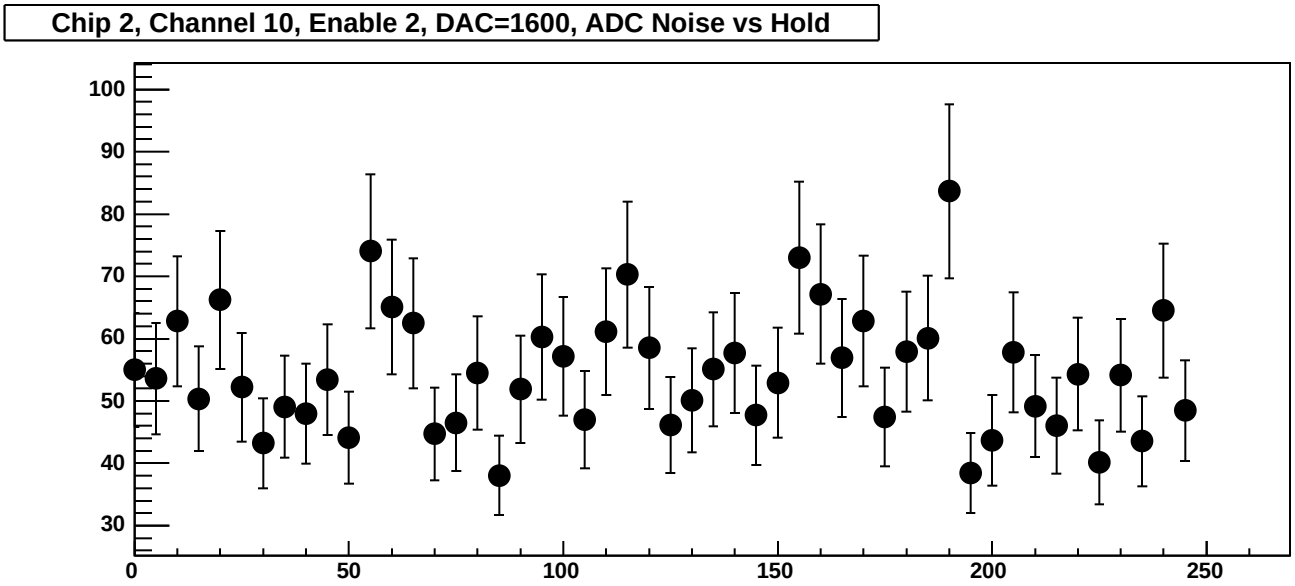
Chip 2, Channel 10, Enable 1, DAC=1600, ADC Noise vs Hold

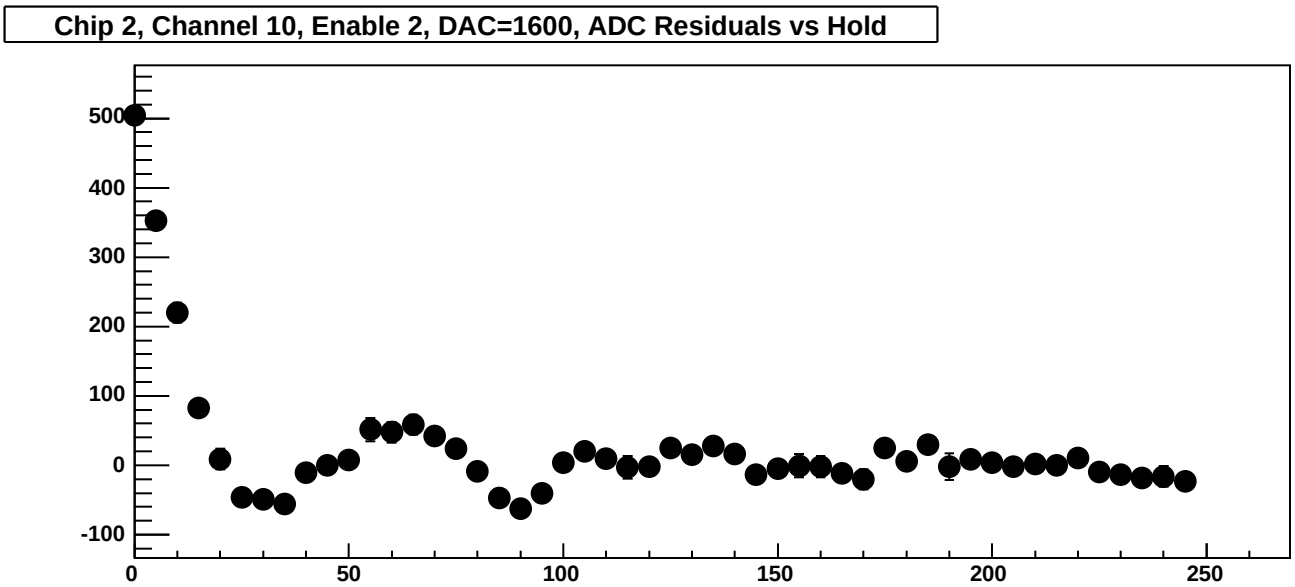


Chip 2, Channel 10, Enable 1, DAC=1600, ADC Residuals vs Hold

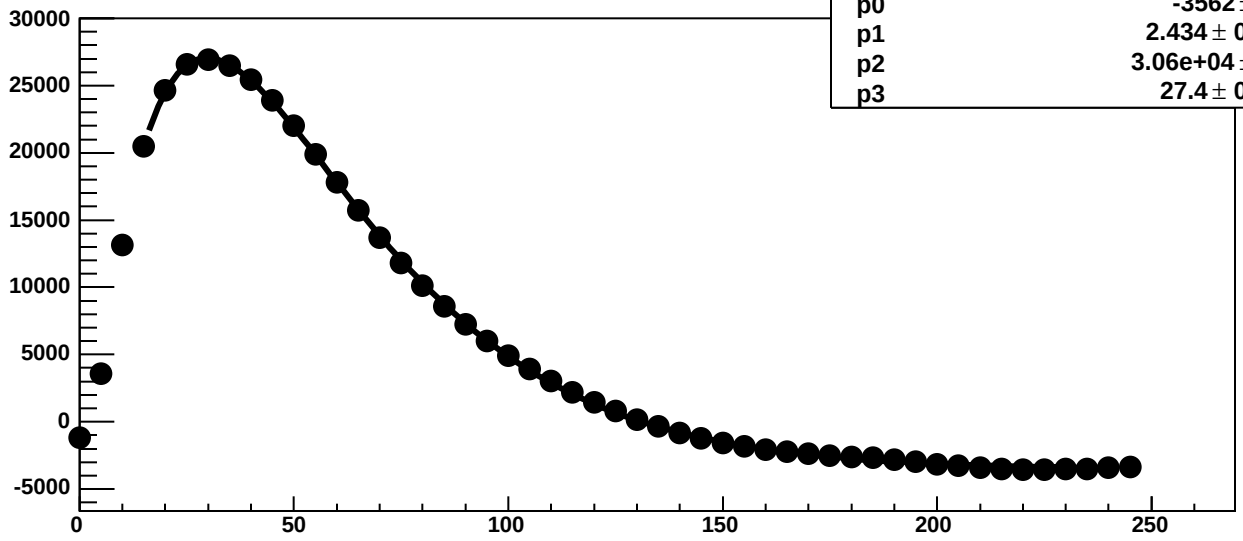



Chip 2, Channel 10, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 283.1 / 41
p0 -375.5 ± 4.244
p1 87.02 ± 0.4503
p2 -7.867e+08 ± 8.095e+06
p3 4.941e+07 ± 2.738e+05
p4 14.93 ± 0.1329
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

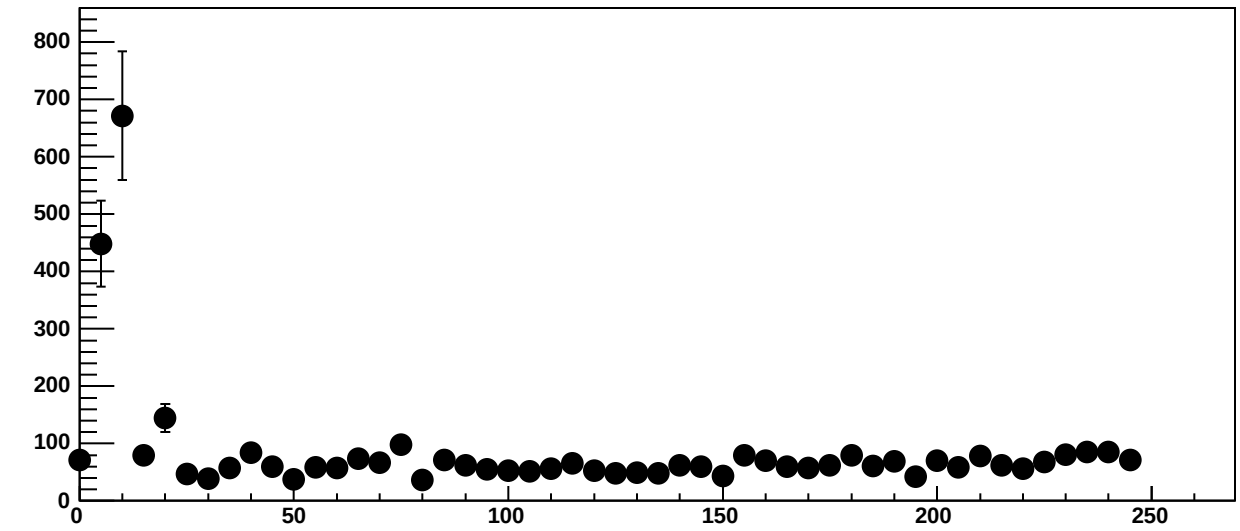

Chip 2, Channel 10, Enable 2, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 2, Channel 10, Enable 2, DAC=1600, ADC Residuals vs Hold
500
400
300
200
100
0
-100
0
50
100
150
200
250

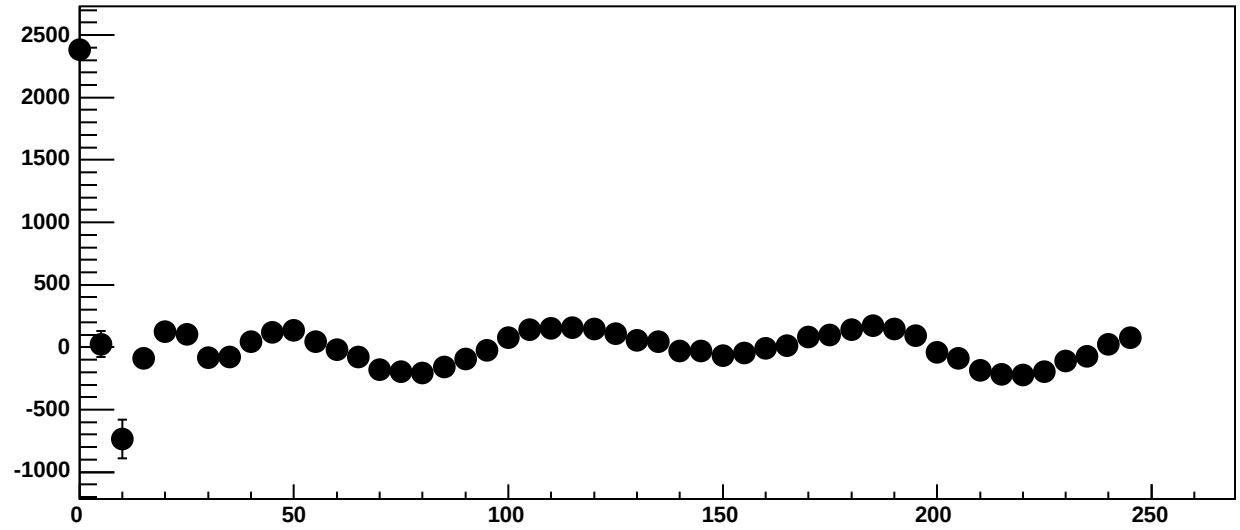
Chip 2, Channel 10, Enable 3!, DAC=1600, ADC Mean vs Hold

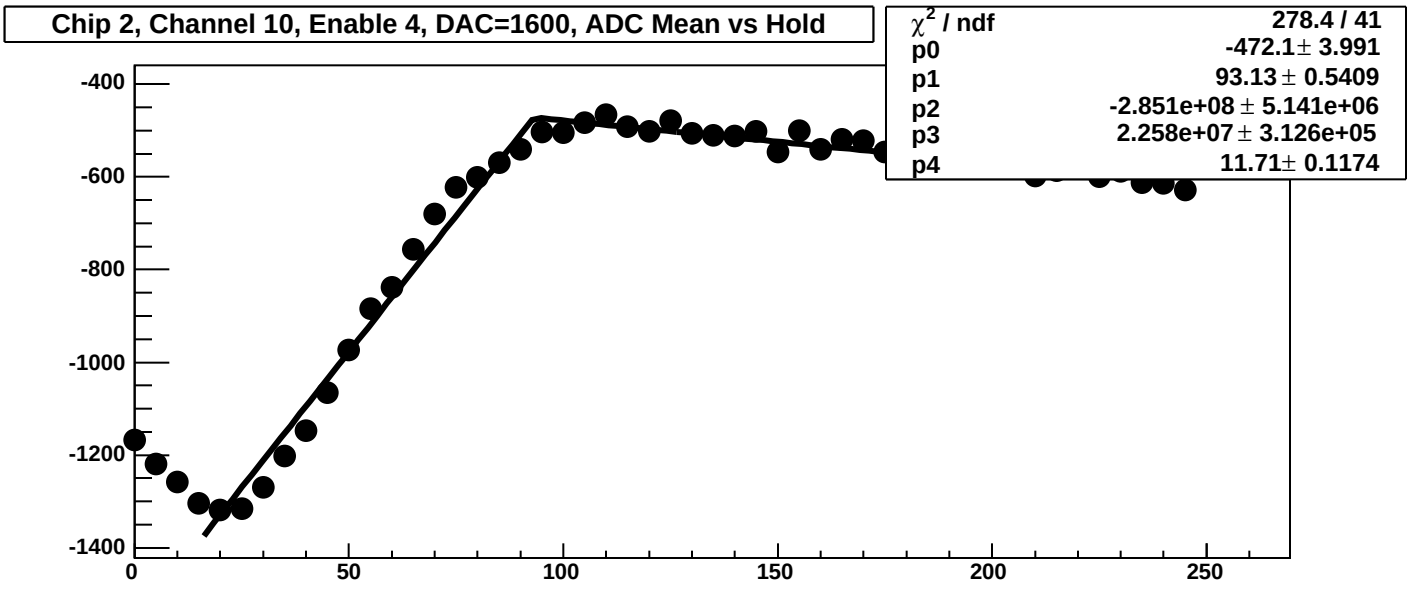


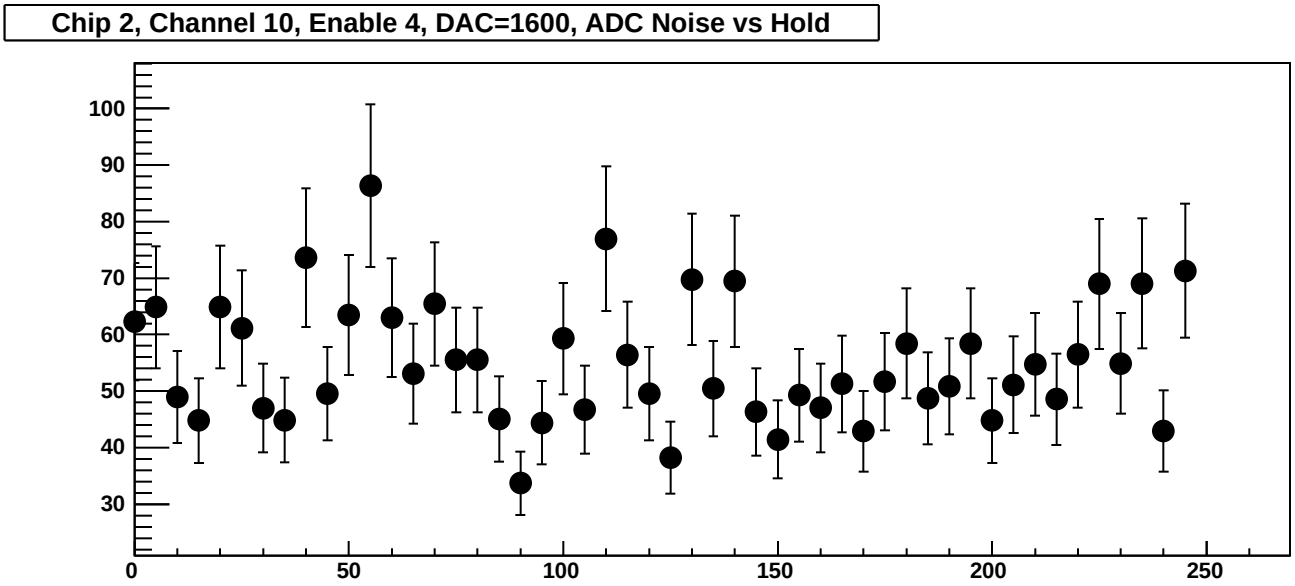
Chip 2, Channel 10, Enable 3!, DAC=1600, ADC Noise vs Hold

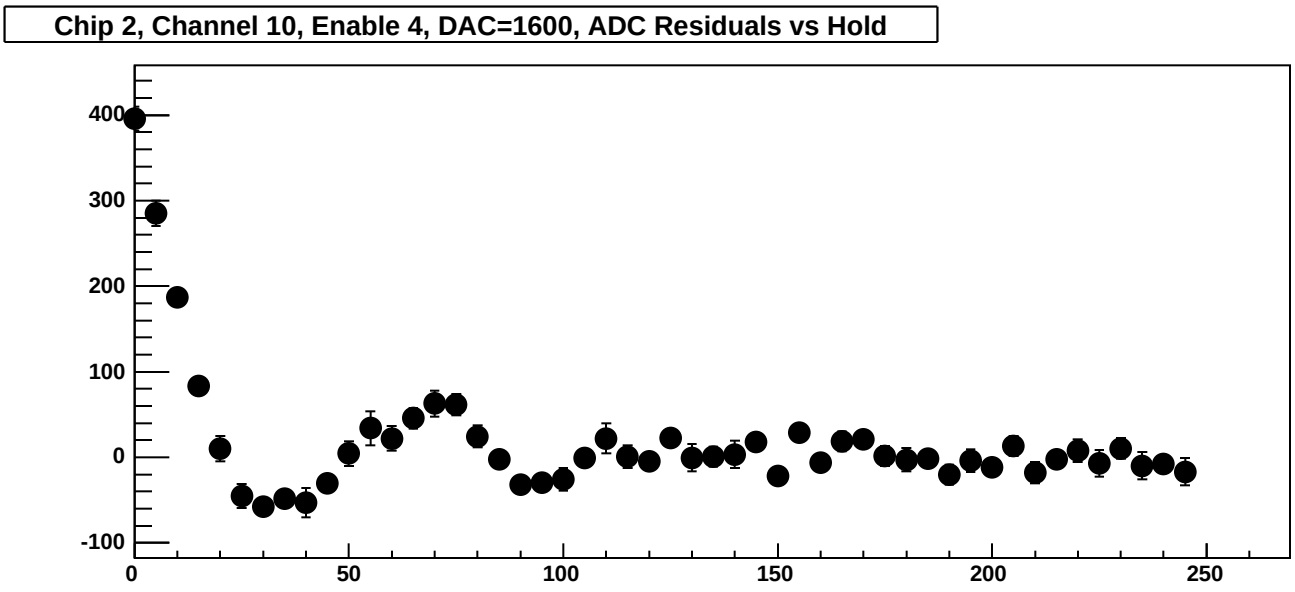


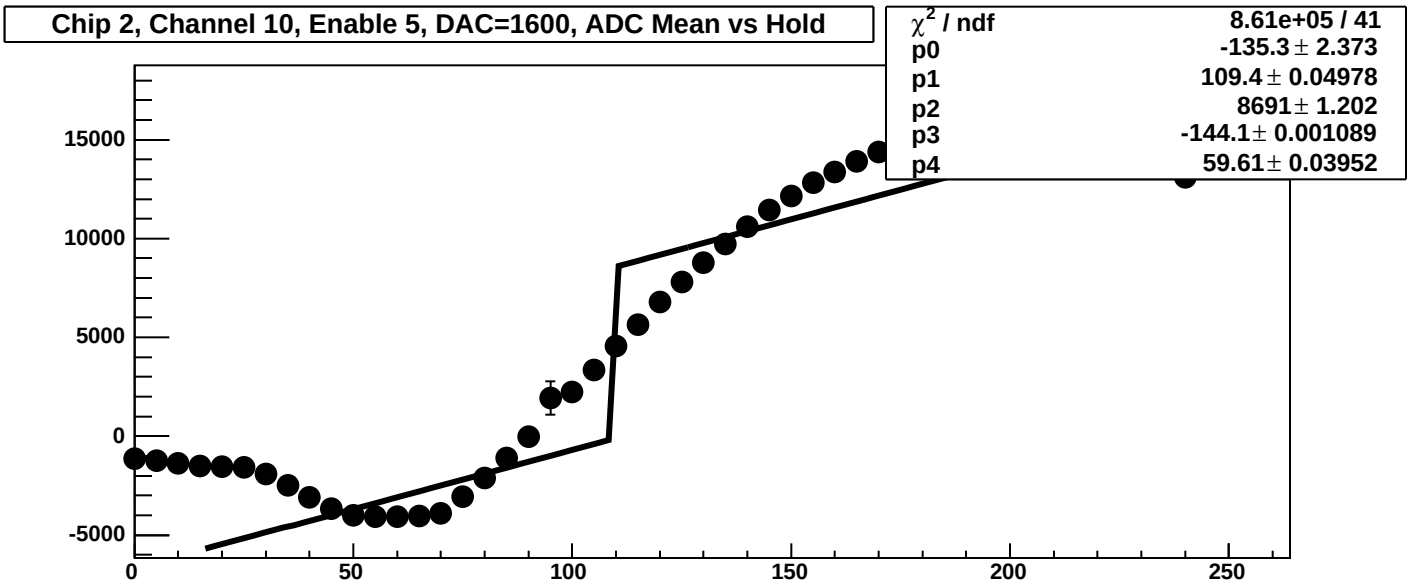
Chip 2, Channel 10, Enable 3!, DAC=1600, ADC Residuals vs Hold

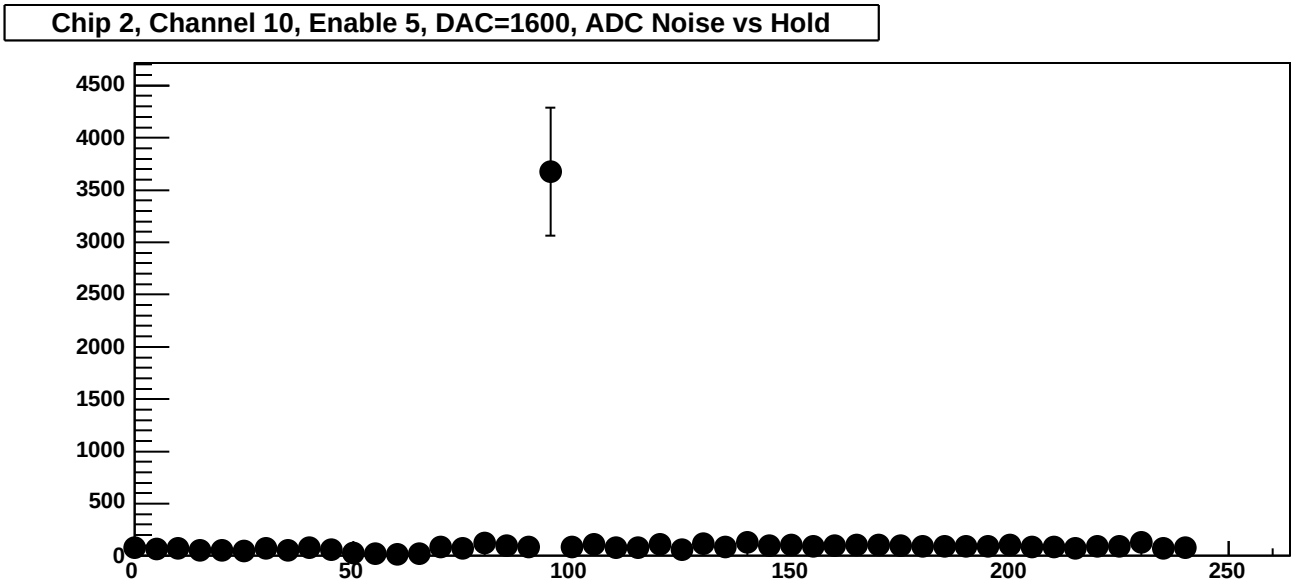


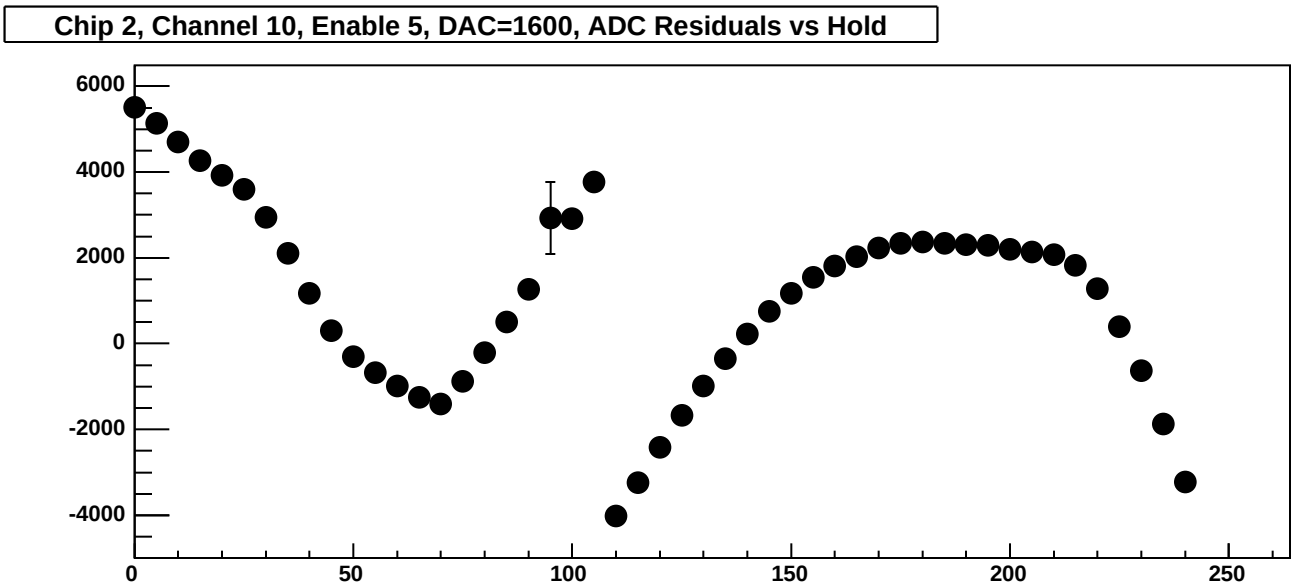

Chip 2, Channel 10, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 278.4 / 41
p0 -472.1 ± 3.991
p1 93.13 ± 0.5409
p2 -2.851e+08 ± 5.141e+06
p3 2.258e+07 ± 3.126e+05
p4 11.71 ± 0.1174
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

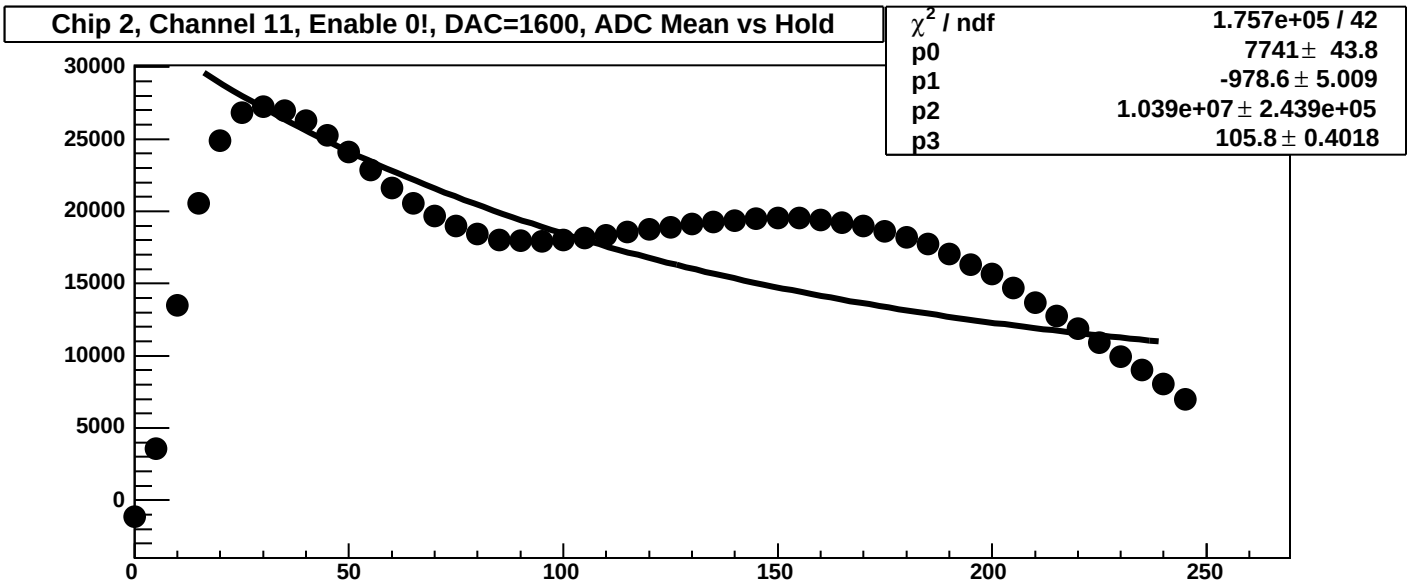

Chip 2, Channel 10, Enable 4, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250

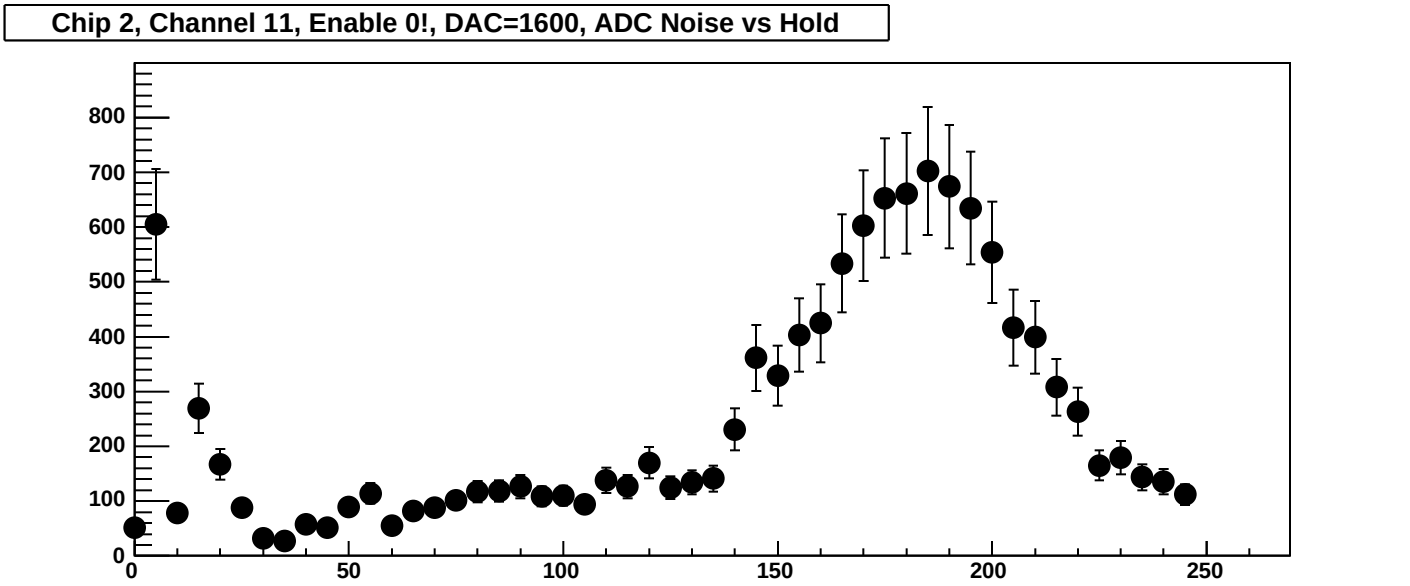

Chip 2, Channel 10, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

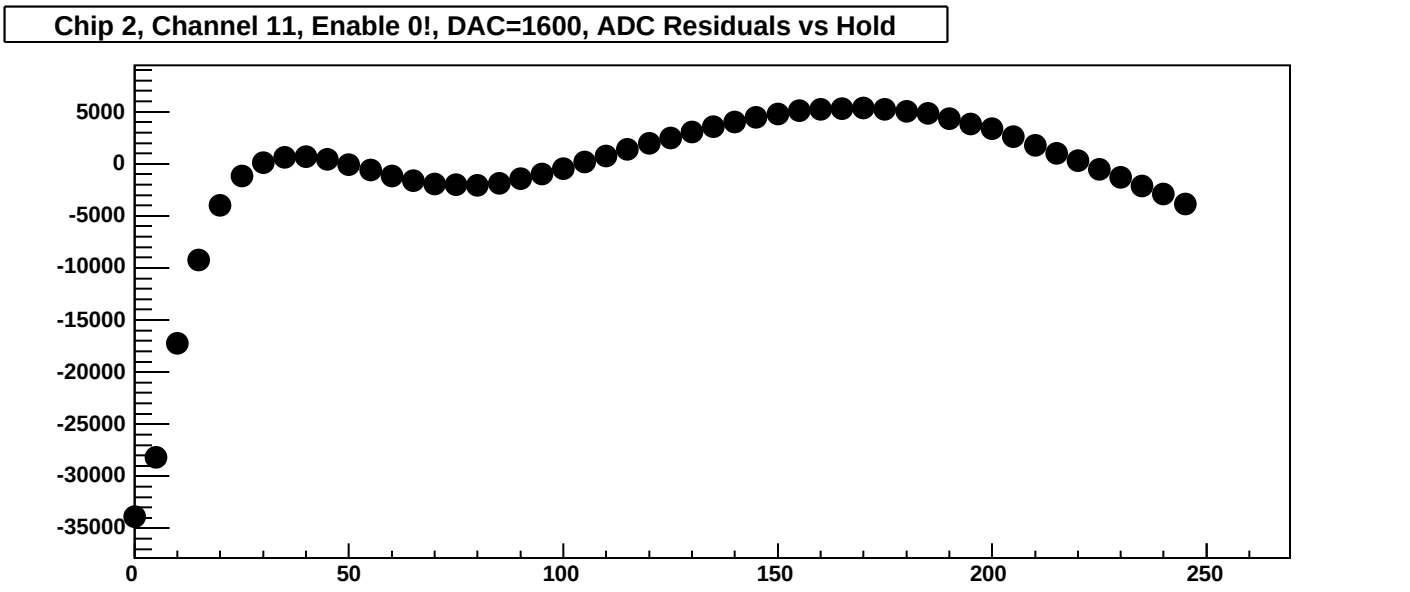

Chip 2, Channel 10, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 8.61e+05 / 41
p0 -135.3 ± 2.373
p1 109.4 ± 0.04978
p2 8691 ± 1.202
p3 -144.1 ± 0.001089
p4 59.61 ± 0.03952
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 2, Channel 10, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250

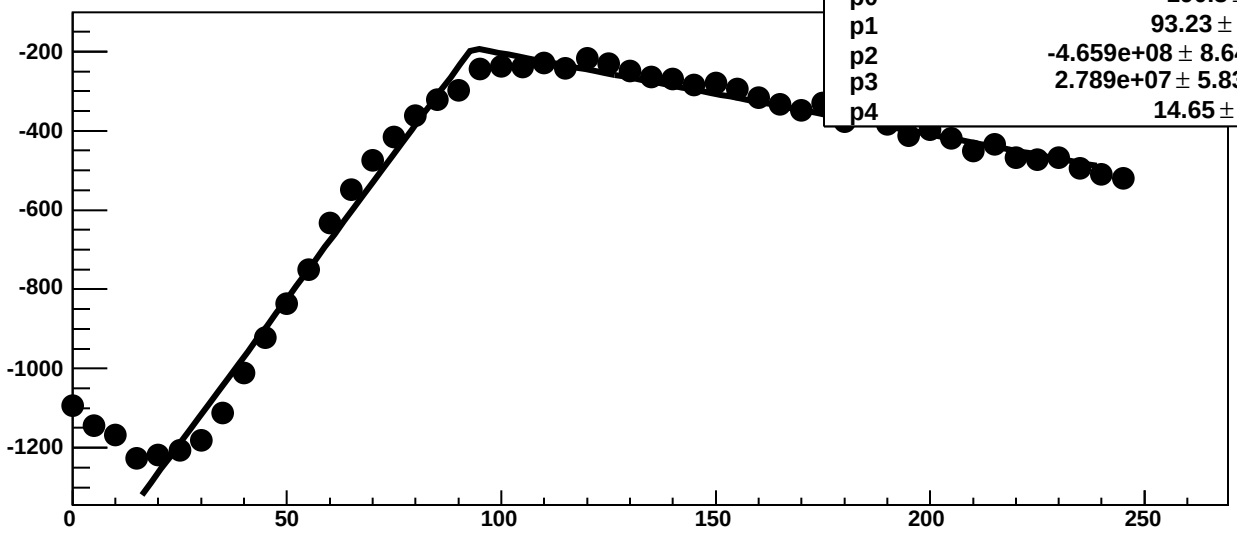

Chip 2, Channel 10, Enable 5, DAC=1600, ADC Residuals vs Hold
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250


Chip 2, Channel 11, Enable 0!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.757e+05 / 42
p0 7741 ± 43.8
p1 -978.6 ± 5.009
p2 1.039e+07 ± 2.439e+05
p3 105.8 ± 0.4018
30000
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

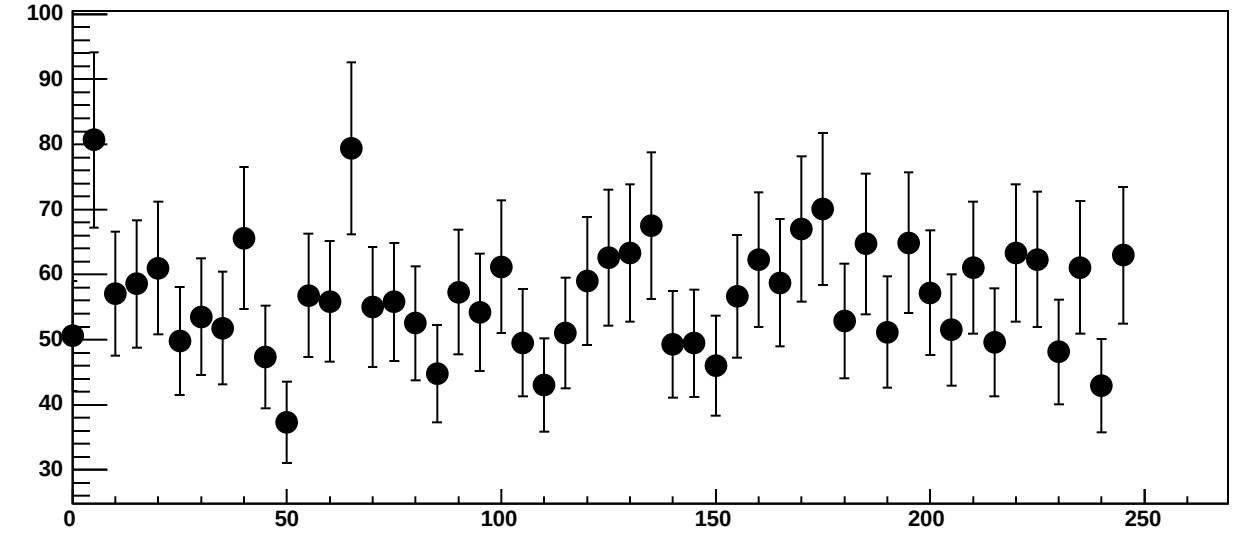

Chip 2, Channel 11, Enable 0!, DAC=1600, ADC Noise vs Hold
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 2, Channel 11, Enable 0!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
-35000
0
50
100
150
200
250

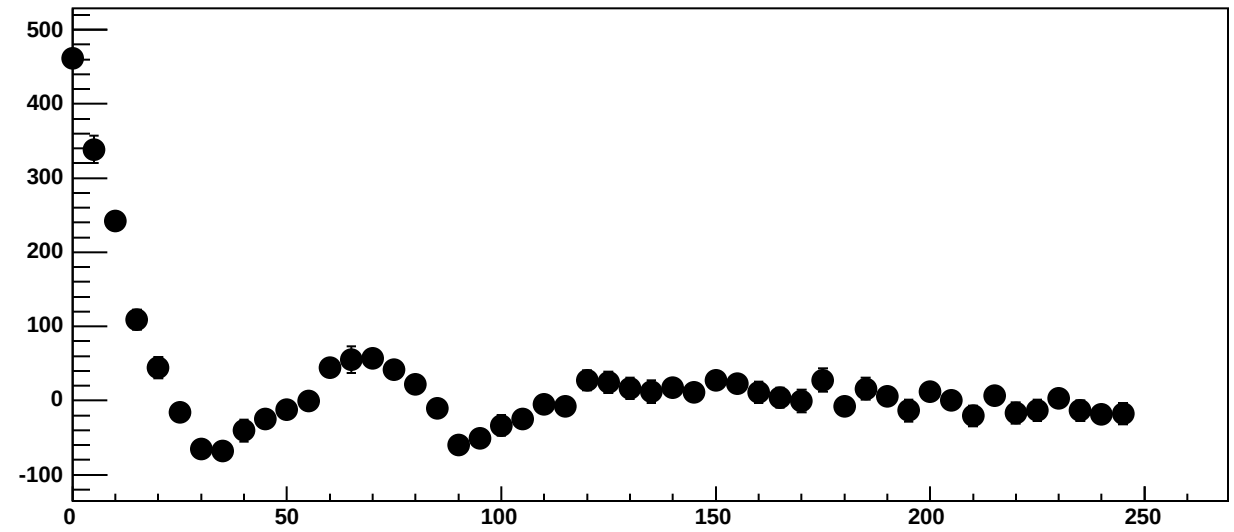
Chip 2, Channel 11, Enable 1, DAC=1600, ADC Mean vs Hold



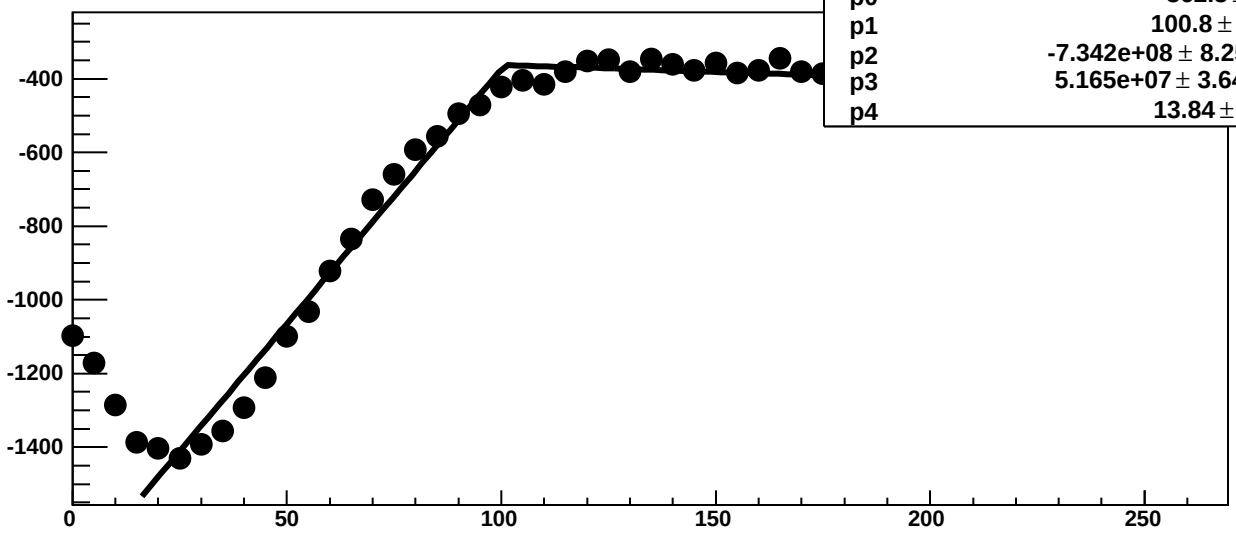
Chip 2, Channel 11, Enable 1, DAC=1600, ADC Noise vs Hold



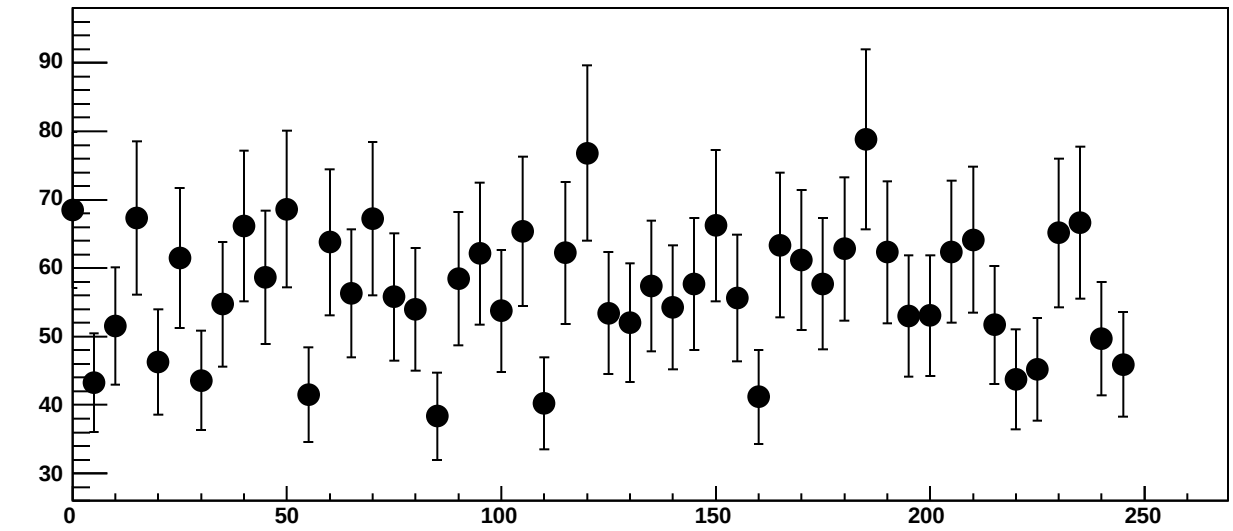
Chip 2, Channel 11, Enable 1, DAC=1600, ADC Residuals vs Hold



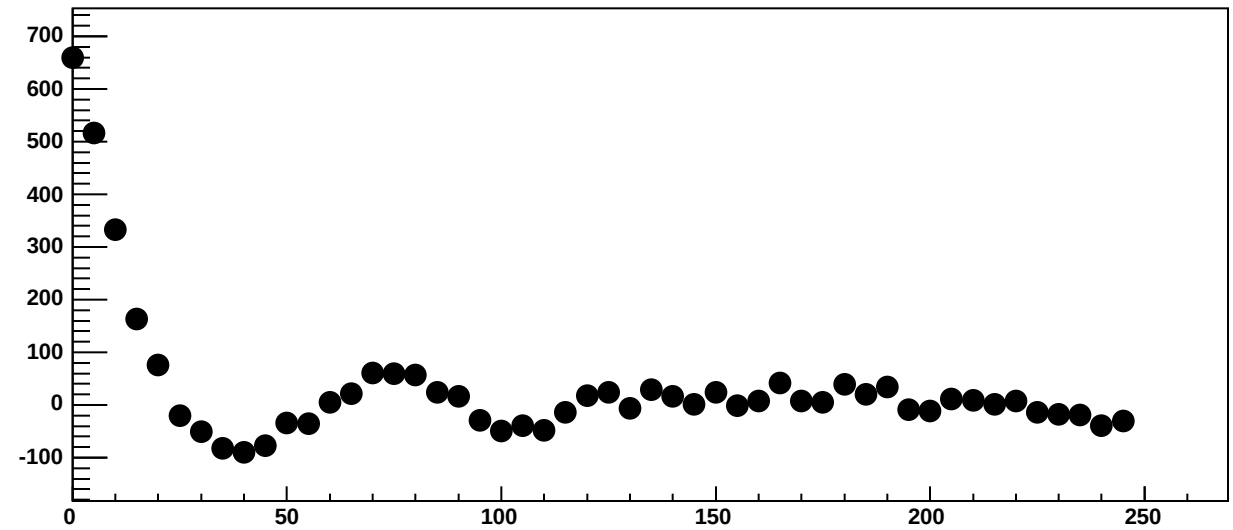
Chip 2, Channel 11, Enable 2, DAC=1600, ADC Mean vs Hold



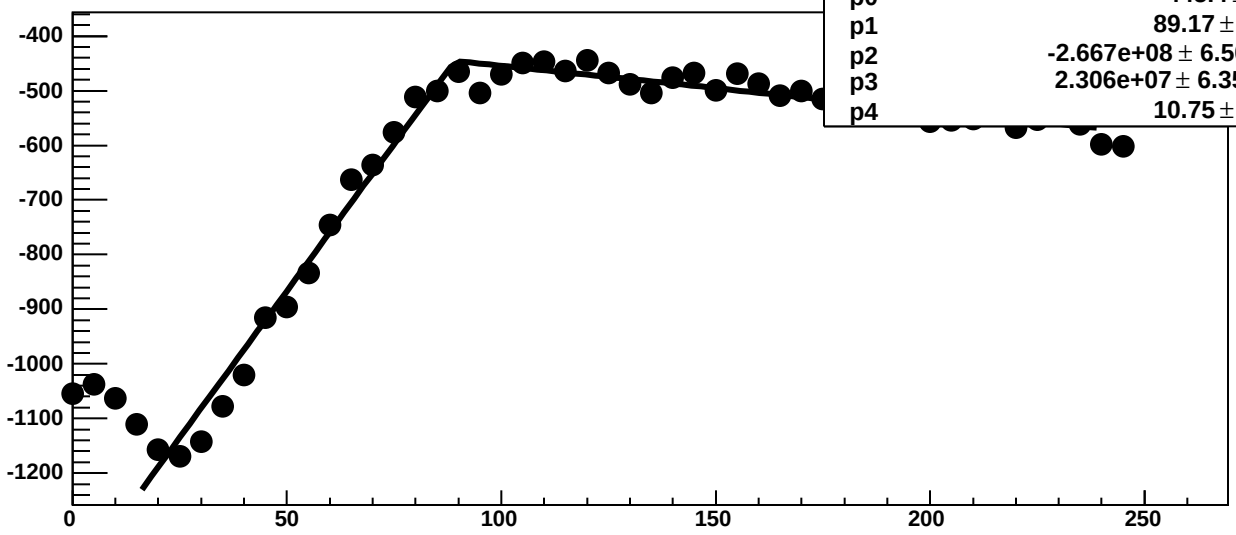
Chip 2, Channel 11, Enable 2, DAC=1600, ADC Noise vs Hold



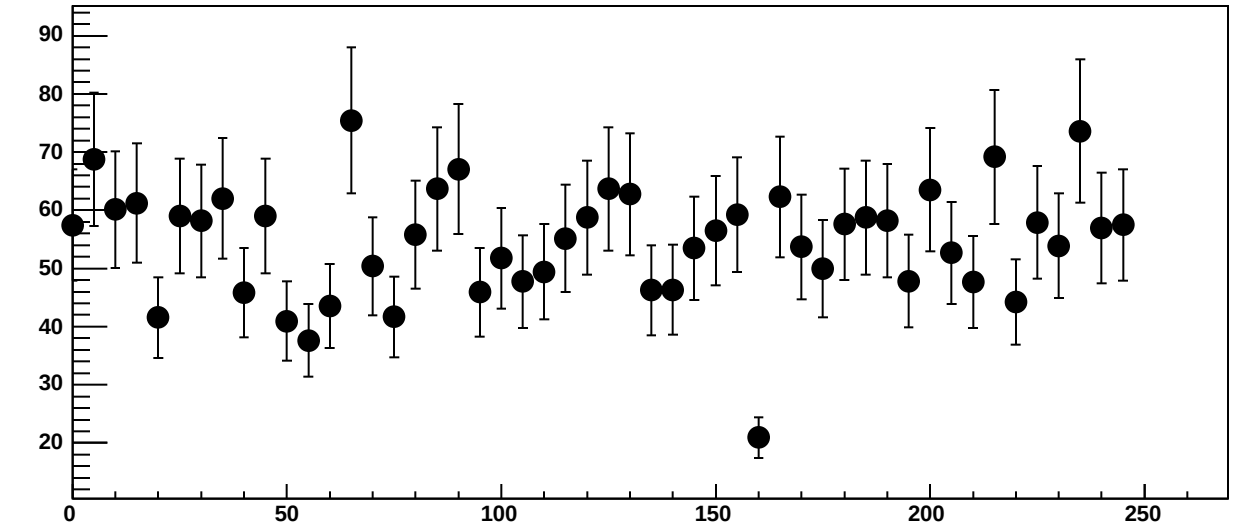
Chip 2, Channel 11, Enable 2, DAC=1600, ADC Residuals vs Hold



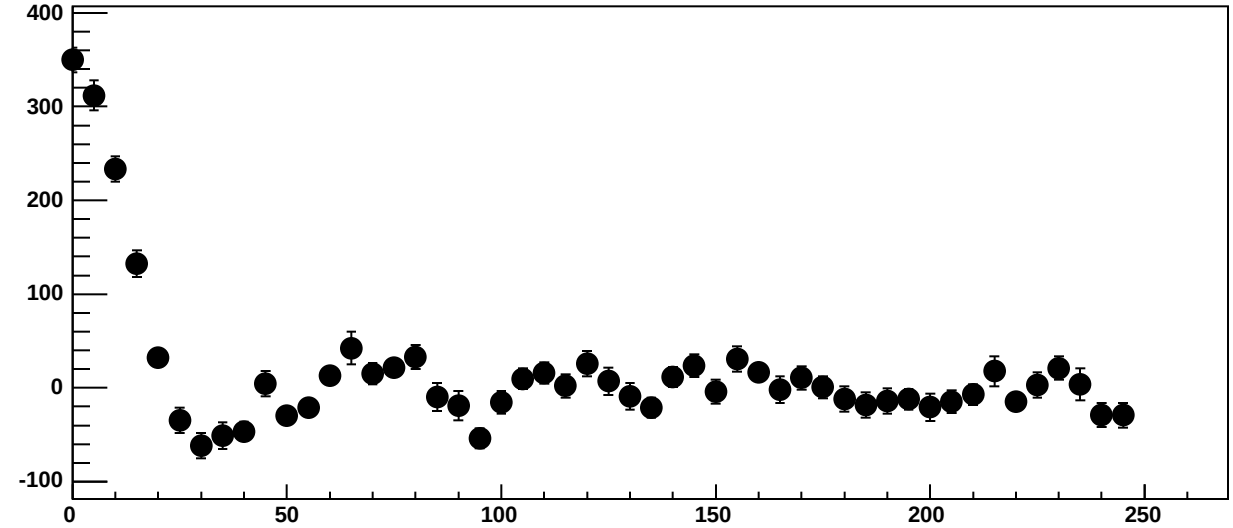
Chip 2, Channel 11, Enable 3, DAC=1600, ADC Mean vs Hold



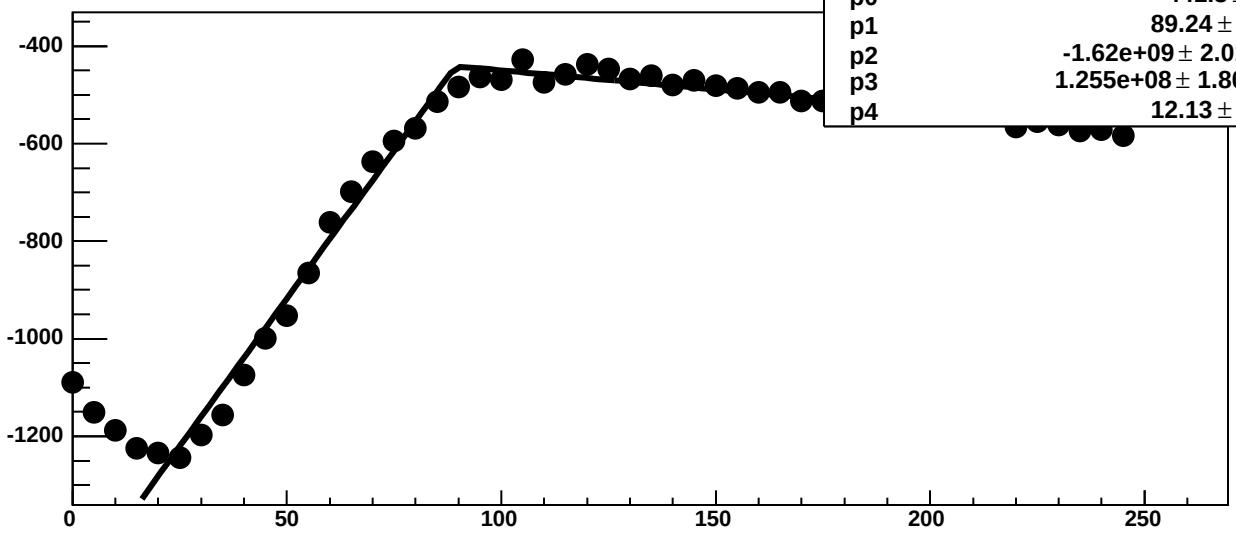
Chip 2, Channel 11, Enable 3, DAC=1600, ADC Noise vs Hold



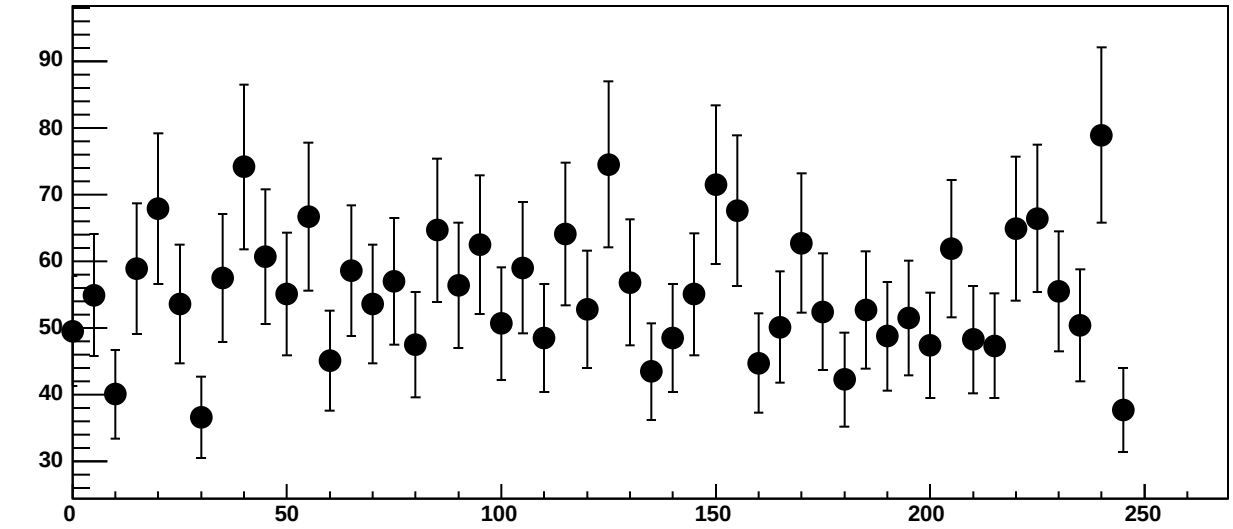
Chip 2, Channel 11, Enable 3, DAC=1600, ADC Residuals vs Hold



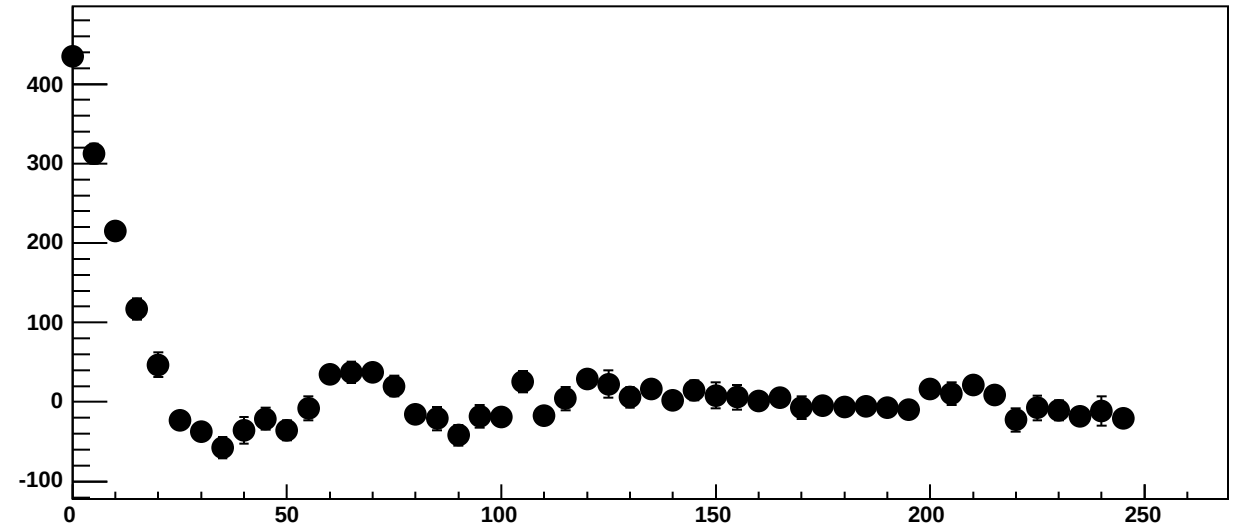
Chip 2, Channel 11, Enable 4, DAC=1600, ADC Mean vs Hold

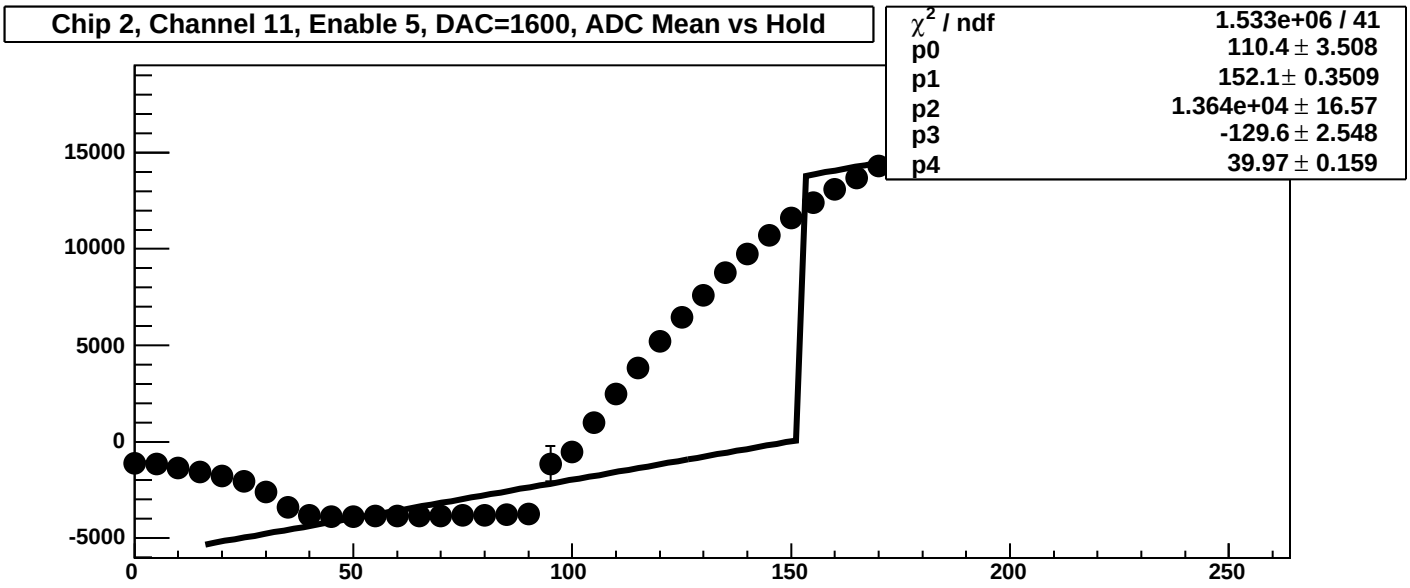


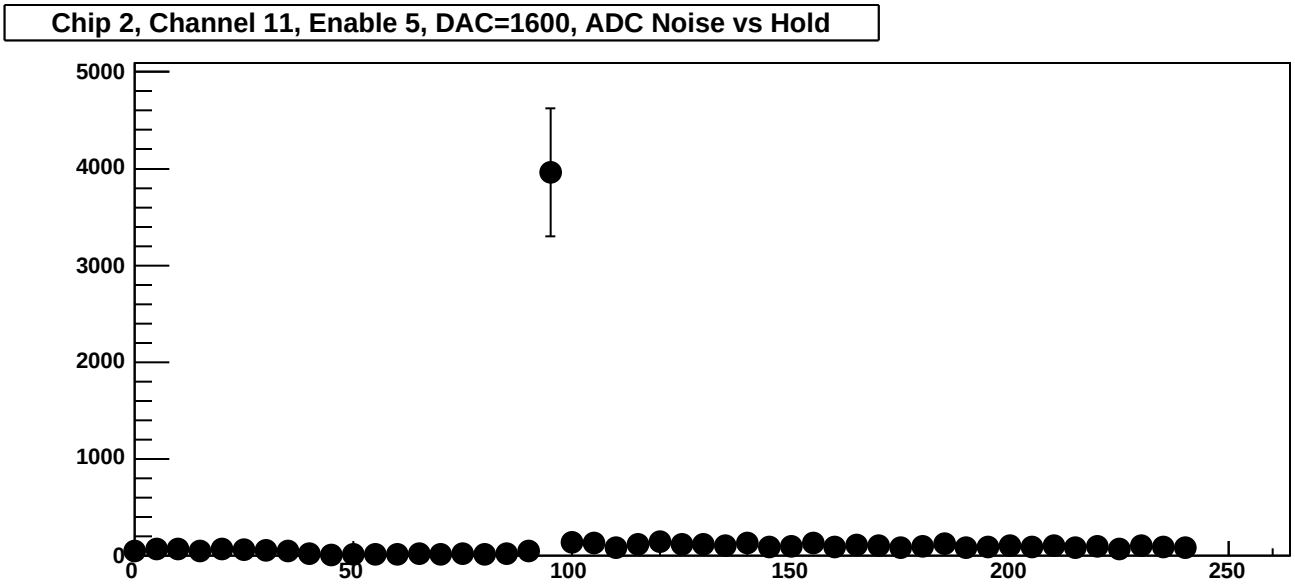
Chip 2, Channel 11, Enable 4, DAC=1600, ADC Noise vs Hold

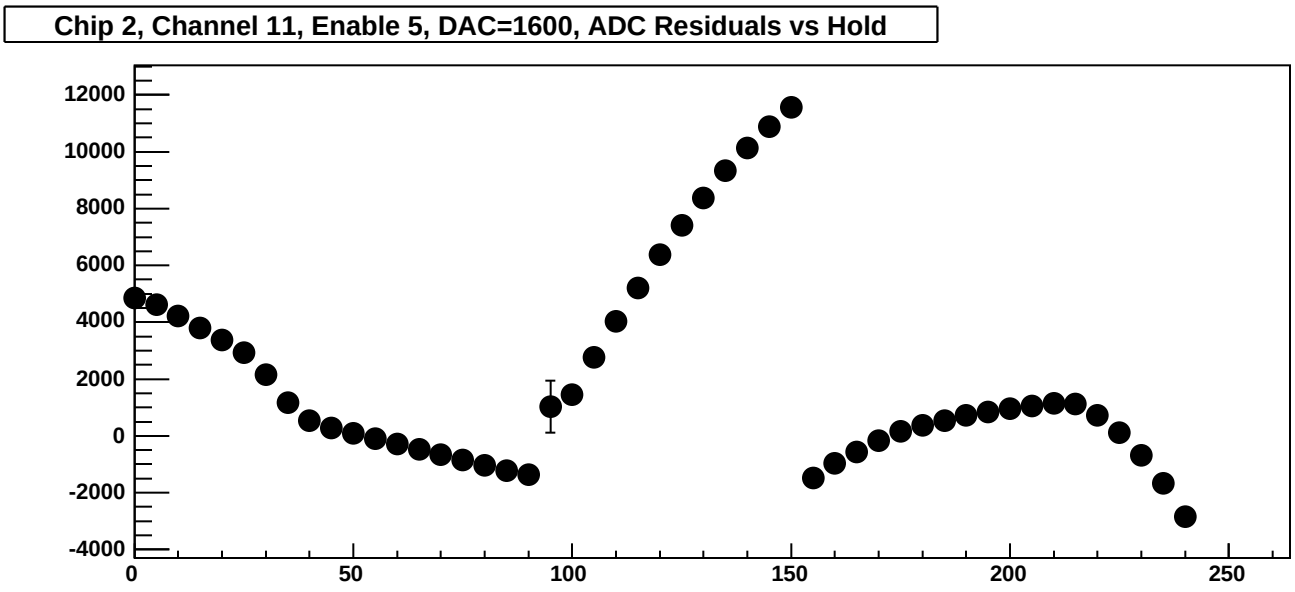


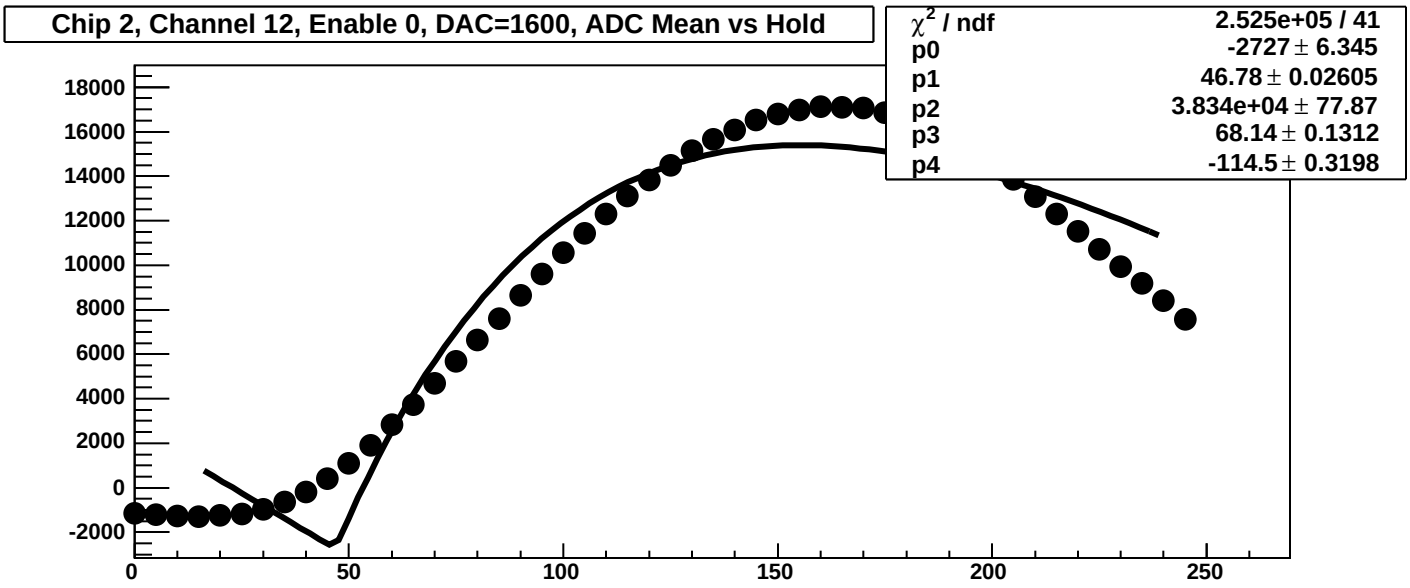
Chip 2, Channel 11, Enable 4, DAC=1600, ADC Residuals vs Hold

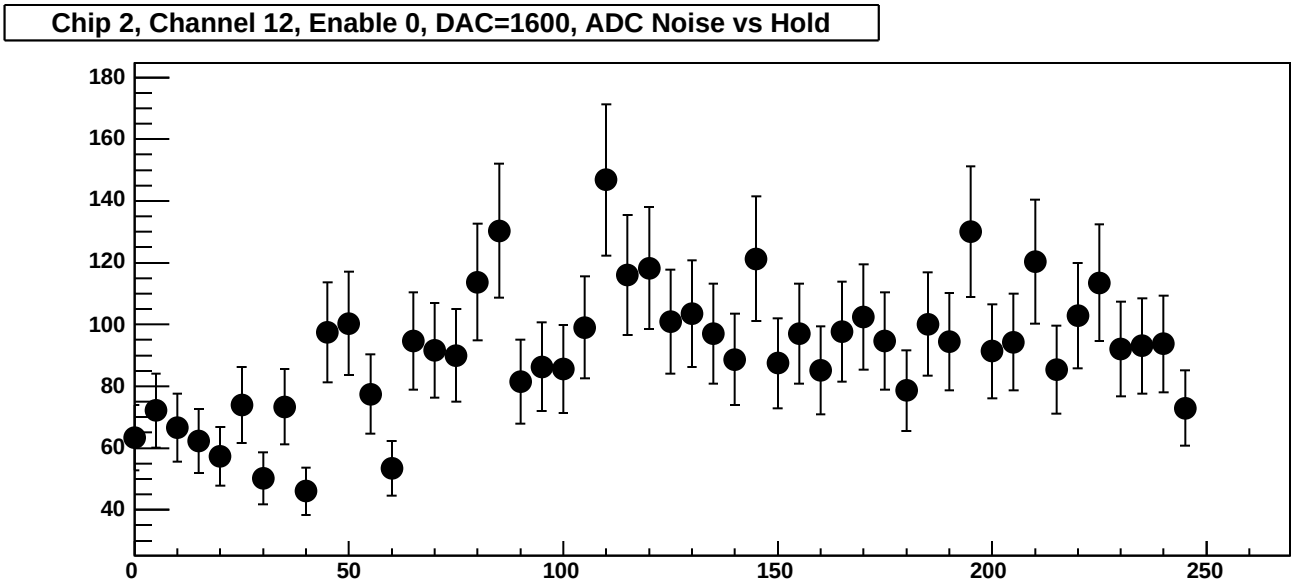


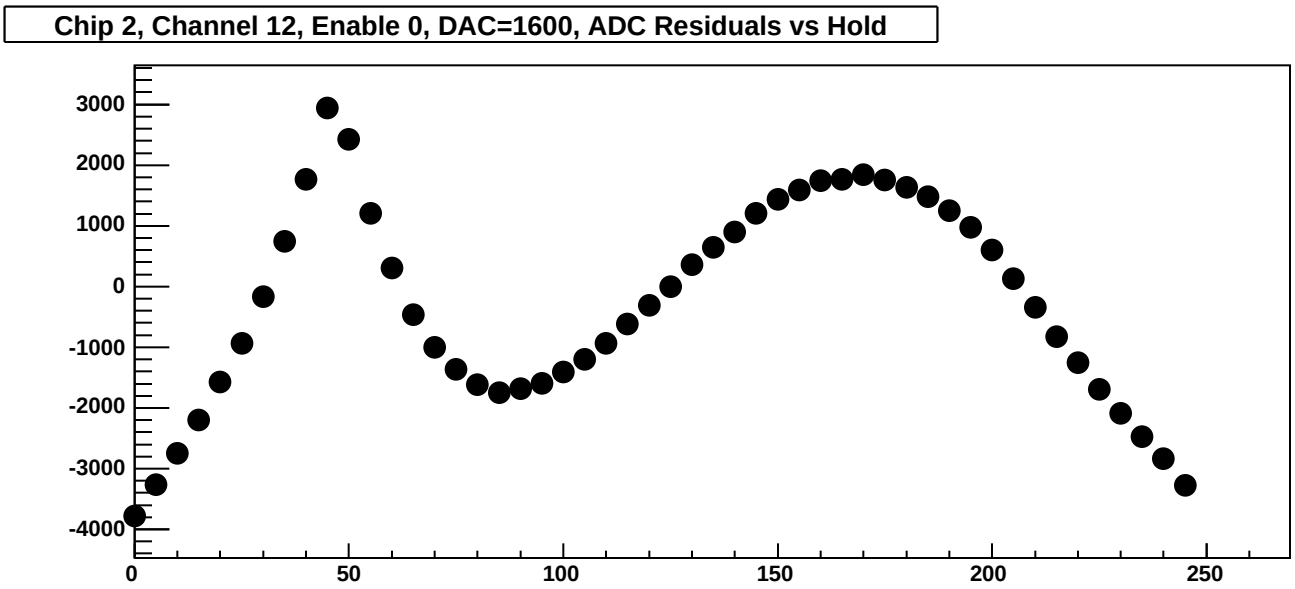

Chip 2, Channel 11, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.533e+06 / 41
p0 110.4 ± 3.508
p1 152.1 ± 0.3509
p2 1.364e+04 ± 16.57
p3 -129.6 ± 2.548
p4 39.97 ± 0.159
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 2, Channel 11, Enable 5, DAC=1600, ADC Noise vs Hold
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

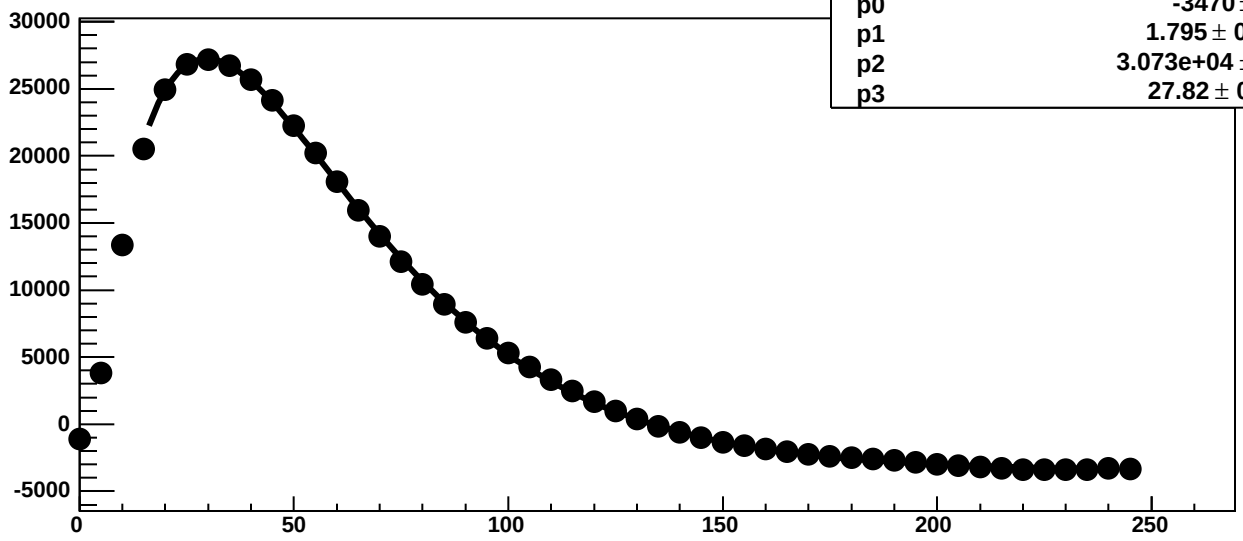

Chip 2, Channel 11, Enable 5, DAC=1600, ADC Residuals vs Hold
12000
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250


Chip 2, Channel 12, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.525e+05 / 41
p0 -2727 ± 6.345
p1 46.78 ± 0.02605
p2 3.834e+04 ± 77.87
p3 68.14 ± 0.1312
p4 -114.5 ± 0.3198
18000
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

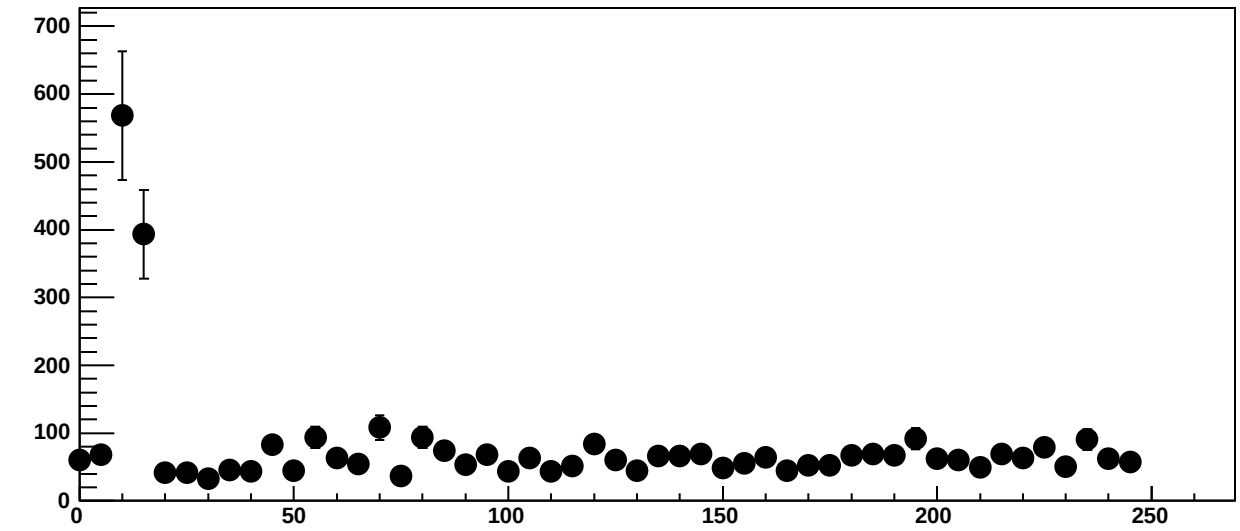

Chip 2, Channel 12, Enable 0, DAC=1600, ADC Noise vs Hold
180
160
140
120
100
80
60
40
0
50
100
150
200
250


Chip 2, Channel 12, Enable 0, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

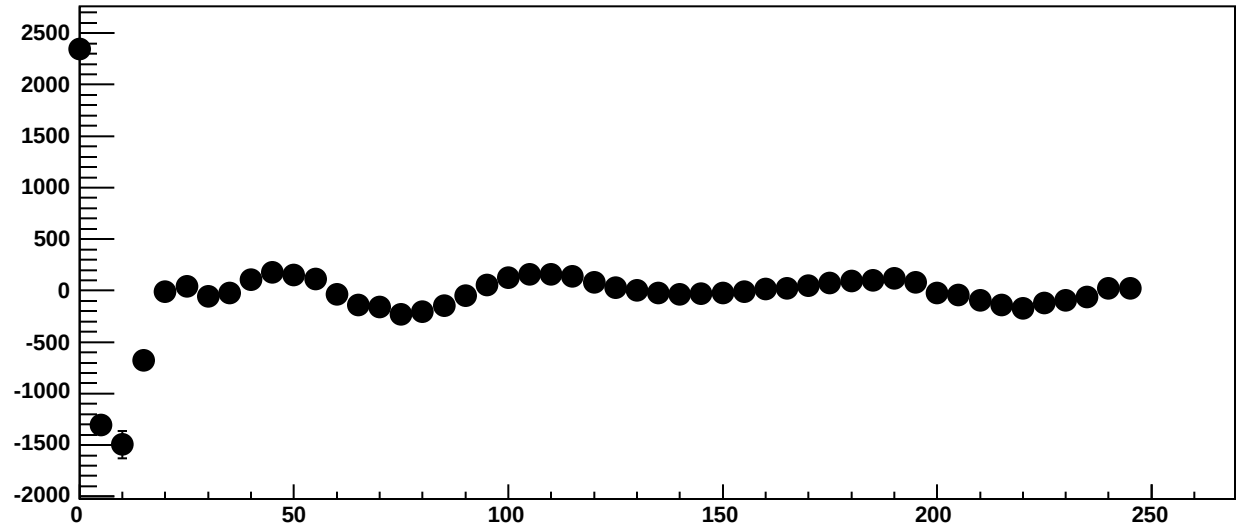
Chip 2, Channel 12, Enable 1!, DAC=1600, ADC Mean vs Hold

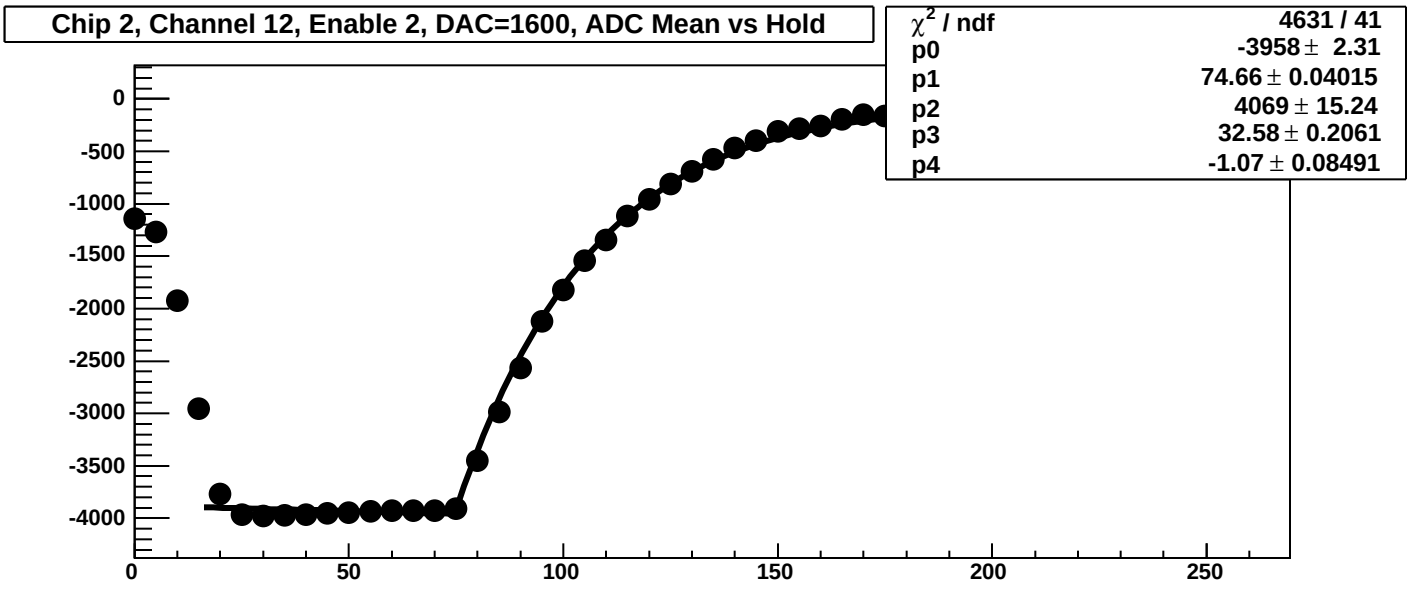


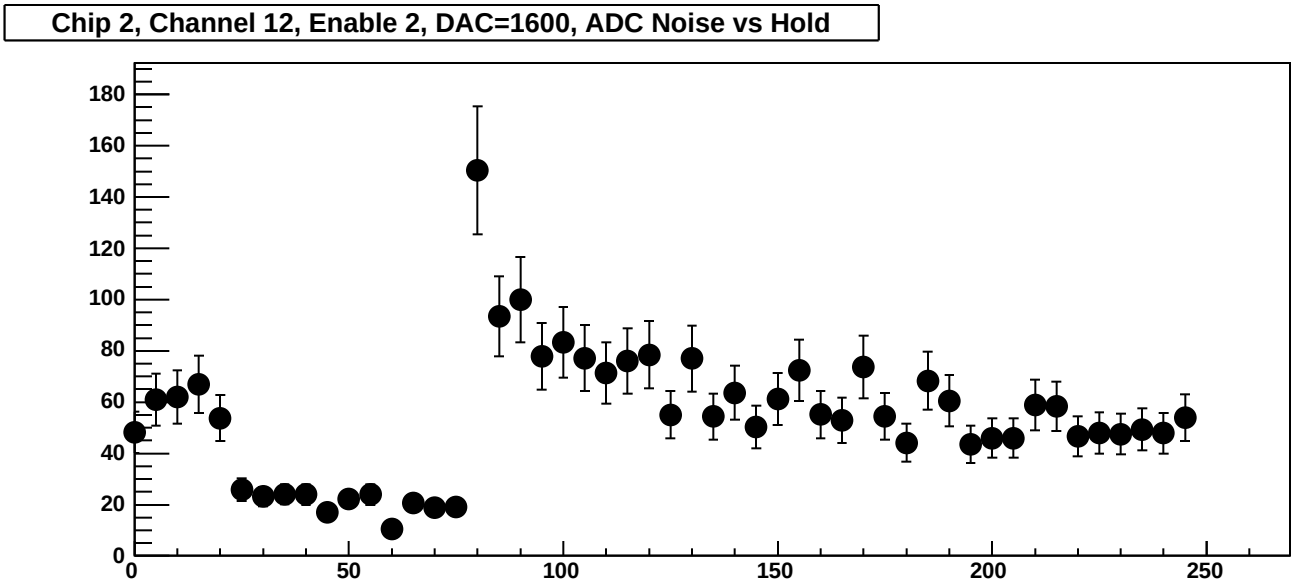
Chip 2, Channel 12, Enable 1!, DAC=1600, ADC Noise vs Hold

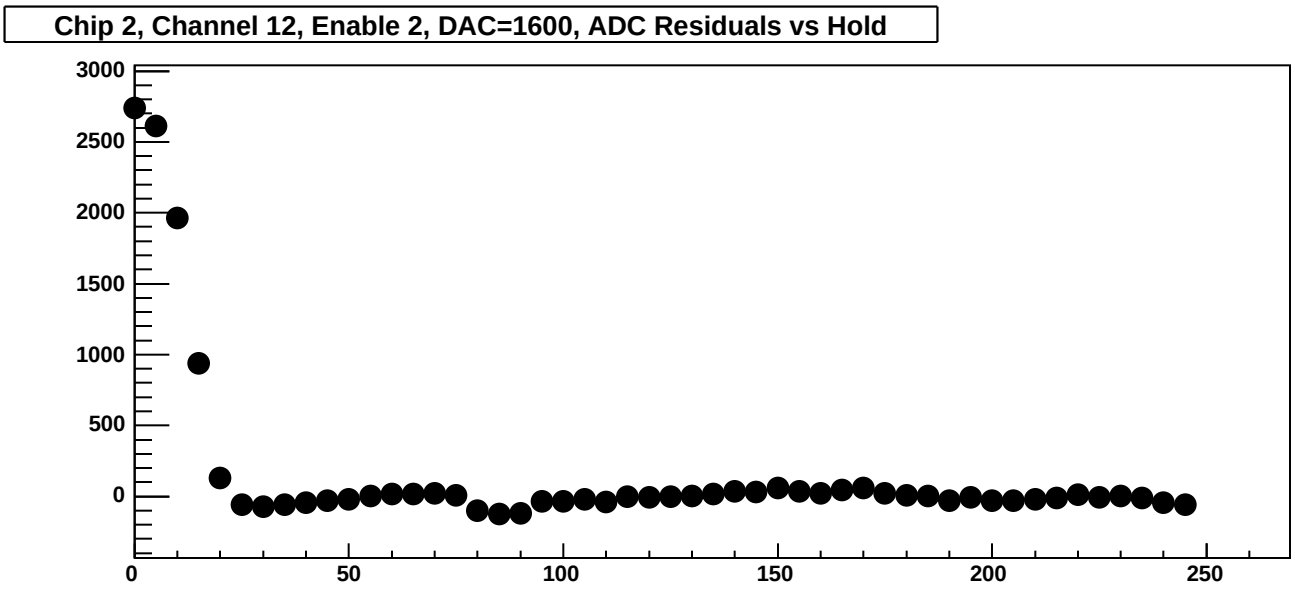


Chip 2, Channel 12, Enable 1!, DAC=1600, ADC Residuals vs Hold

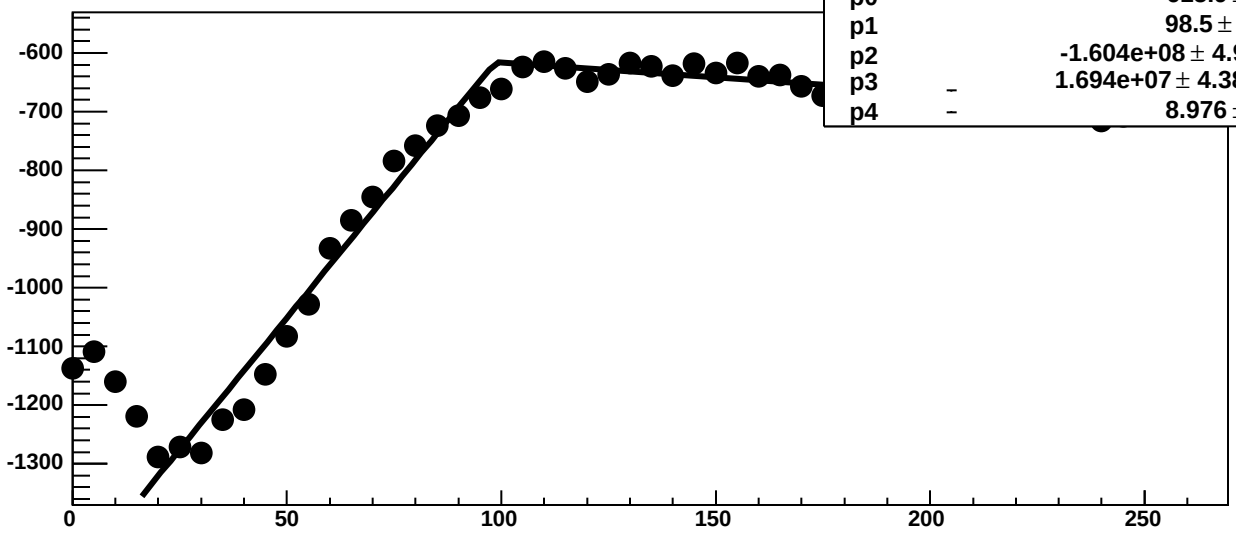



Chip 2, Channel 12, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 4631 / 41
p0 -3958 ± 2.31
p1 74.66 ± 0.04015
p2 4069 ± 15.24
p3 32.58 ± 0.2061
p4 -1.07 ± 0.08491
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
0
50
100
150
200
250

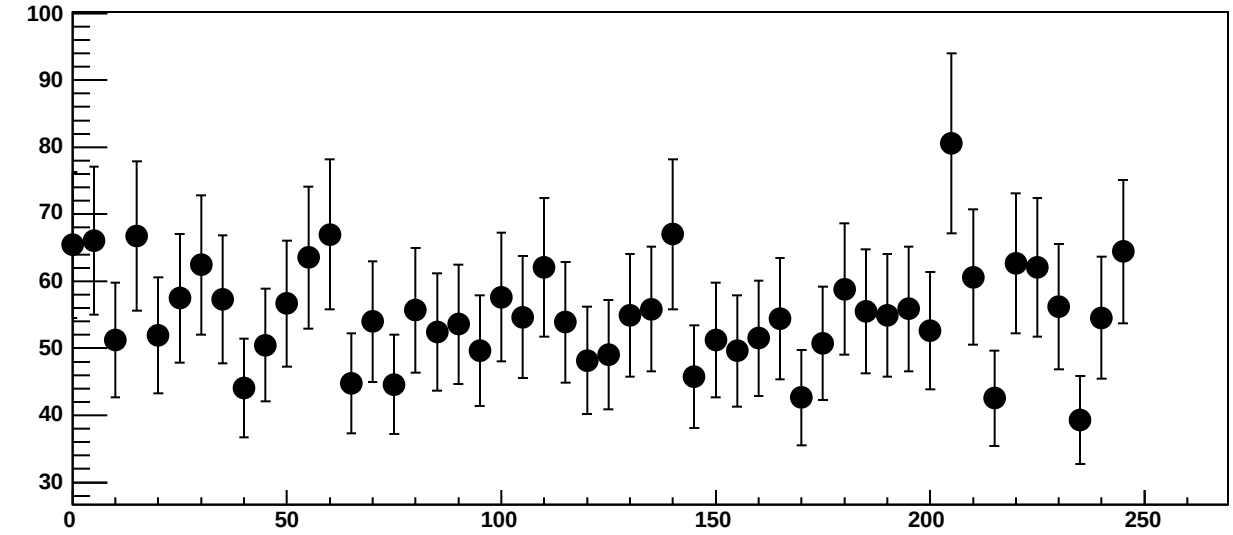

Chip 2, Channel 12, Enable 2, DAC=1600, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 2, Channel 12, Enable 2, DAC=1600, ADC Residuals vs Hold
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250

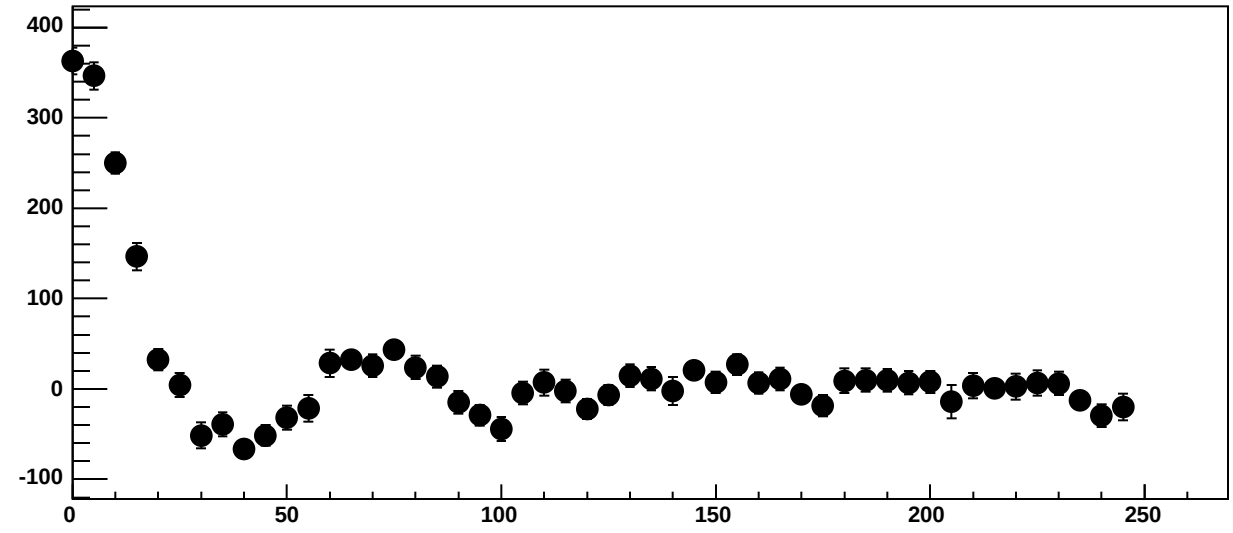
Chip 2, Channel 12, Enable 3, DAC=1600, ADC Mean vs Hold

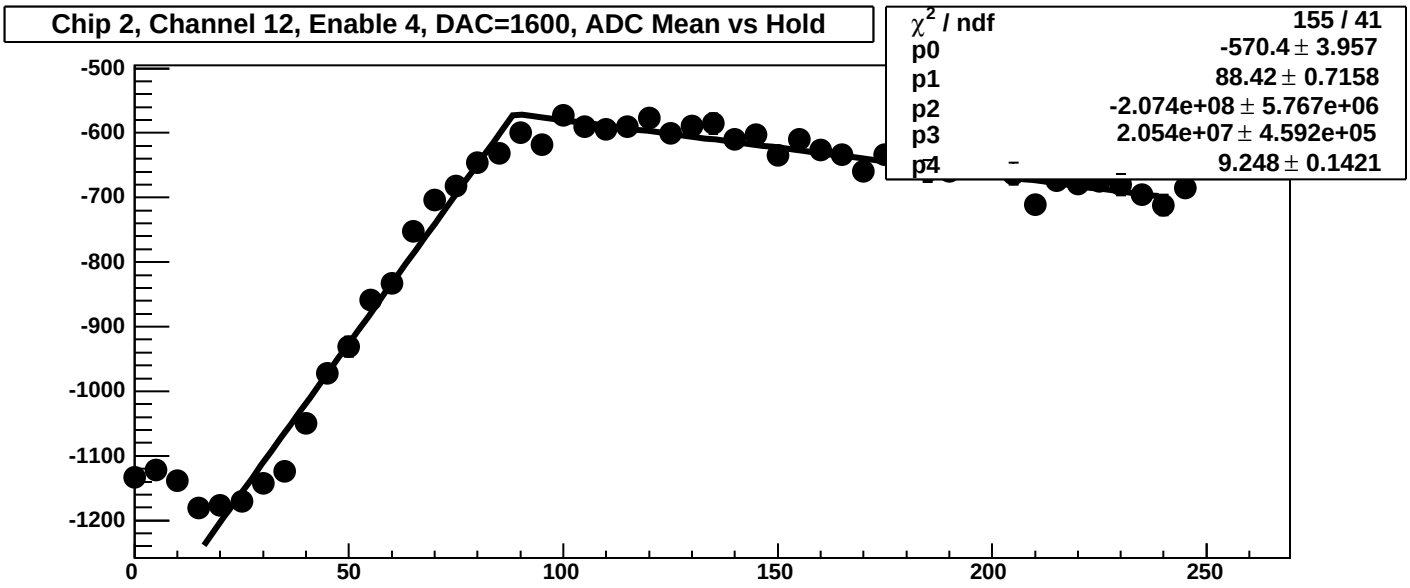


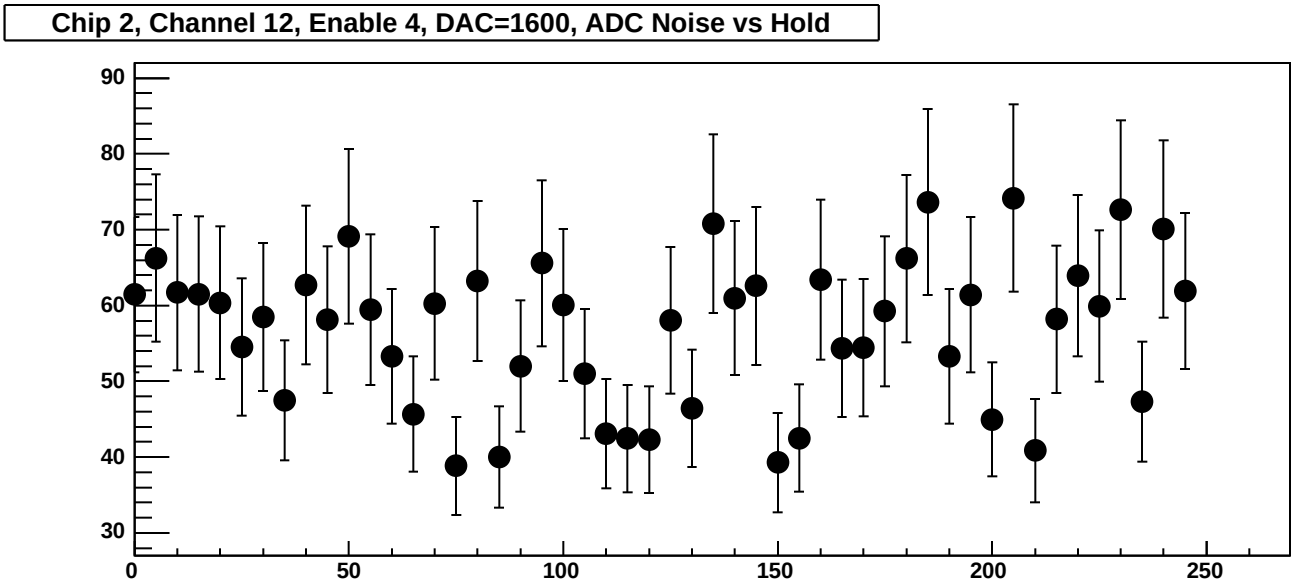
Chip 2, Channel 12, Enable 3, DAC=1600, ADC Noise vs Hold

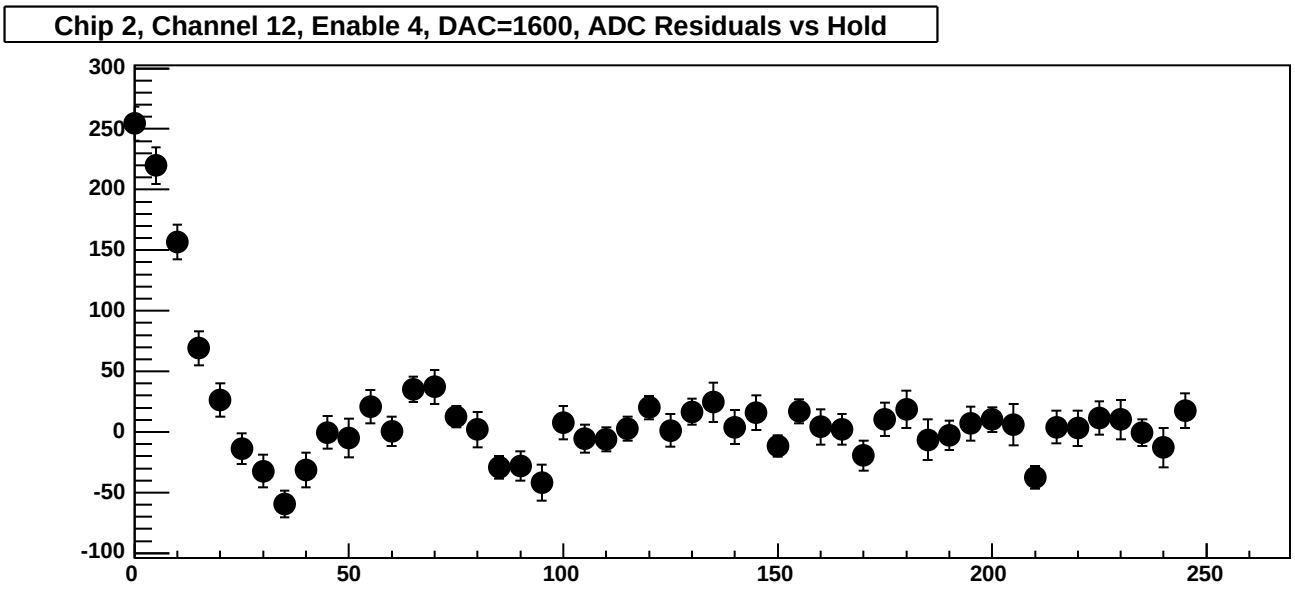


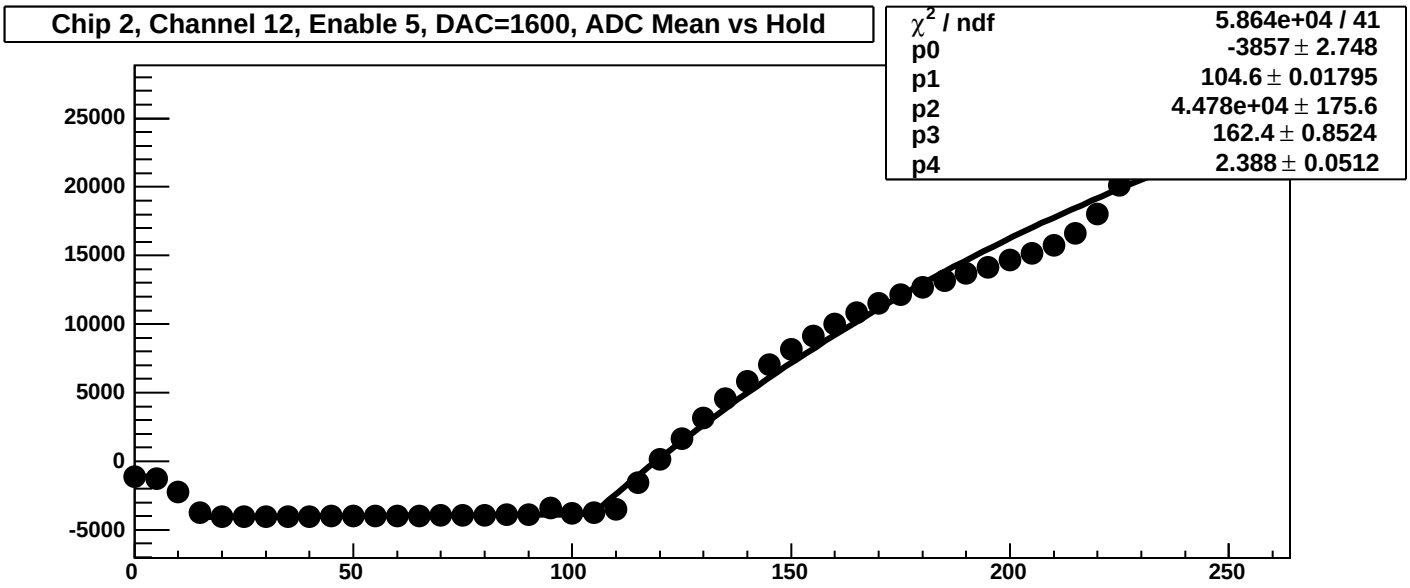
Chip 2, Channel 12, Enable 3, DAC=1600, ADC Residuals vs Hold

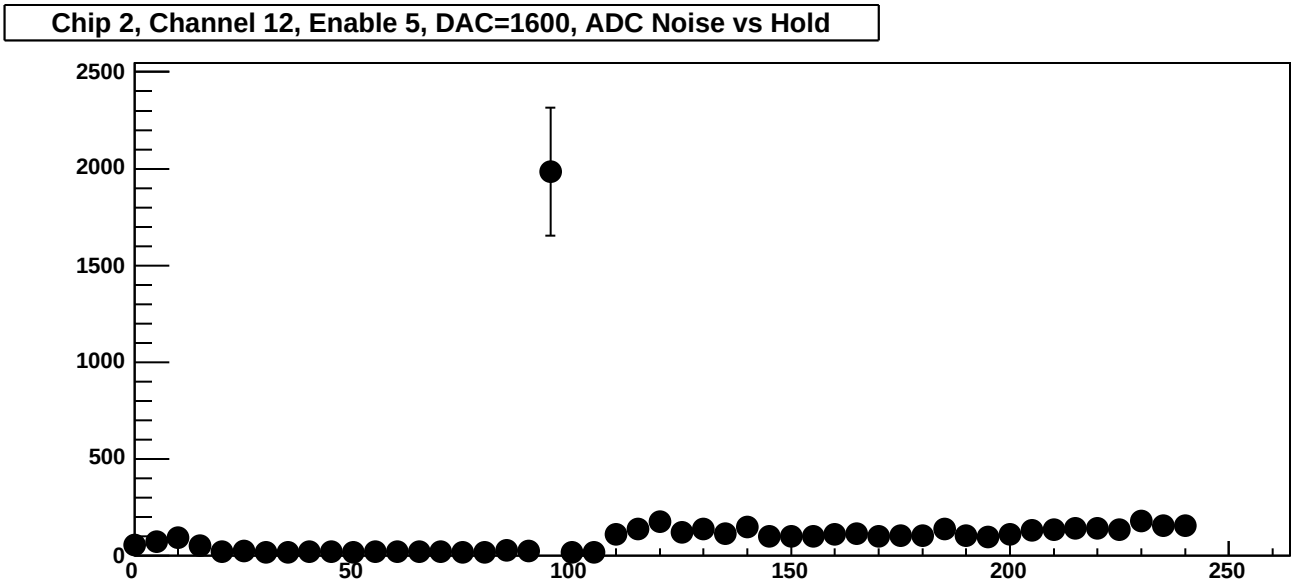


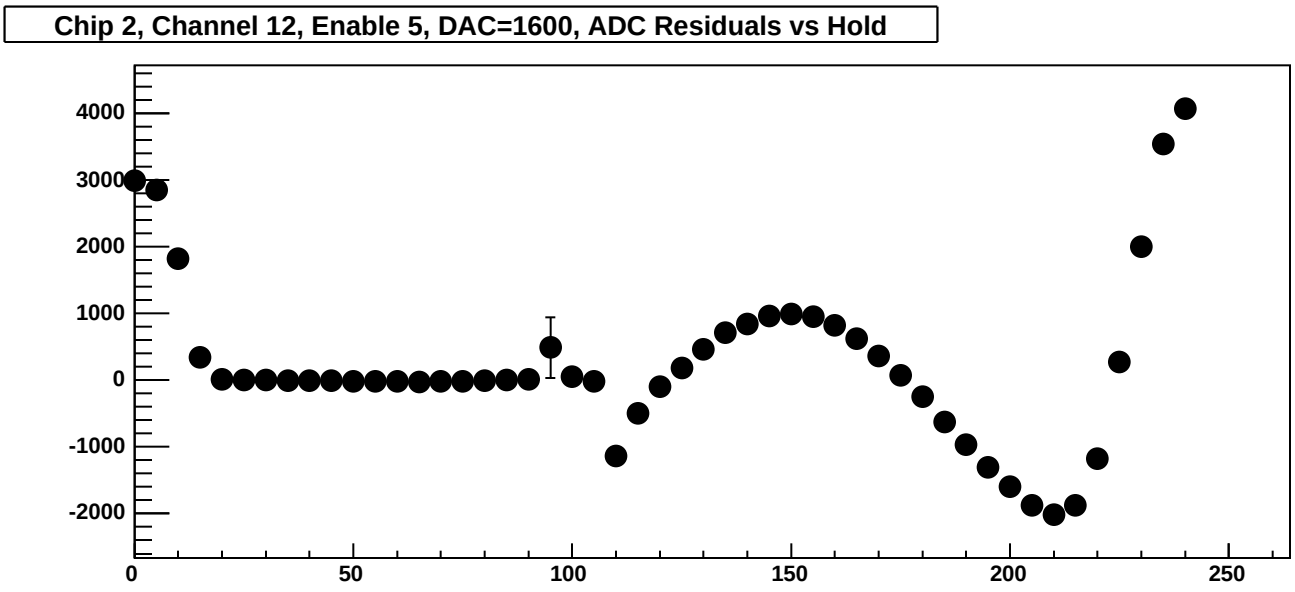

Chip 2, Channel 12, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 155 / 41
p0 -570.4 ± 3.957
p1 88.42 ± 0.7158
p2 -2.074e+08 ± 5.767e+06
p3 2.054e+07 ± 4.592e+05
p4 9.248 ± 0.1421
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
50
100
150
200
250


Chip 2, Channel 12, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

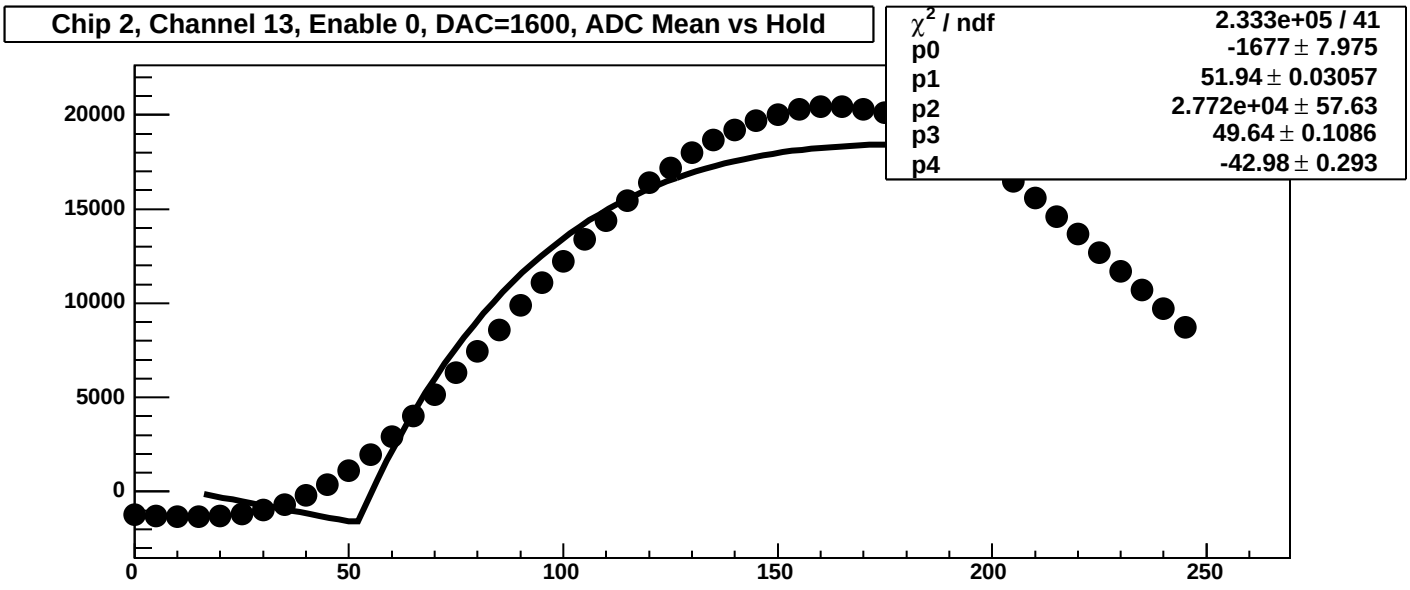

Chip 2, Channel 12, Enable 4, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250


Chip 2, Channel 12, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.864e+04 / 41
p0 -3857 ± 2.748
p1 104.6 ± 0.01795
p2 4.478e+04 ± 175.6
p3 162.4 ± 0.8524
p4 2.388 ± 0.0512
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

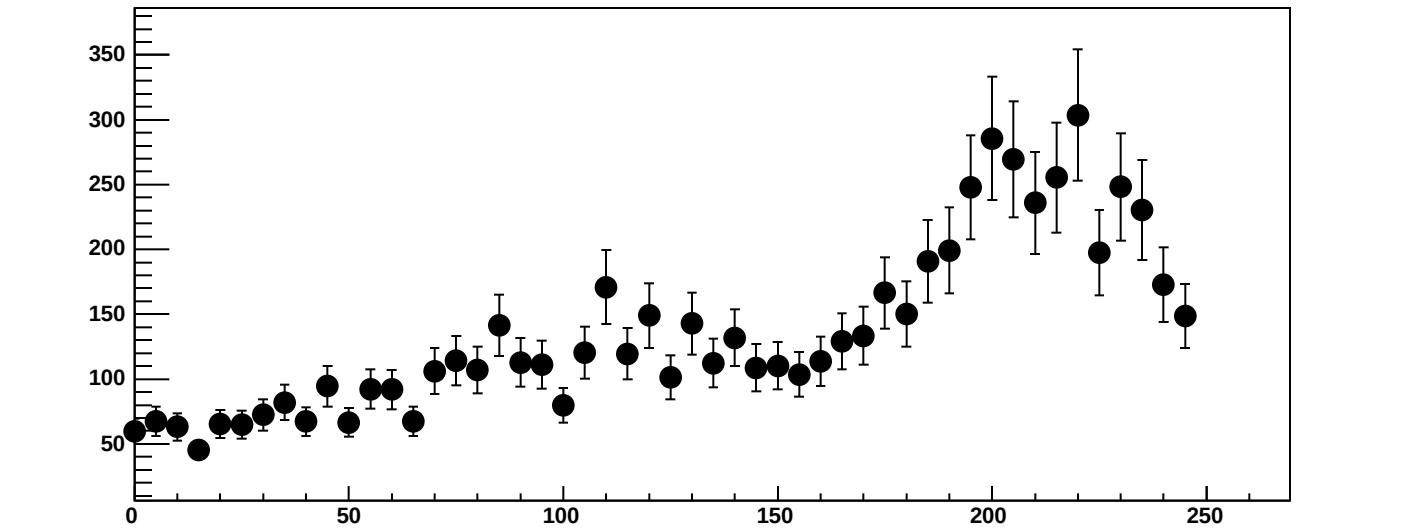

Chip 2, Channel 12, Enable 5, DAC=1600, ADC Noise vs Hold
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 2, Channel 12, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
3000
2000
1000
0
-1000
-2000
0
50
100
150
200
250

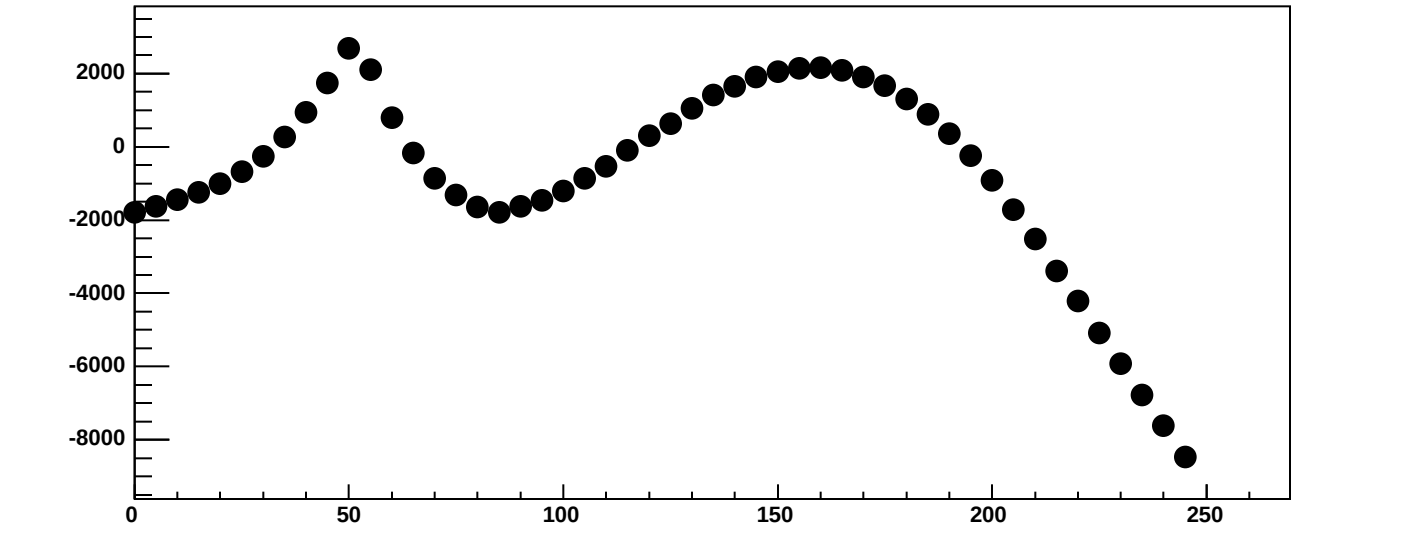
Chip 2, Channel 13, Enable 0, DAC=1600, ADC Mean vs Hold



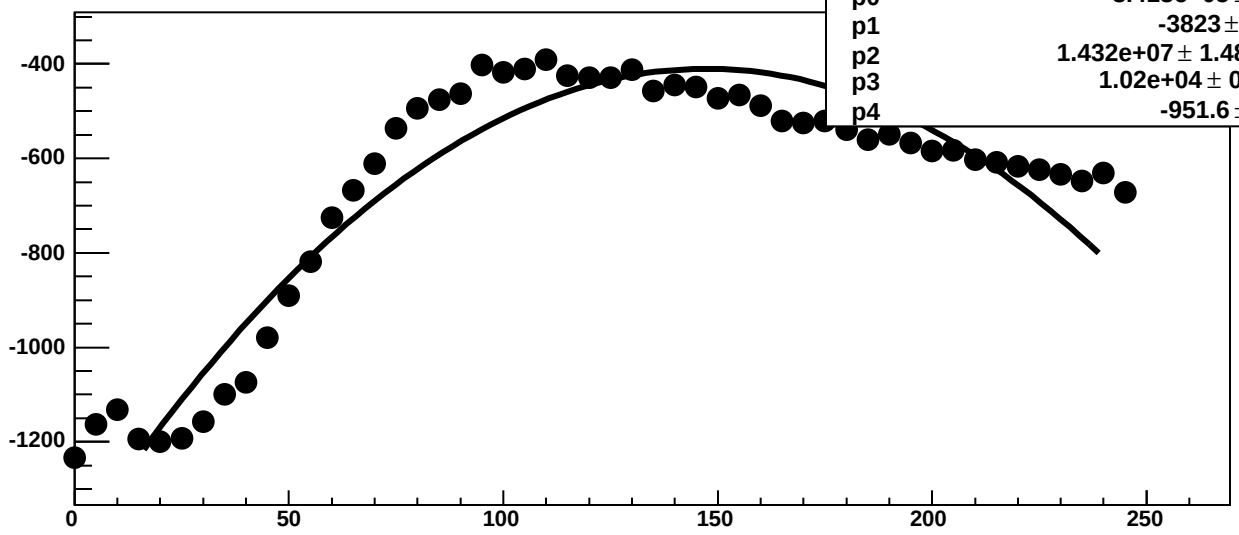
Chip 2, Channel 13, Enable 0, DAC=1600, ADC Noise vs Hold



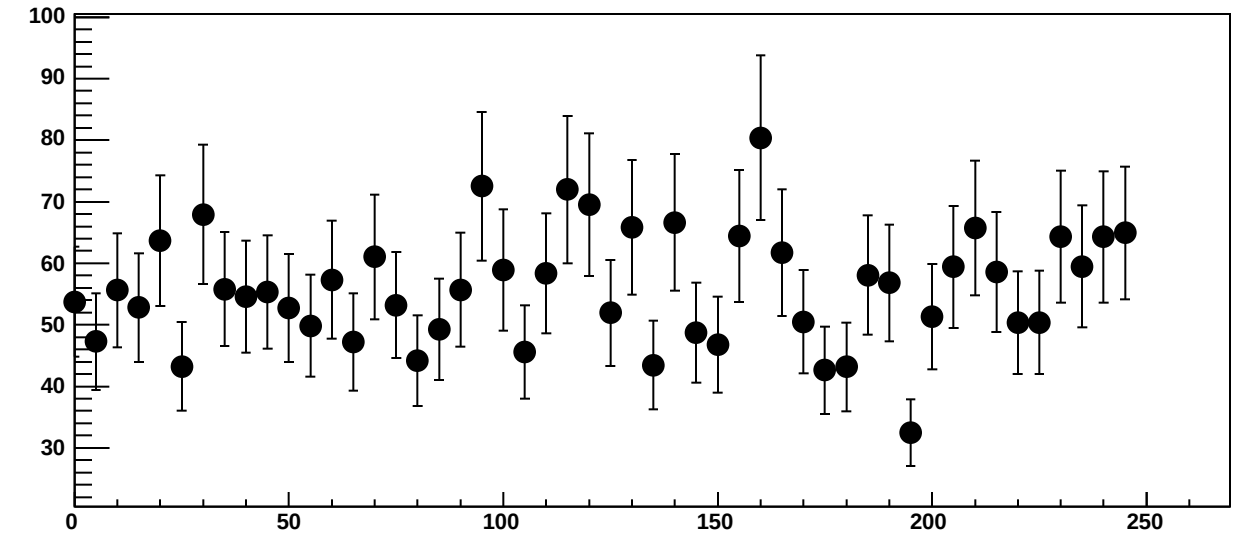
Chip 2, Channel 13, Enable 0, DAC=1600, ADC Residuals vs Hold



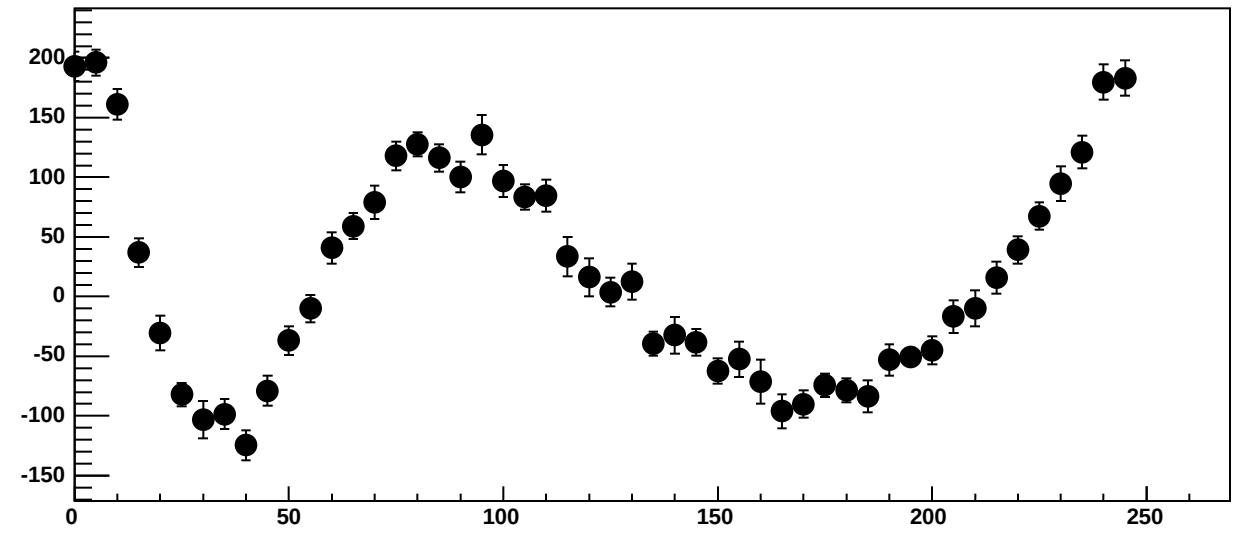
Chip 2, Channel 13, Enable 1, DAC=1600, ADC Mean vs Hold



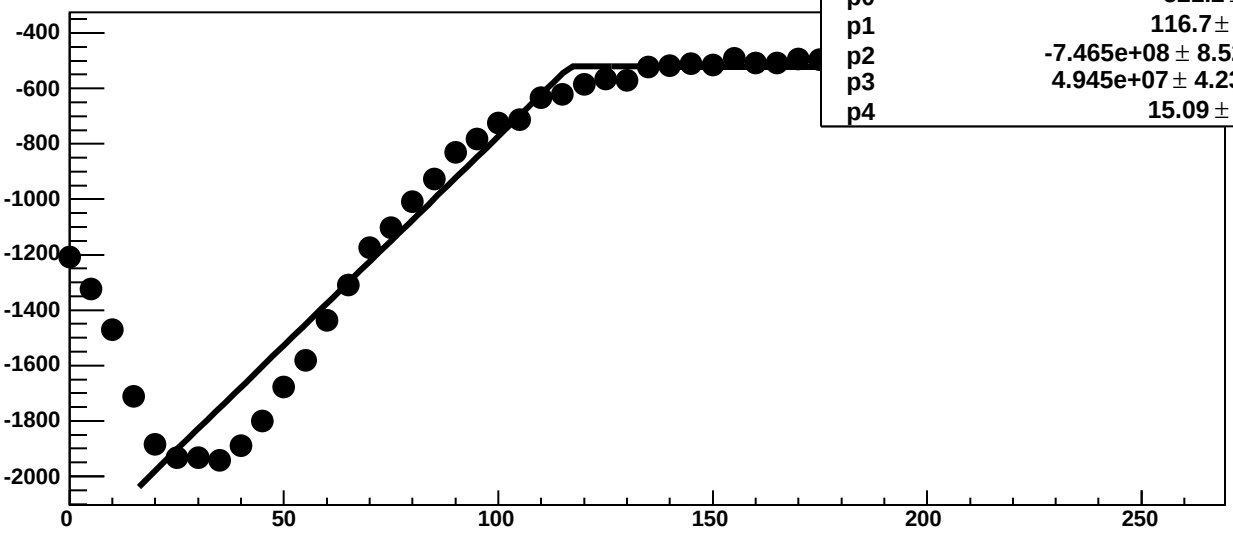
Chip 2, Channel 13, Enable 1, DAC=1600, ADC Noise vs Hold



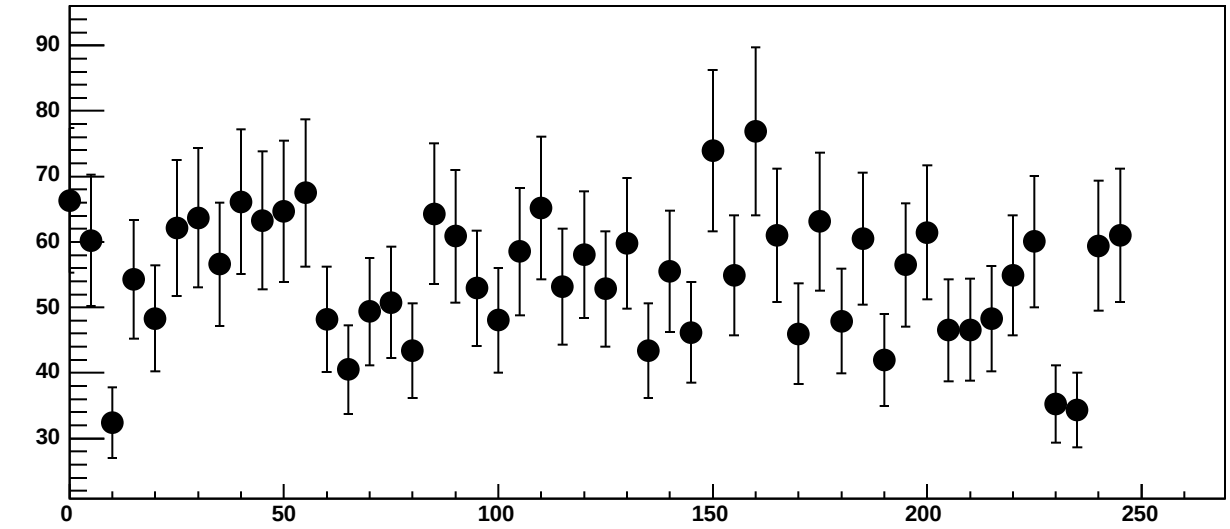
Chip 2, Channel 13, Enable 1, DAC=1600, ADC Residuals vs Hold



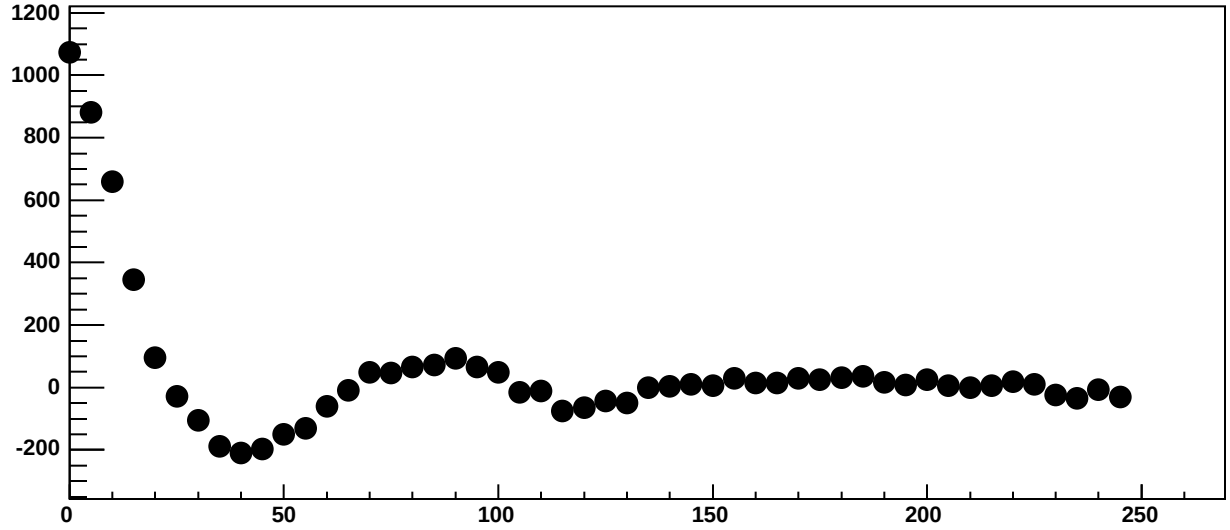
Chip 2, Channel 13, Enable 2, DAC=1600, ADC Mean vs Hold



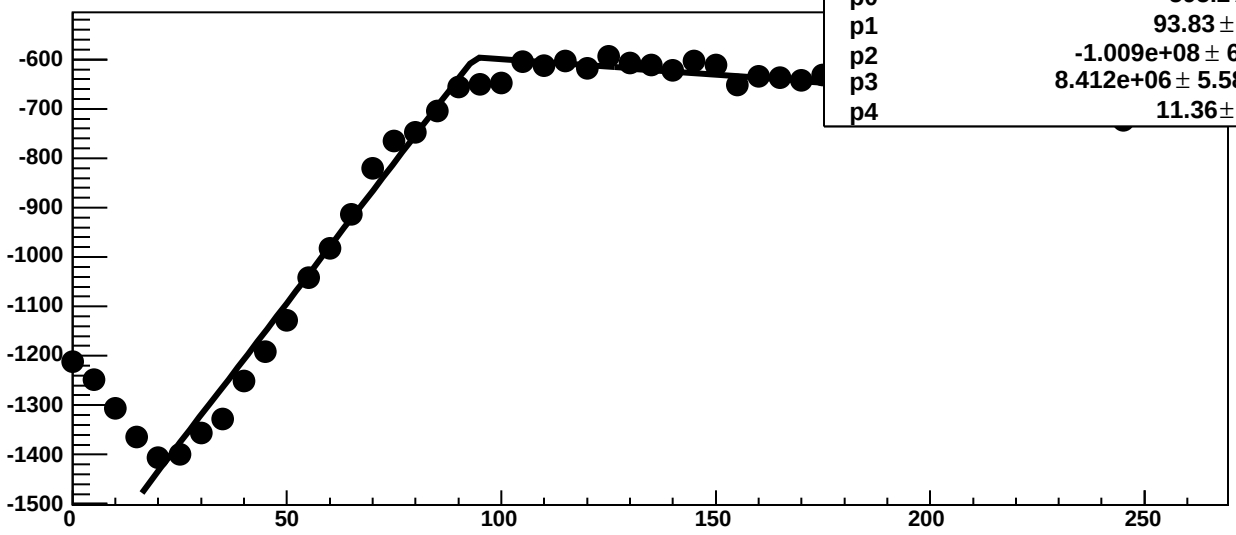
Chip 2, Channel 13, Enable 2, DAC=1600, ADC Noise vs Hold



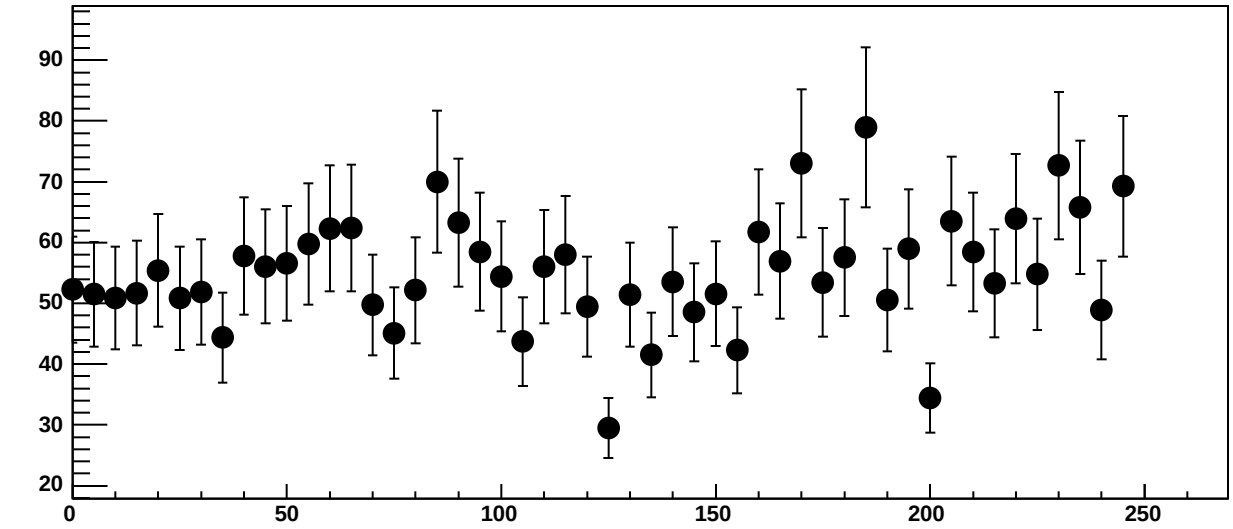
Chip 2, Channel 13, Enable 2, DAC=1600, ADC Residuals vs Hold



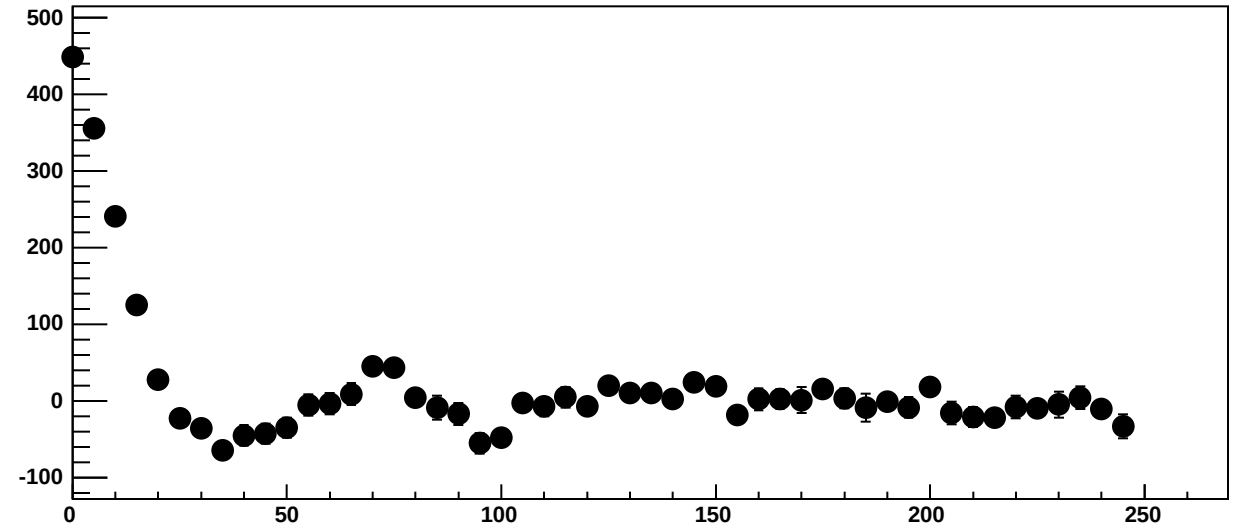
Chip 2, Channel 13, Enable 3, DAC=1600, ADC Mean vs Hold



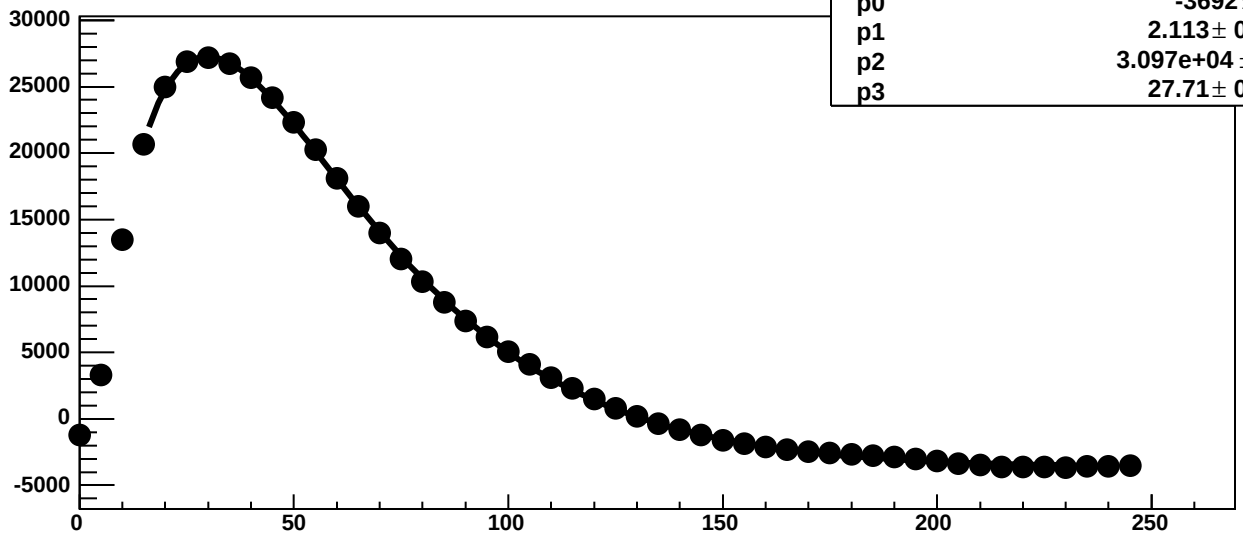
Chip 2, Channel 13, Enable 3, DAC=1600, ADC Noise vs Hold



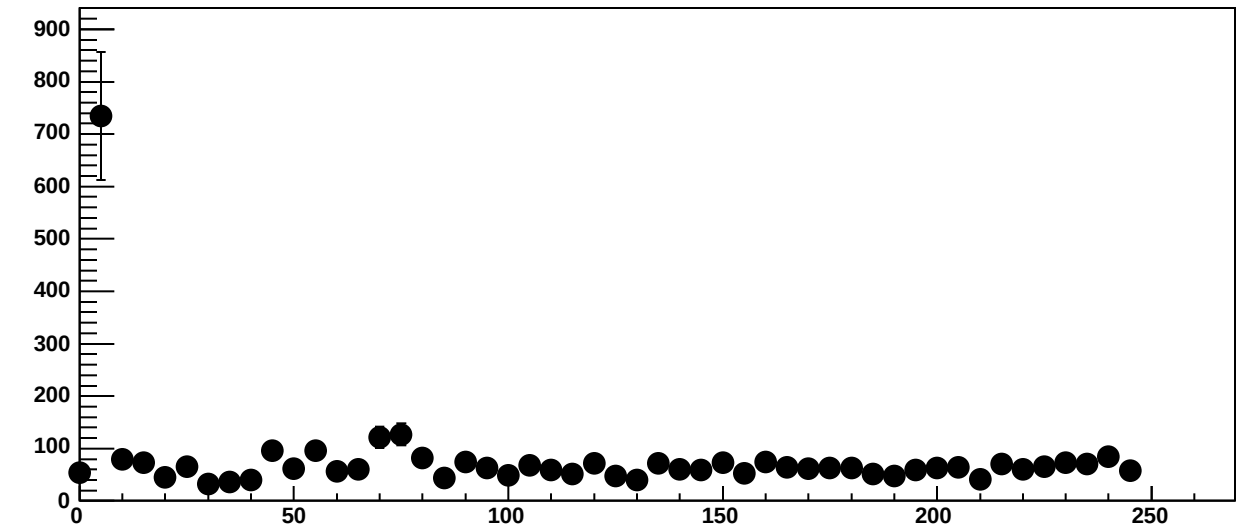
Chip 2, Channel 13, Enable 3, DAC=1600, ADC Residuals vs Hold



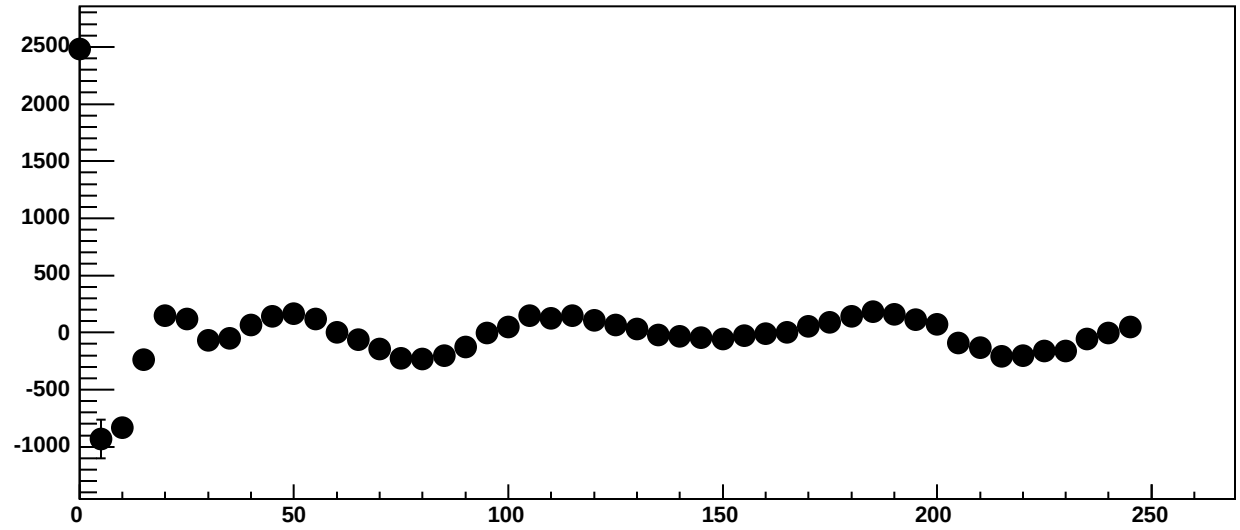
Chip 2, Channel 13, Enable 4!, DAC=1600, ADC Mean vs Hold

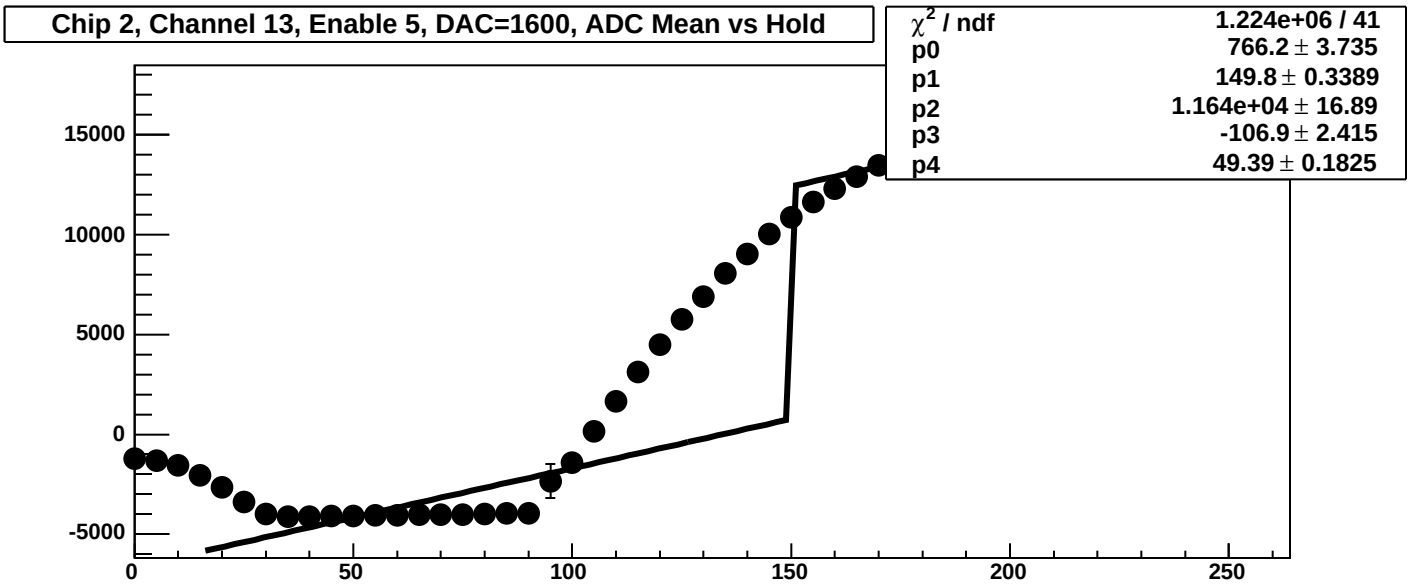


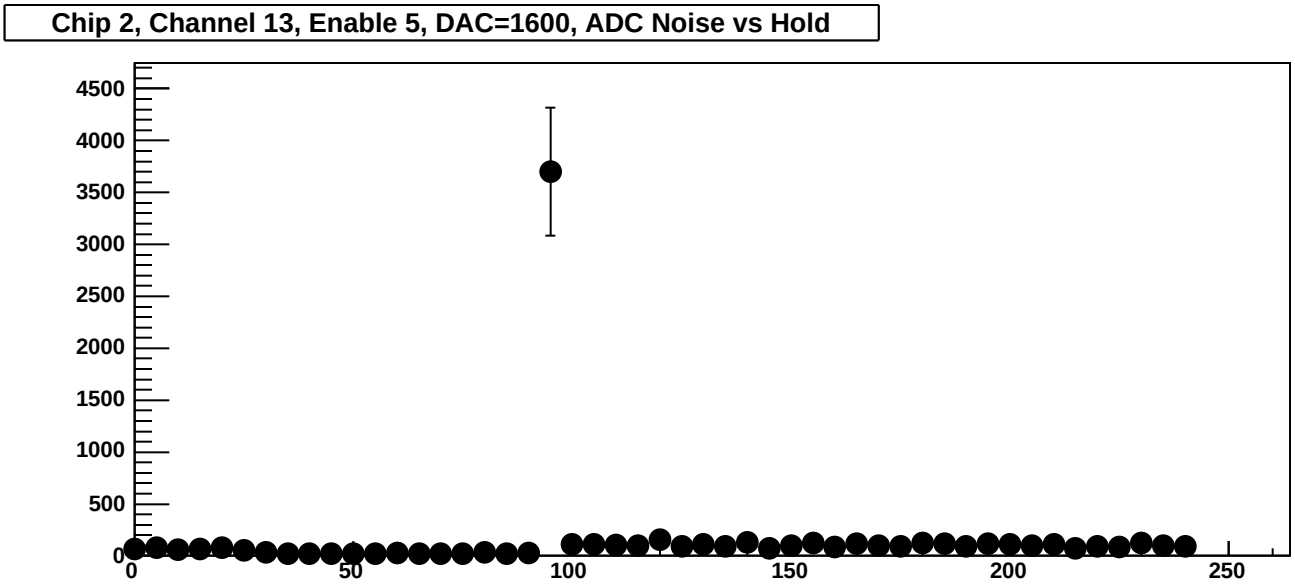
Chip 2, Channel 13, Enable 4!, DAC=1600, ADC Noise vs Hold

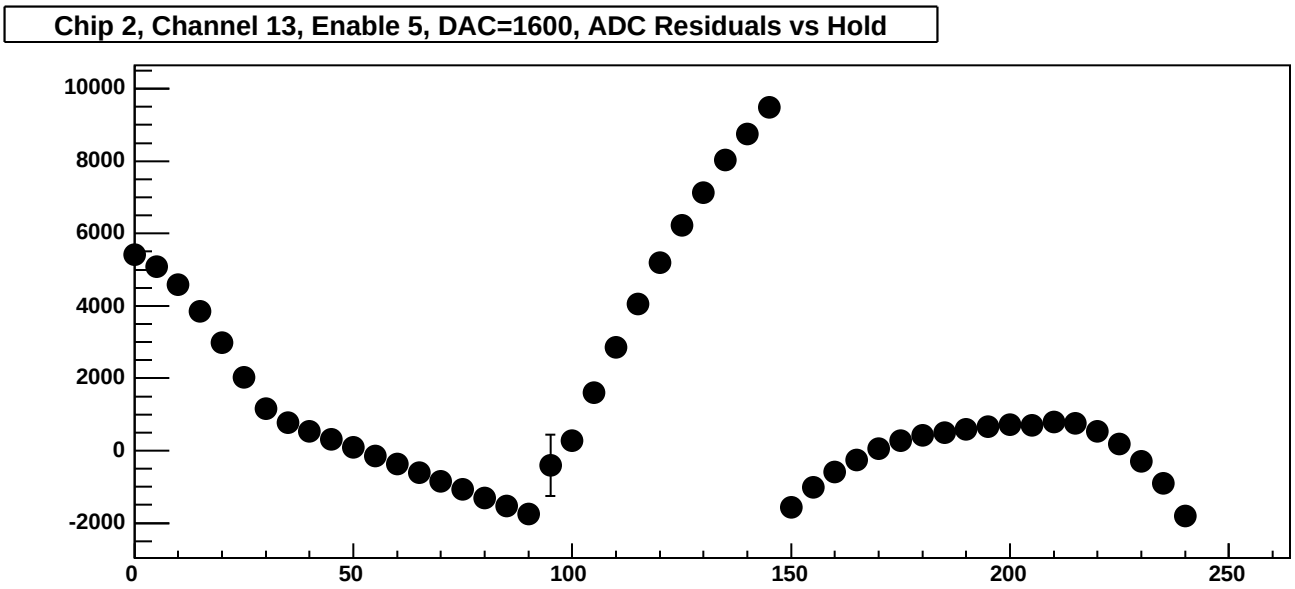


Chip 2, Channel 13, Enable 4!, DAC=1600, ADC Residuals vs Hold

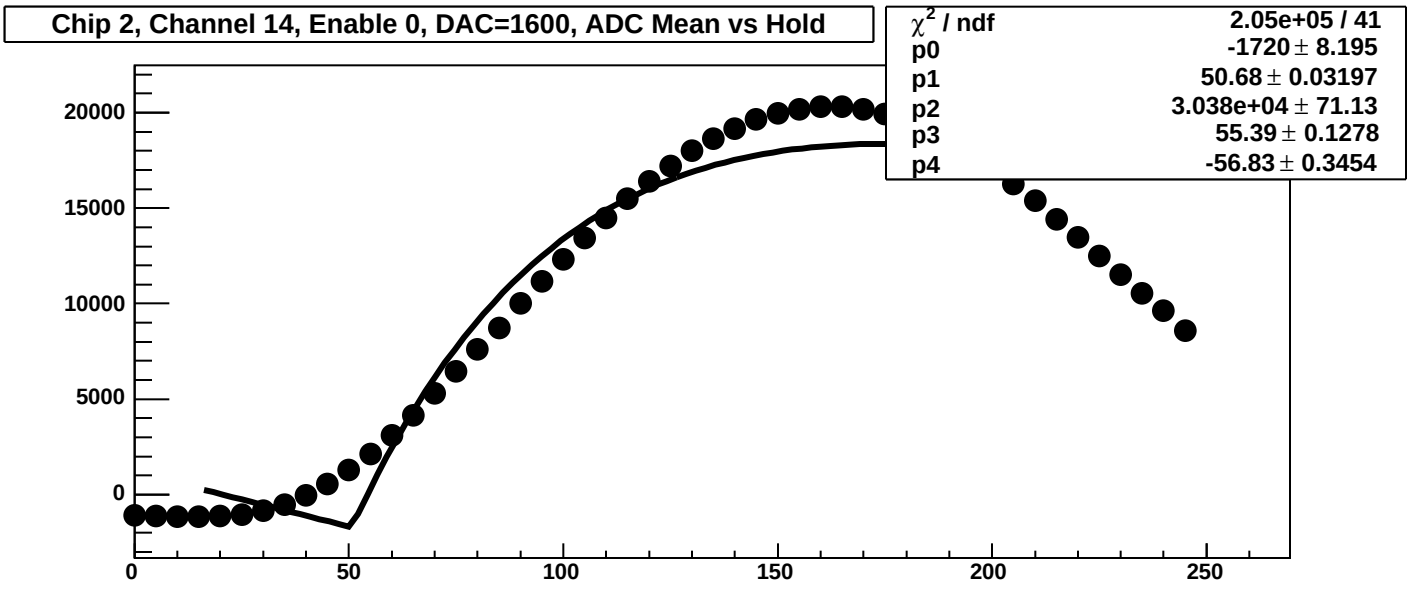



Chip 2, Channel 13, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.224e+06 / 41
p0 766.2 ± 3.735
p1 149.8 ± 0.3389
p2 1.164e+04 ± 16.89
p3 -106.9 ± 2.415
p4 49.39 ± 0.1825
15000
10000
5000
0
-5000
0
50
100
150
200
250

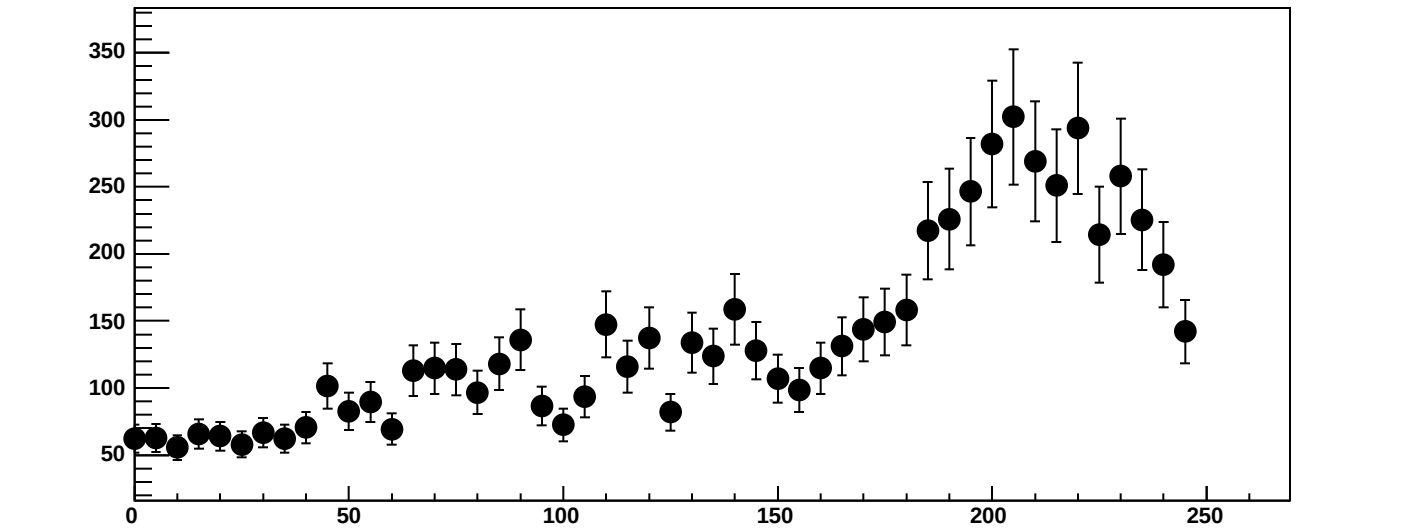

Chip 2, Channel 13, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 2, Channel 13, Enable 5, DAC=1600, ADC Residuals vs Hold
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

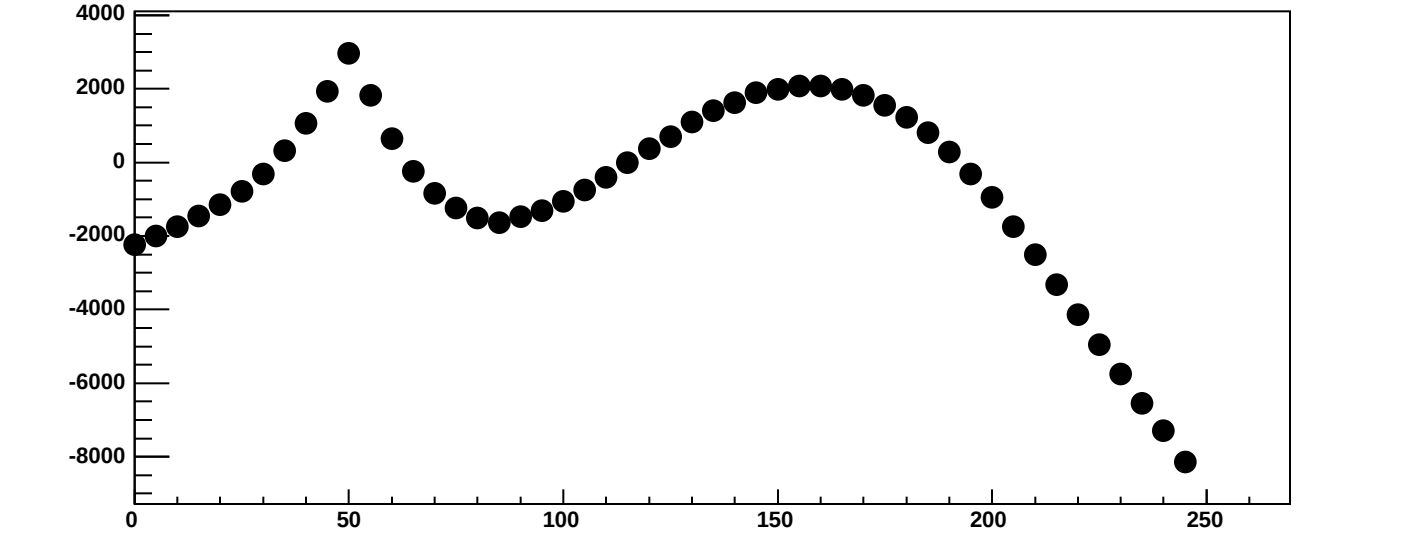
Chip 2, Channel 14, Enable 0, DAC=1600, ADC Mean vs Hold



Chip 2, Channel 14, Enable 0, DAC=1600, ADC Noise vs Hold

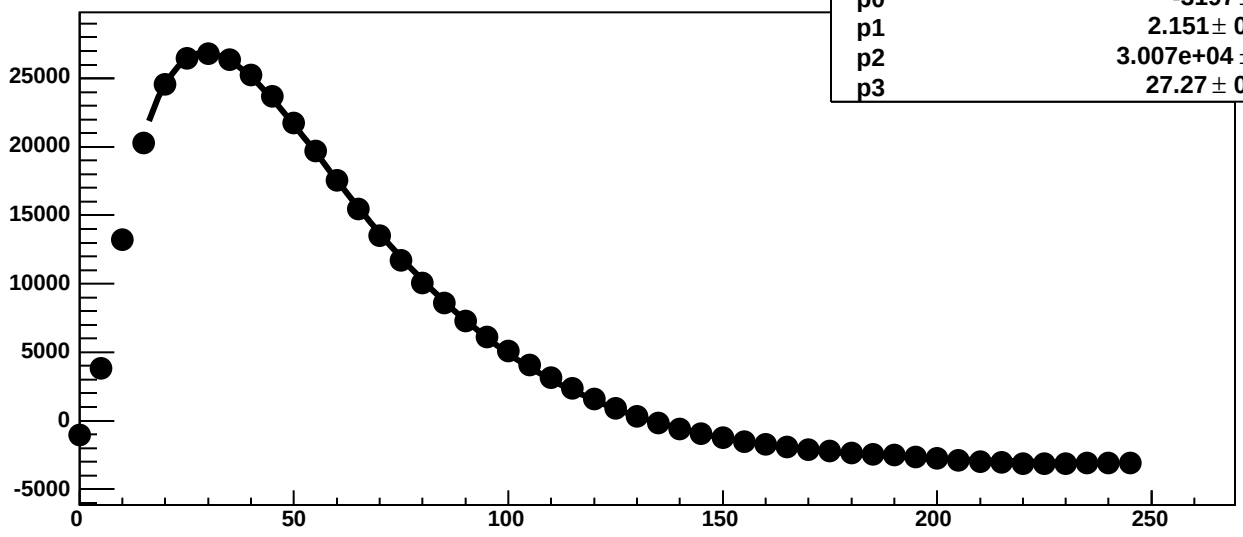


Chip 2, Channel 14, Enable 0, DAC=1600, ADC Residuals vs Hold

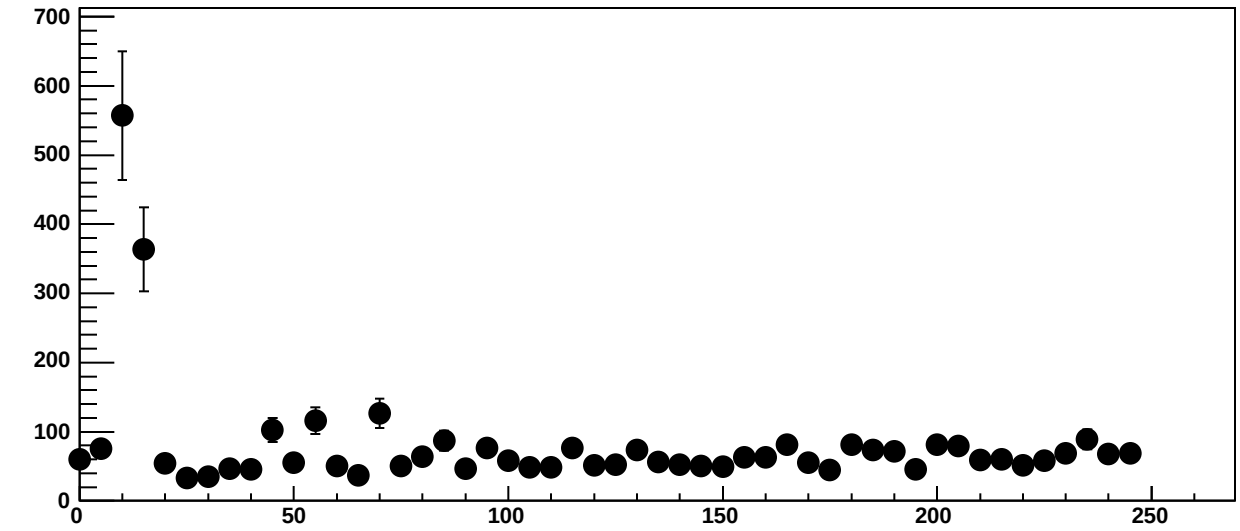


Chip 2, Channel 14, Enable 1!, DAC=1600, ADC Mean vs Hold

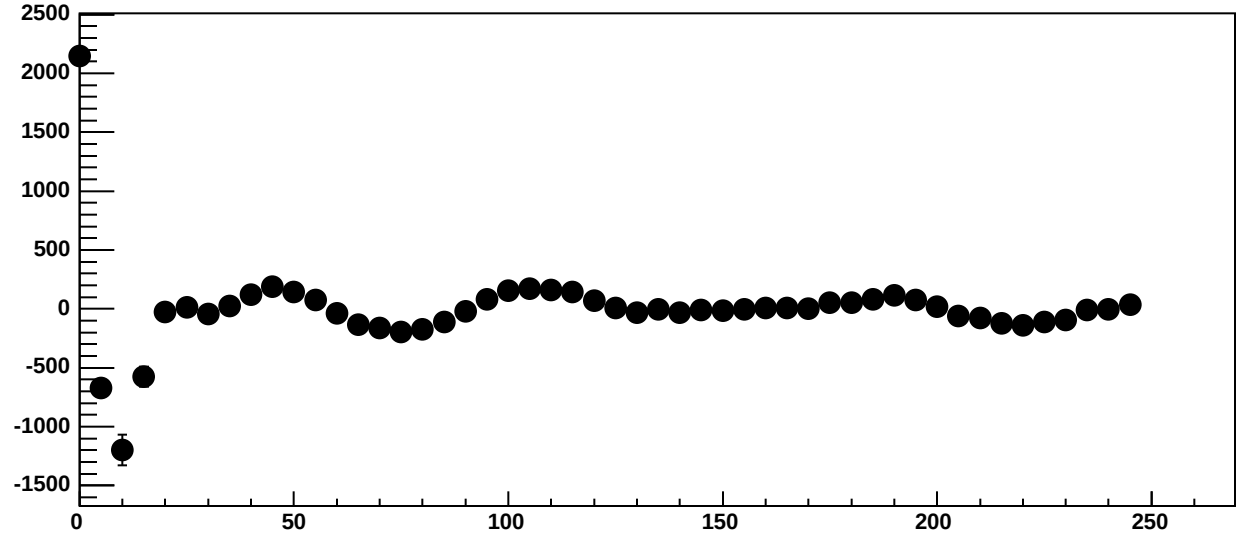
χ^2 / ndf	2414 / 42
p0	-3197 ± 3.877
p1	2.151 ± 0.02302
p2	3.007e+04 ± 5.148
p3	27.27 ± 0.01229

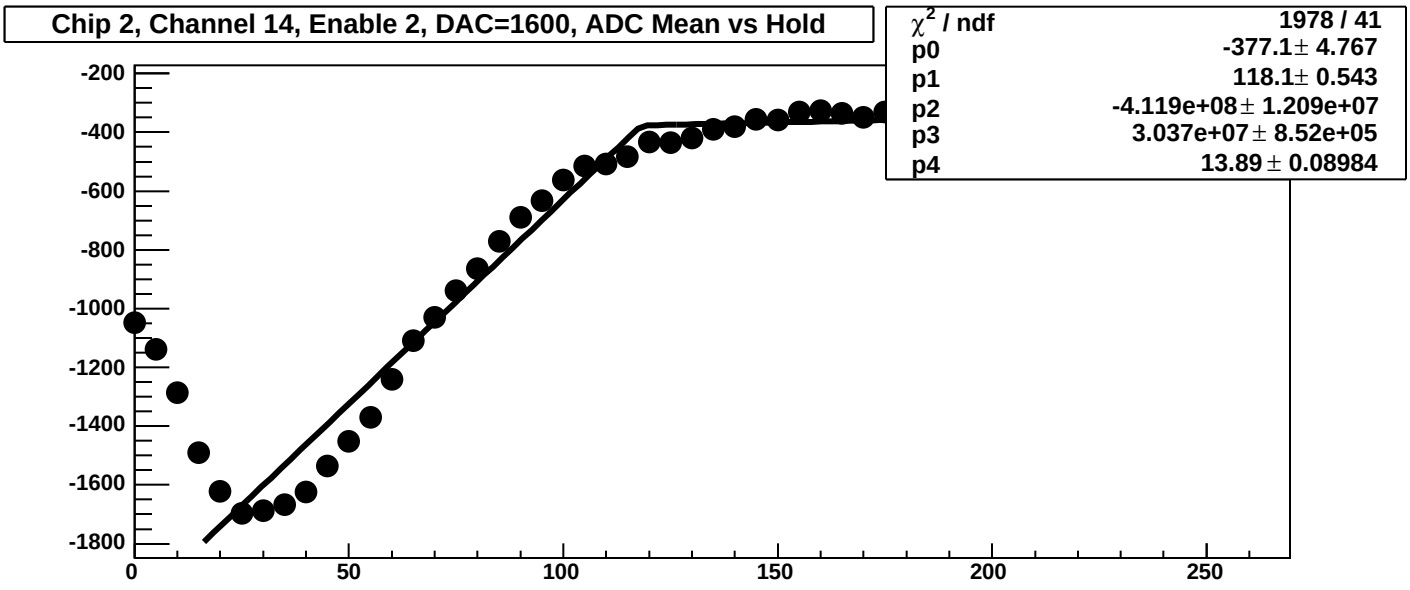


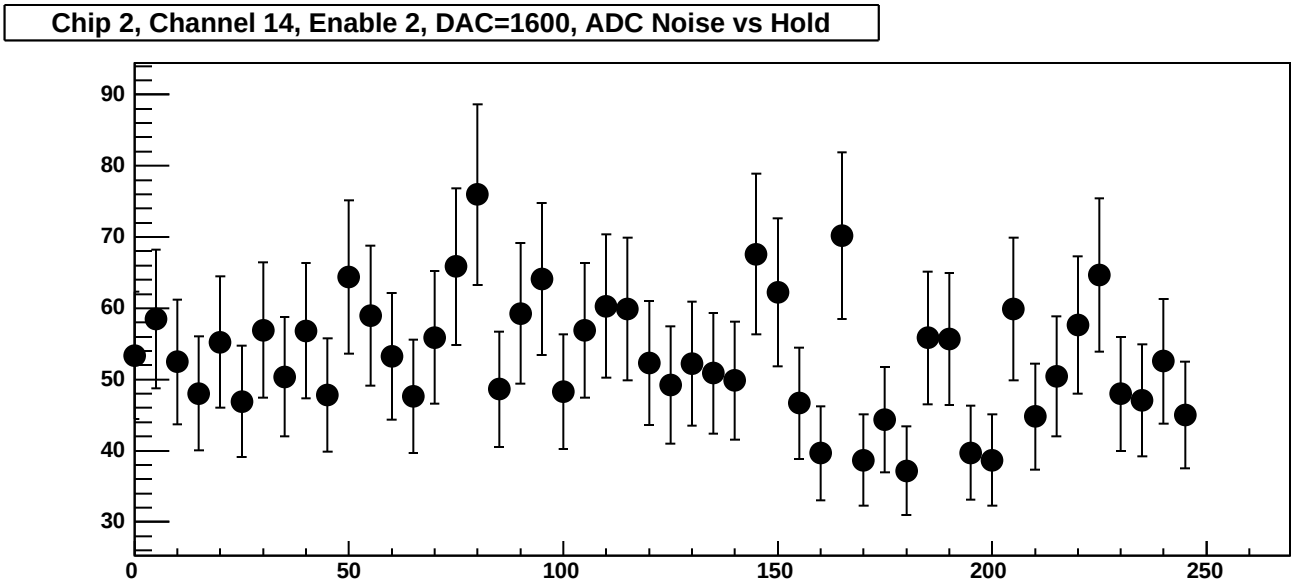
Chip 2, Channel 14, Enable 1!, DAC=1600, ADC Noise vs Hold

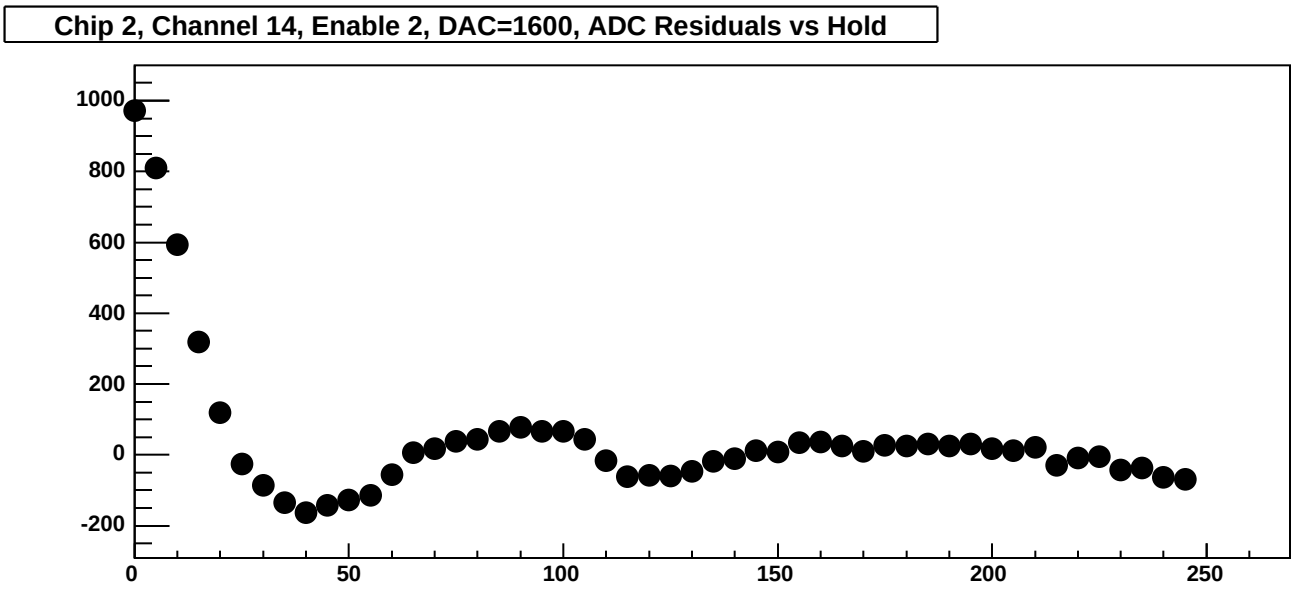


Chip 2, Channel 14, Enable 1!, DAC=1600, ADC Residuals vs Hold



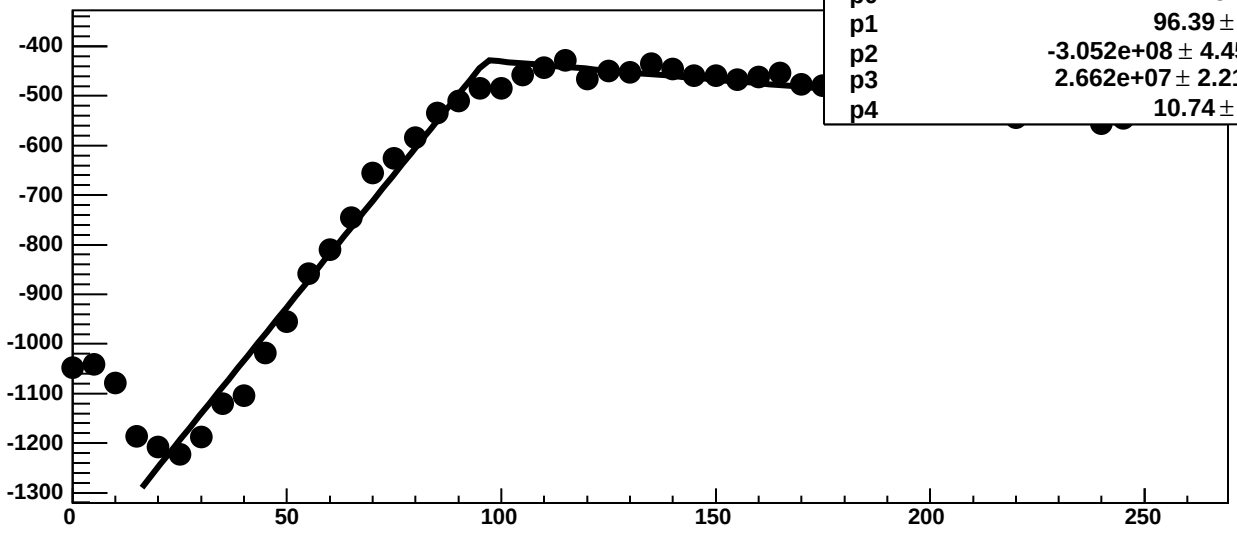

Chip 2, Channel 14, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 1978 / 41
p0 -377.1 ± 4.767
p1 118.1 ± 0.543
p2 -4.119e+08 ± 1.209e+07
p3 3.037e+07 ± 8.52e+05
p4 13.89 ± 0.08984
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250


Chip 2, Channel 14, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

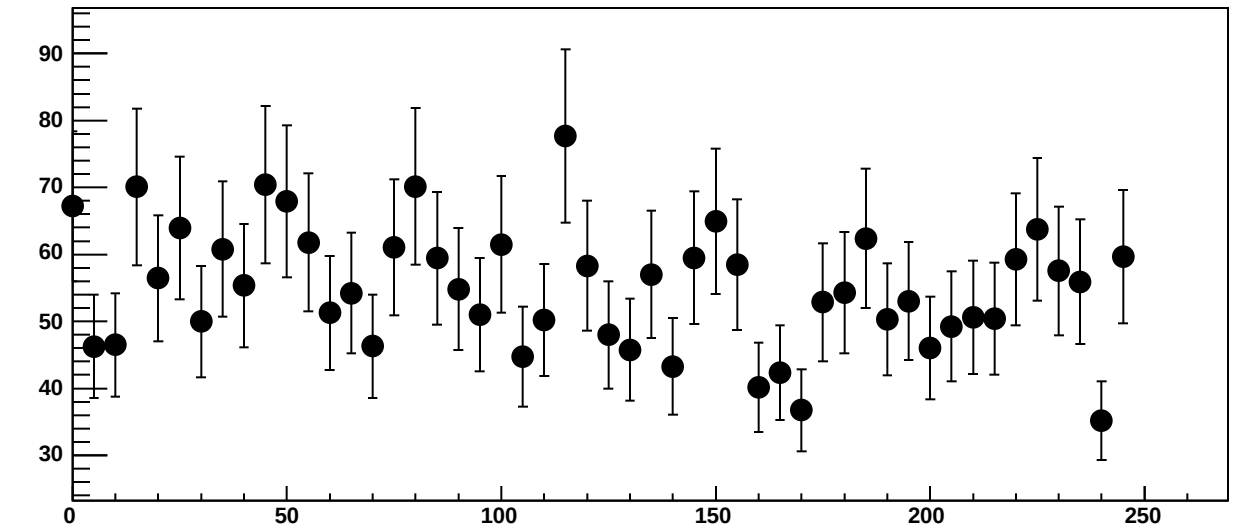

Chip 2, Channel 14, Enable 2, DAC=1600, ADC Residuals vs Hold
1000
800
600
400
200
0
-200
0
50
100
150
200
250

Chip 2, Channel 14, Enable 3, DAC=1600, ADC Mean vs Hold

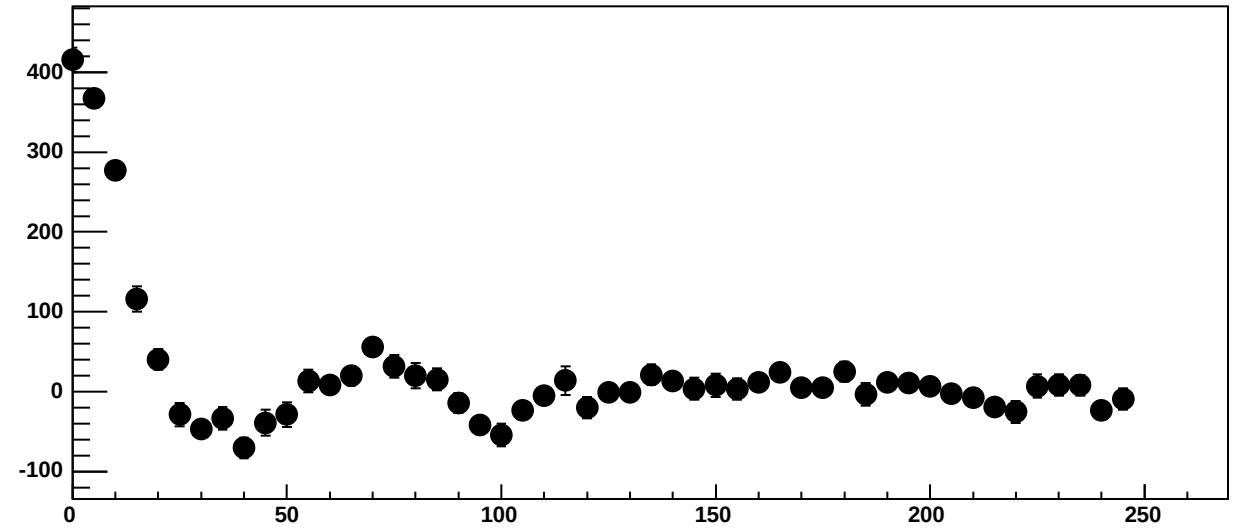
χ^2 / ndf	240 / 41
p0	-428 ± 4.183
p1	96.39 ± 0.6622
p2	-3.052e+08 ± 4.459e+06
p3	2.662e+07 ± 2.214e+05
p4	10.74 ± 0.1317

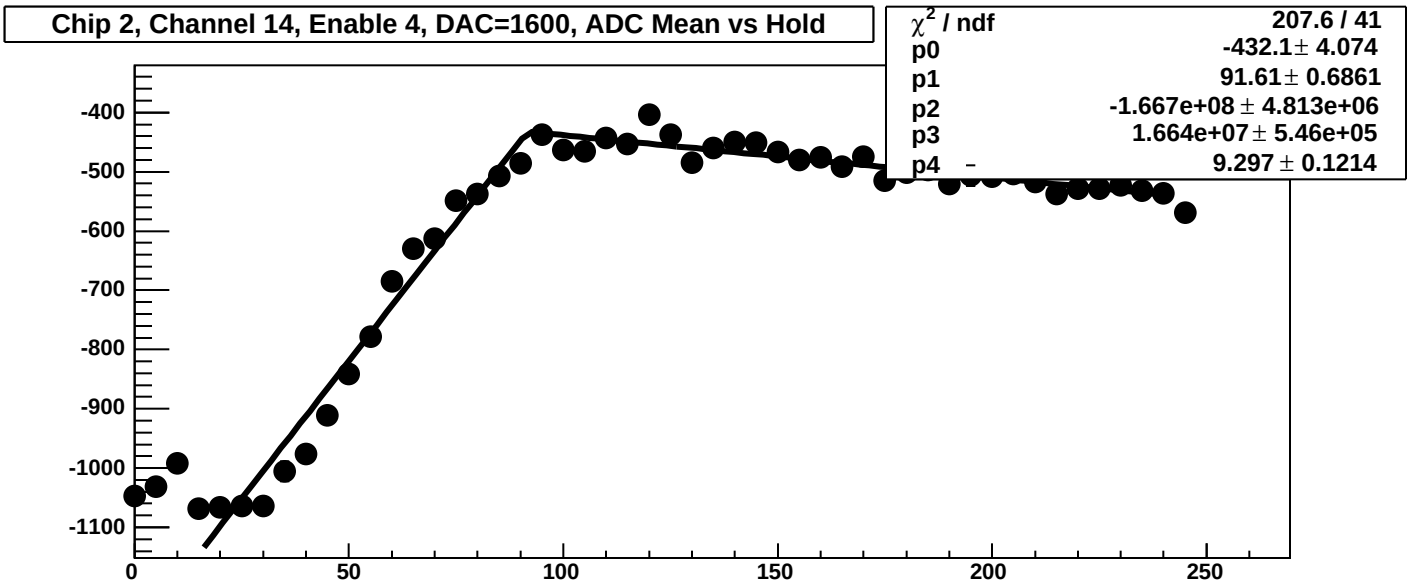


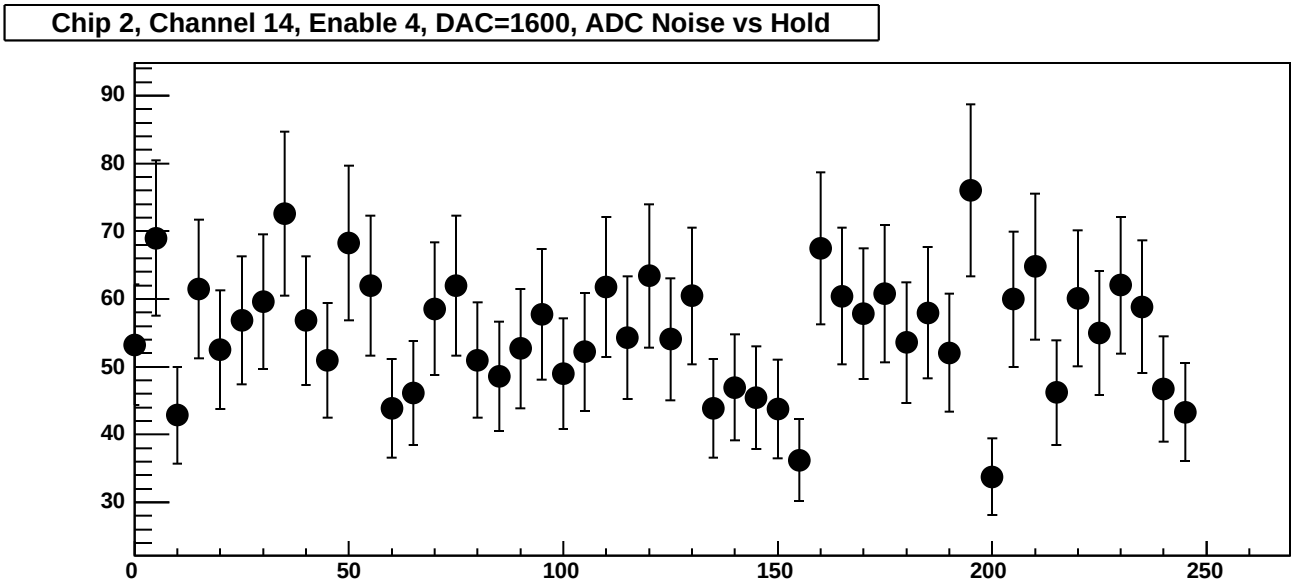
Chip 2, Channel 14, Enable 3, DAC=1600, ADC Noise vs Hold

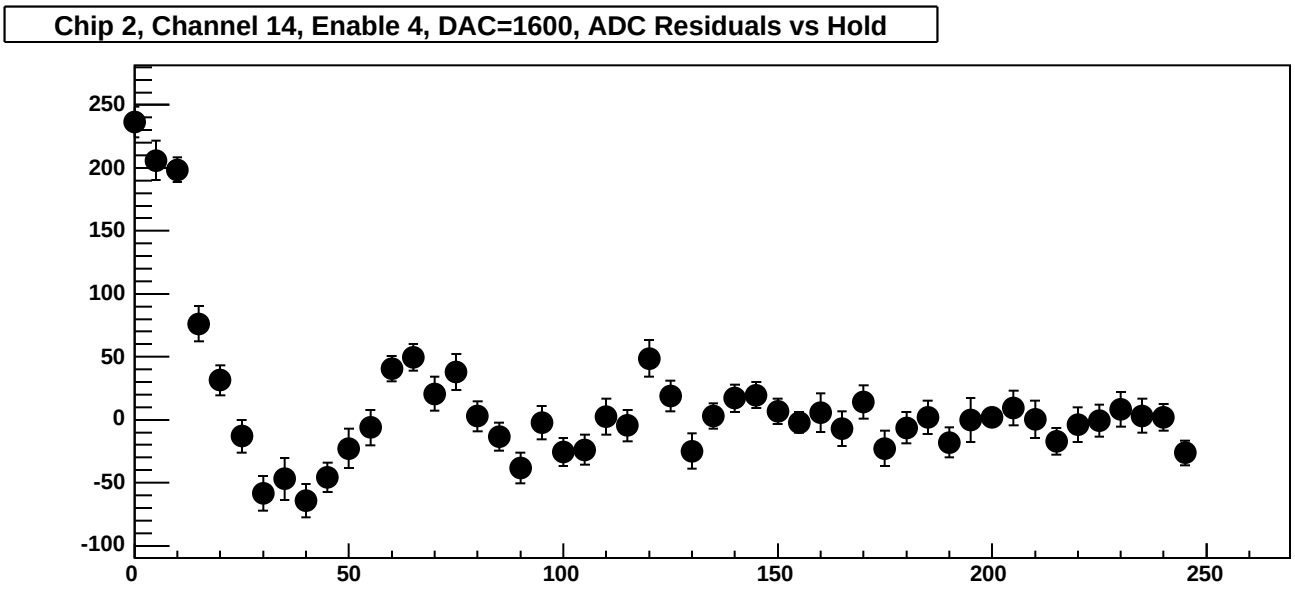


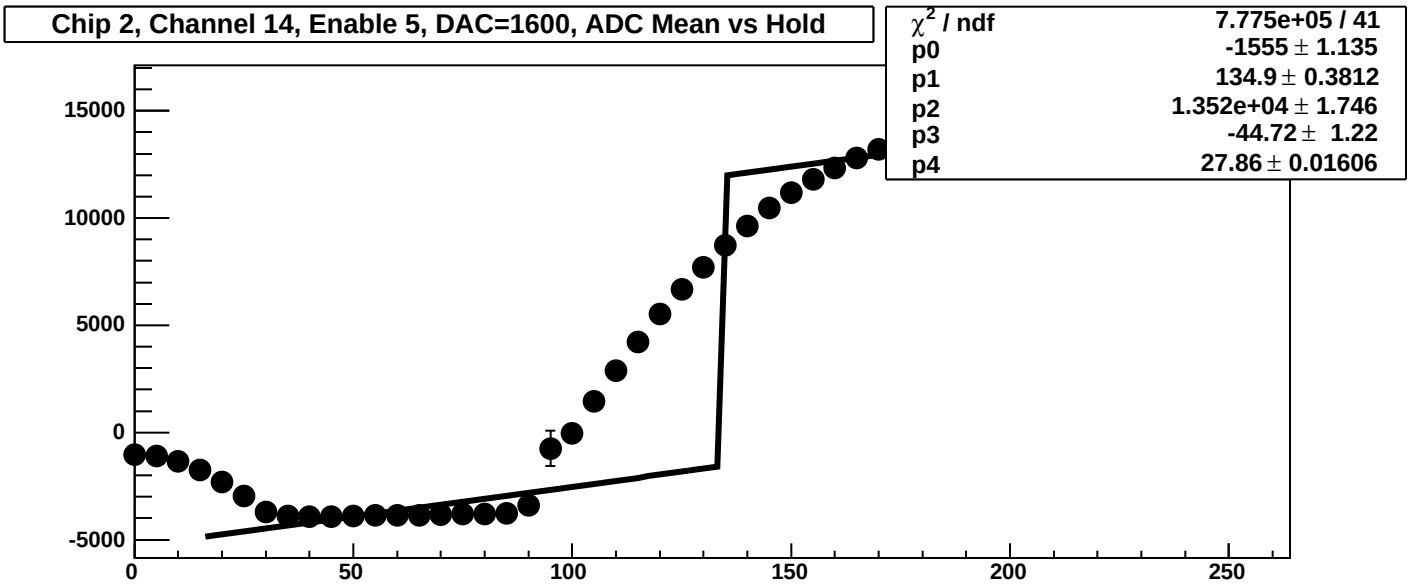
Chip 2, Channel 14, Enable 3, DAC=1600, ADC Residuals vs Hold

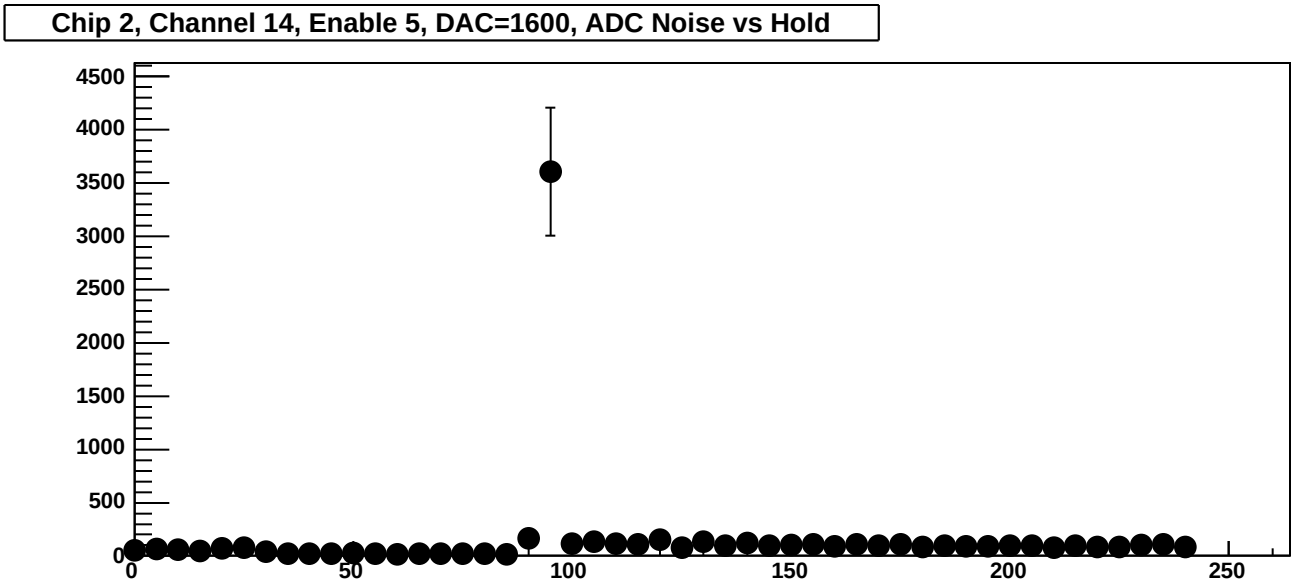


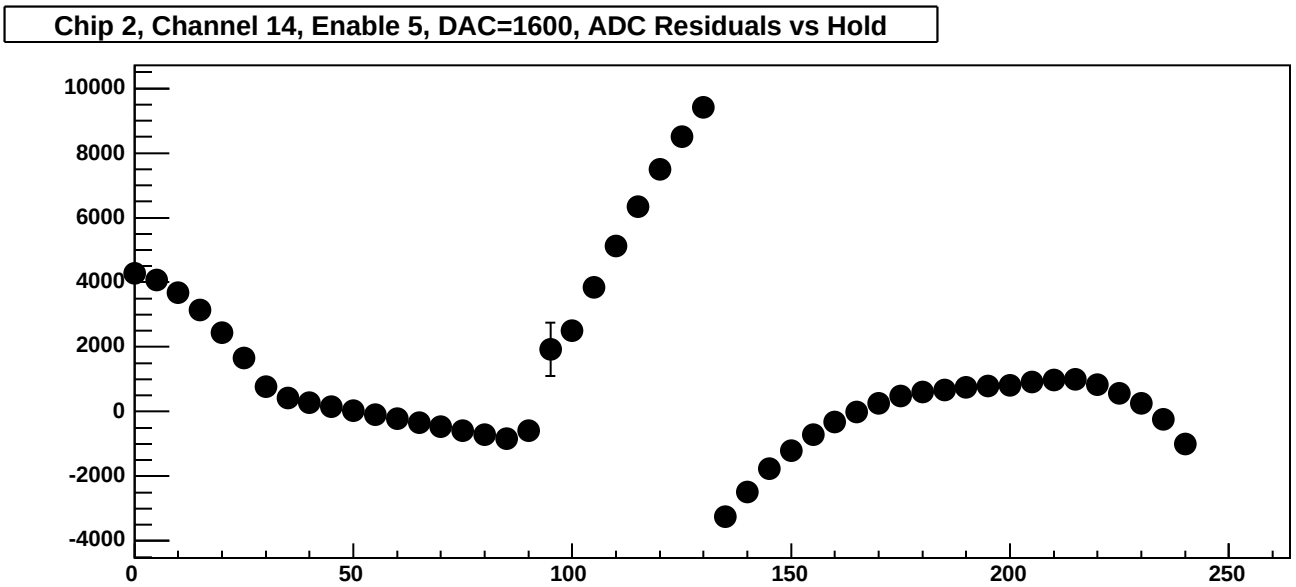

Chip 2, Channel 14, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 207.6 / 41
p0 -432.1 ± 4.074
p1 91.61 ± 0.6861
p2 -1.667e+08 ± 4.813e+06
p3 1.664e+07 ± 5.46e+05
p4 9.297 ± 0.1214
-400
-500
-600
-700
-800
-900
-1000
-1100
0
50
100
150
200
250


Chip 2, Channel 14, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

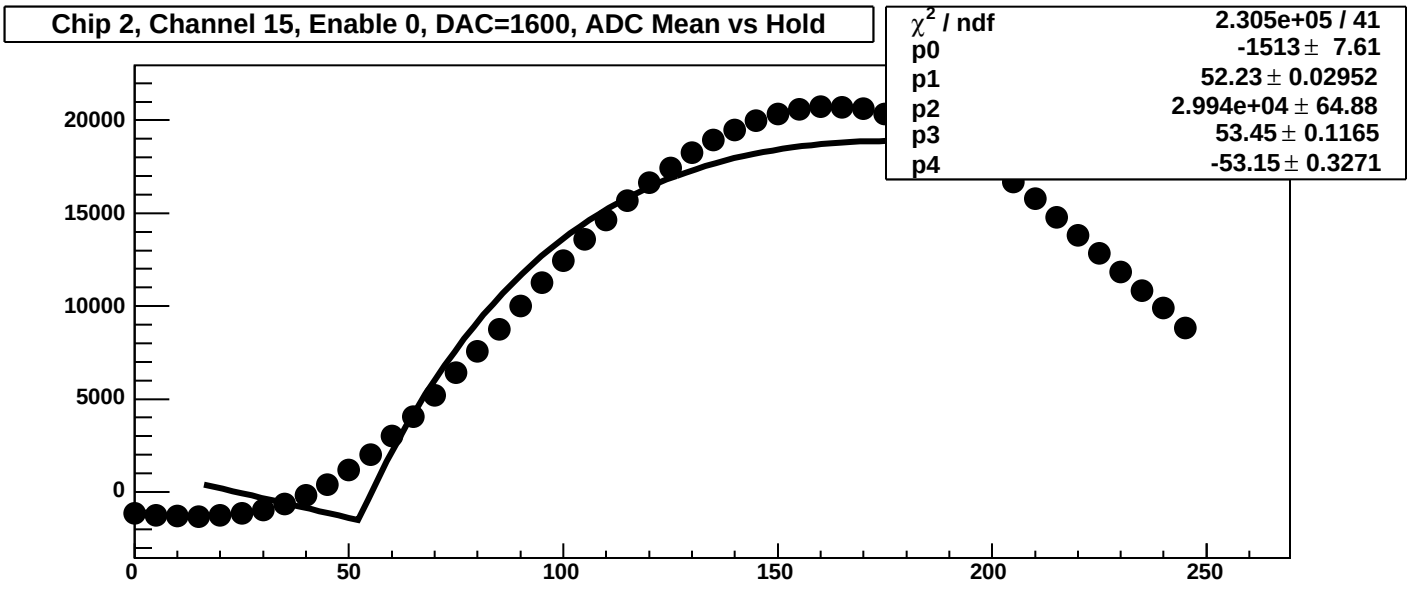

Chip 2, Channel 14, Enable 4, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250


Chip 2, Channel 14, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.775e+05 / 41
p0 -1555 ± 1.135
p1 134.9 ± 0.3812
p2 1.352e+04 ± 1.746
p3 -44.72 ± 1.22
p4 27.86 ± 0.01606
15000
10000
5000
0
-5000
0
50
100
150
200
250

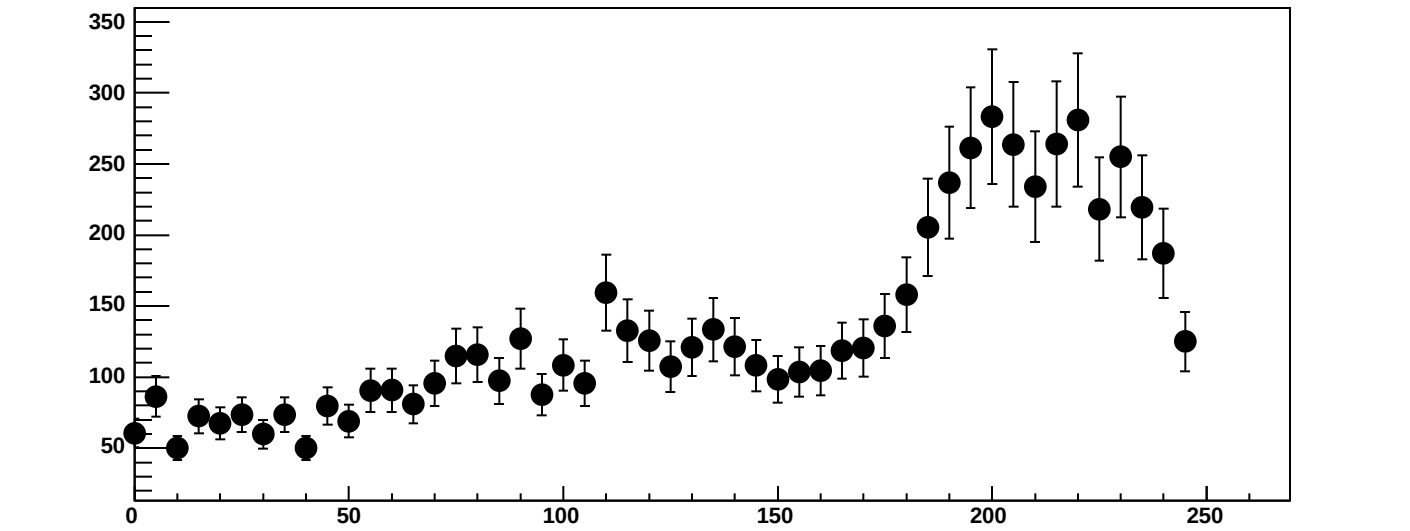

Chip 2, Channel 14, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 2, Channel 14, Enable 5, DAC=1600, ADC Residuals vs Hold
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

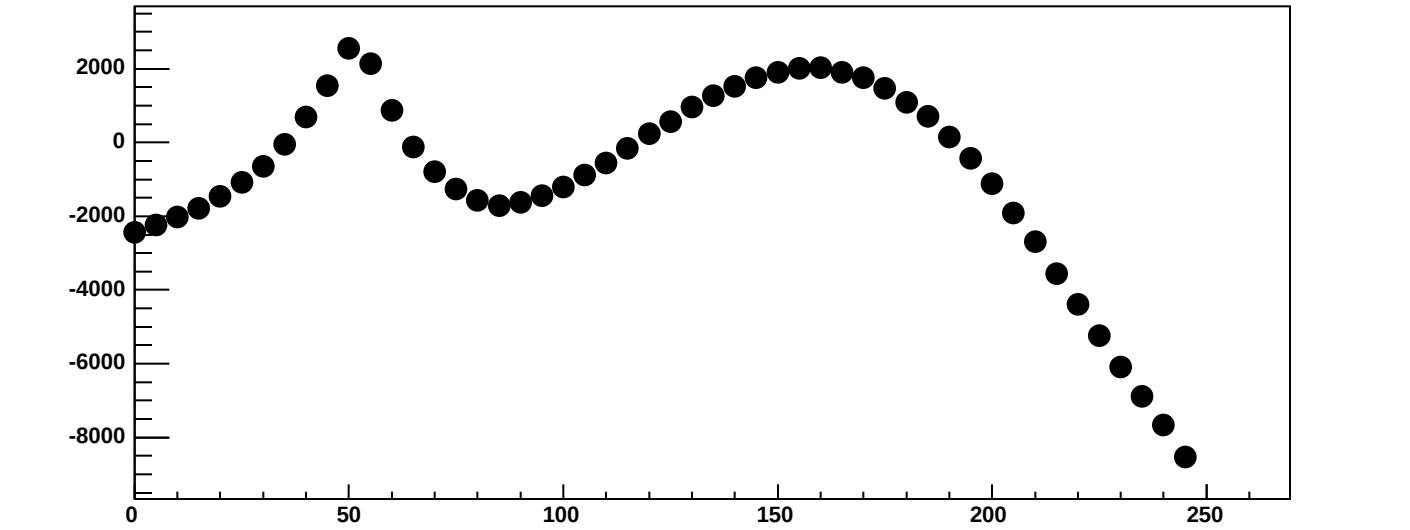
Chip 2, Channel 15, Enable 0, DAC=1600, ADC Mean vs Hold



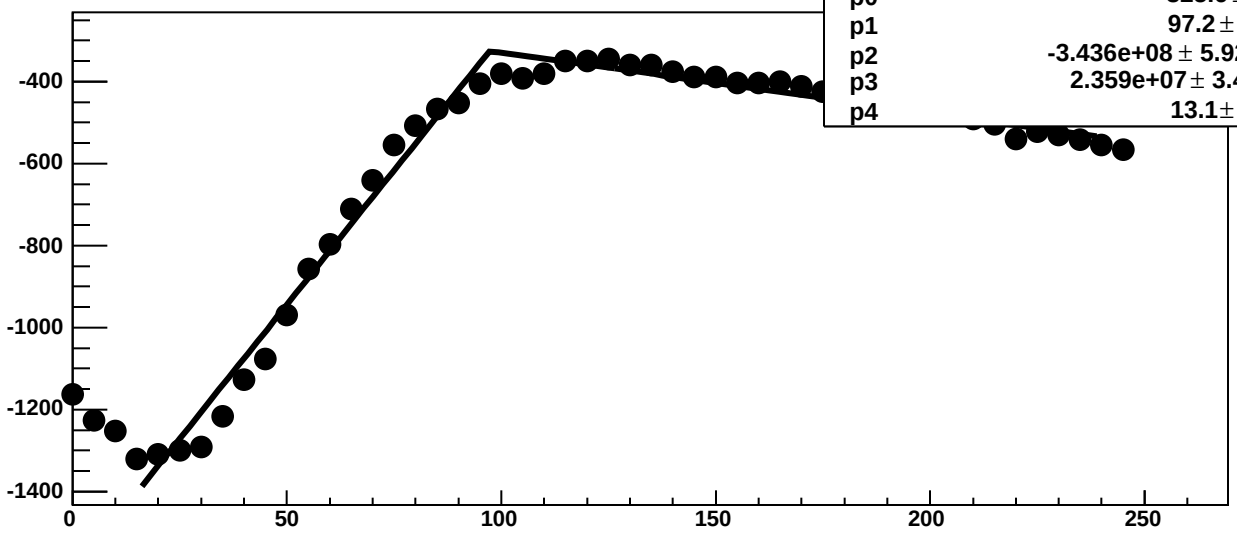
Chip 2, Channel 15, Enable 0, DAC=1600, ADC Noise vs Hold



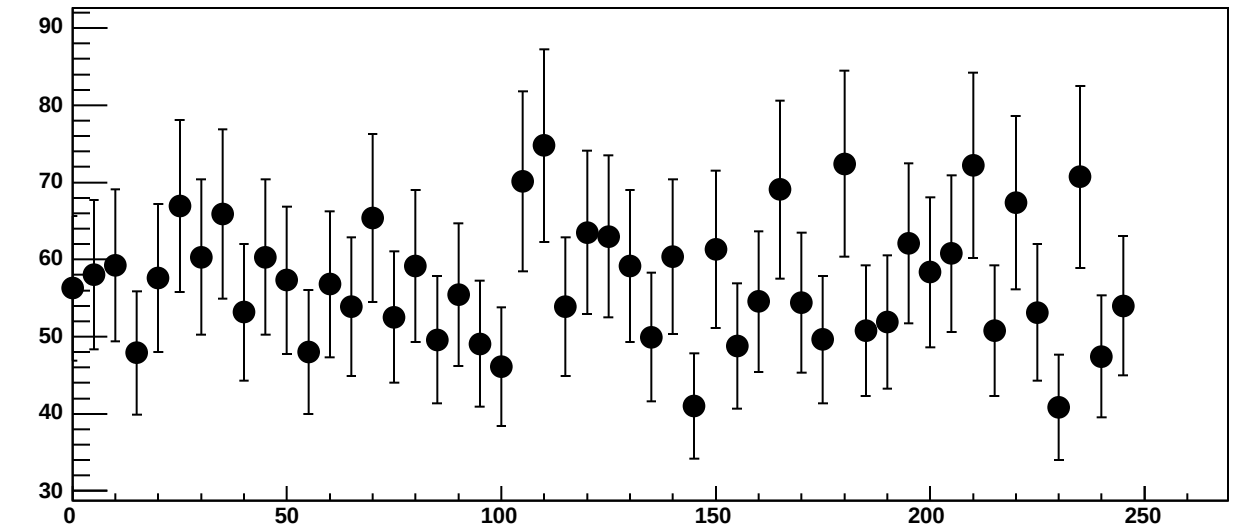
Chip 2, Channel 15, Enable 0, DAC=1600, ADC Residuals vs Hold



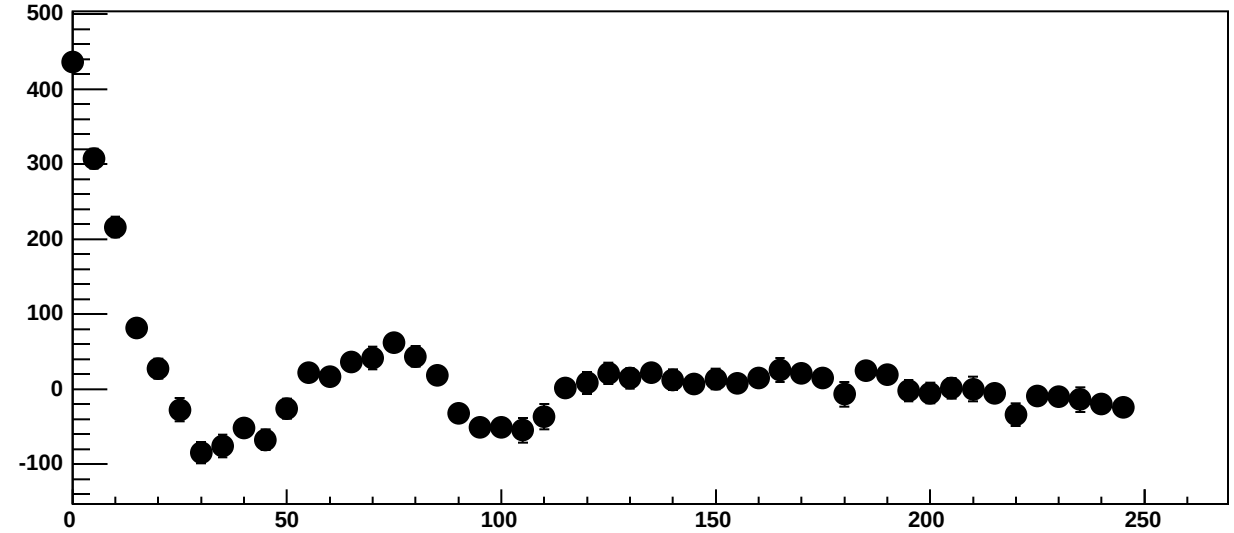
Chip 2, Channel 15, Enable 1, DAC=1600, ADC Mean vs Hold

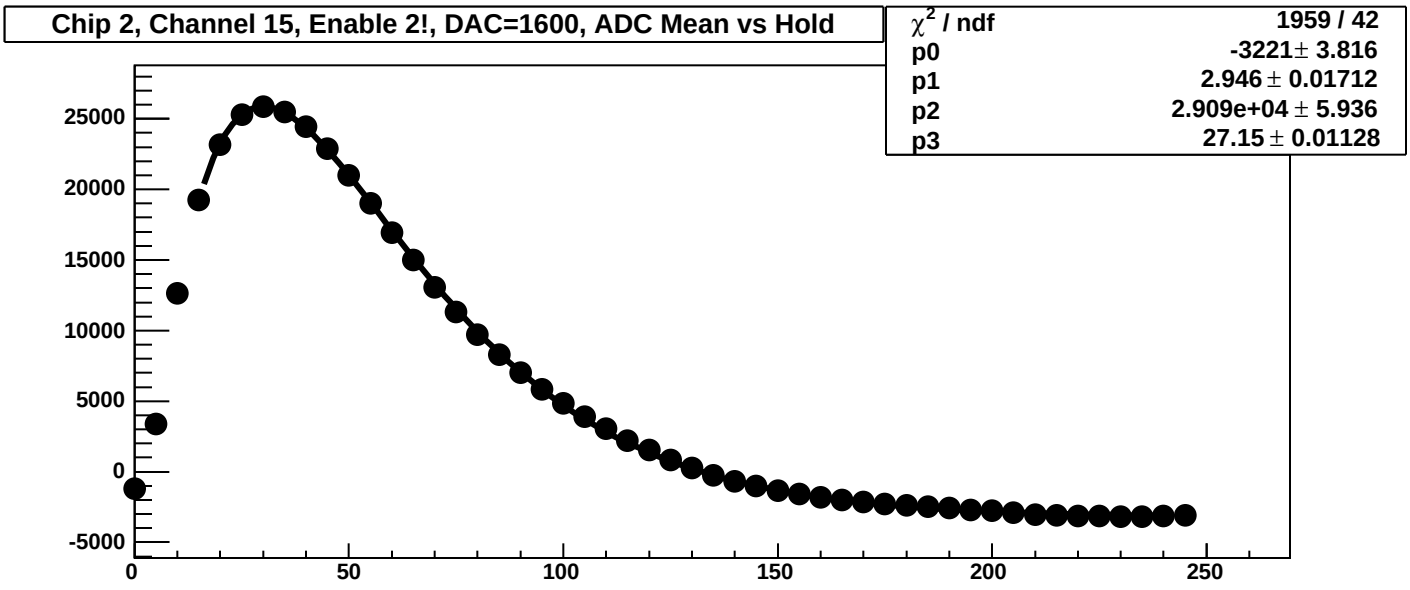


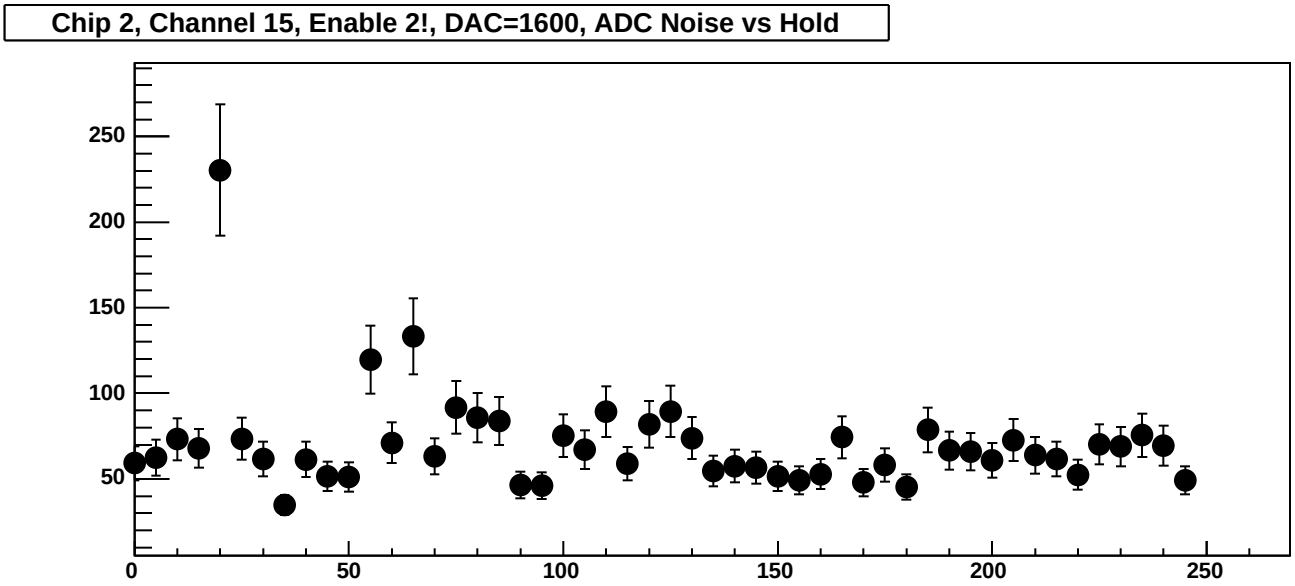
Chip 2, Channel 15, Enable 1, DAC=1600, ADC Noise vs Hold

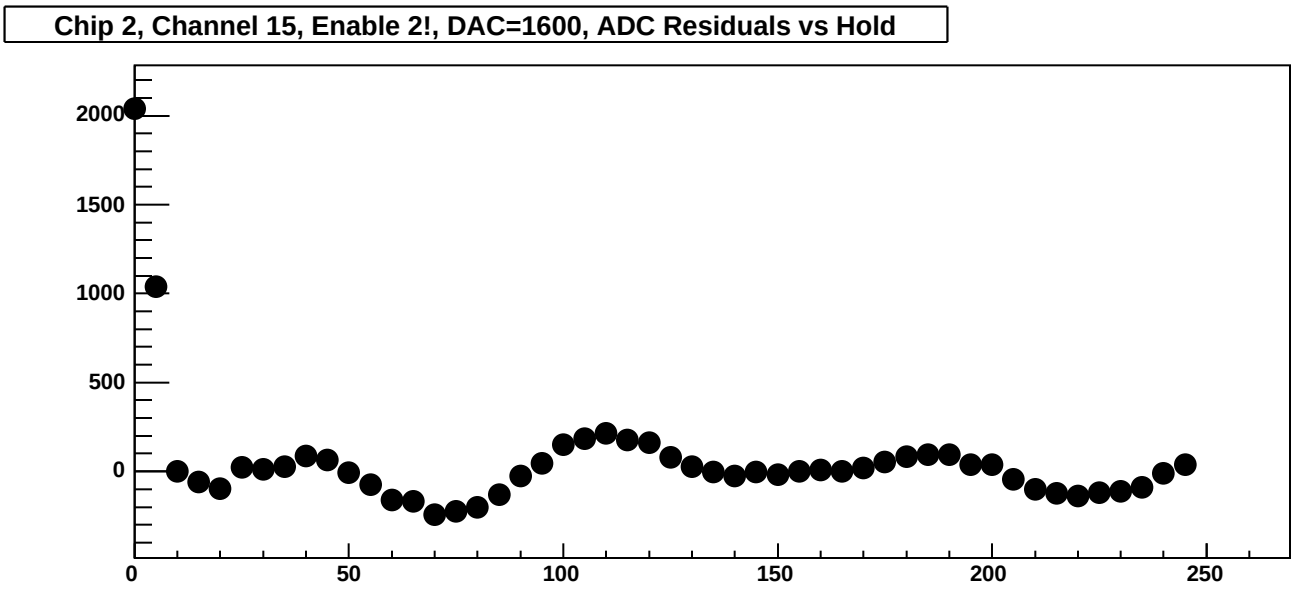


Chip 2, Channel 15, Enable 1, DAC=1600, ADC Residuals vs Hold

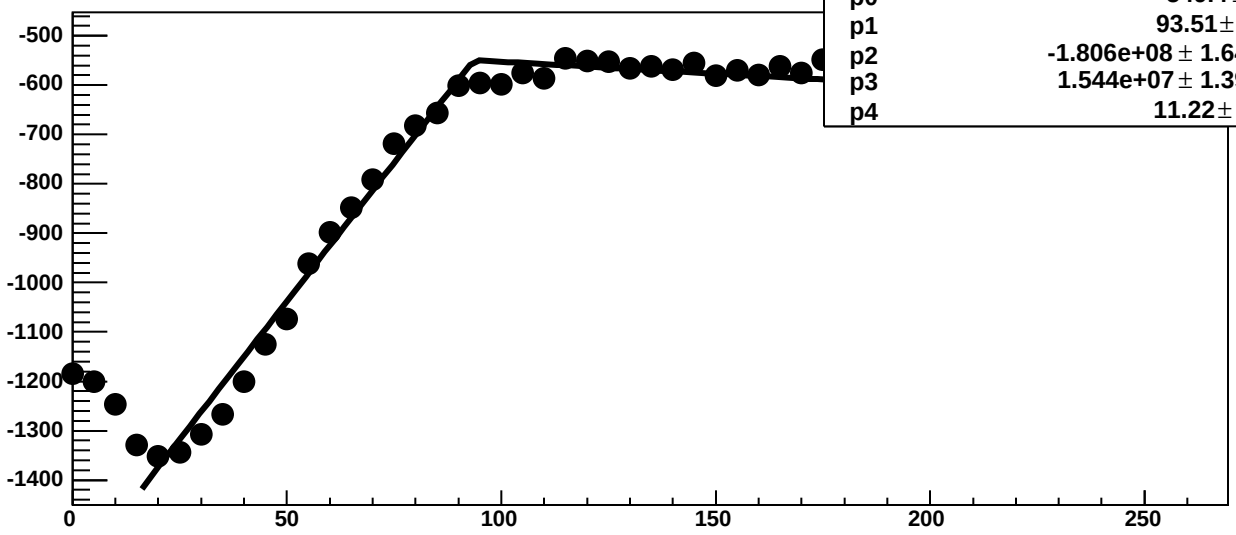



Chip 2, Channel 15, Enable 2!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1959 / 42
p0 -3221 ± 3.816
p1 2.946 ± 0.01712
p2 2.909e+04 ± 5.936
p3 27.15 ± 0.01128
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

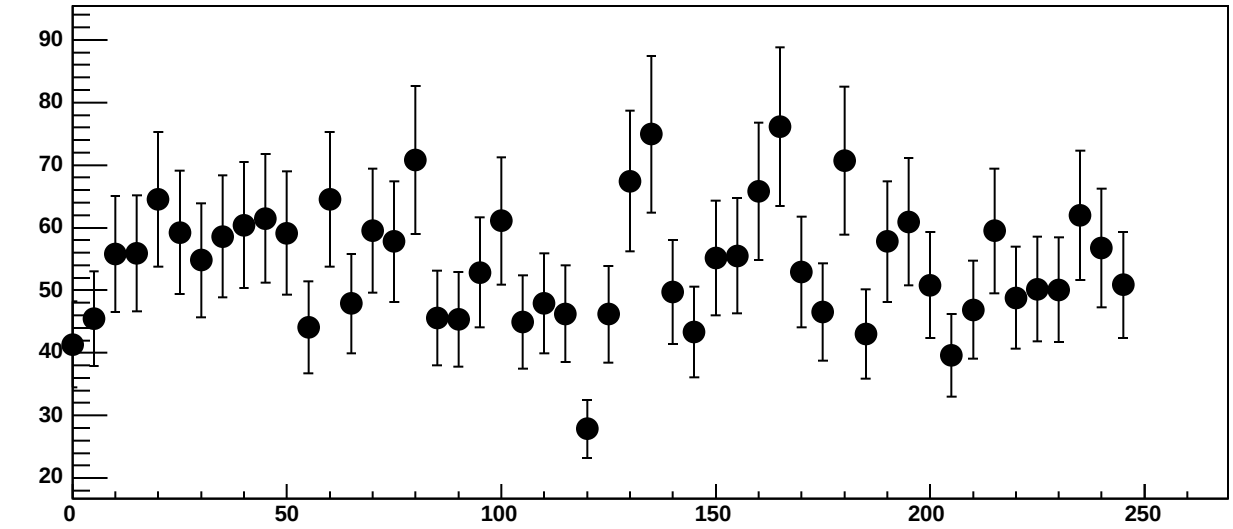

Chip 2, Channel 15, Enable 2!, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
50
100
150
200
250


Chip 2, Channel 15, Enable 2!, DAC=1600, ADC Residuals vs Hold
2000
1500
1000
500
0
0
50
100
150
200
250

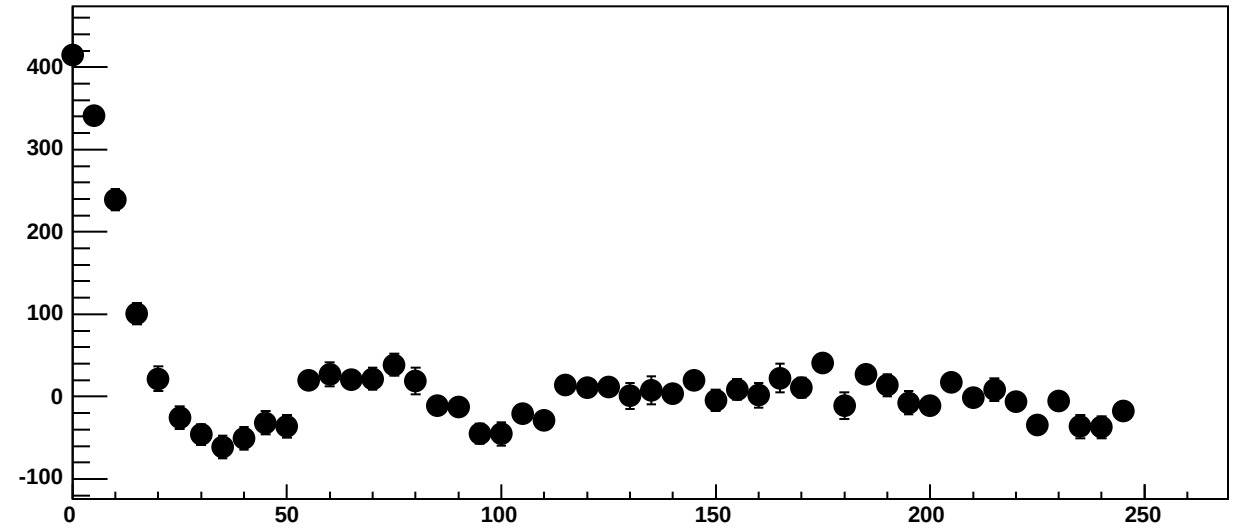
Chip 2, Channel 15, Enable 3, DAC=1600, ADC Mean vs Hold



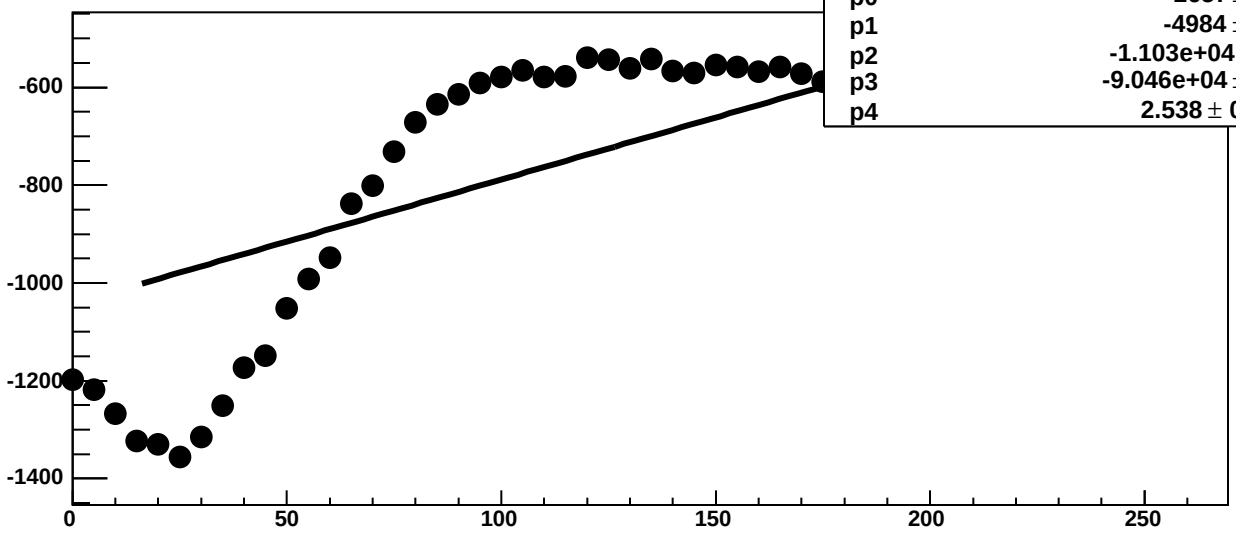
Chip 2, Channel 15, Enable 3, DAC=1600, ADC Noise vs Hold



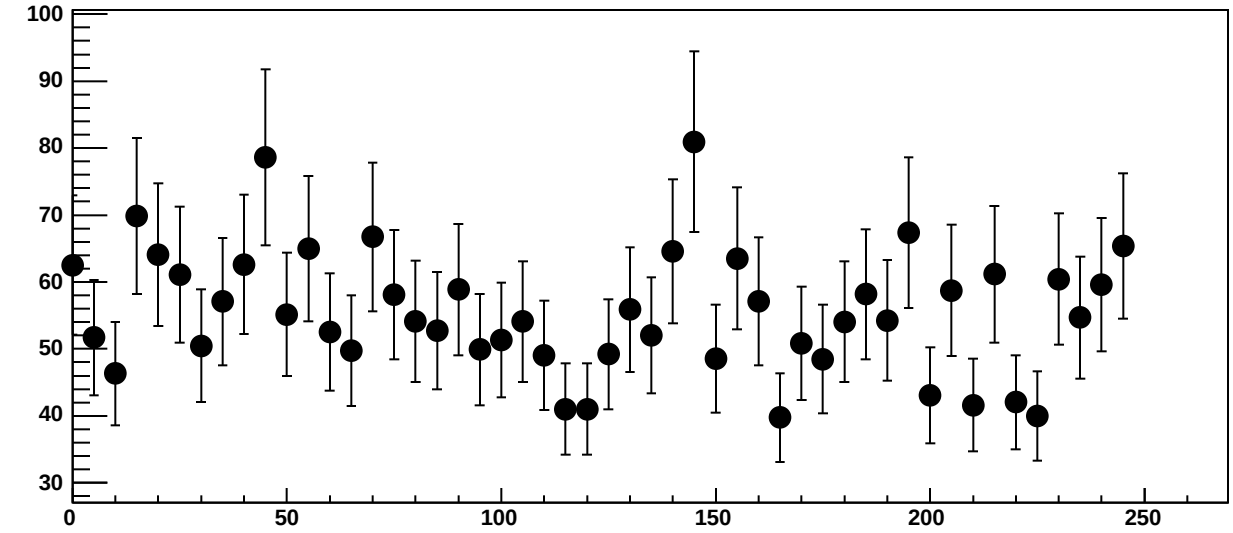
Chip 2, Channel 15, Enable 3, DAC=1600, ADC Residuals vs Hold



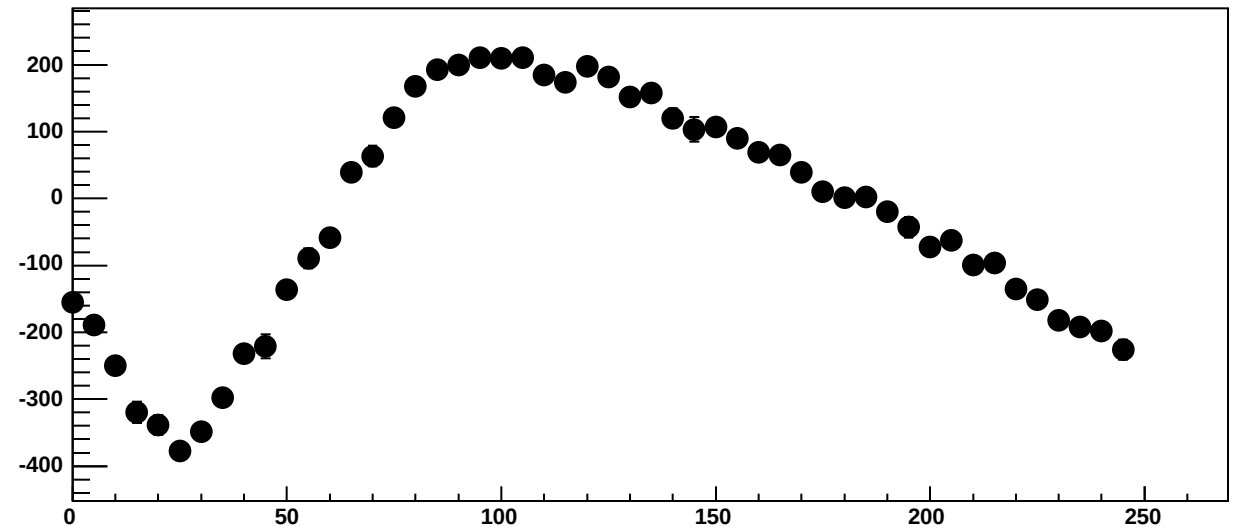
Chip 2, Channel 15, Enable 4, DAC=1600, ADC Mean vs Hold



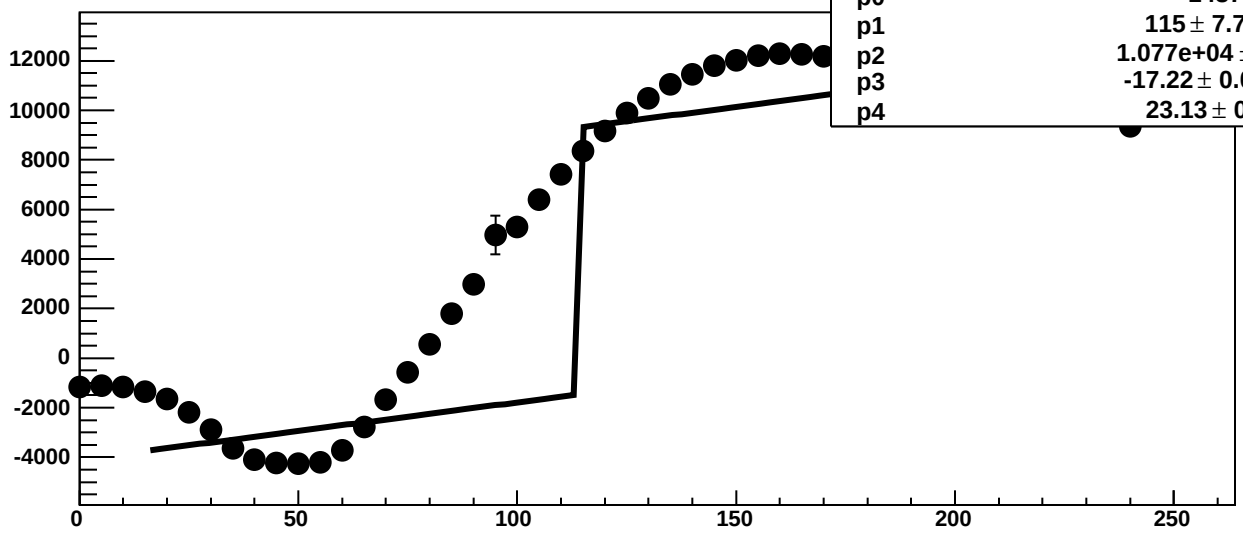
Chip 2, Channel 15, Enable 4, DAC=1600, ADC Noise vs Hold



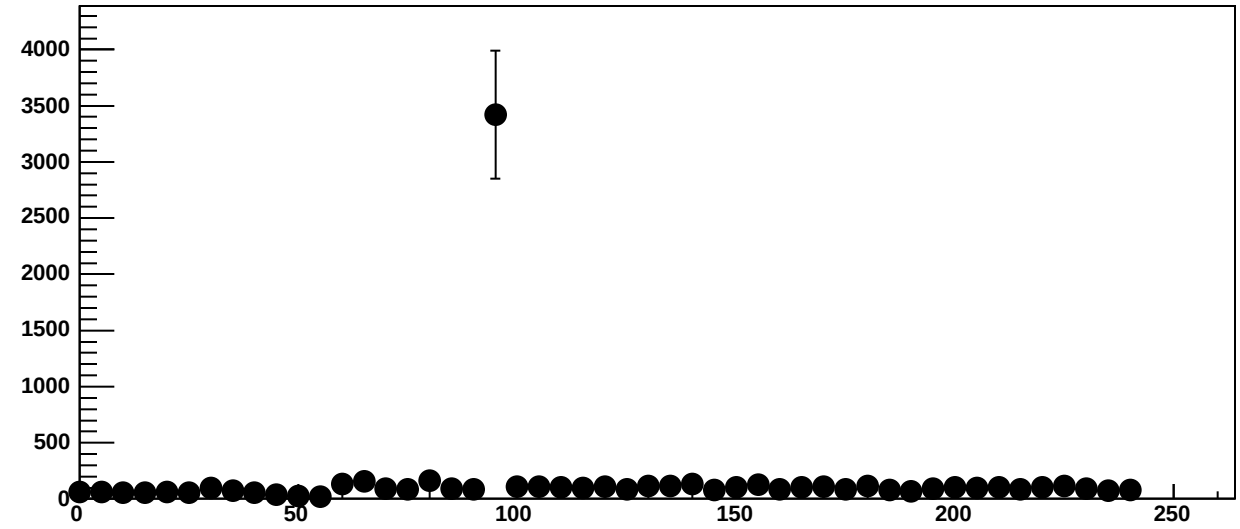
Chip 2, Channel 15, Enable 4, DAC=1600, ADC Residuals vs Hold



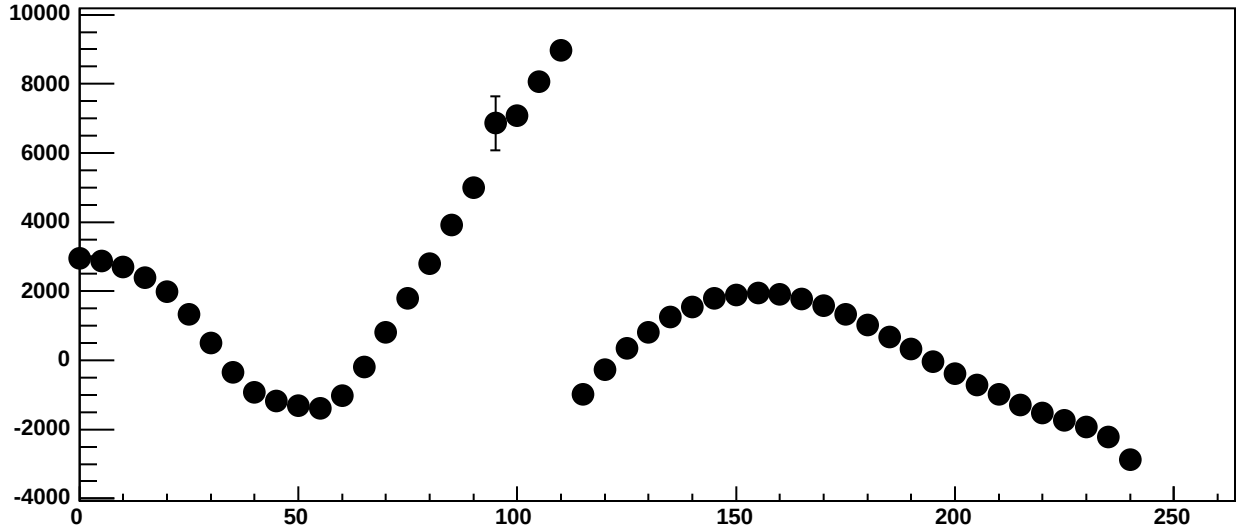
Chip 2, Channel 15, Enable 5, DAC=1600, ADC Mean vs Hold



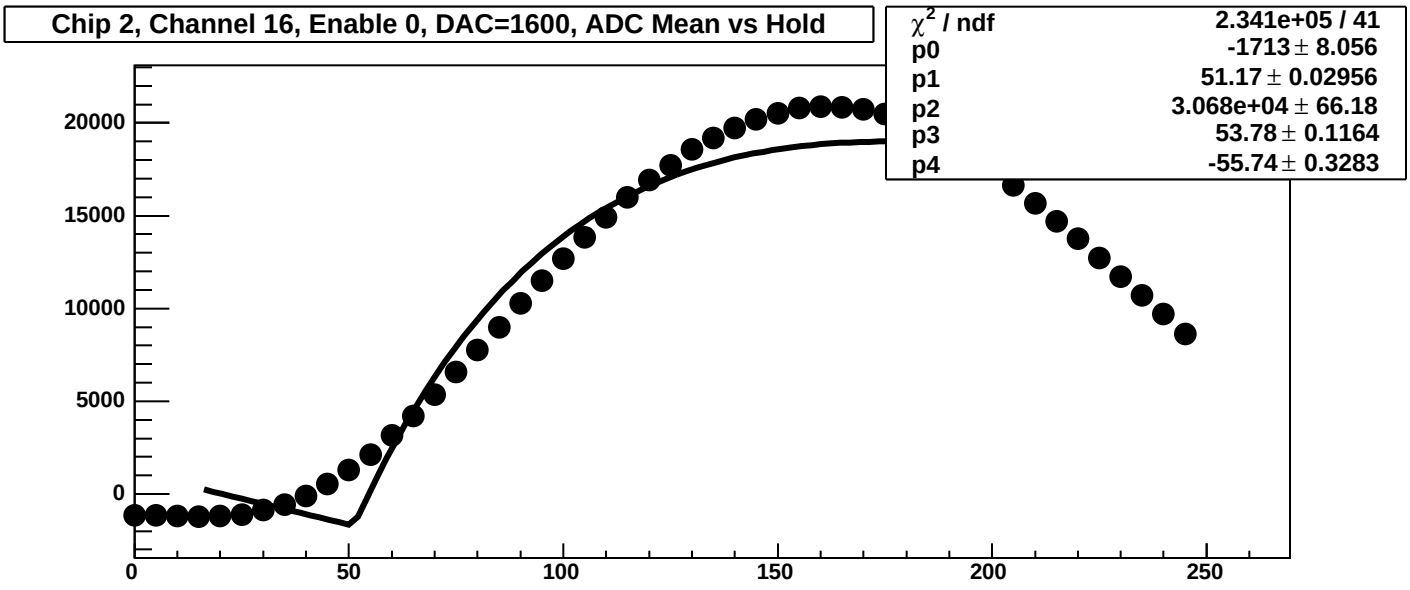
Chip 2, Channel 15, Enable 5, DAC=1600, ADC Noise vs Hold



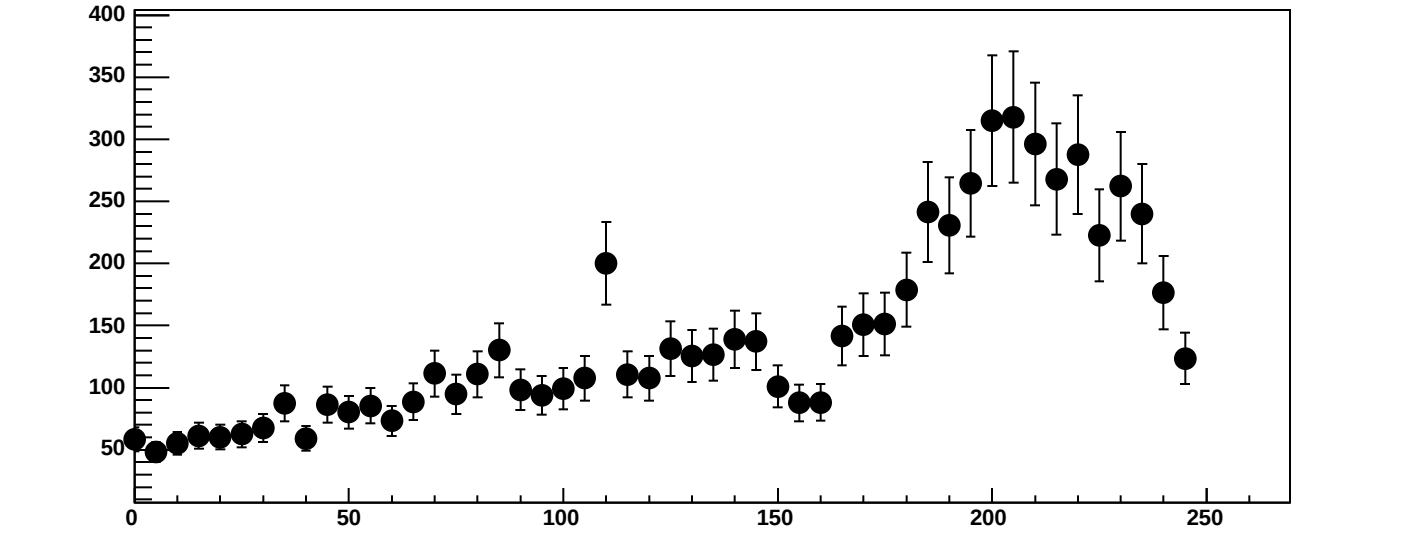
Chip 2, Channel 15, Enable 5, DAC=1600, ADC Residuals vs Hold



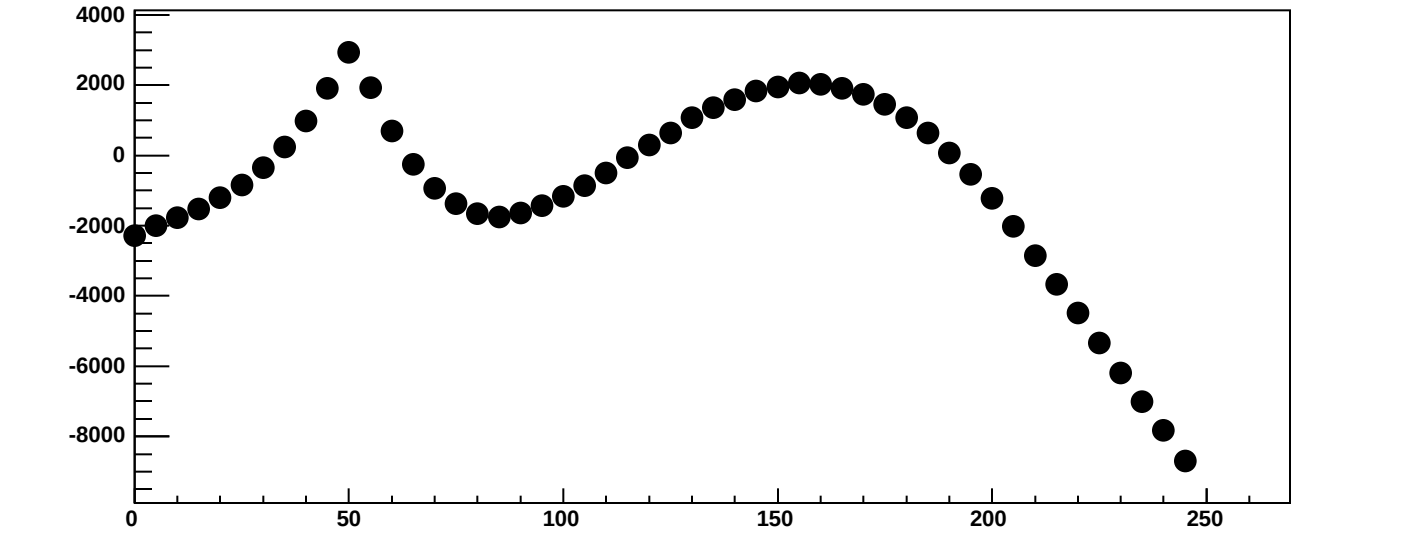
Chip 2, Channel 16, Enable 0, DAC=1600, ADC Mean vs Hold



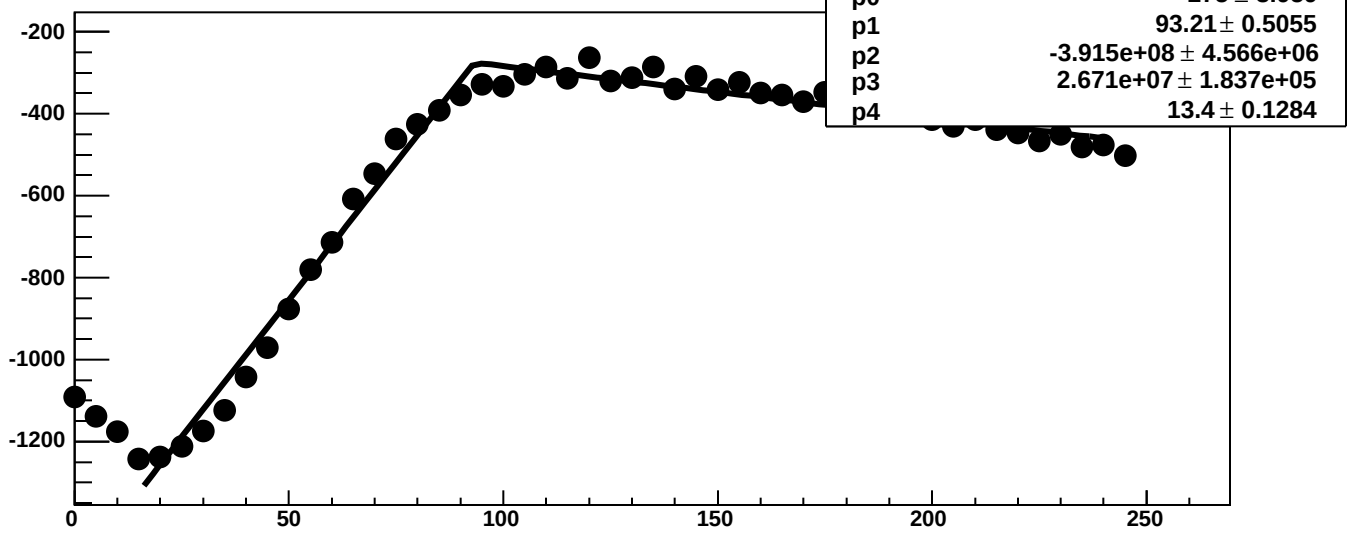
Chip 2, Channel 16, Enable 0, DAC=1600, ADC Noise vs Hold



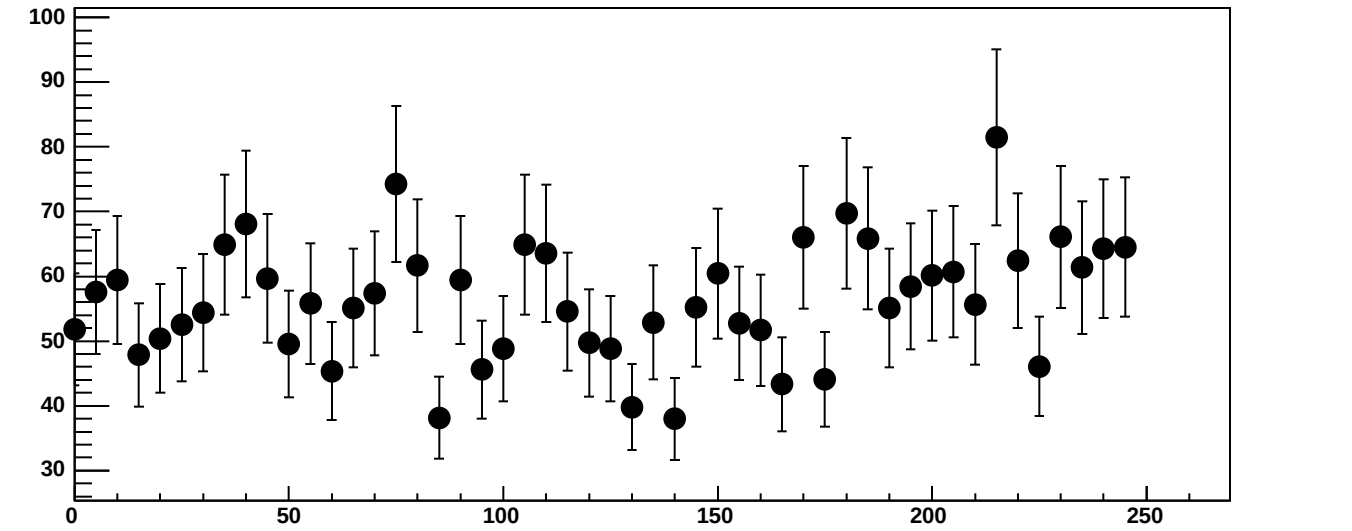
Chip 2, Channel 16, Enable 0, DAC=1600, ADC Residuals vs Hold



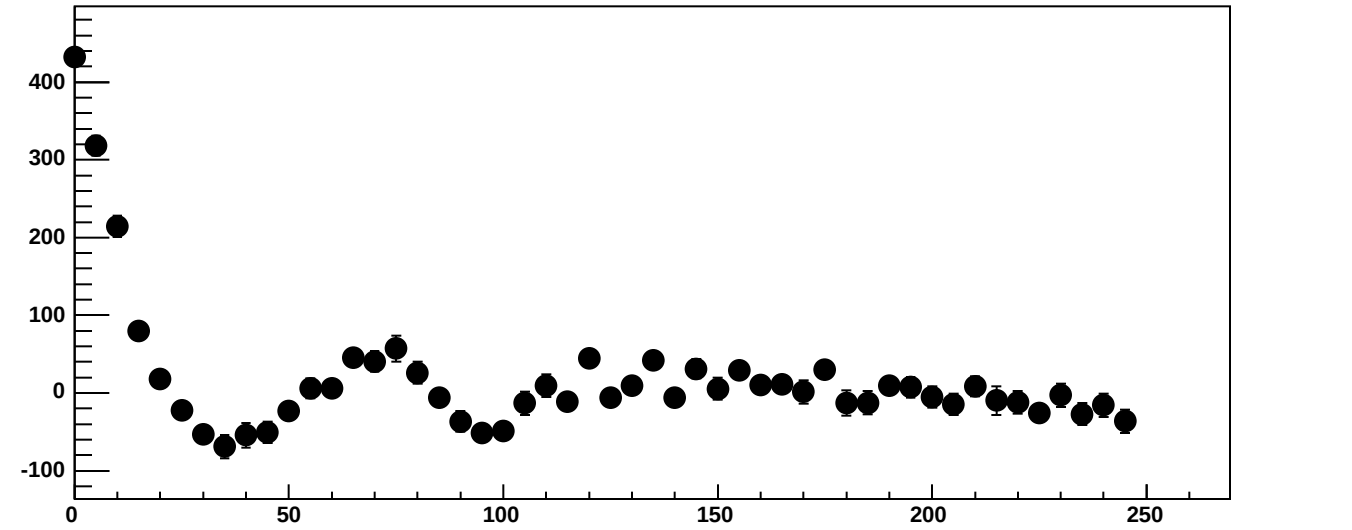
Chip 2, Channel 16, Enable 1, DAC=1600, ADC Mean vs Hold



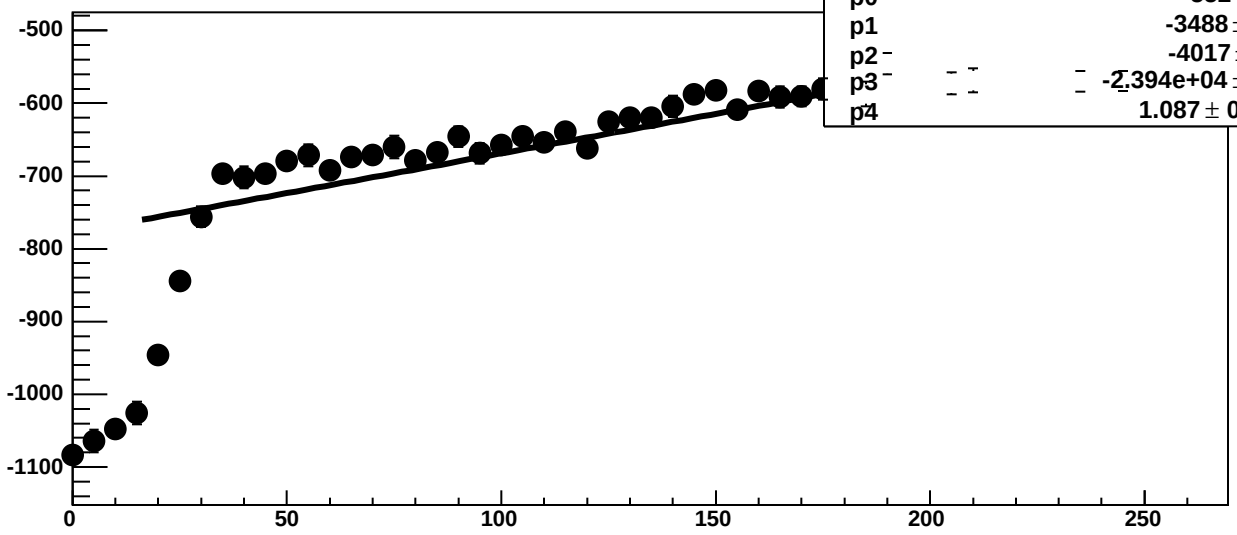
Chip 2, Channel 16, Enable 1, DAC=1600, ADC Noise vs Hold



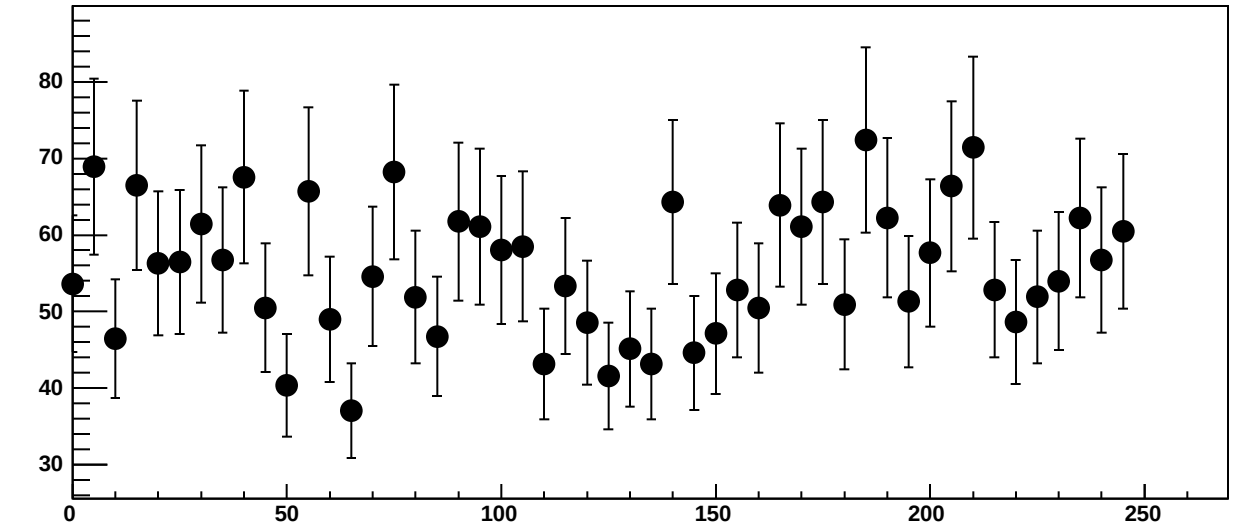
Chip 2, Channel 16, Enable 1, DAC=1600, ADC Residuals vs Hold



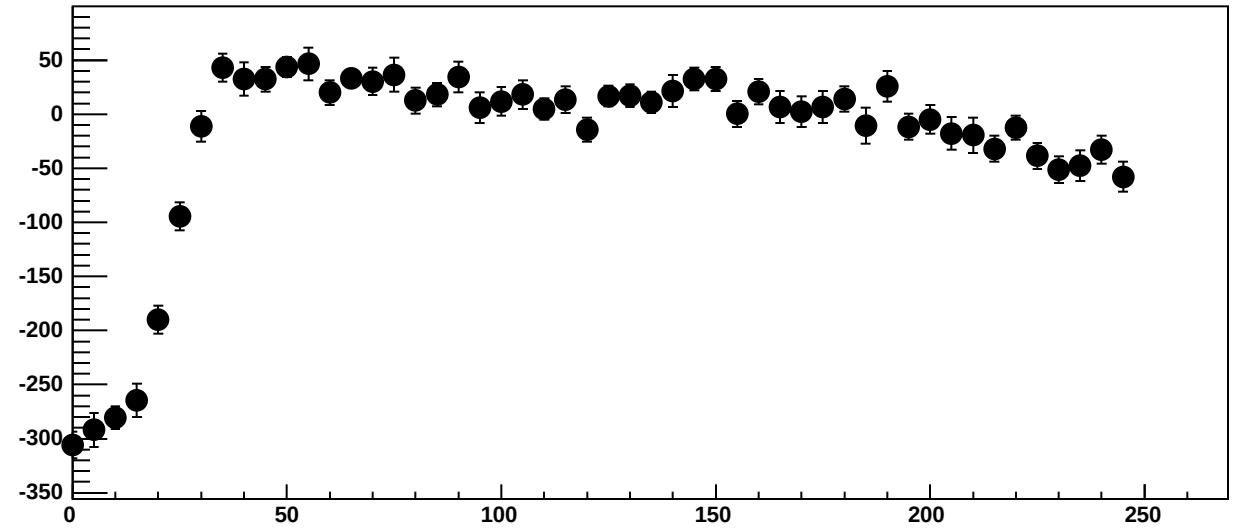
Chip 2, Channel 16, Enable 2, DAC=1600, ADC Mean vs Hold

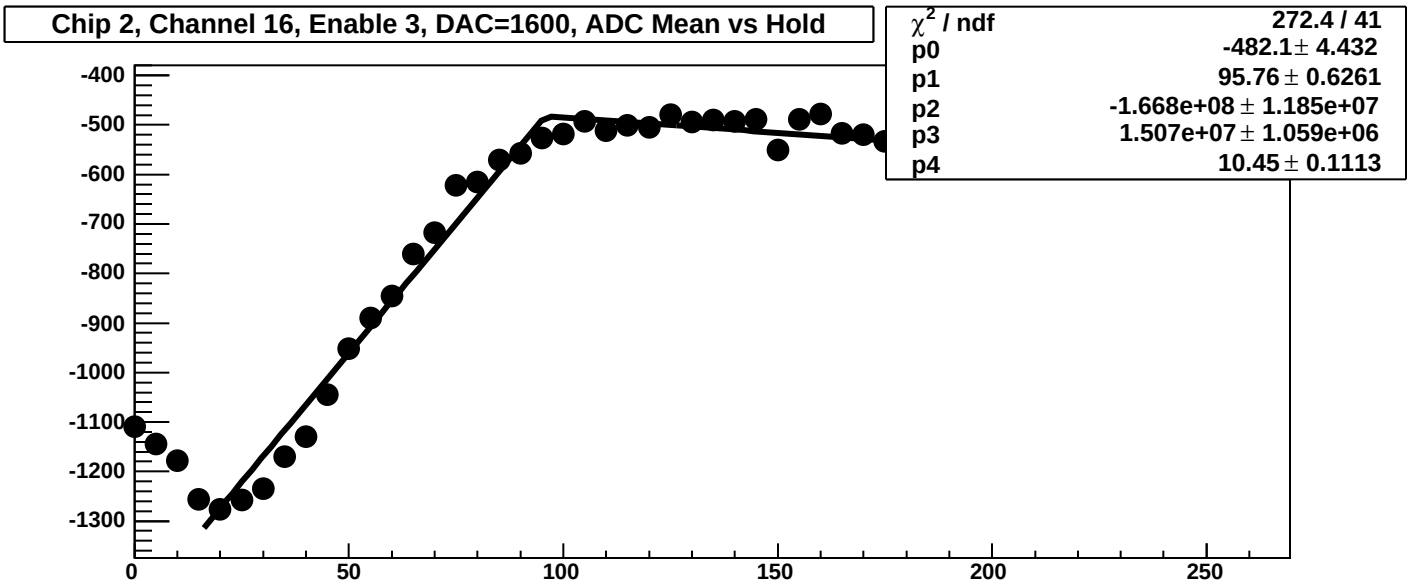


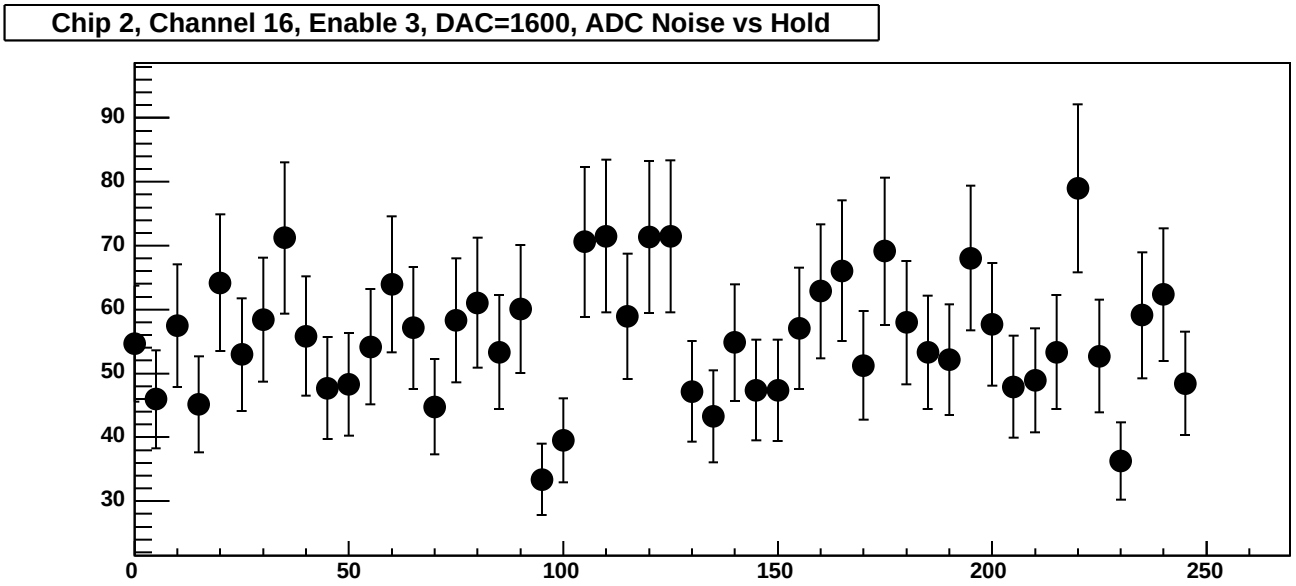
Chip 2, Channel 16, Enable 2, DAC=1600, ADC Noise vs Hold

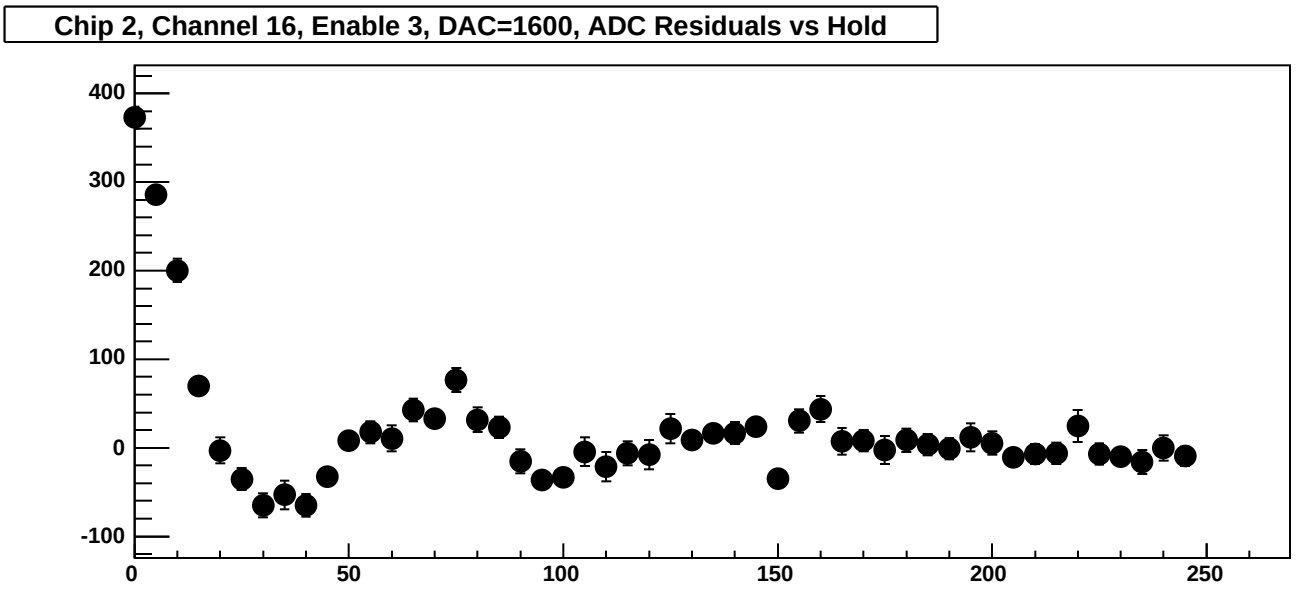


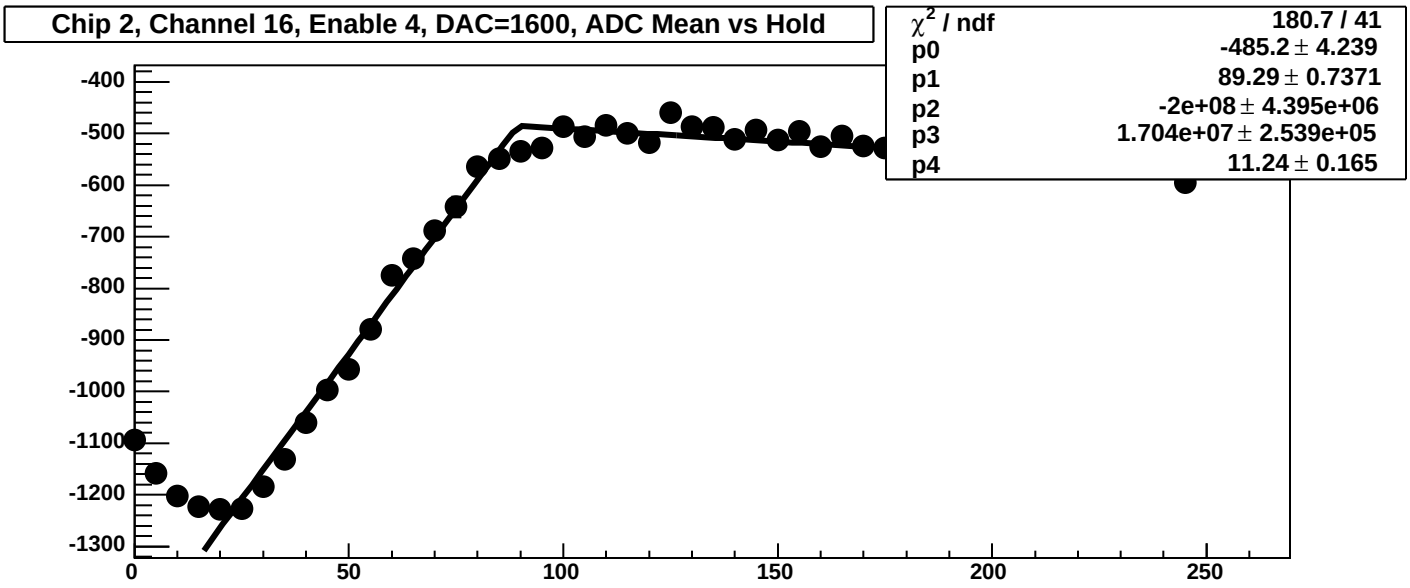
Chip 2, Channel 16, Enable 2, DAC=1600, ADC Residuals vs Hold

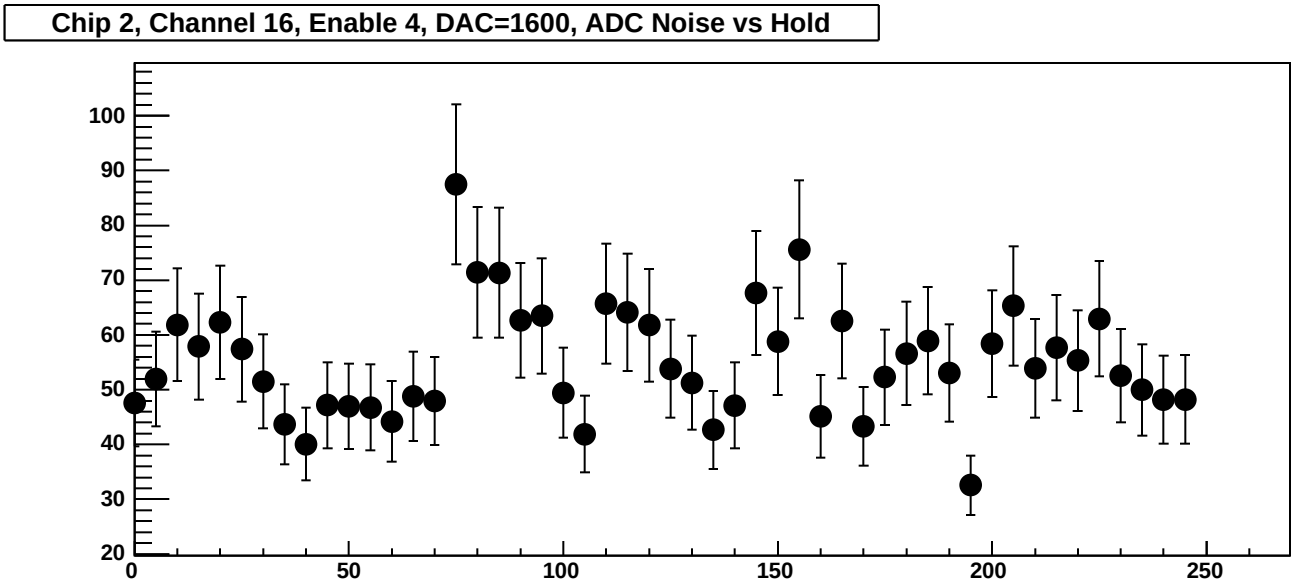


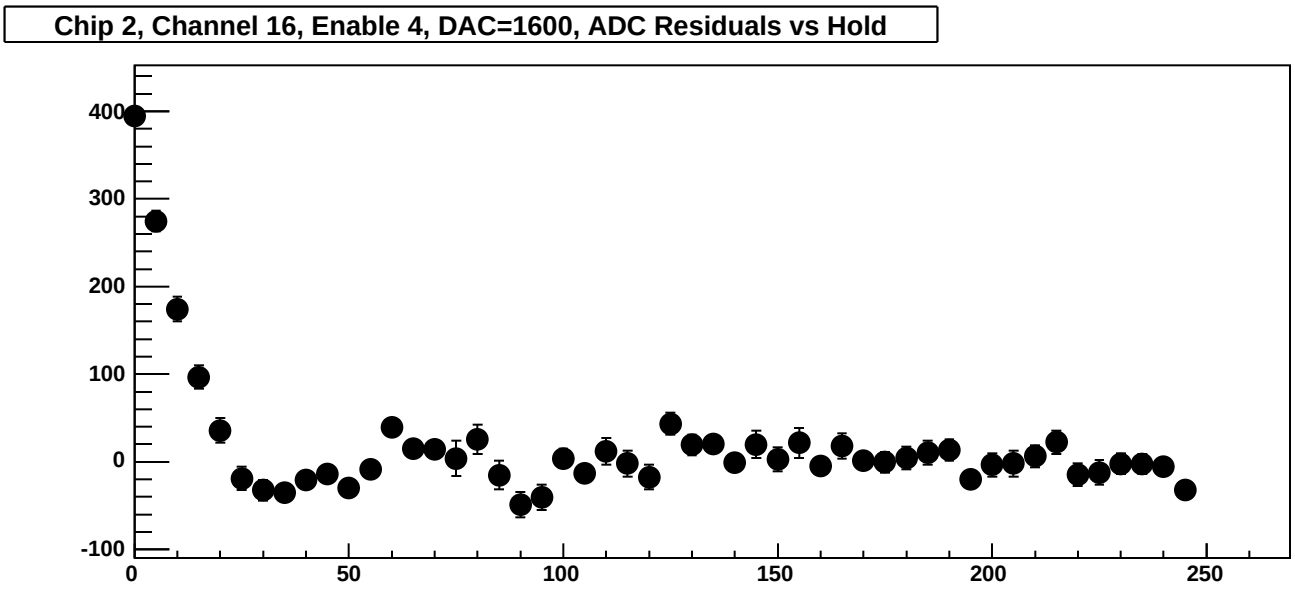

Chip 2, Channel 16, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 272.4 / 41
p0 -482.1 ± 4.432
p1 95.76 ± 0.6261
p2 -1.668e+08 ± 1.185e+07
p3 1.507e+07 ± 1.059e+06
p4 10.45 ± 0.1113
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
50
100
150
200
250


Chip 2, Channel 16, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

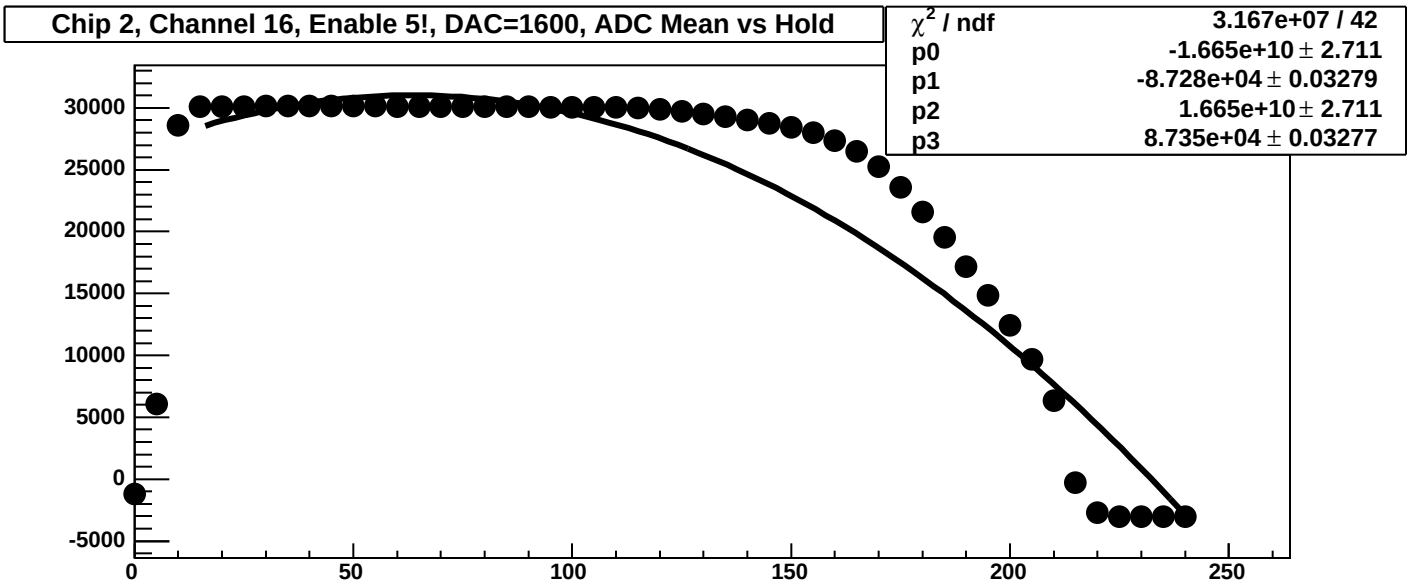

Chip 2, Channel 16, Enable 3, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 2, Channel 16, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 180.7 / 41
p0 -485.2 ± 4.239
p1 89.29 ± 0.7371
p2 -2e+08 ± 4.395e+06
p3 1.704e+07 ± 2.539e+05
p4 11.24 ± 0.165
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
50
100
150
200
250

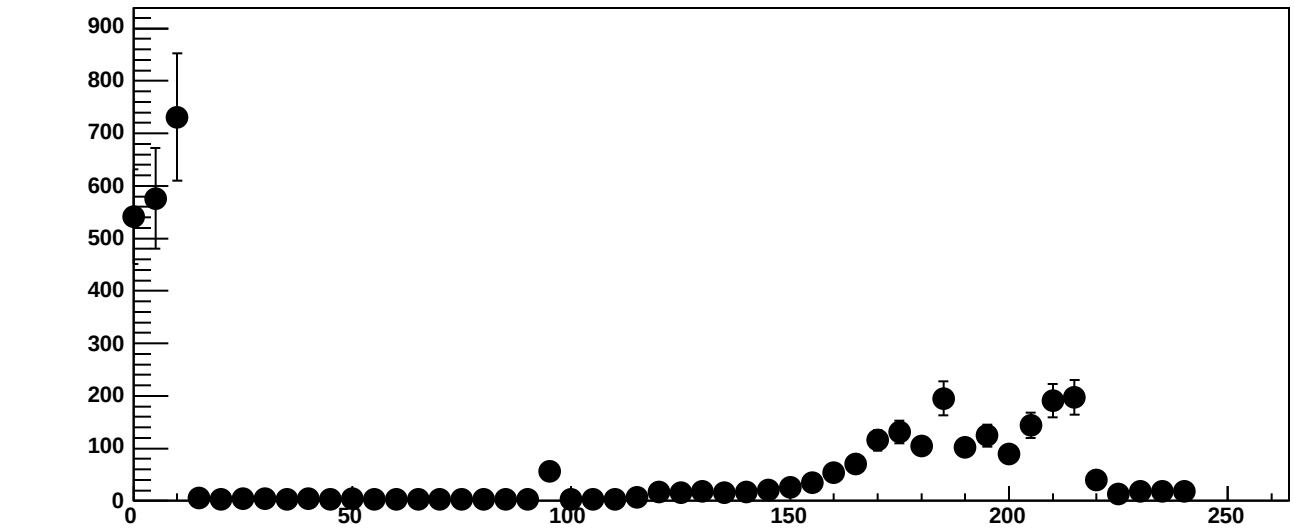

Chip 2, Channel 16, Enable 4, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 2, Channel 16, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

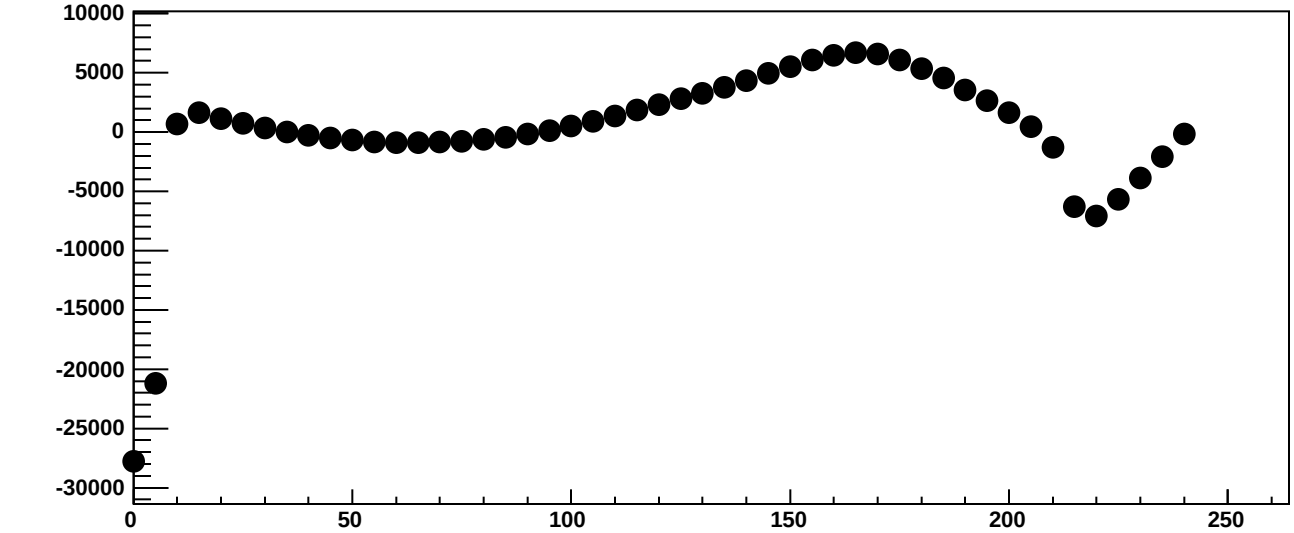
Chip 2, Channel 16, Enable 5!, DAC=1600, ADC Mean vs Hold

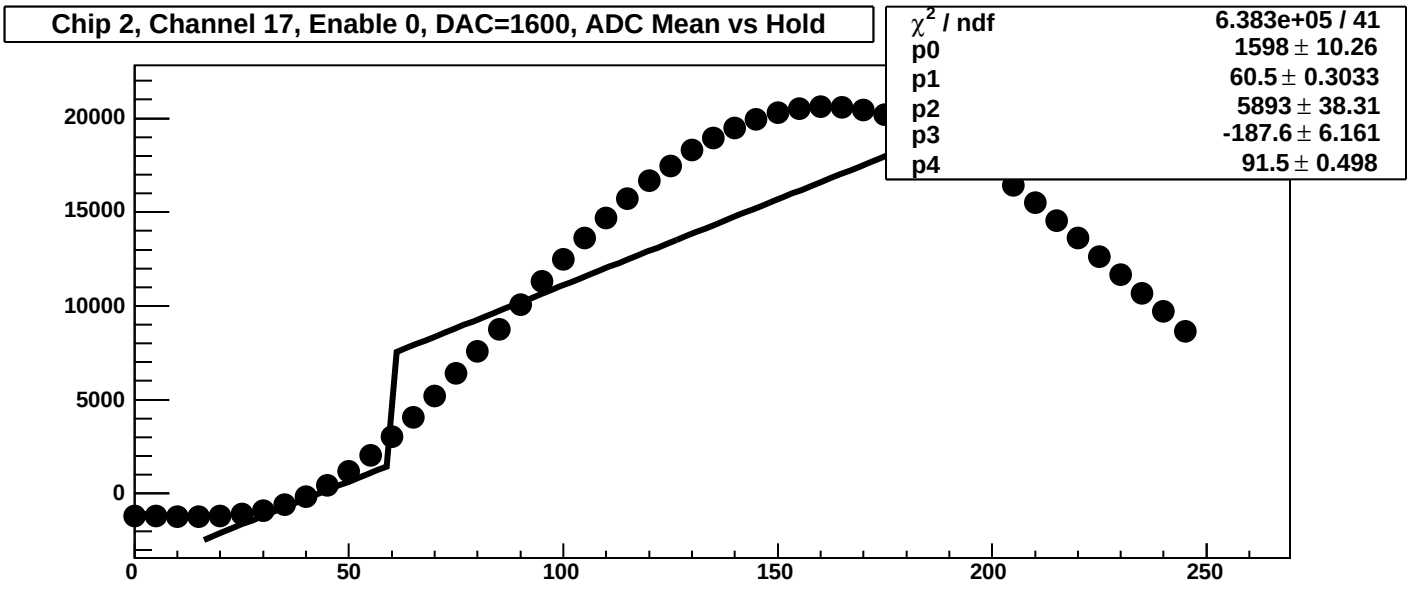


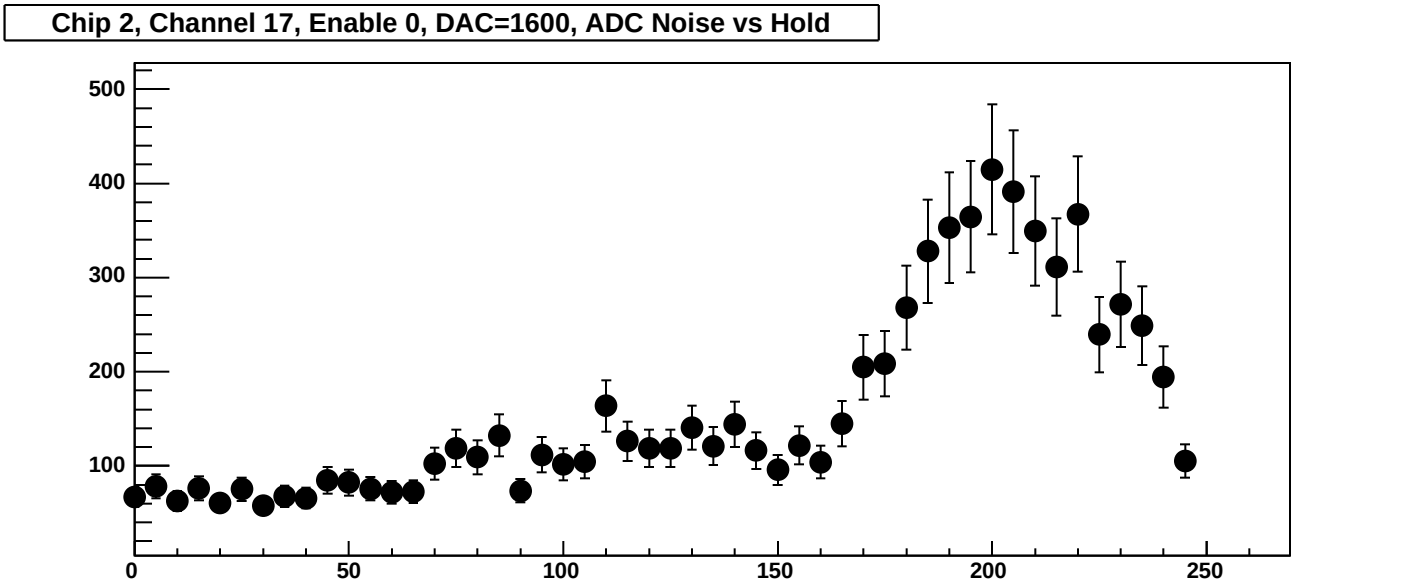
Chip 2, Channel 16, Enable 5!, DAC=1600, ADC Noise vs Hold

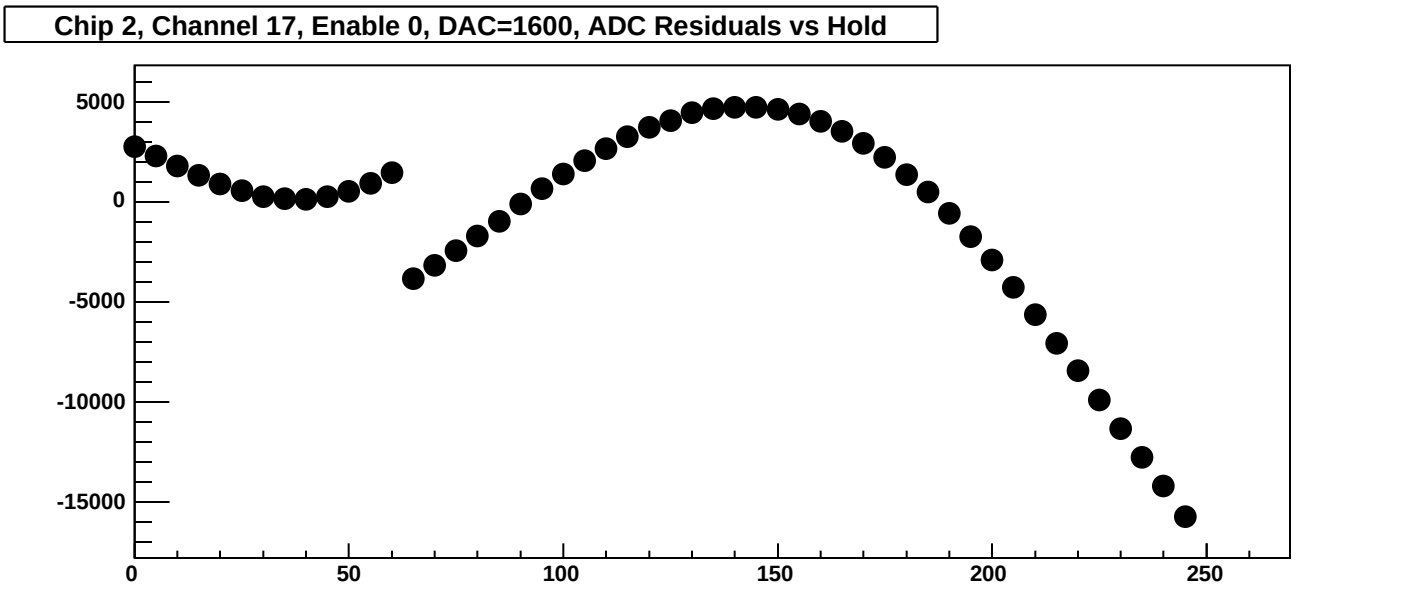


Chip 2, Channel 16, Enable 5!, DAC=1600, ADC Residuals vs Hold

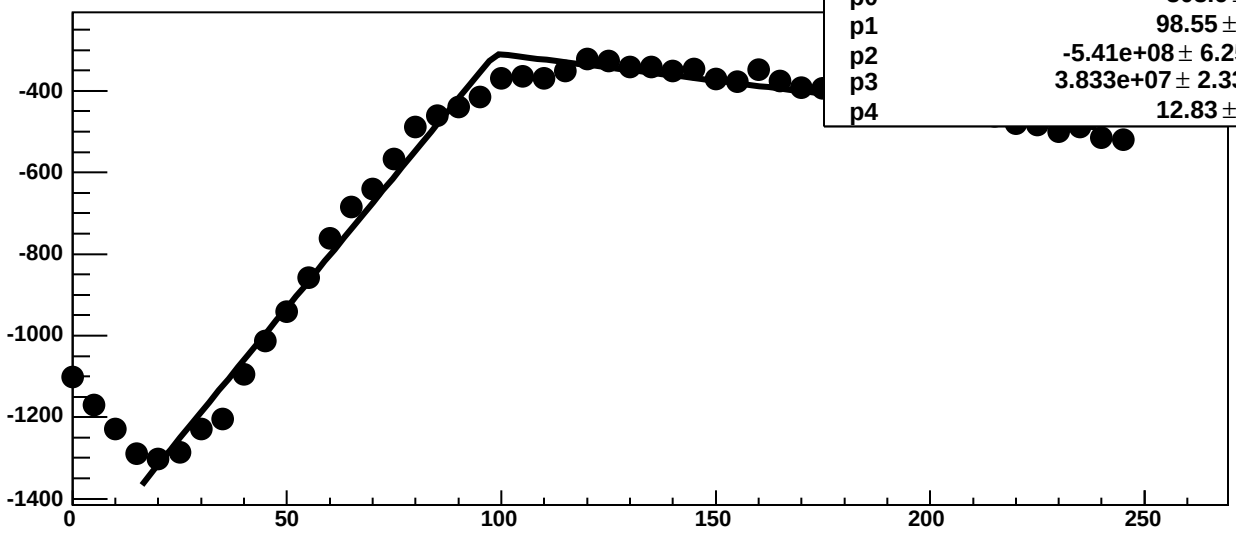



Chip 2, Channel 17, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 6.383e+05 / 41
p0 1598 ± 10.26
p1 60.5 ± 0.3033
p2 5893 ± 38.31
p3 -187.6 ± 6.161
p4 91.5 ± 0.498
20000
15000
10000
5000
0
0
50
100
150
200
250

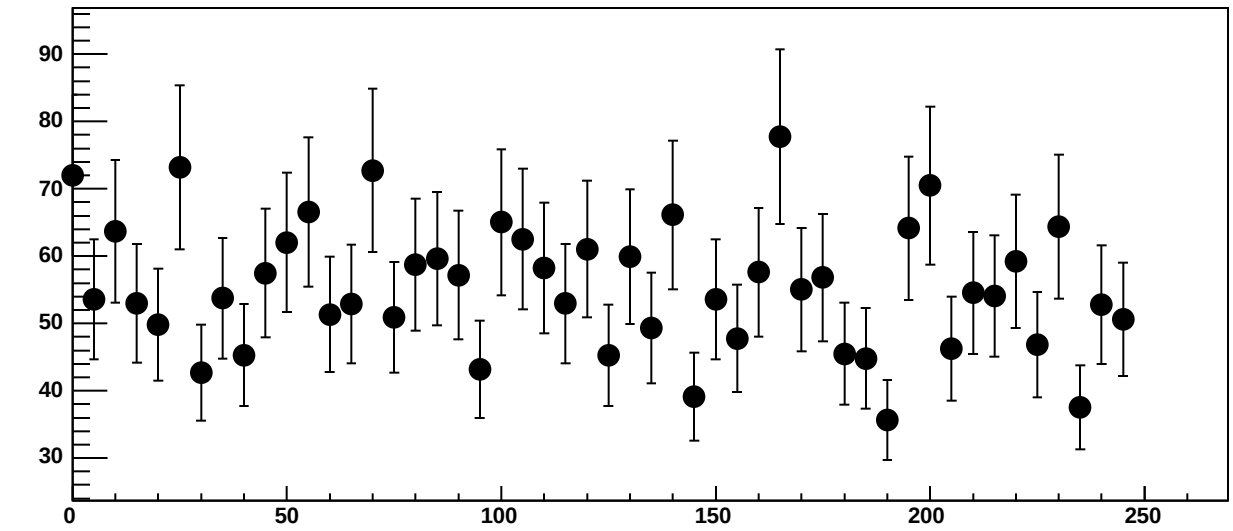

Chip 2, Channel 17, Enable 0, DAC=1600, ADC Noise vs Hold
500
400
300
200
100
0
50
100
150
200
250


Chip 2, Channel 17, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
0
50
100
150
200
250

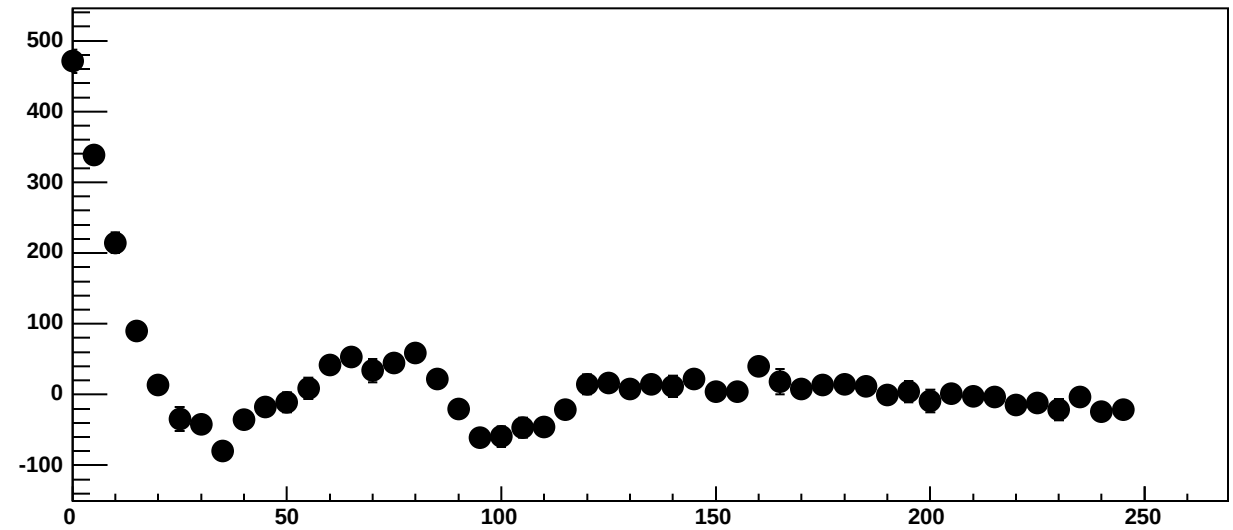
Chip 2, Channel 17, Enable 1, DAC=1600, ADC Mean vs Hold



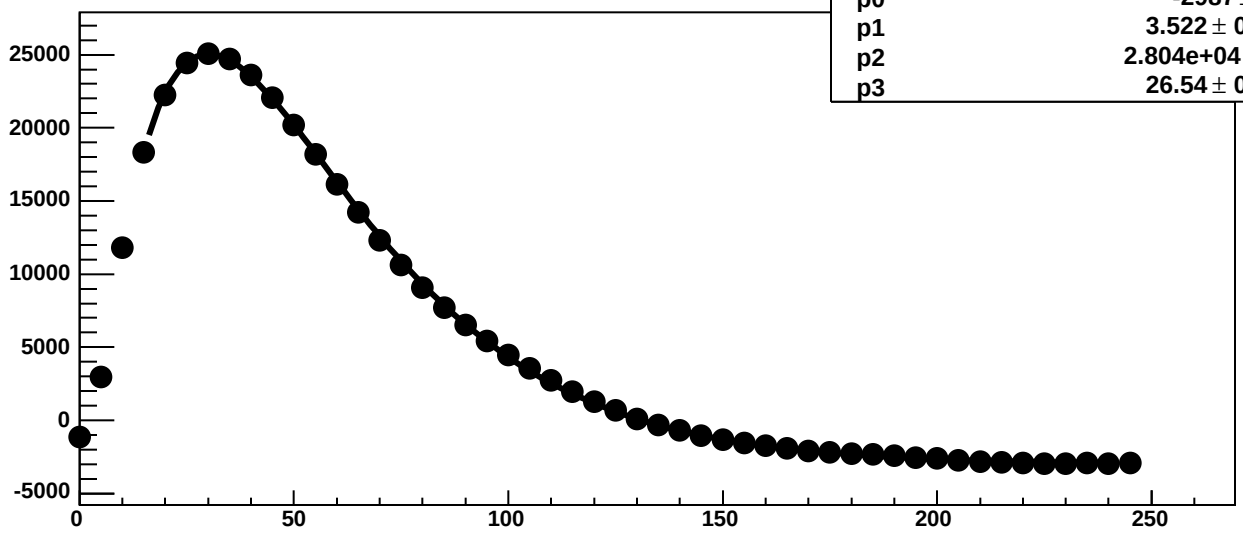
Chip 2, Channel 17, Enable 1, DAC=1600, ADC Noise vs Hold



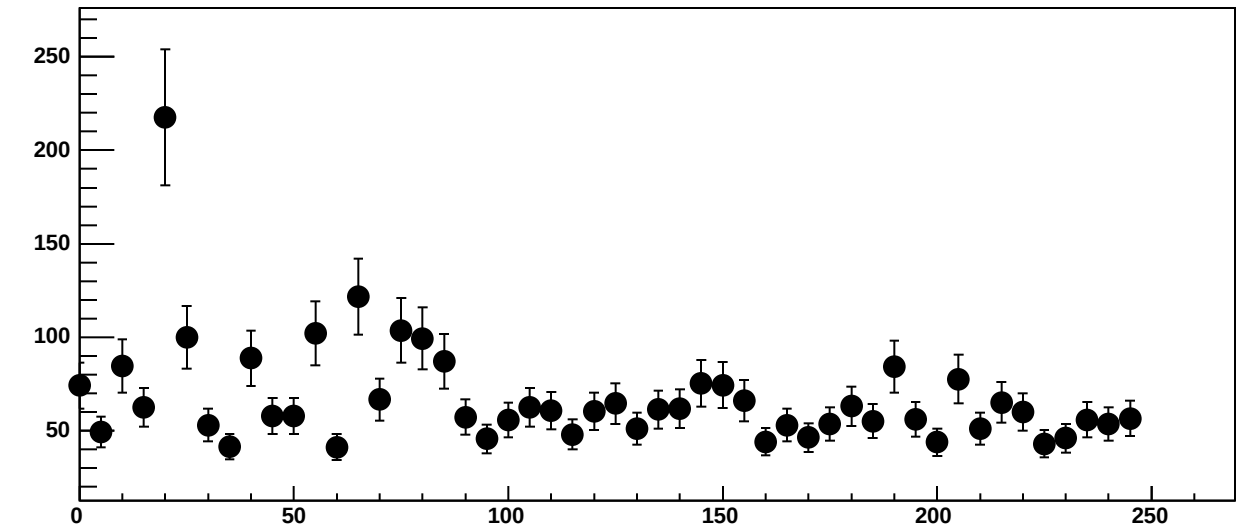
Chip 2, Channel 17, Enable 1, DAC=1600, ADC Residuals vs Hold



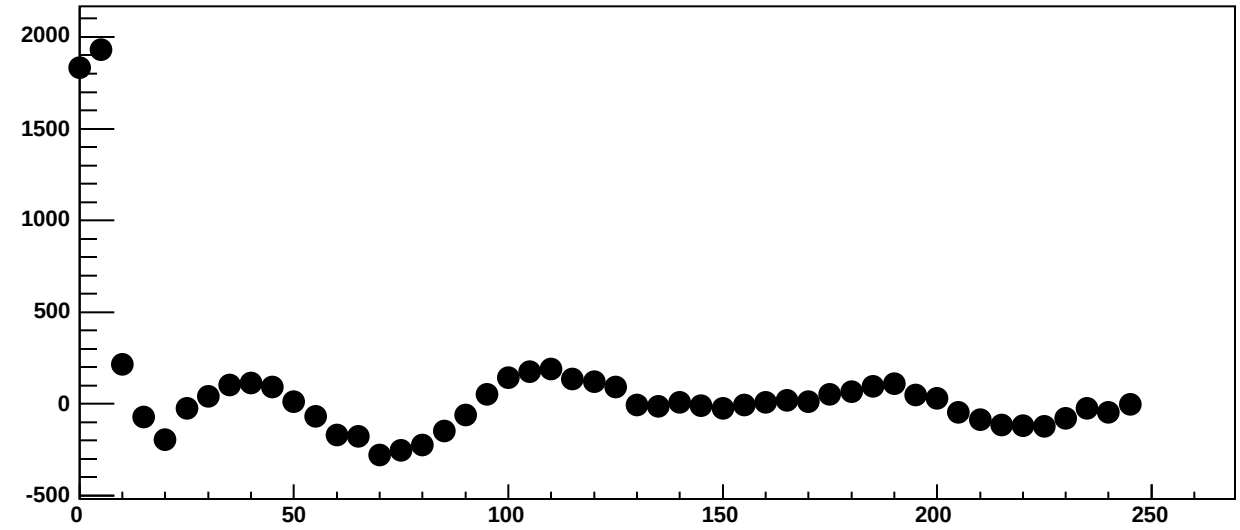
Chip 2, Channel 17, Enable 2!, DAC=1600, ADC Mean vs Hold



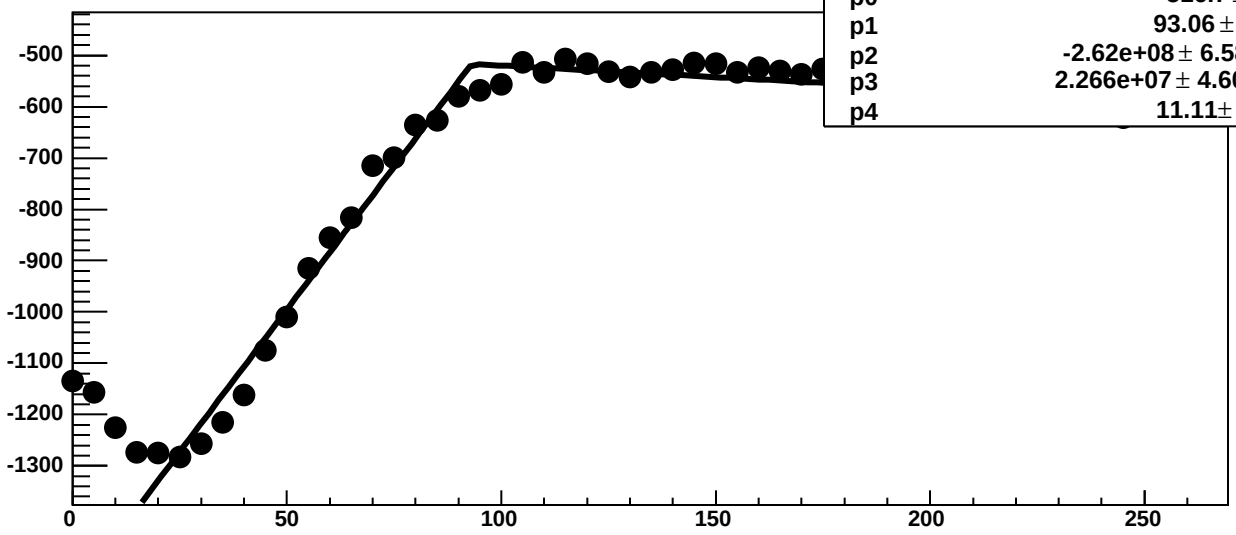
Chip 2, Channel 17, Enable 2!, DAC=1600, ADC Noise vs Hold



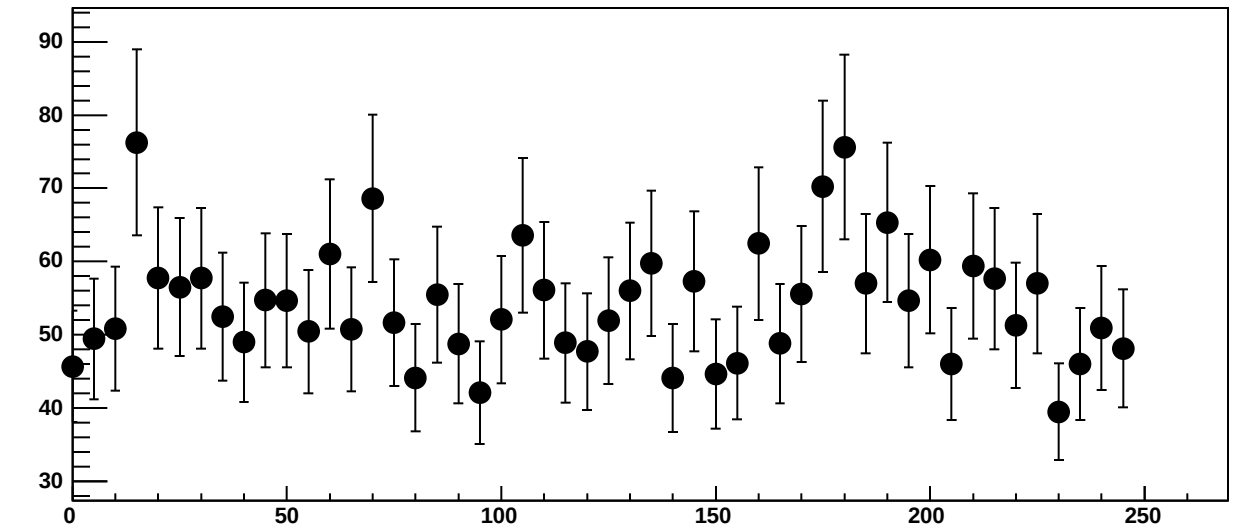
Chip 2, Channel 17, Enable 2!, DAC=1600, ADC Residuals vs Hold



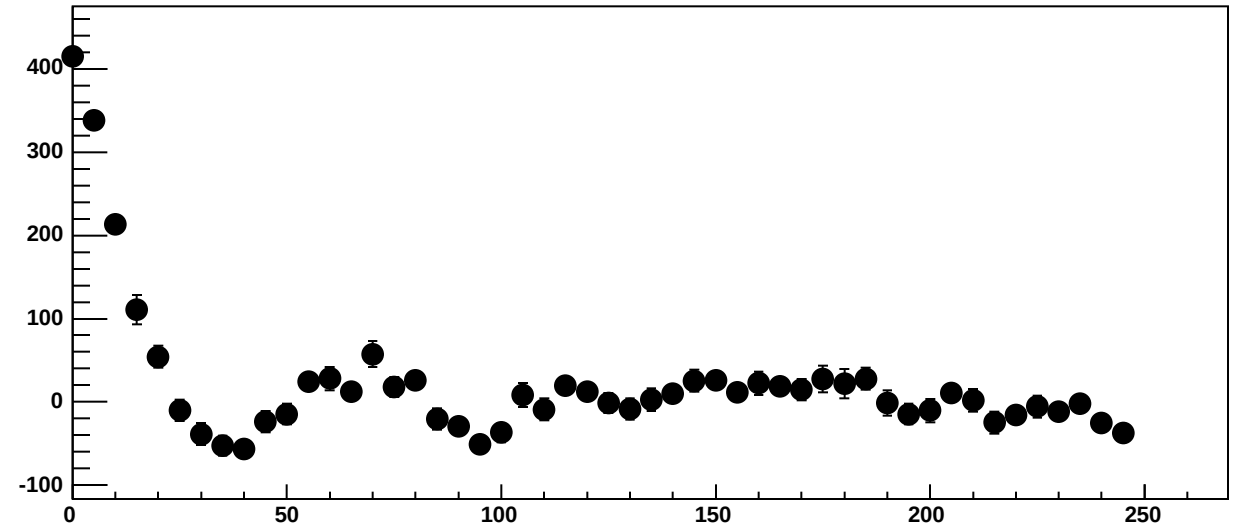
Chip 2, Channel 17, Enable 3, DAC=1600, ADC Mean vs Hold



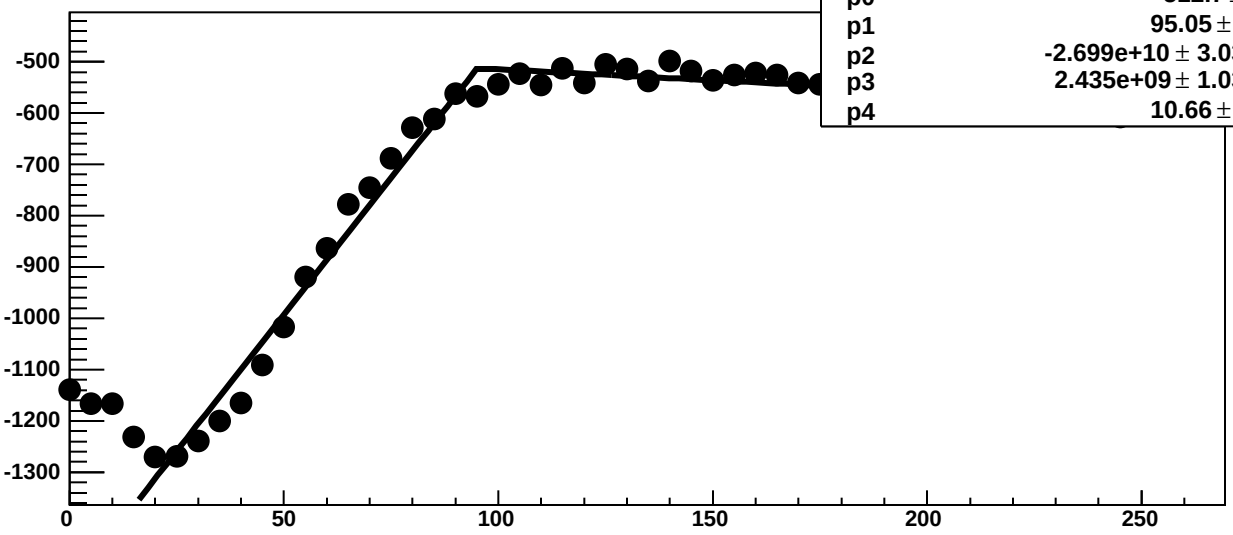
Chip 2, Channel 17, Enable 3, DAC=1600, ADC Noise vs Hold



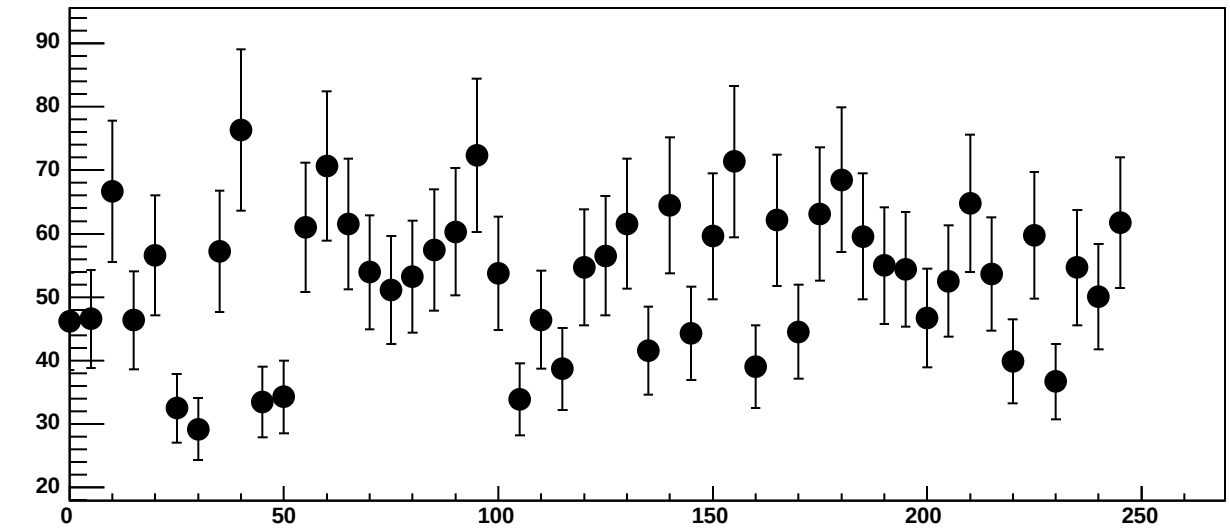
Chip 2, Channel 17, Enable 3, DAC=1600, ADC Residuals vs Hold



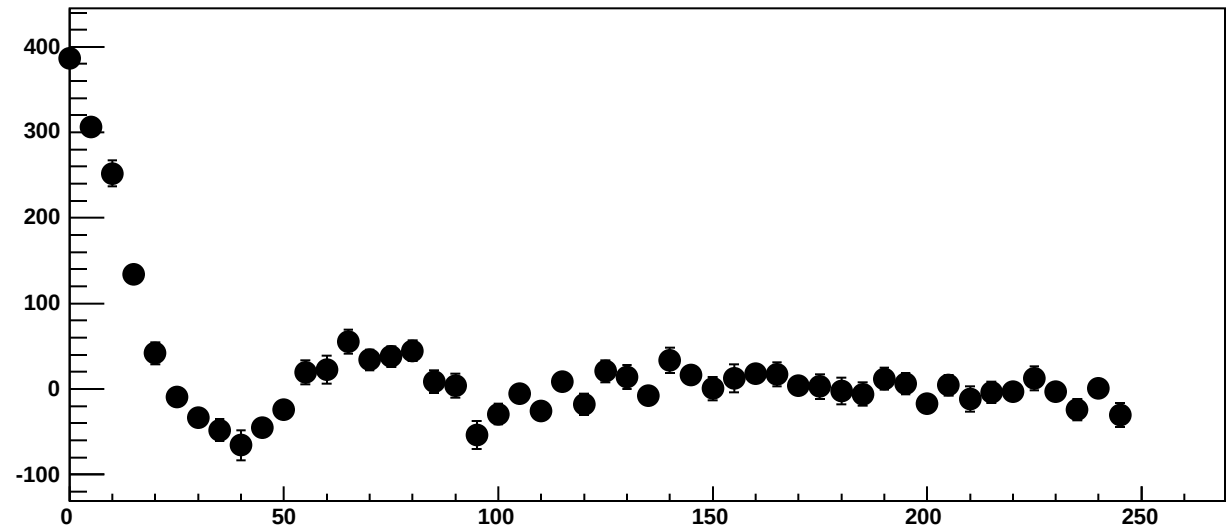
Chip 2, Channel 17, Enable 4, DAC=1600, ADC Mean vs Hold

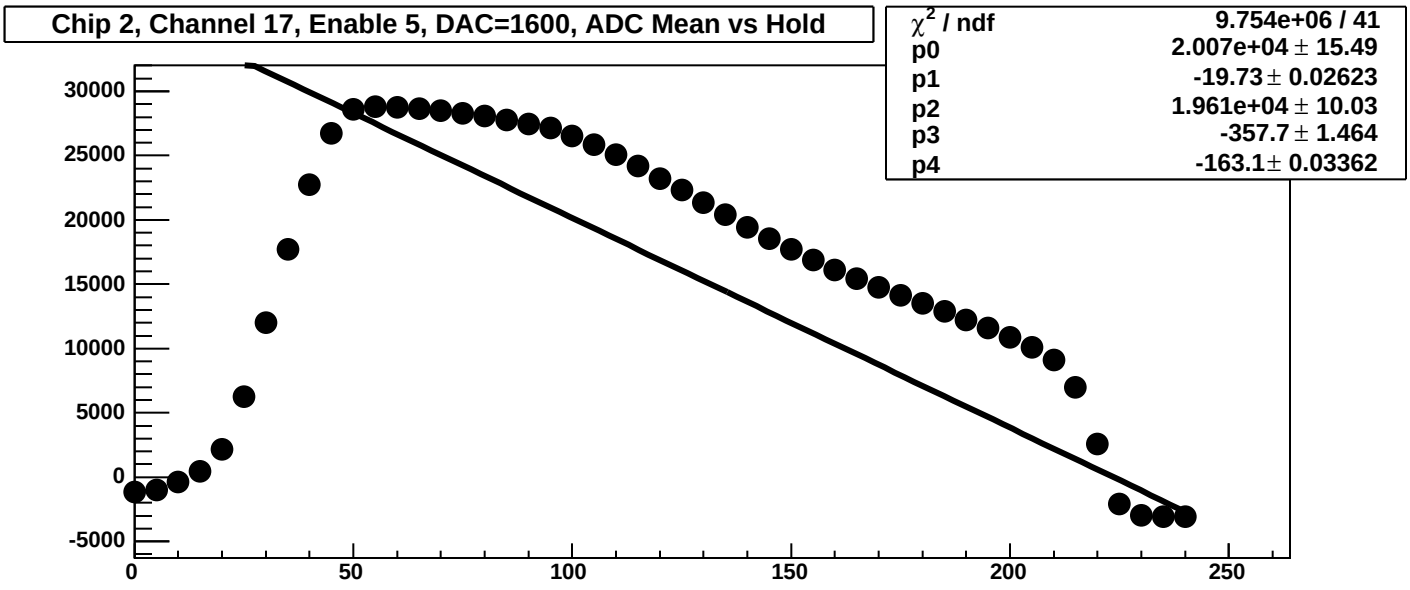


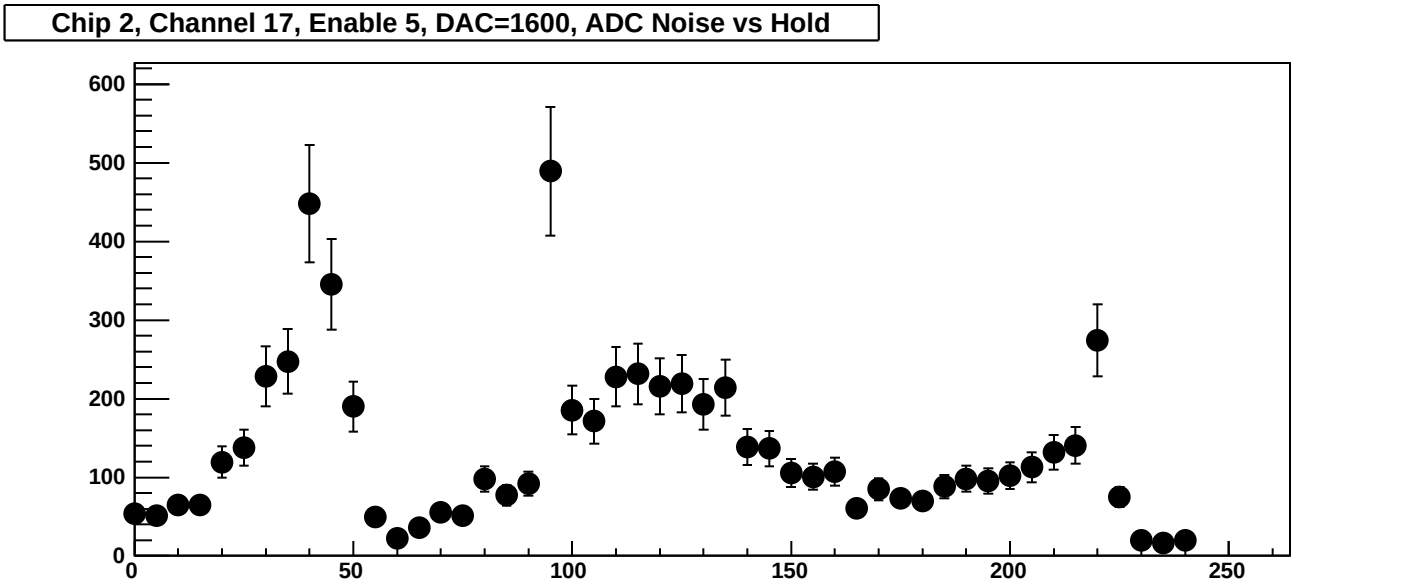
Chip 2, Channel 17, Enable 4, DAC=1600, ADC Noise vs Hold

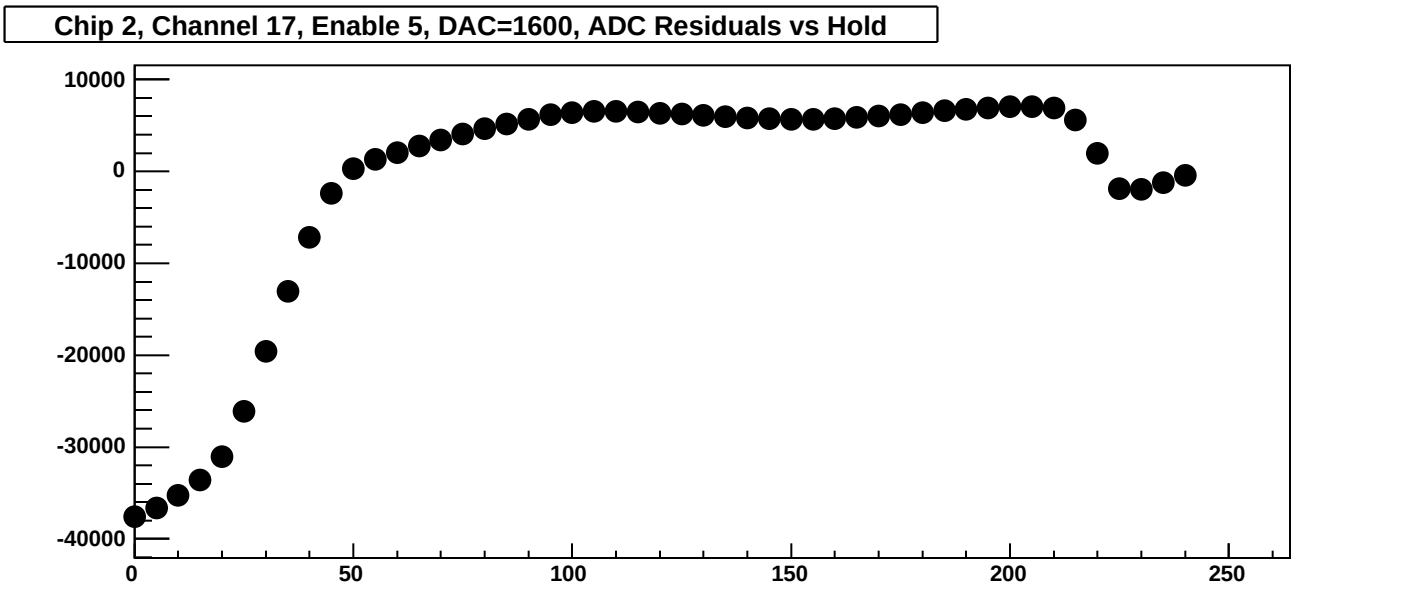


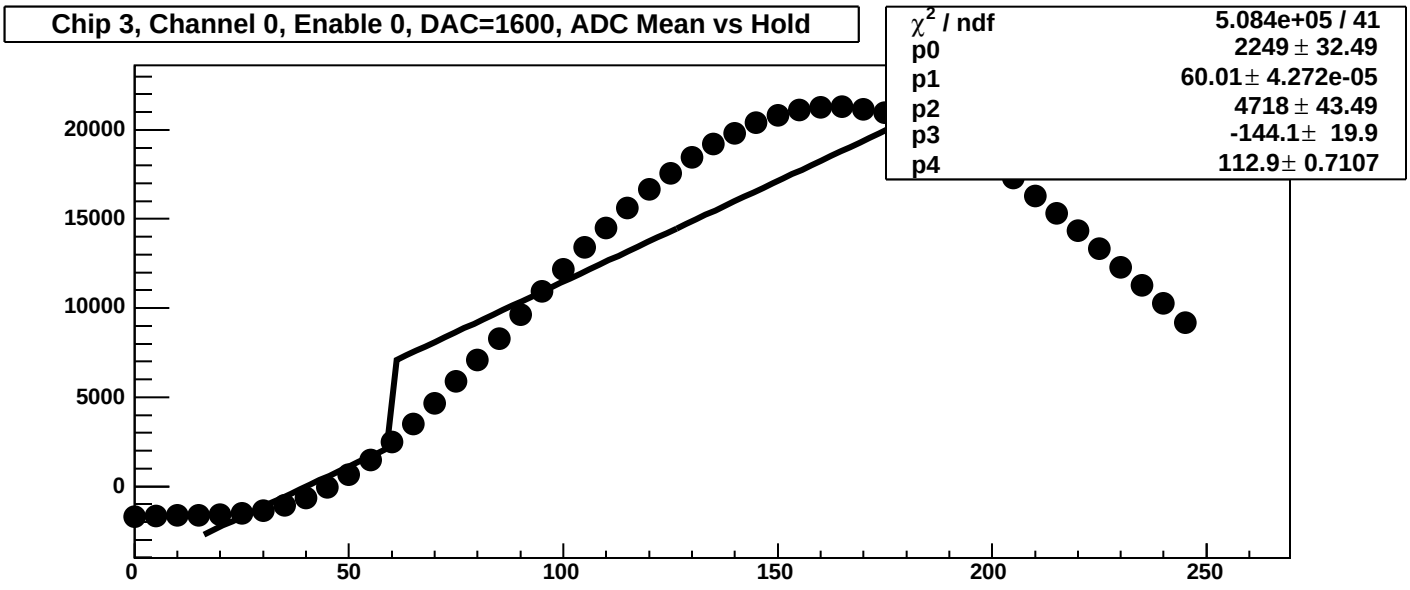
Chip 2, Channel 17, Enable 4, DAC=1600, ADC Residuals vs Hold

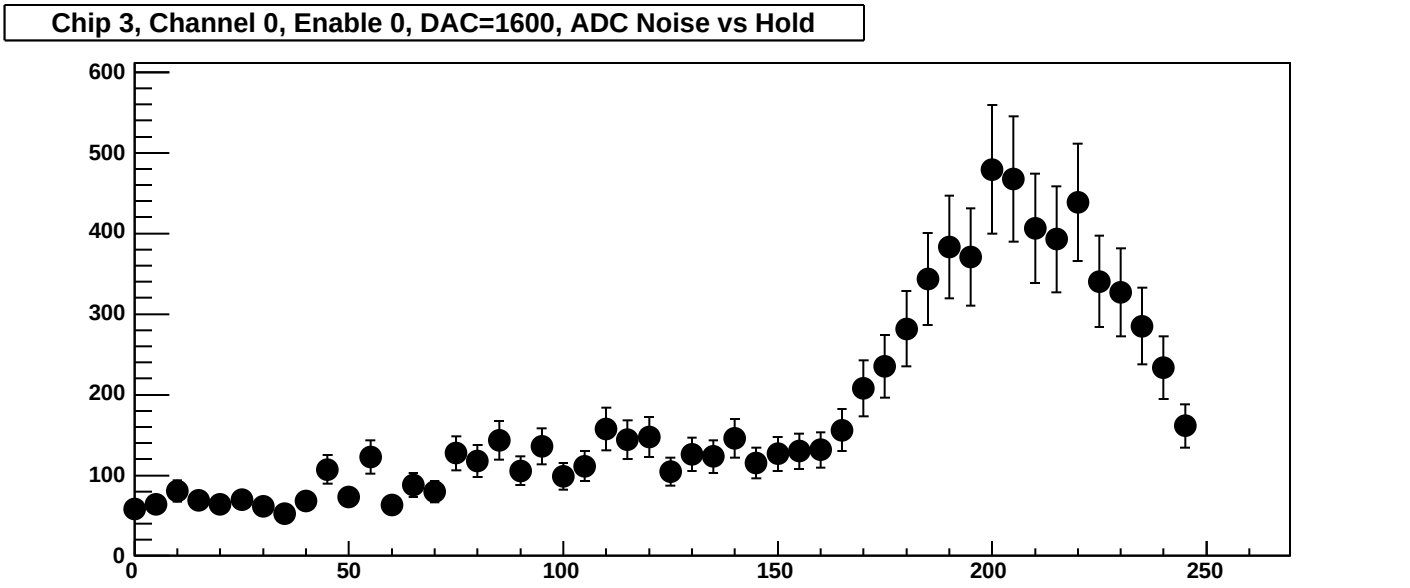


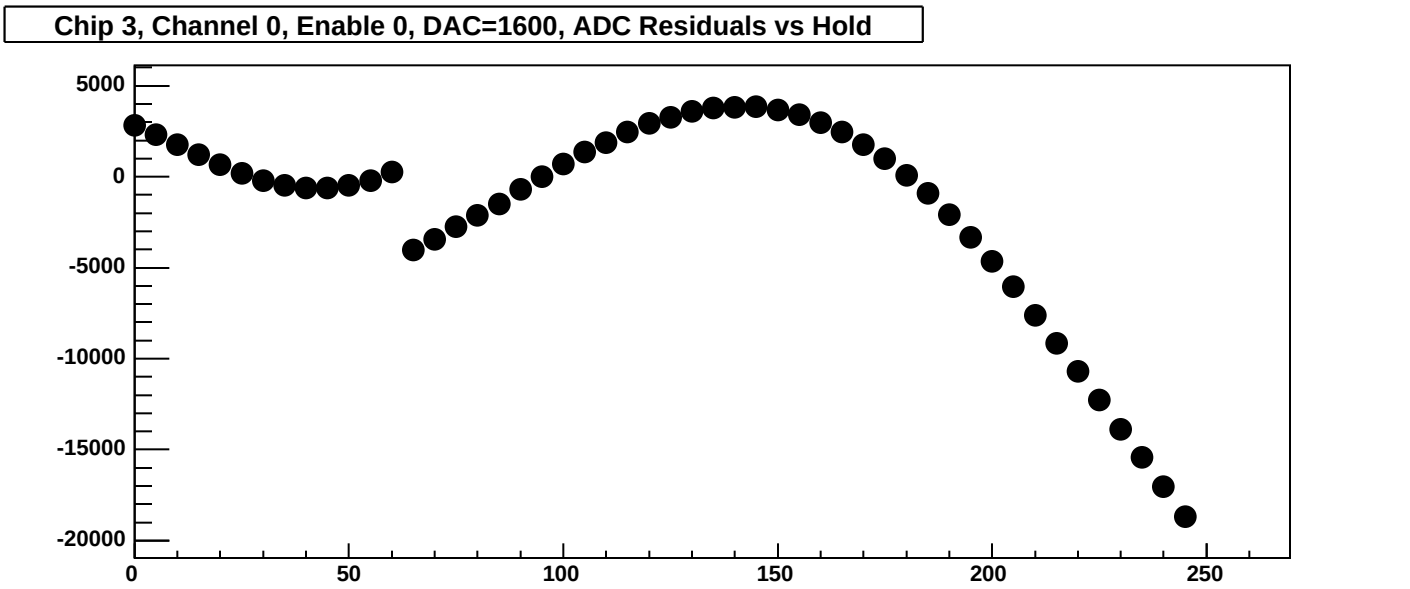

Chip 2, Channel 17, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 9.754e+06 / 41
p0 2.007e+04 ± 15.49
p1 -19.73 ± 0.02623
p2 1.961e+04 ± 10.03
p3 -357.7 ± 1.464
p4 -163.1 ± 0.03362
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 2, Channel 17, Enable 5, DAC=1600, ADC Noise vs Hold
600
500
400
300
200
100
0
0
50
100
150
200
250

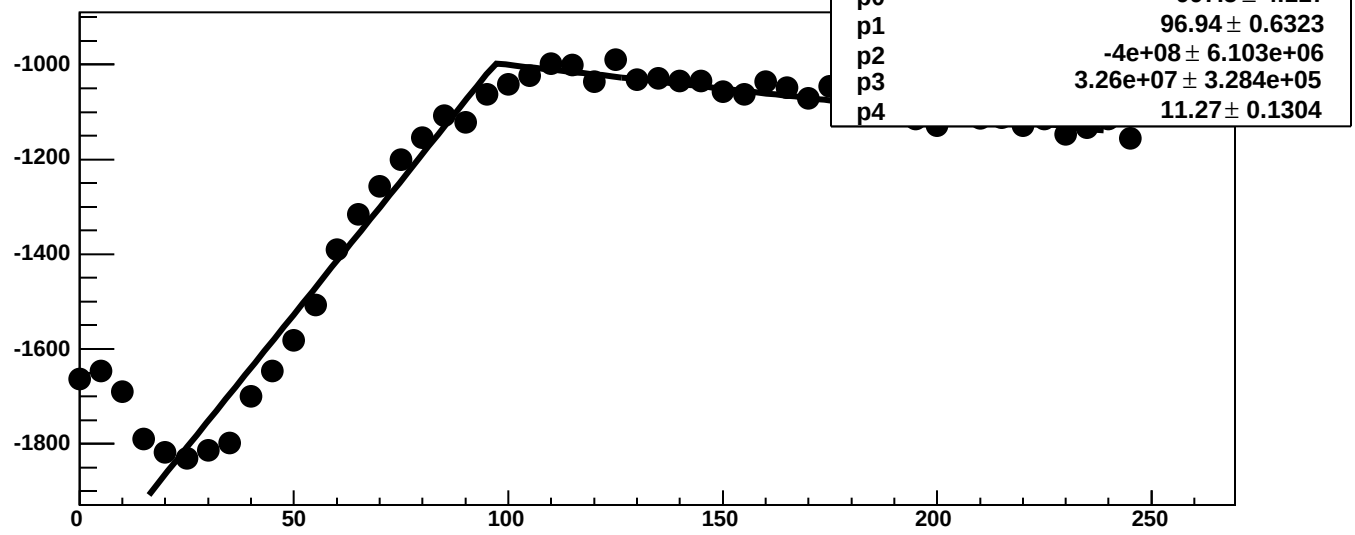

Chip 2, Channel 17, Enable 5, DAC=1600, ADC Residuals vs Hold
10000
0
-10000
-20000
-30000
-40000
0
50
100
150
200
250


Chip 3, Channel 0, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.084e+05 / 41
p0 2249 ± 32.49
p1 60.01± 4.272e-05
p2 4718 ± 43.49
p3 -144.1± 19.9
p4 112.9± 0.7107
20000
15000
10000
5000
0
0
50
100
150
200
250

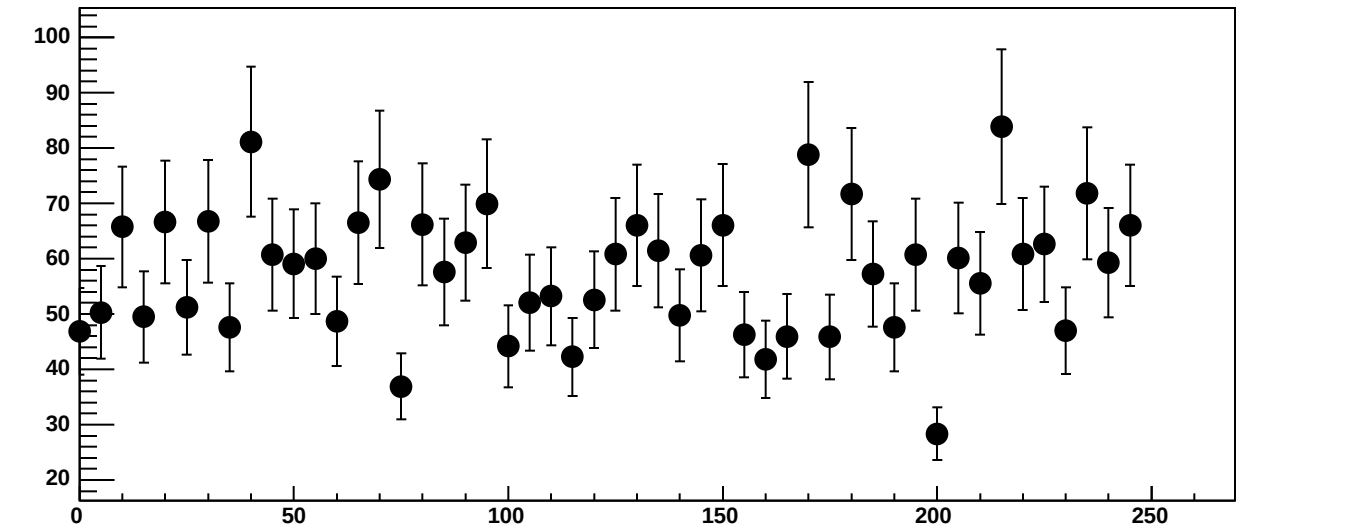

Chip 3, Channel 0, Enable 0, DAC=1600, ADC Noise vs Hold
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 3, Channel 0, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

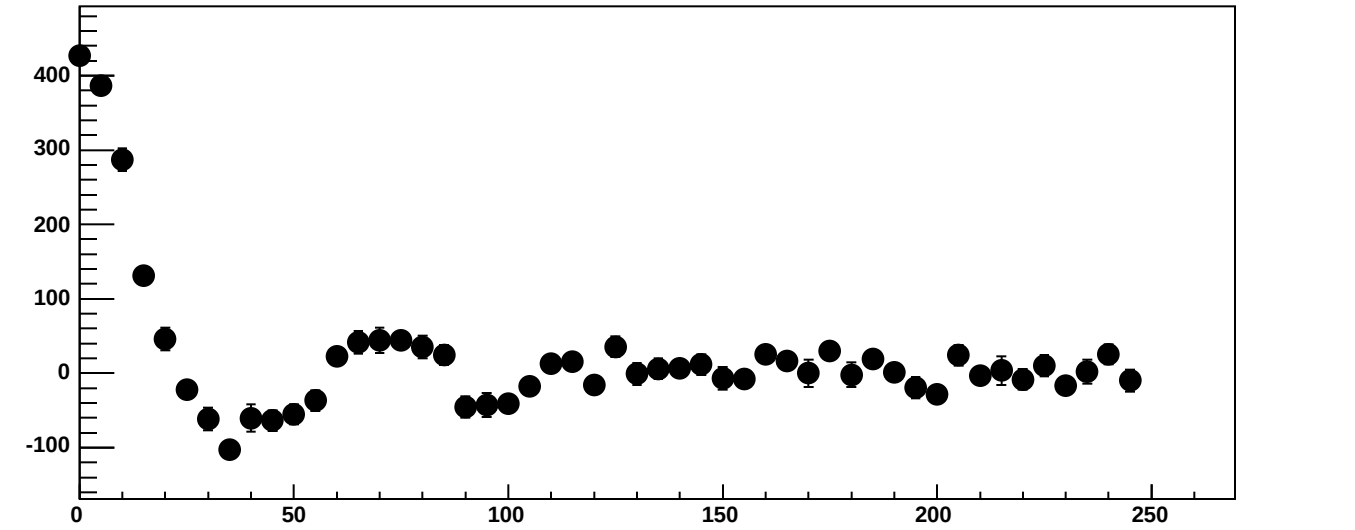
Chip 3, Channel 0, Enable 1, DAC=1600, ADC Mean vs Hold

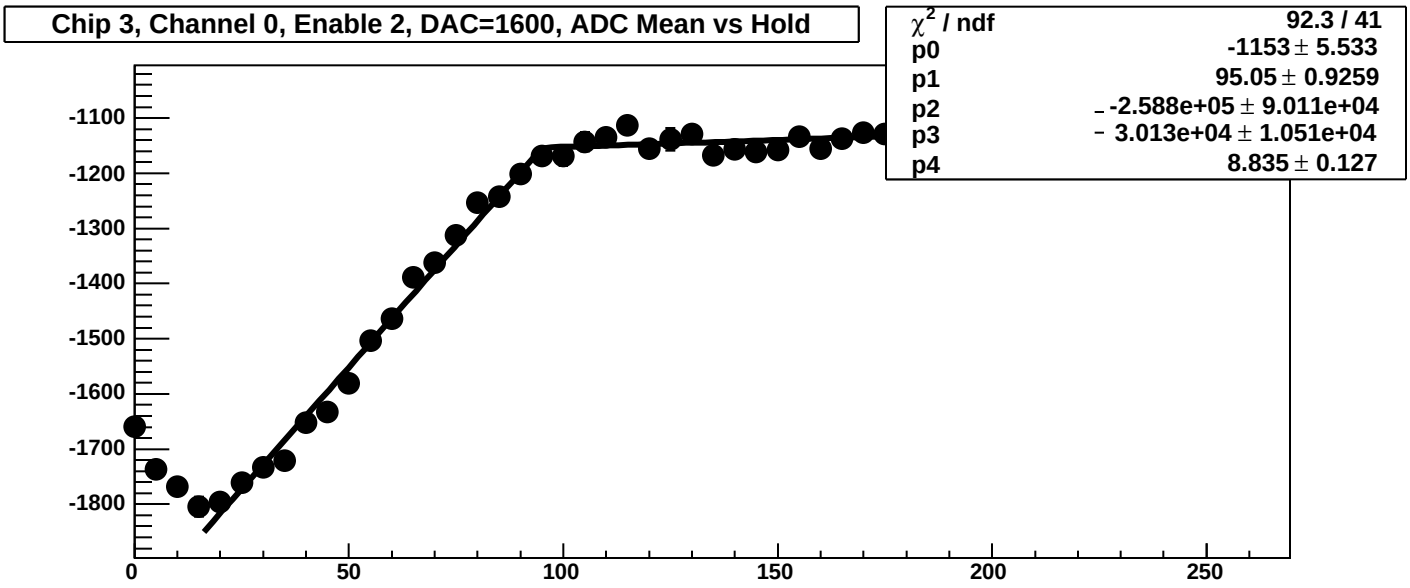


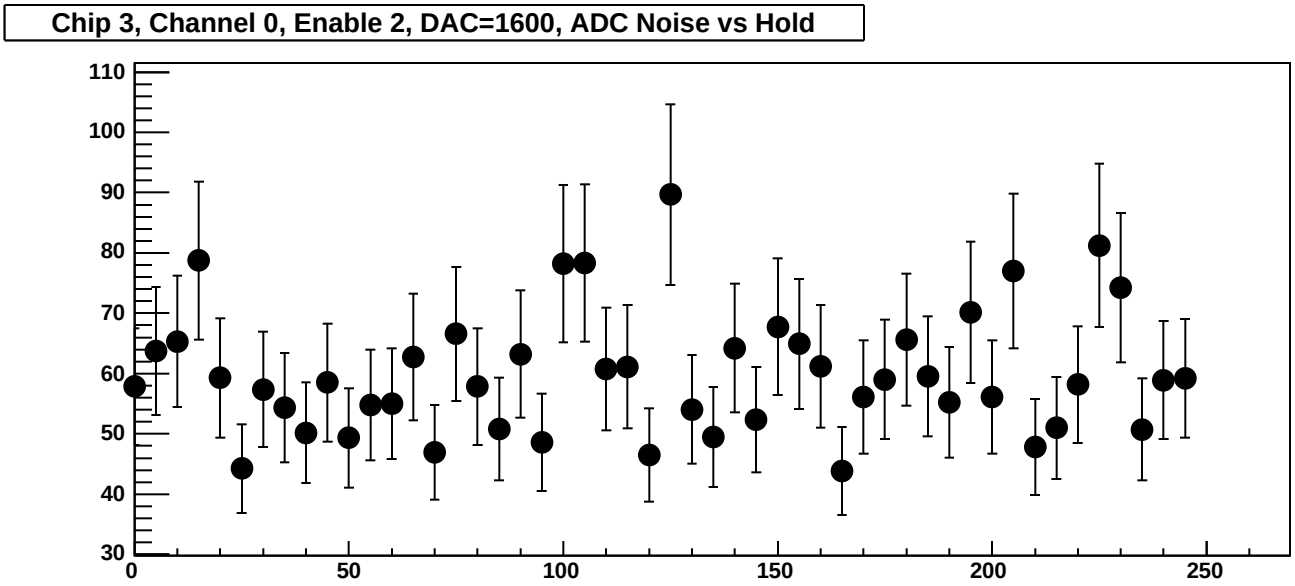
Chip 3, Channel 0, Enable 1, DAC=1600, ADC Noise vs Hold

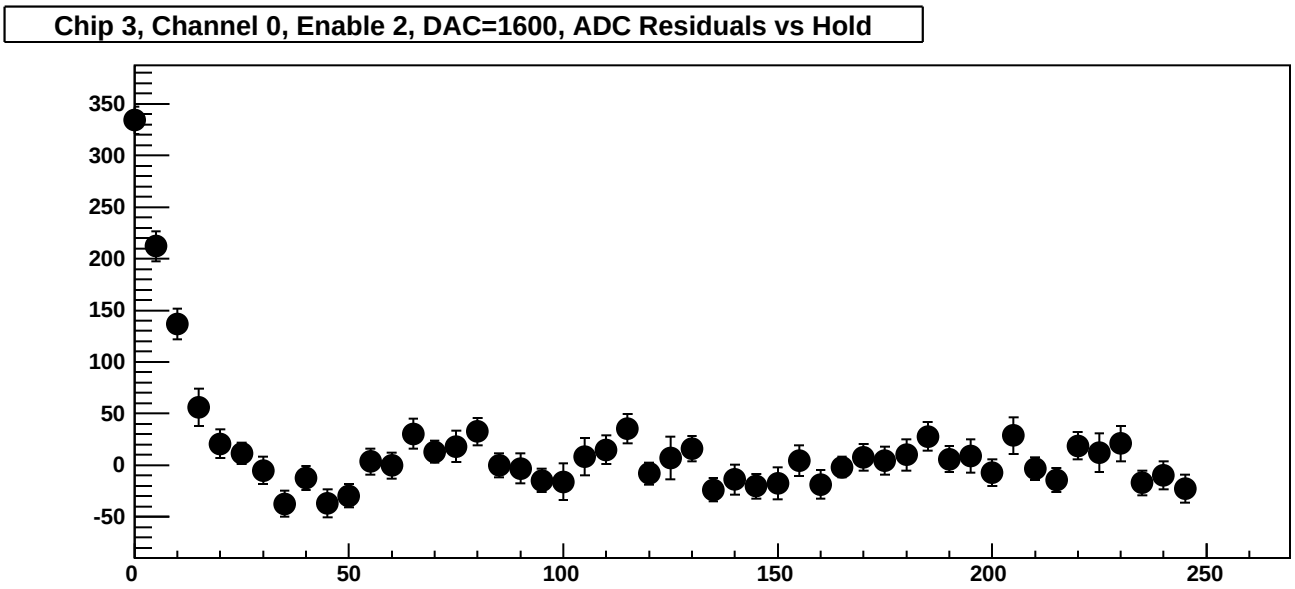


Chip 3, Channel 0, Enable 1, DAC=1600, ADC Residuals vs Hold

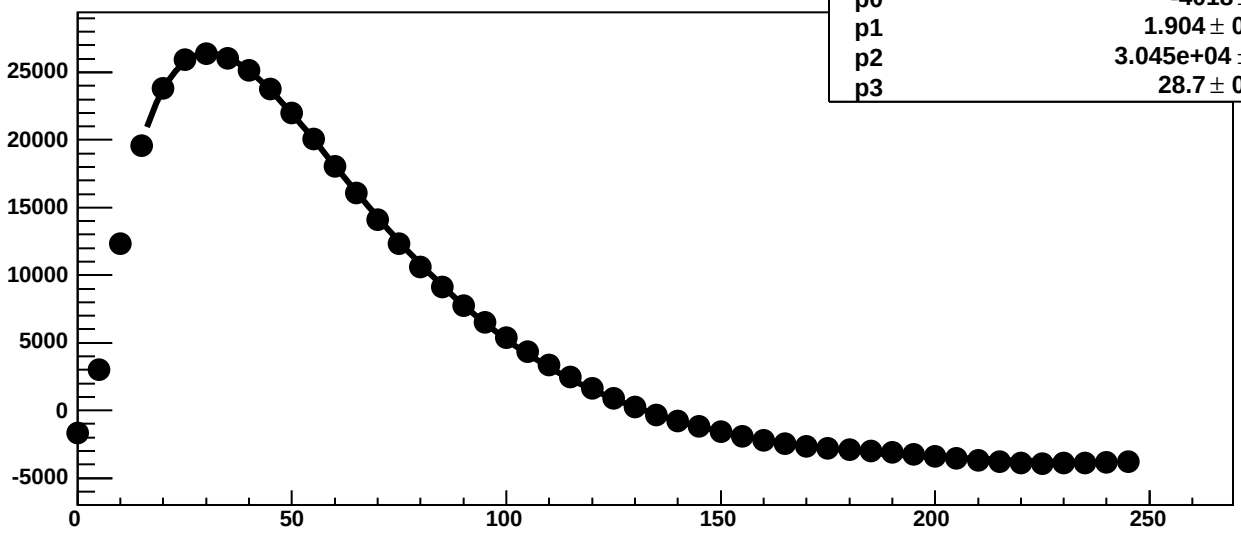



Chip 3, Channel 0, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 92.3 / 41
p0 -1153 ± 5.533
p1 95.05 ± 0.9259
p2 -2.588e+05 ± 9.011e+04
p3 3.013e+04 ± 1.051e+04
p4 8.835 ± 0.127
-1100
-1200
-1300
-1400
-1500
-1600
-1700
-1800
0
50
100
150
200
250

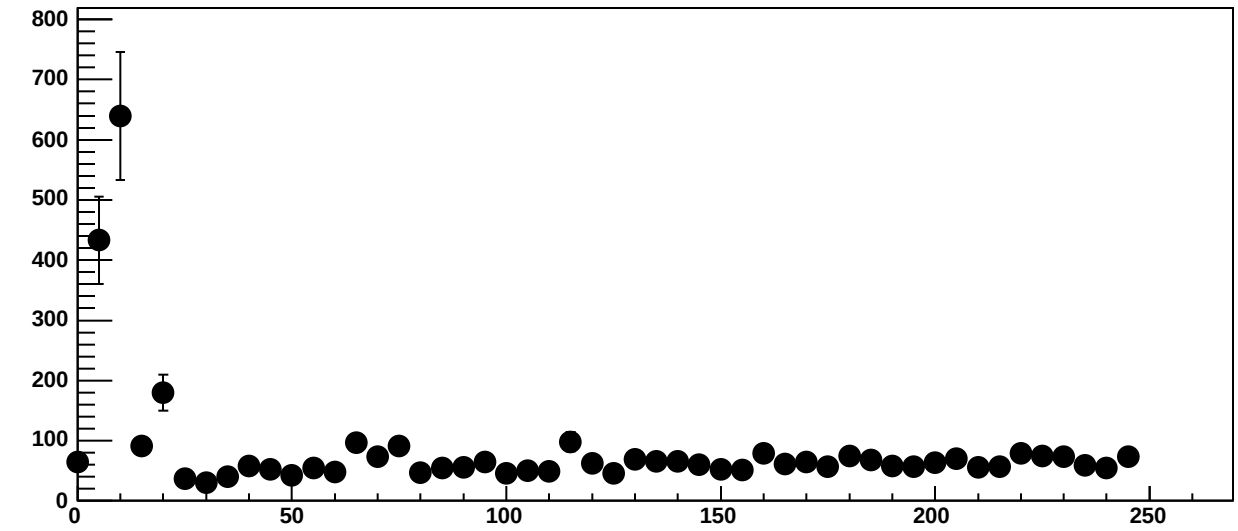

Chip 3, Channel 0, Enable 2, DAC=1600, ADC Noise vs Hold
110
100
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 3, Channel 0, Enable 2, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
0
50
100
150
200
250

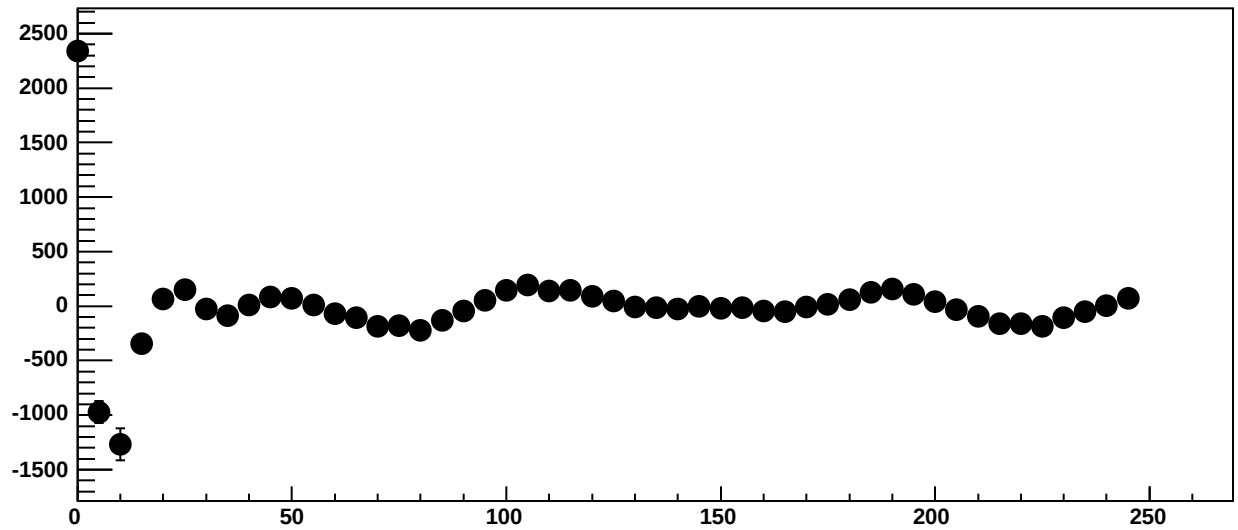
Chip 3, Channel 0, Enable 3!, DAC=1600, ADC Mean vs Hold



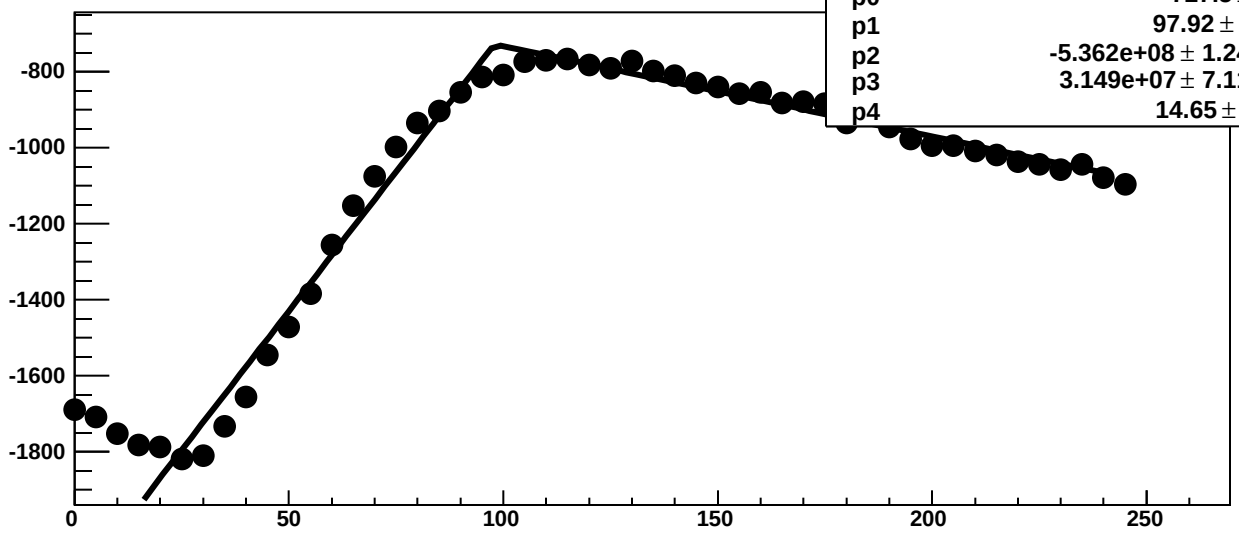
Chip 3, Channel 0, Enable 3!, DAC=1600, ADC Noise vs Hold



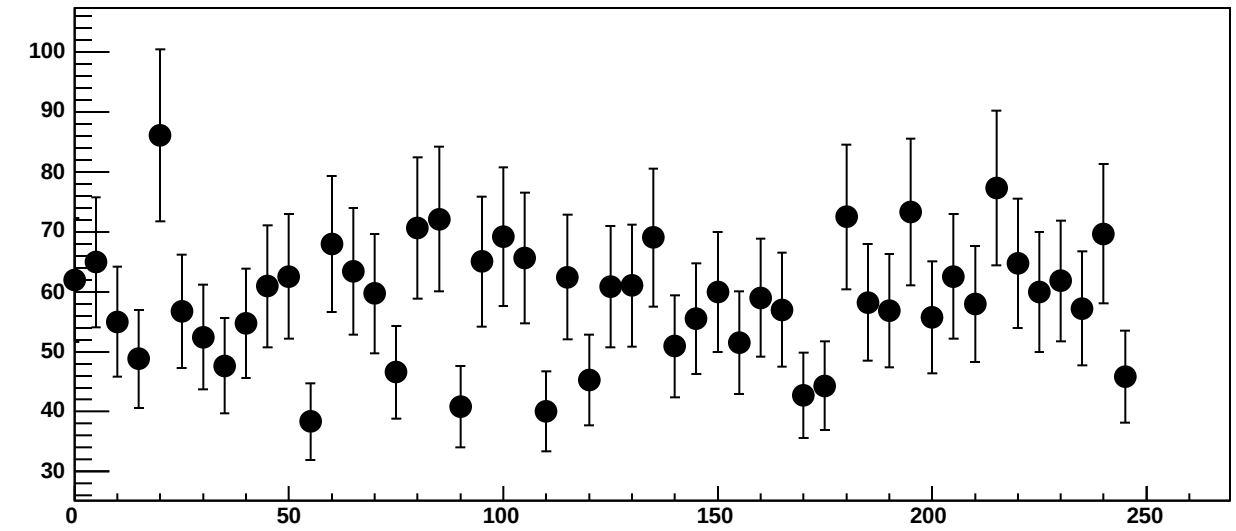
Chip 3, Channel 0, Enable 3!, DAC=1600, ADC Residuals vs Hold



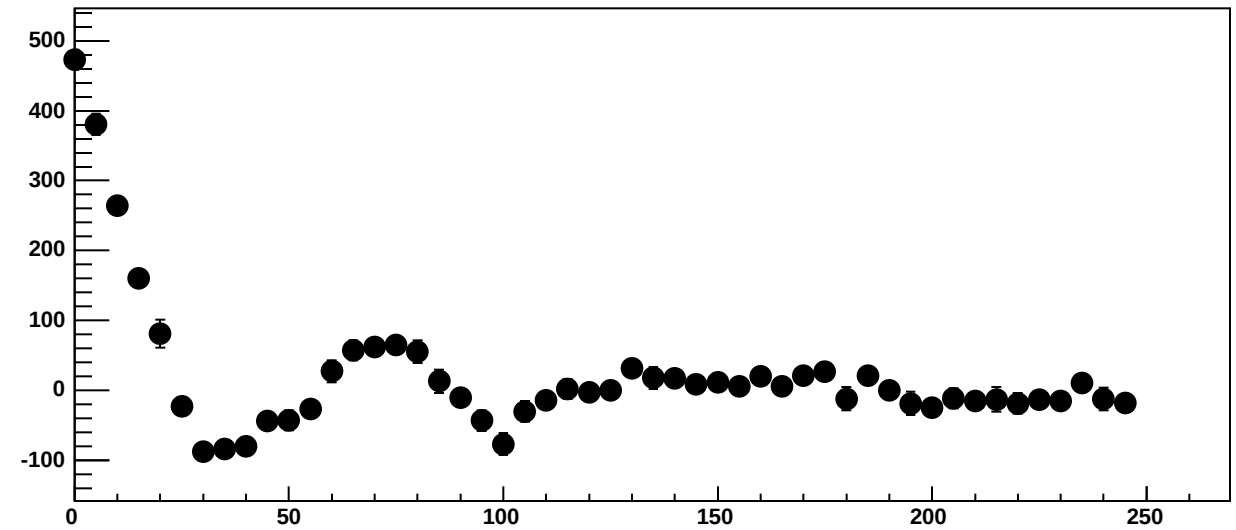
Chip 3, Channel 0, Enable 4, DAC=1600, ADC Mean vs Hold

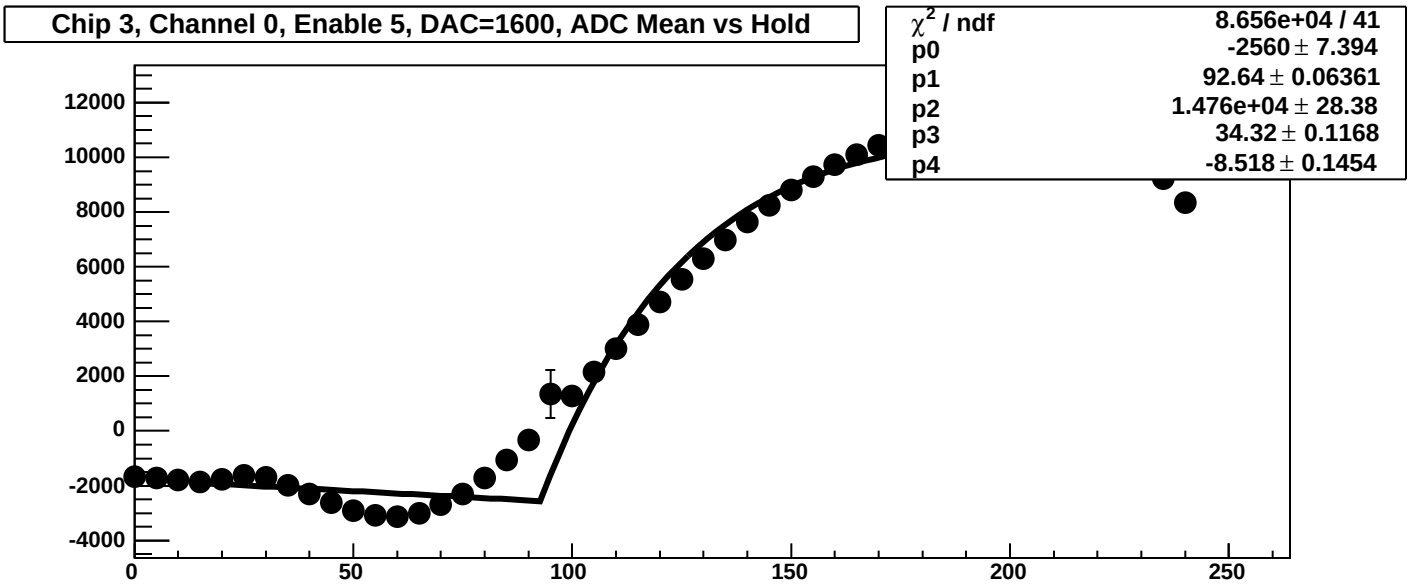


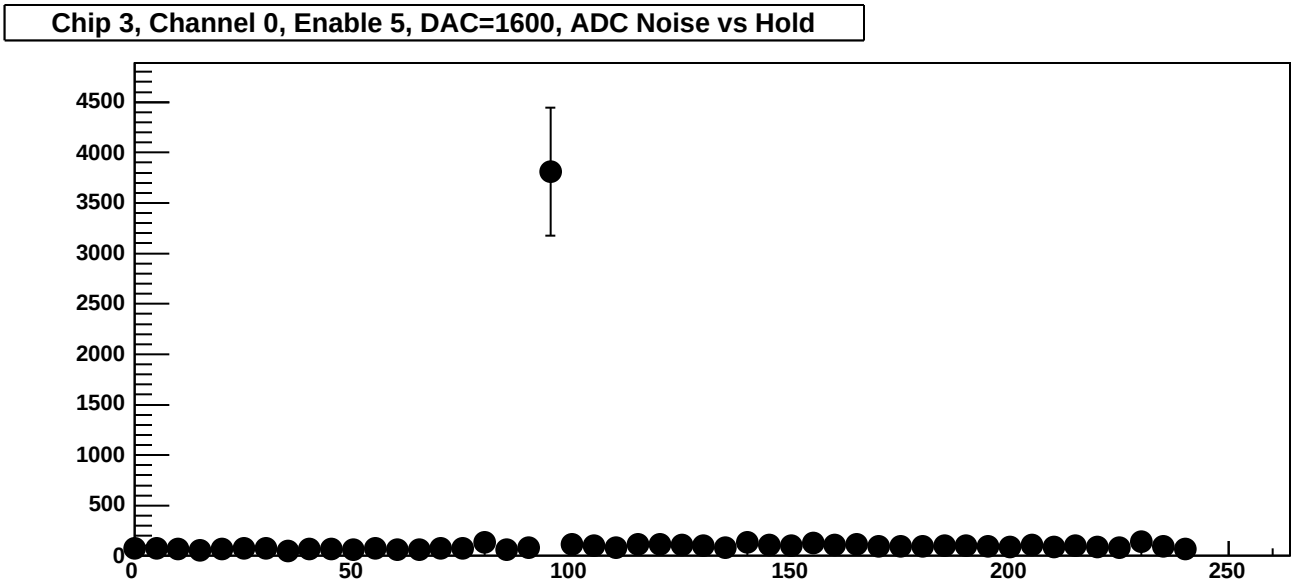
Chip 3, Channel 0, Enable 4, DAC=1600, ADC Noise vs Hold

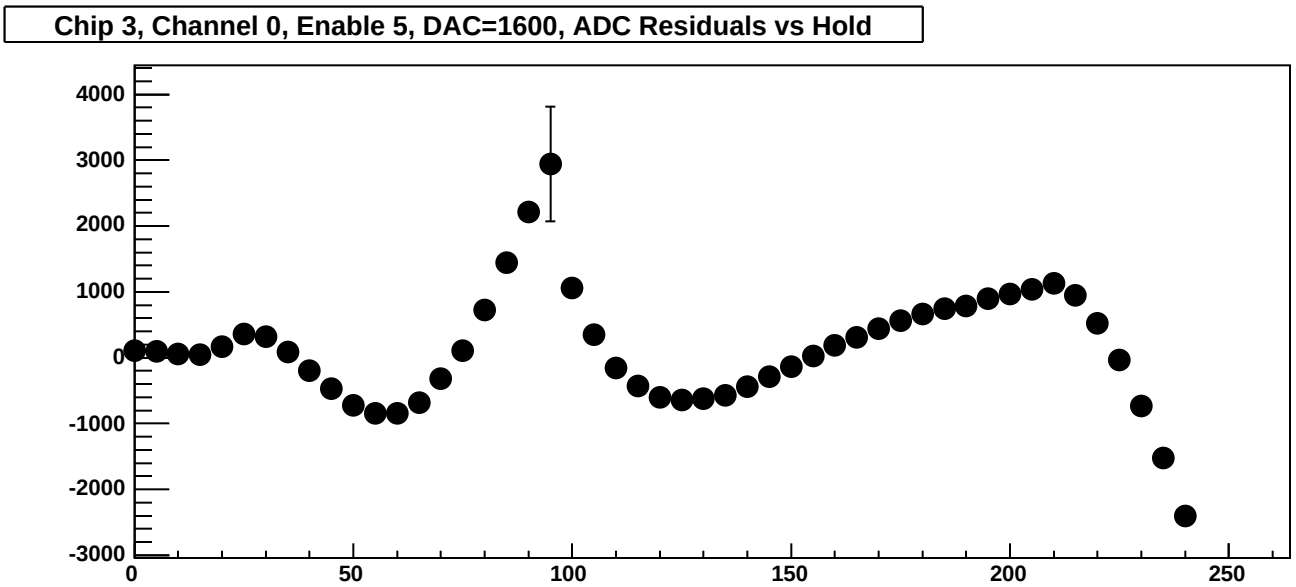


Chip 3, Channel 0, Enable 4, DAC=1600, ADC Residuals vs Hold

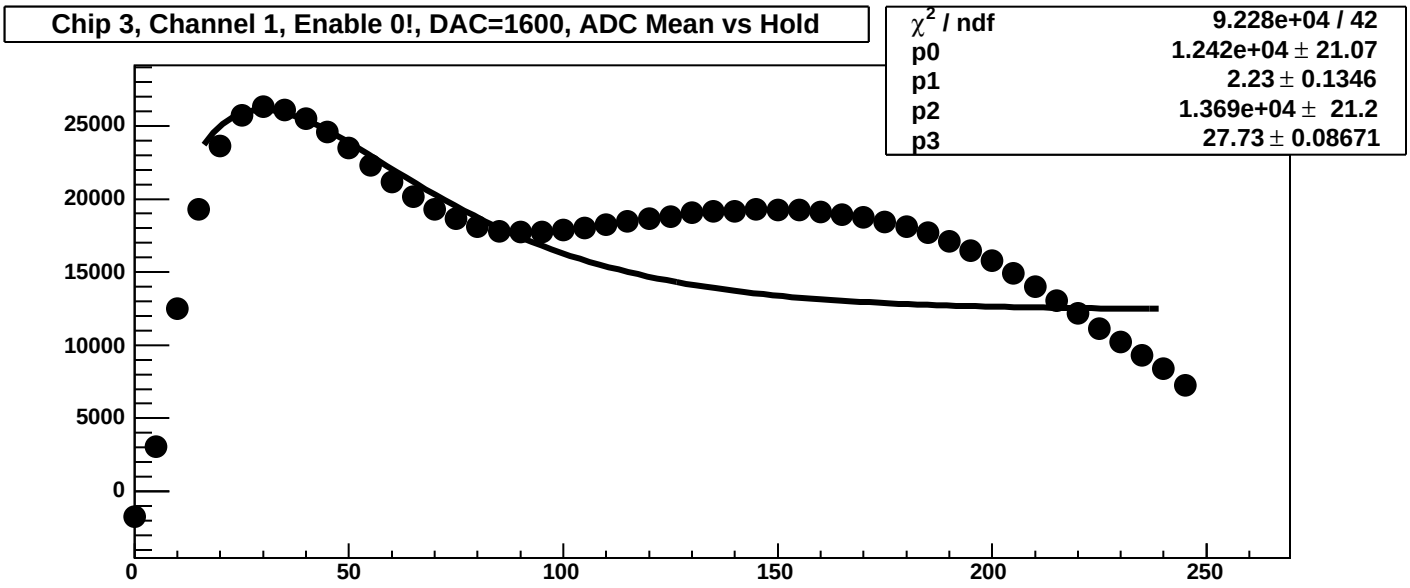



Chip 3, Channel 0, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 8.656e+04 / 41
p0 -2560 ± 7.394
p1 92.64 ± 0.06361
p2 1.476e+04 ± 28.38
p3 34.32 ± 0.1168
p4 -8.518 ± 0.1454
12000
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

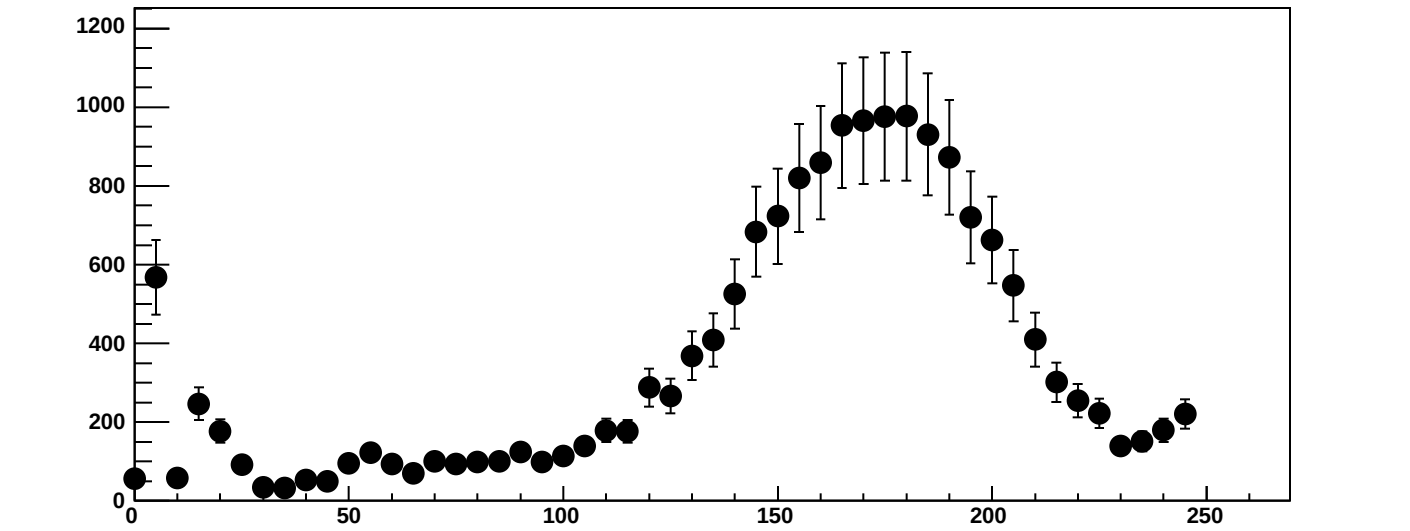

Chip 3, Channel 0, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 3, Channel 0, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

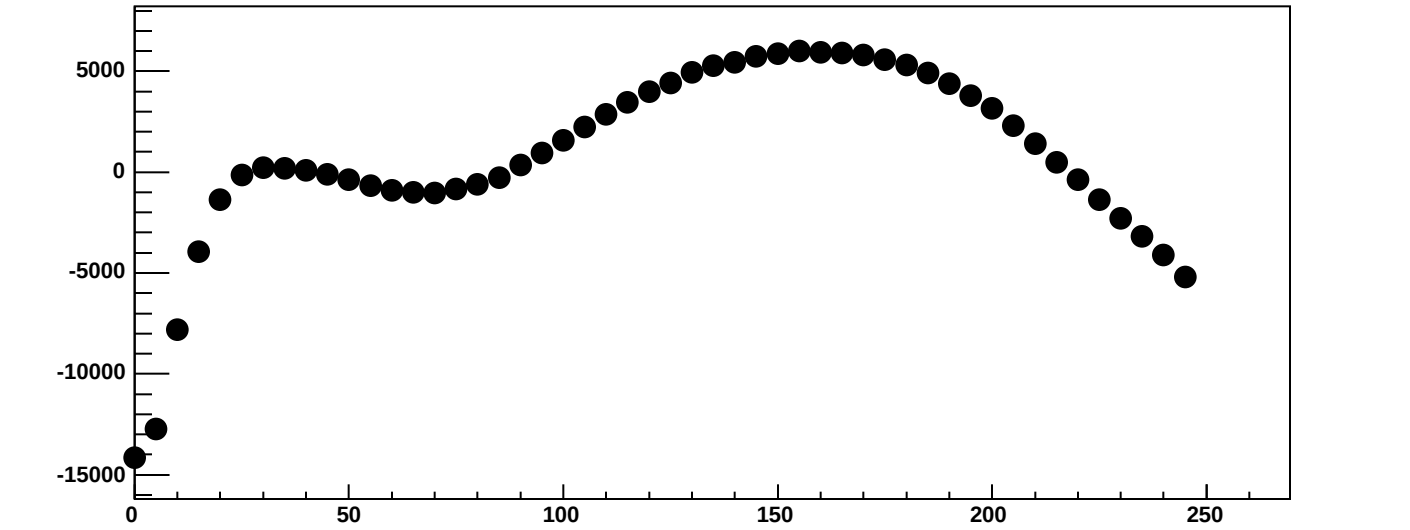
Chip 3, Channel 1, Enable 0!, DAC=1600, ADC Mean vs Hold



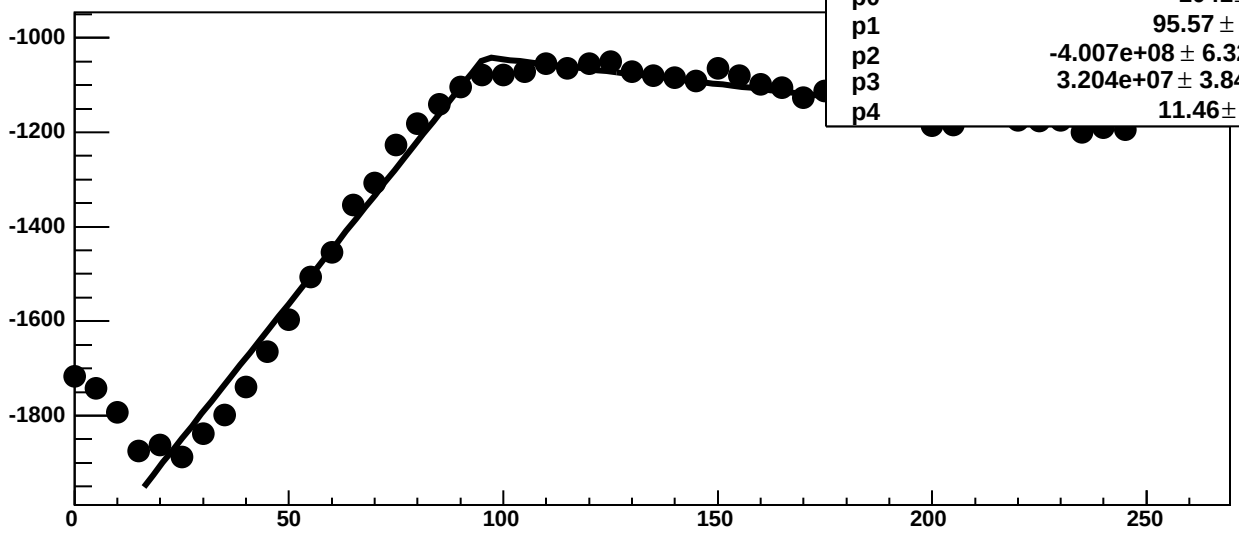
Chip 3, Channel 1, Enable 0!, DAC=1600, ADC Noise vs Hold



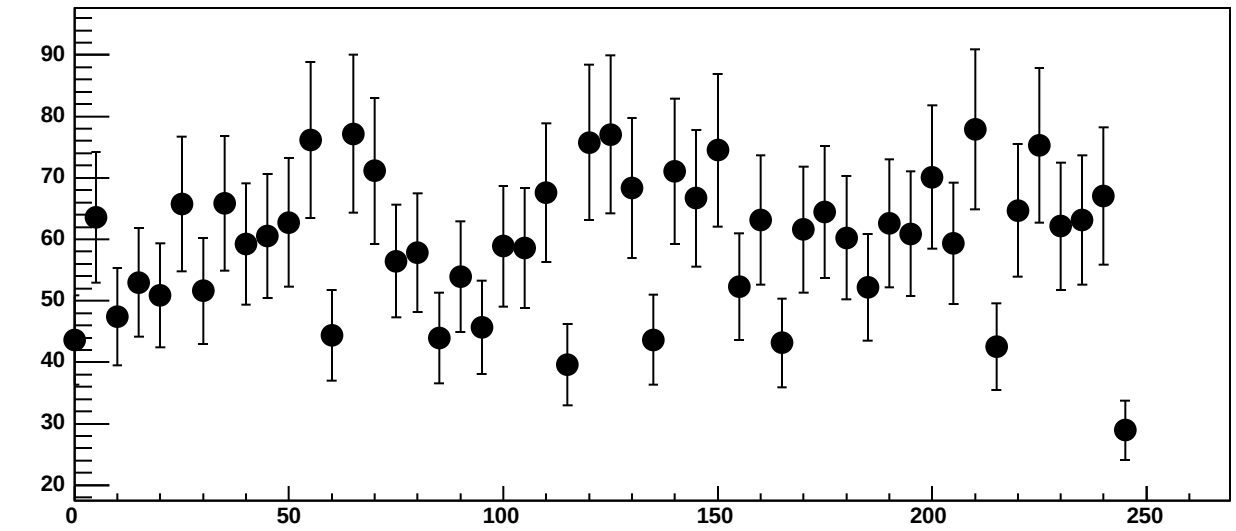
Chip 3, Channel 1, Enable 0!, DAC=1600, ADC Residuals vs Hold



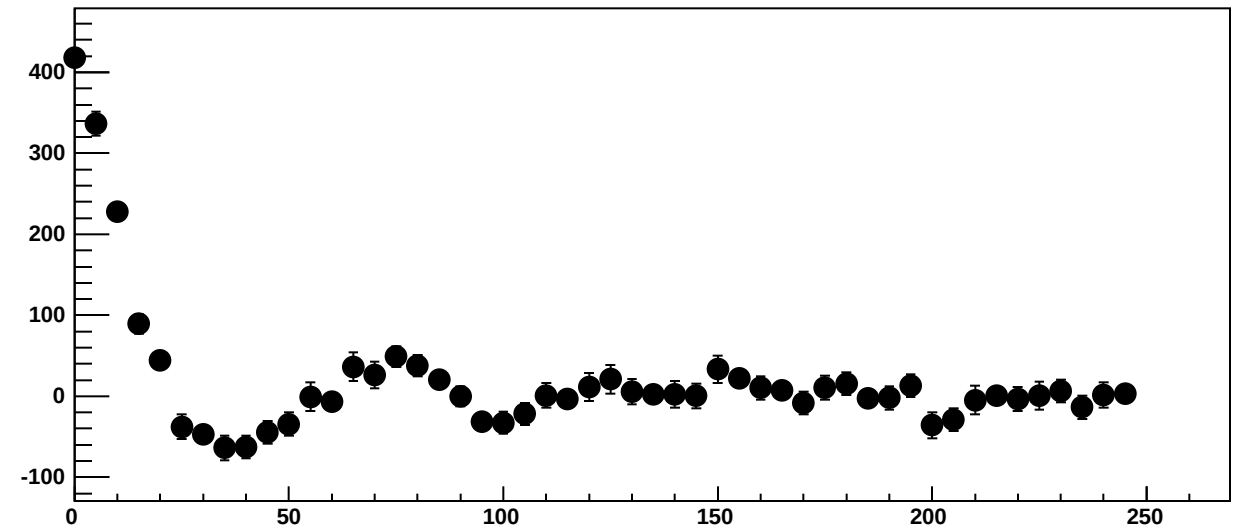
Chip 3, Channel 1, Enable 1, DAC=1600, ADC Mean vs Hold



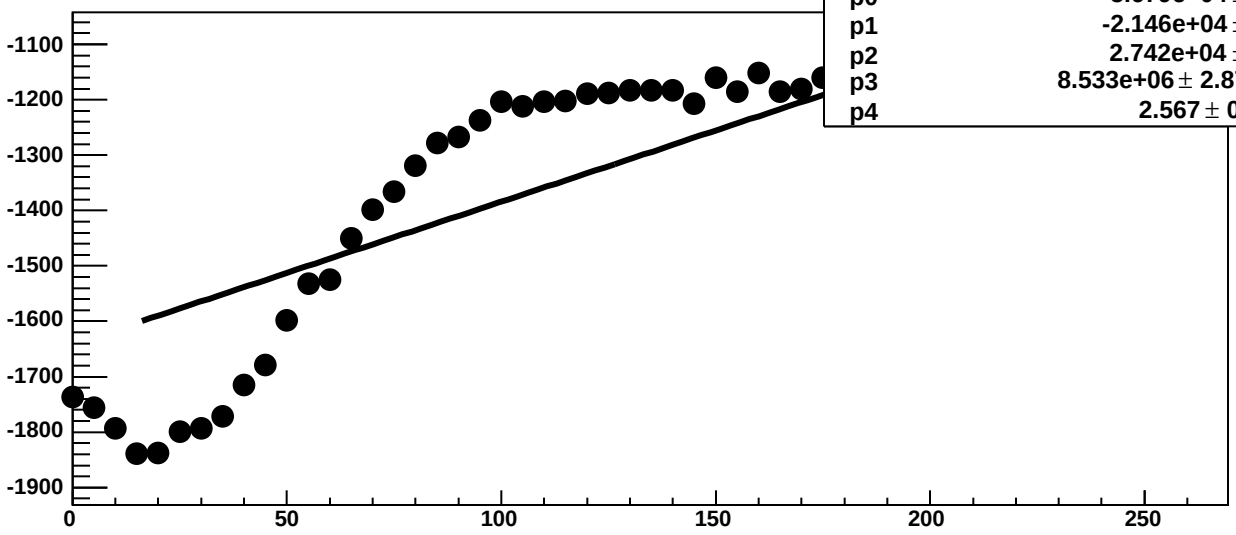
Chip 3, Channel 1, Enable 1, DAC=1600, ADC Noise vs Hold



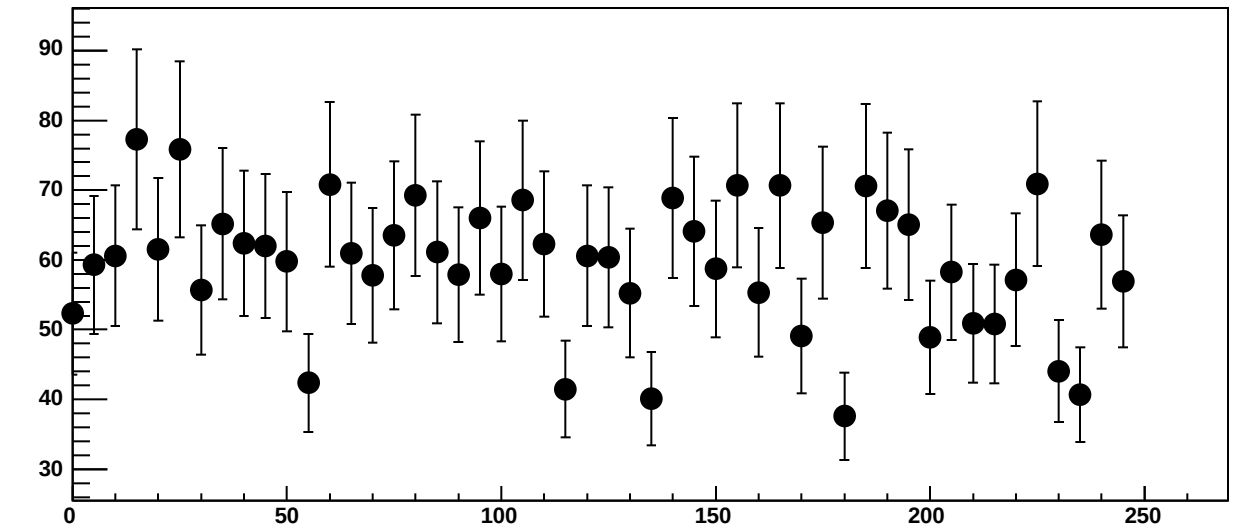
Chip 3, Channel 1, Enable 1, DAC=1600, ADC Residuals vs Hold



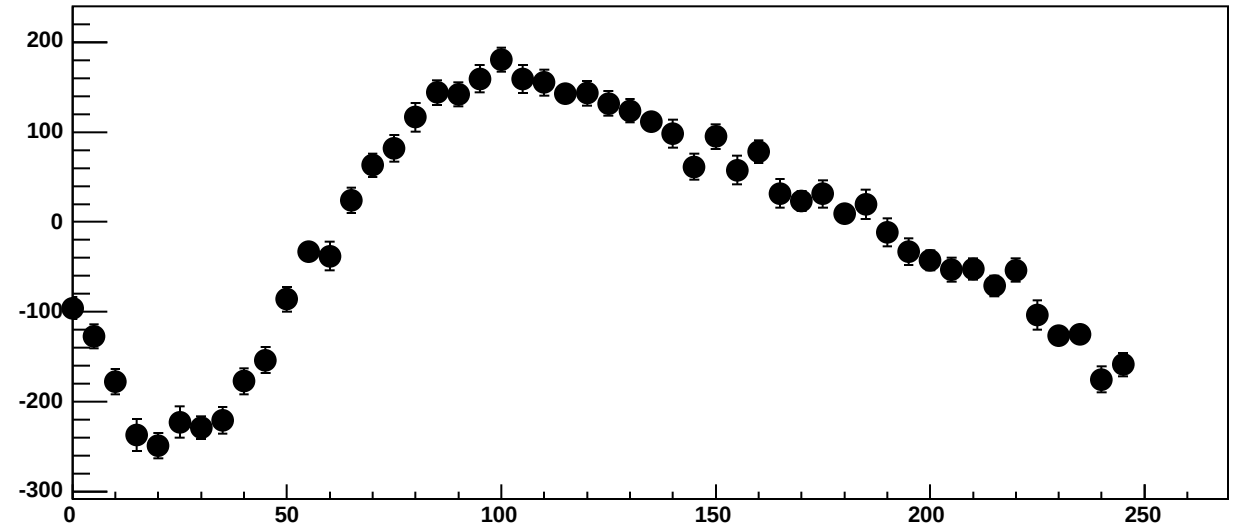
Chip 3, Channel 1, Enable 2, DAC=1600, ADC Mean vs Hold



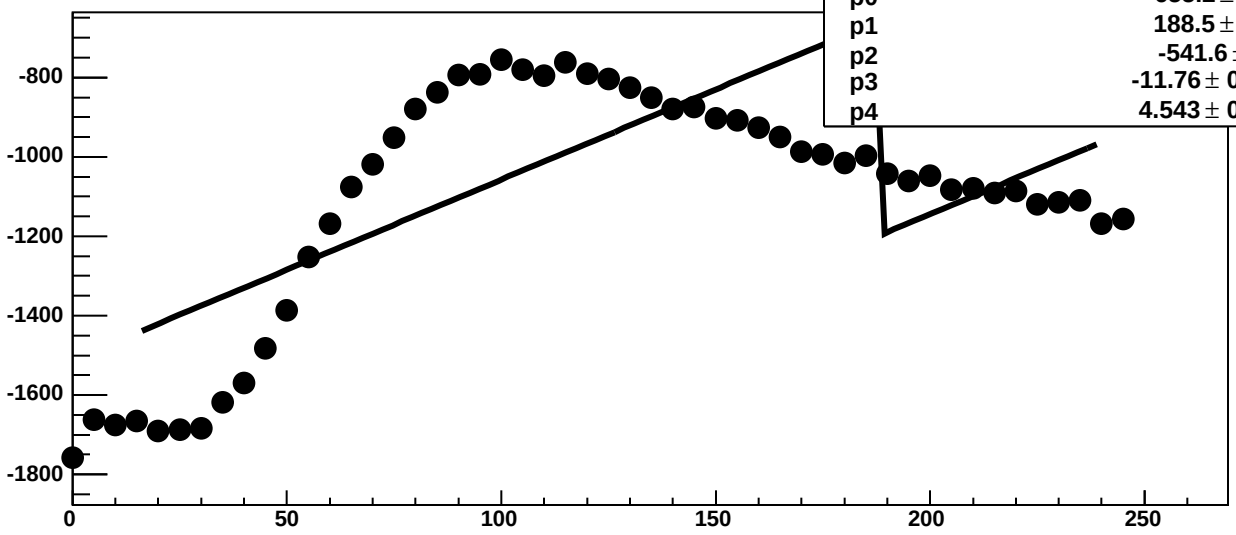
Chip 3, Channel 1, Enable 2, DAC=1600, ADC Noise vs Hold



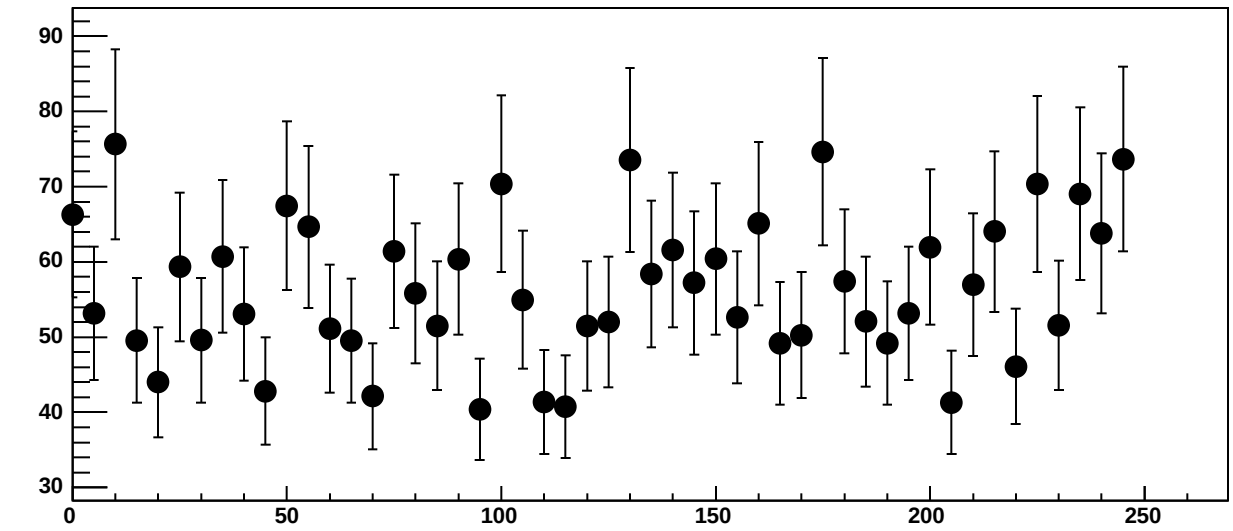
Chip 3, Channel 1, Enable 2, DAC=1600, ADC Residuals vs Hold



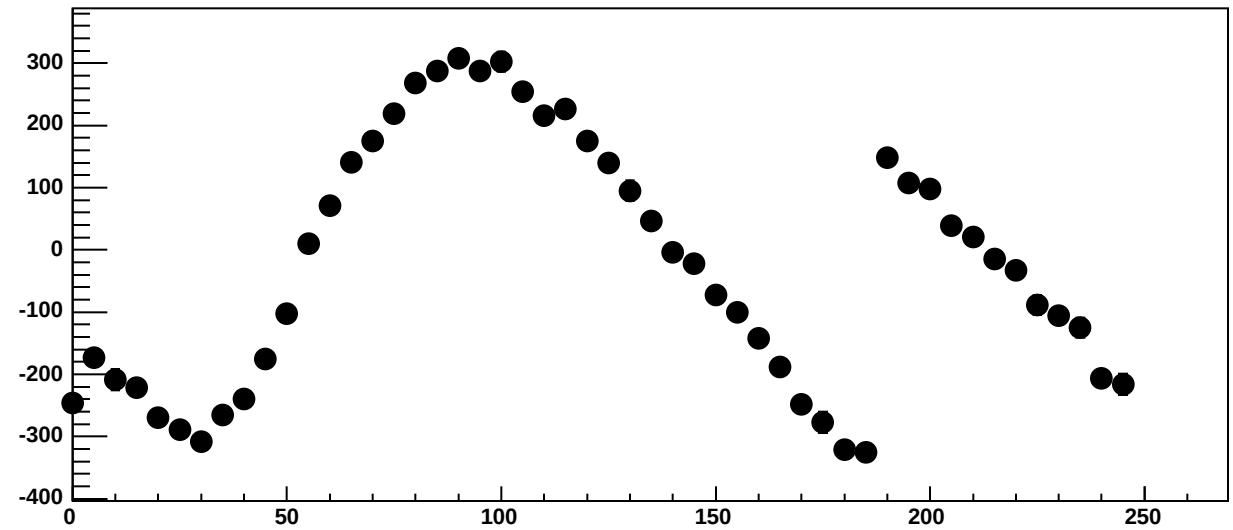
Chip 3, Channel 1, Enable 3, DAC=1600, ADC Mean vs Hold



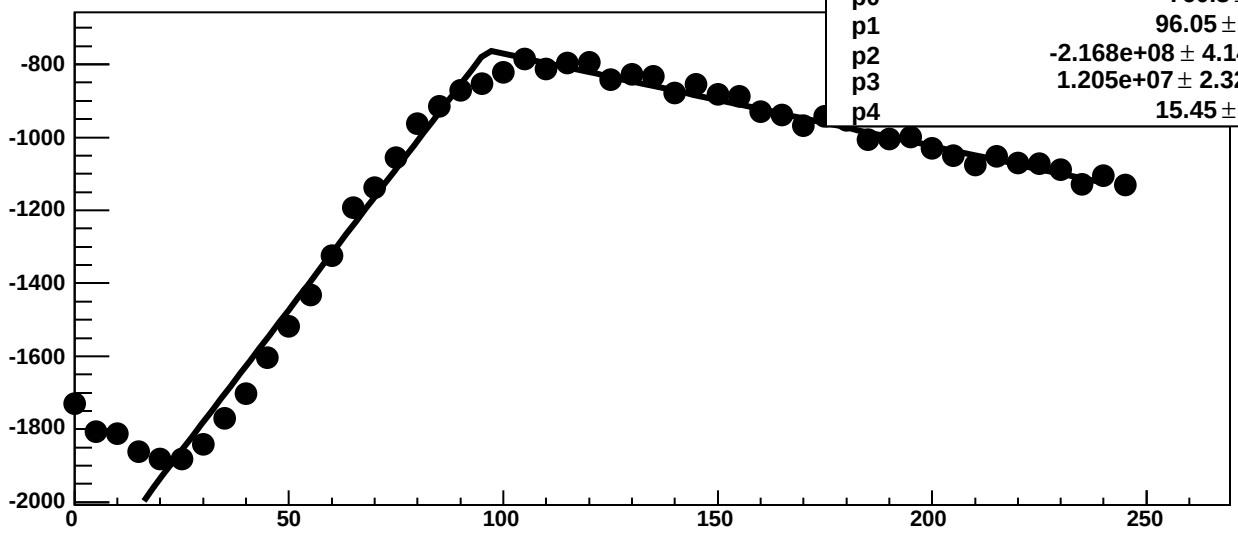
Chip 3, Channel 1, Enable 3, DAC=1600, ADC Noise vs Hold



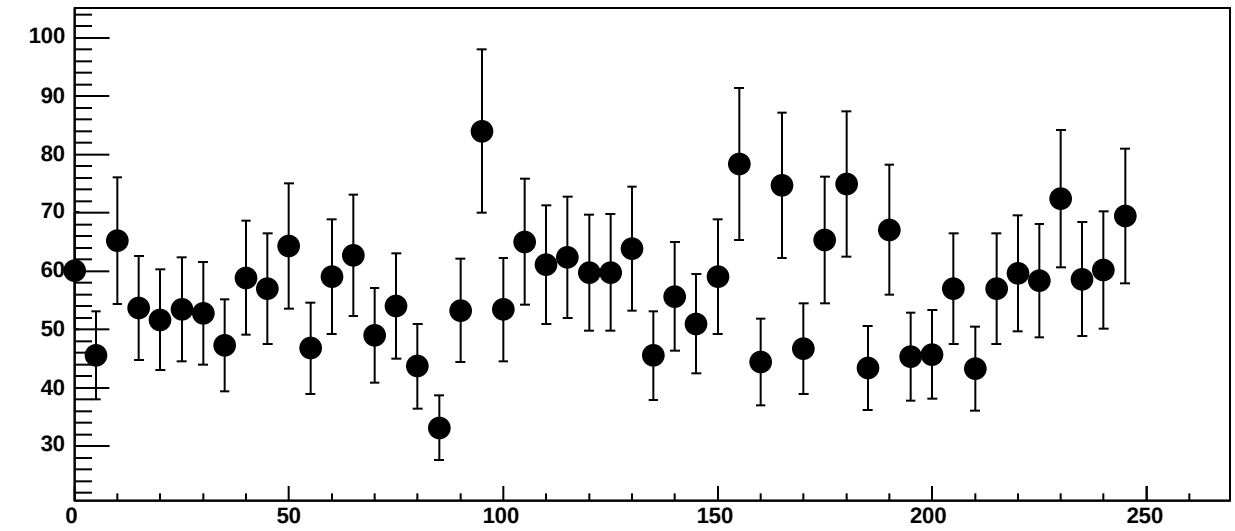
Chip 3, Channel 1, Enable 3, DAC=1600, ADC Residuals vs Hold



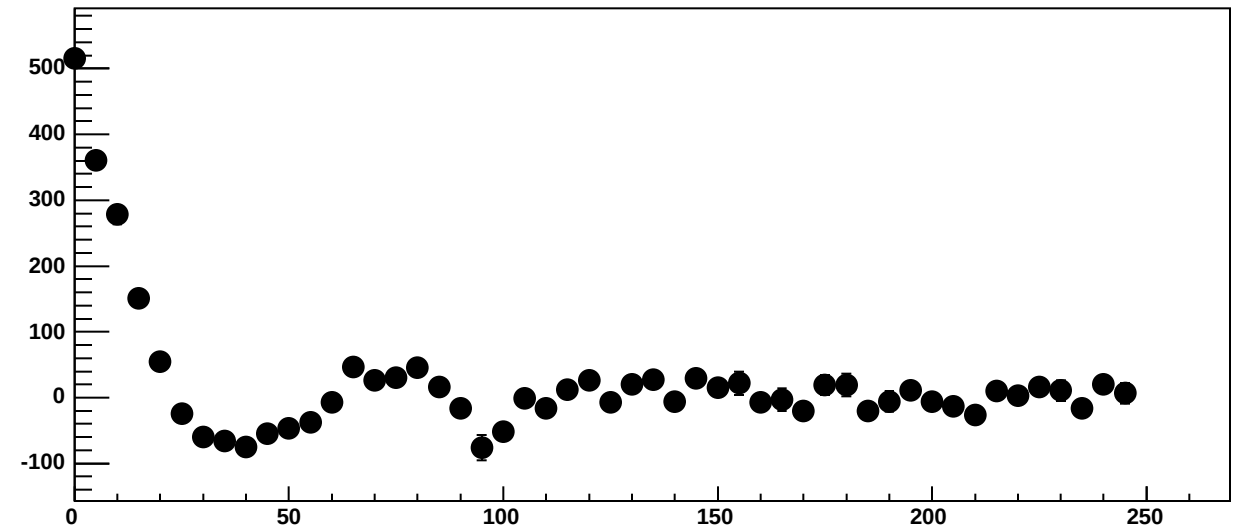
Chip 3, Channel 1, Enable 4, DAC=1600, ADC Mean vs Hold

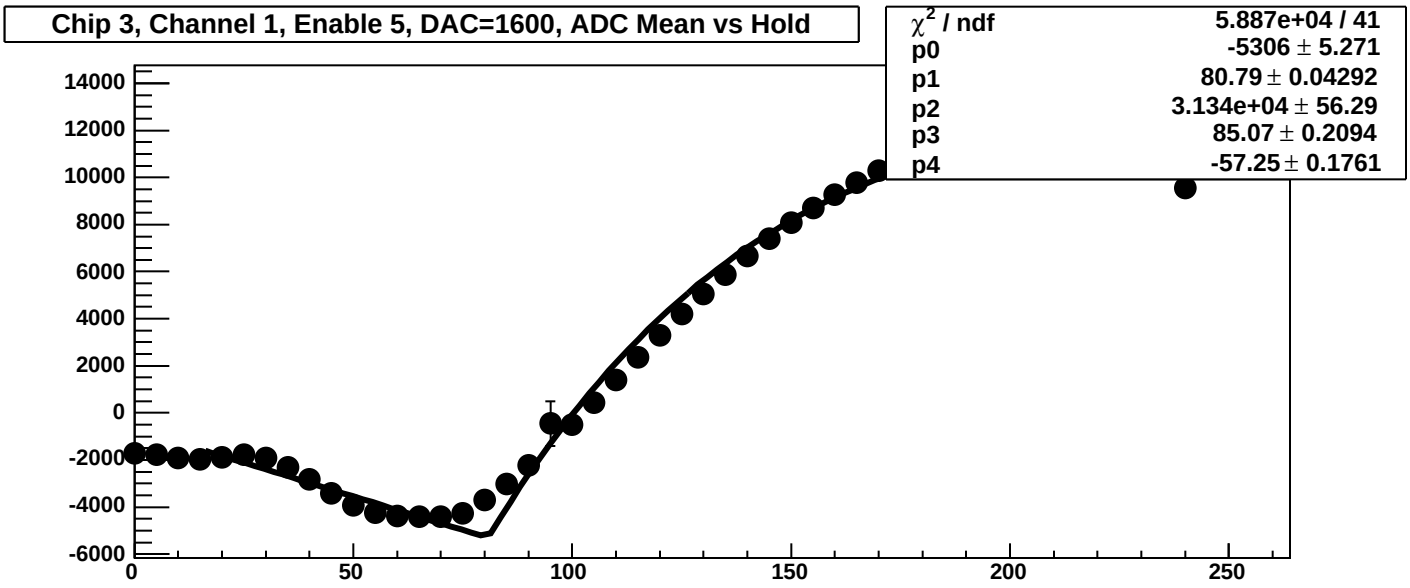


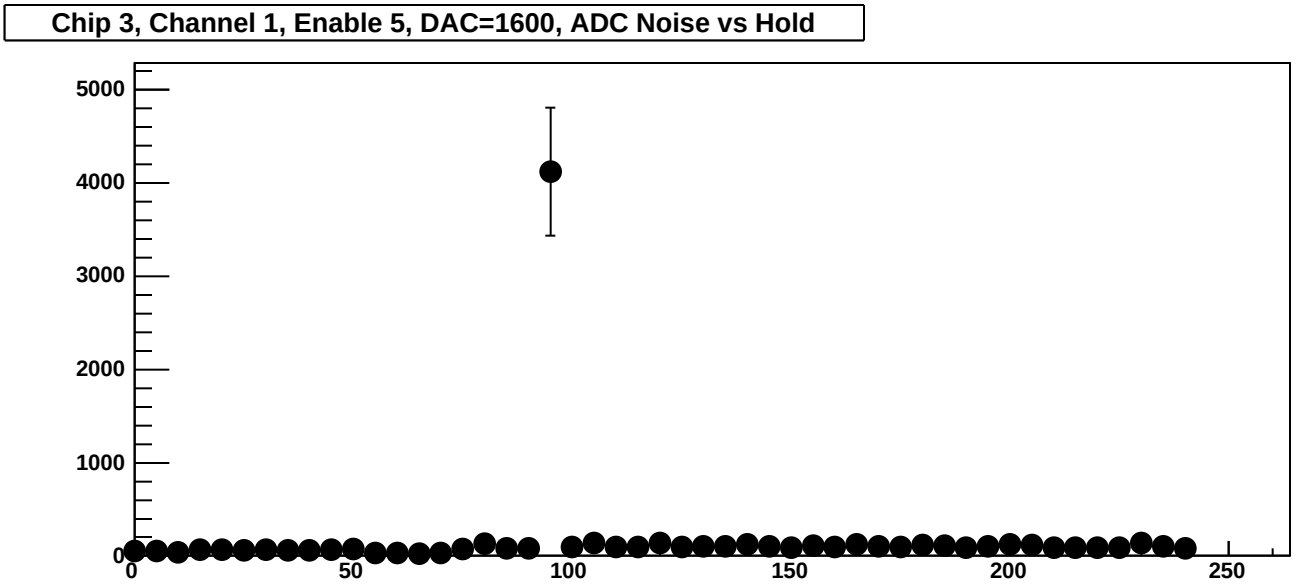
Chip 3, Channel 1, Enable 4, DAC=1600, ADC Noise vs Hold

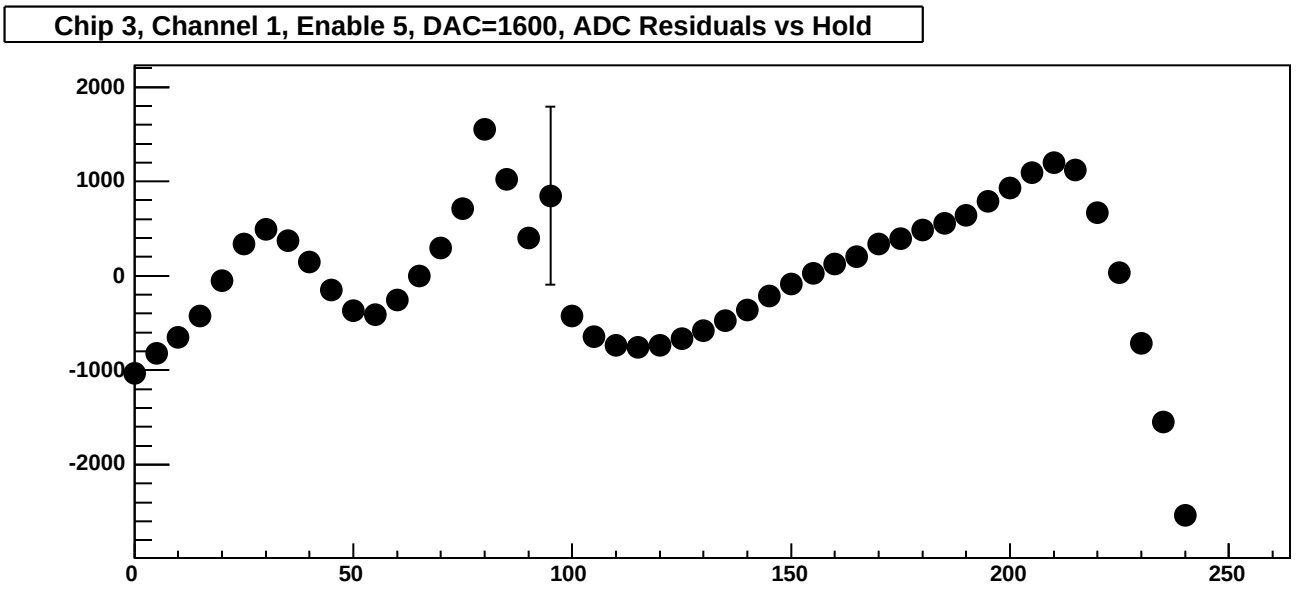


Chip 3, Channel 1, Enable 4, DAC=1600, ADC Residuals vs Hold

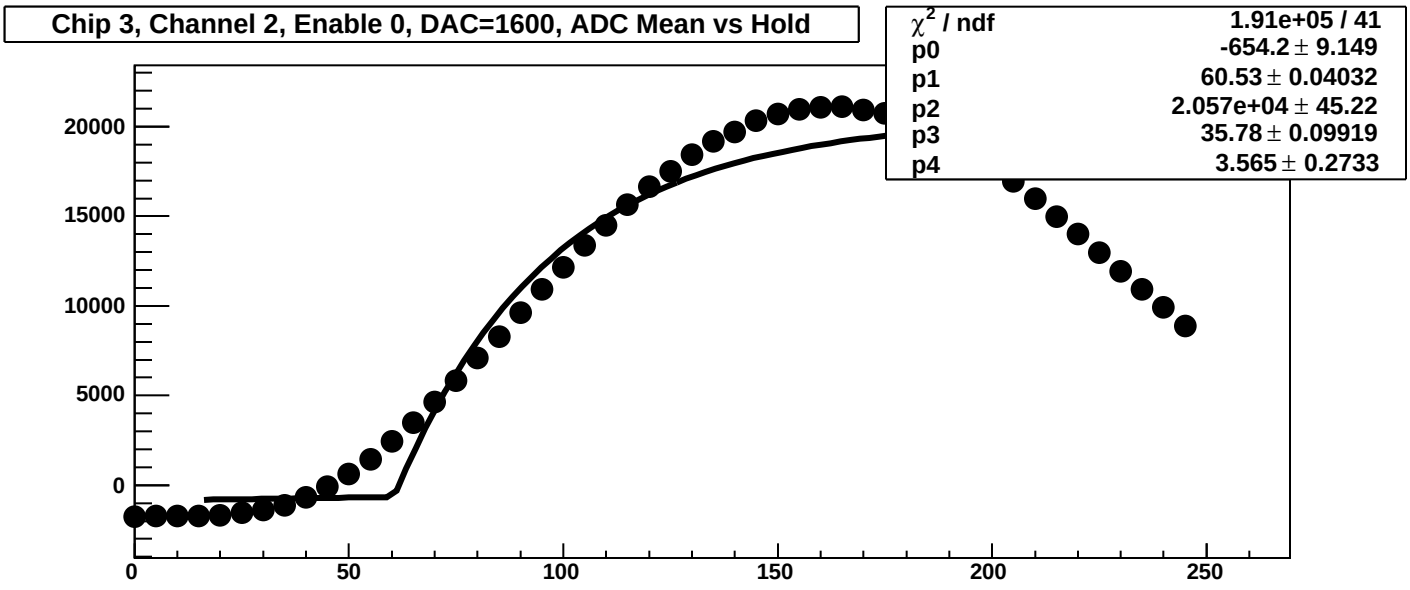



Chip 3, Channel 1, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.887e+04 / 41
p0 -5306 ± 5.271
p1 80.79 ± 0.04292
p2 3.134e+04 ± 56.29
p3 85.07 ± 0.2094
p4 -57.25 ± 0.1761
14000
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

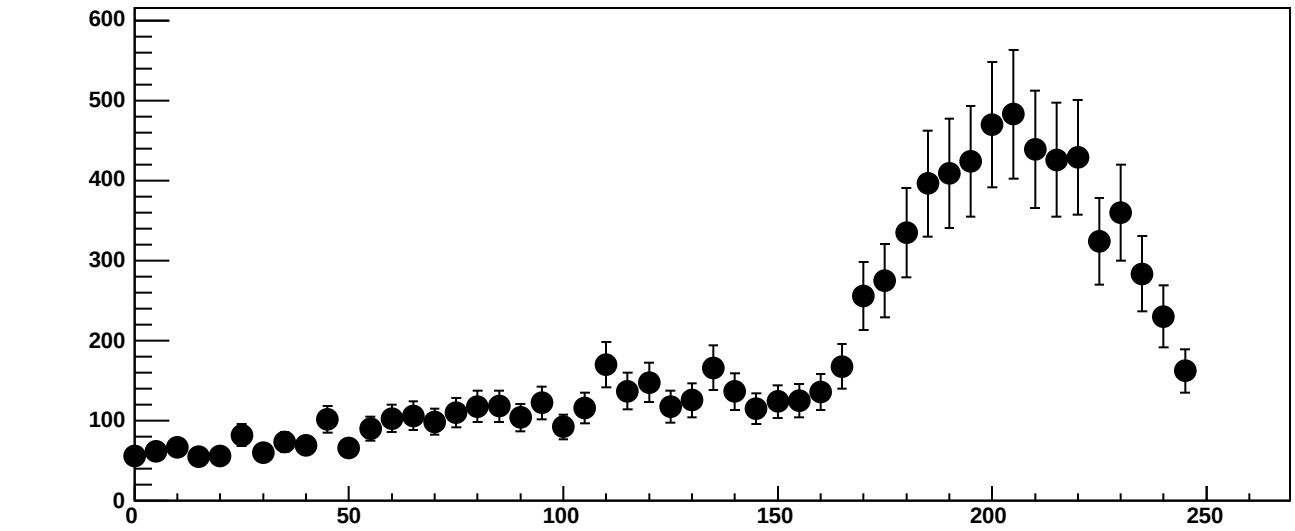

Chip 3, Channel 1, Enable 5, DAC=1600, ADC Noise vs Hold
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 3, Channel 1, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
0
50
100
150
200
250

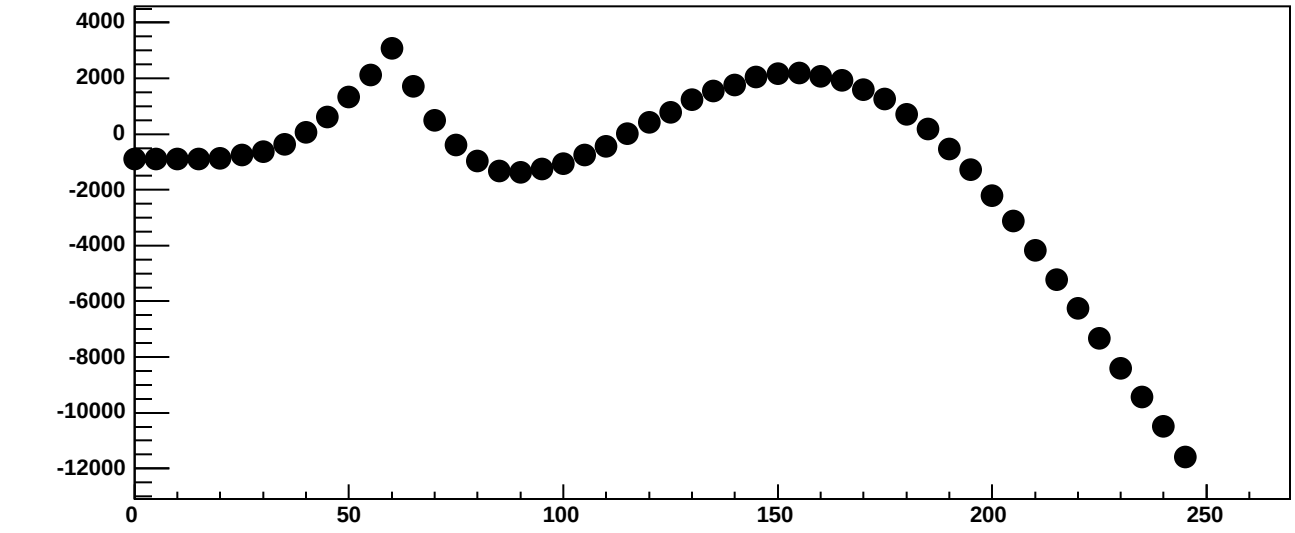
Chip 3, Channel 2, Enable 0, DAC=1600, ADC Mean vs Hold



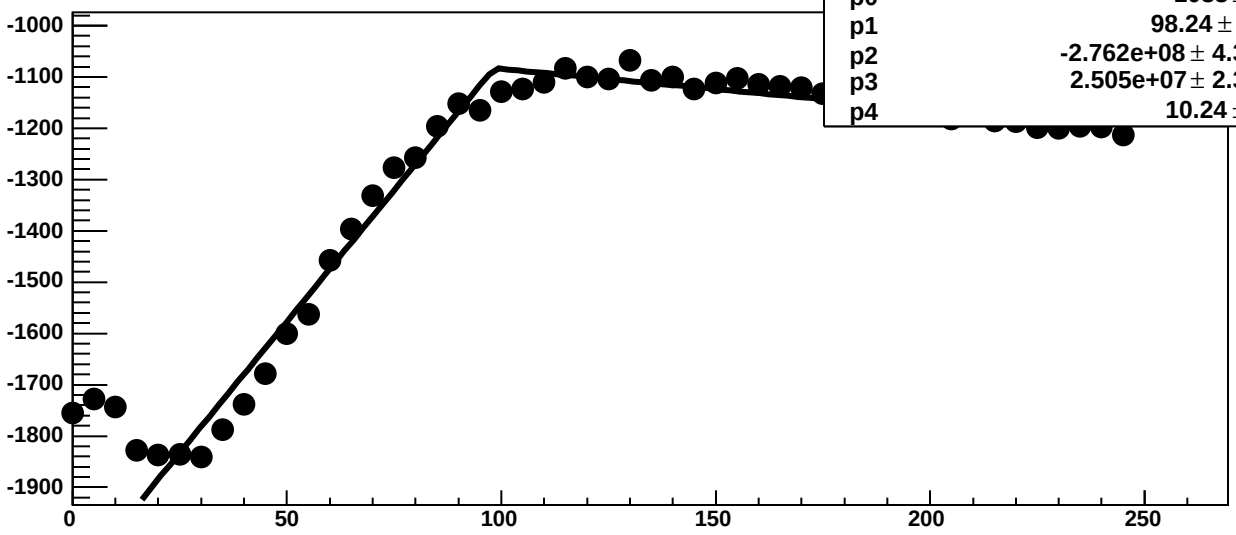
Chip 3, Channel 2, Enable 0, DAC=1600, ADC Noise vs Hold



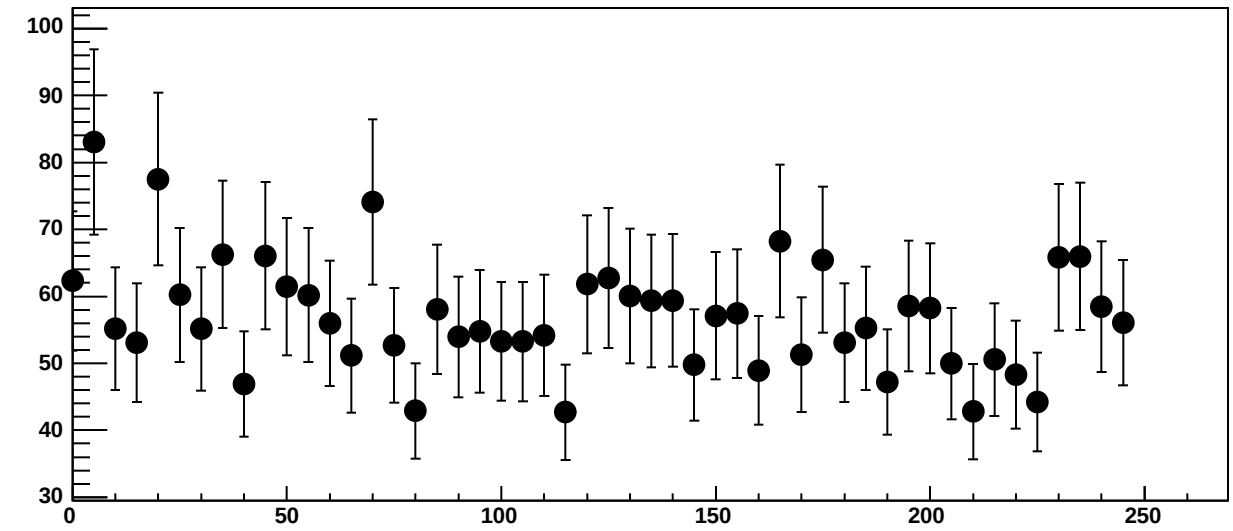
Chip 3, Channel 2, Enable 0, DAC=1600, ADC Residuals vs Hold



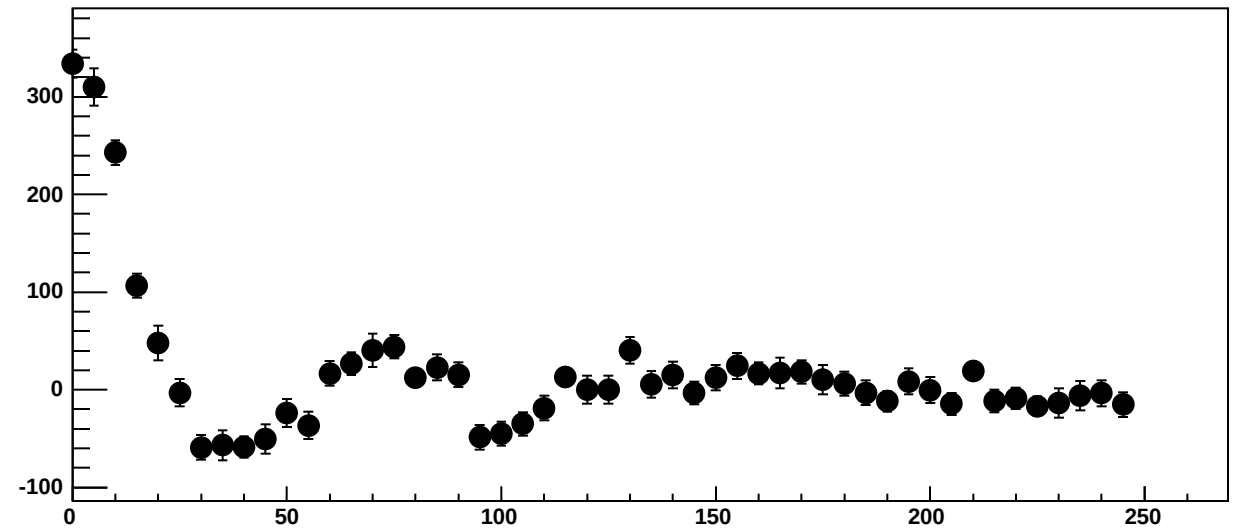
Chip 3, Channel 2, Enable 1, DAC=1600, ADC Mean vs Hold

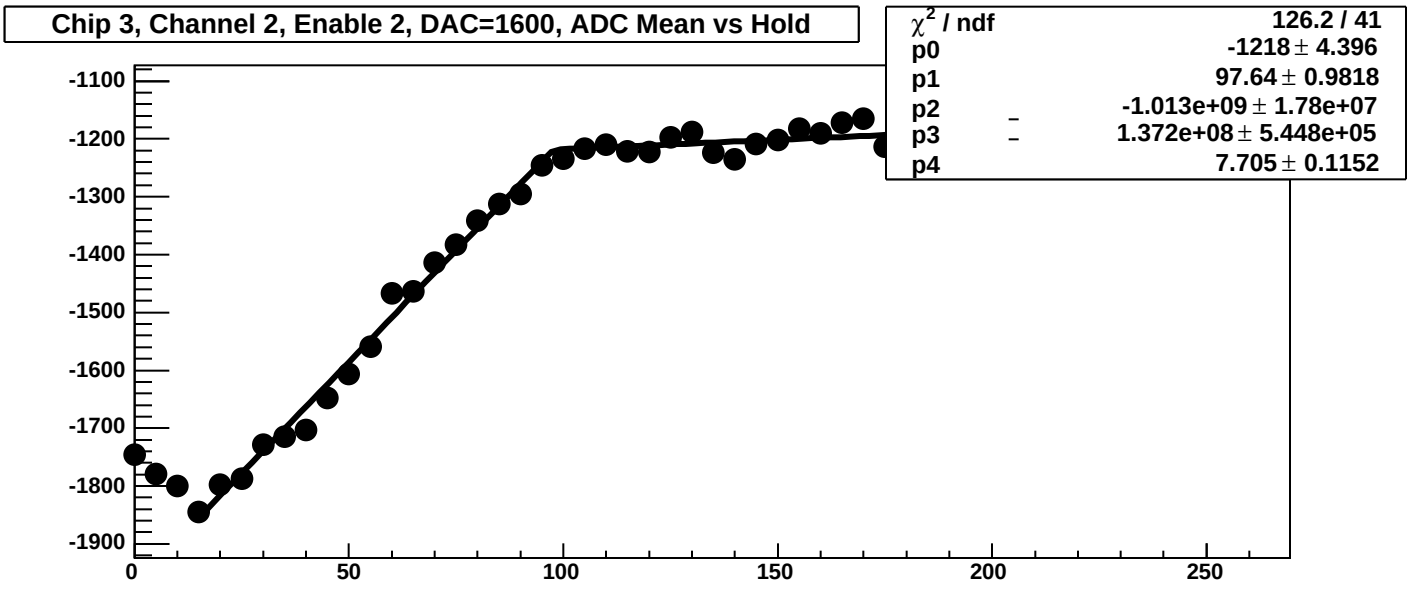


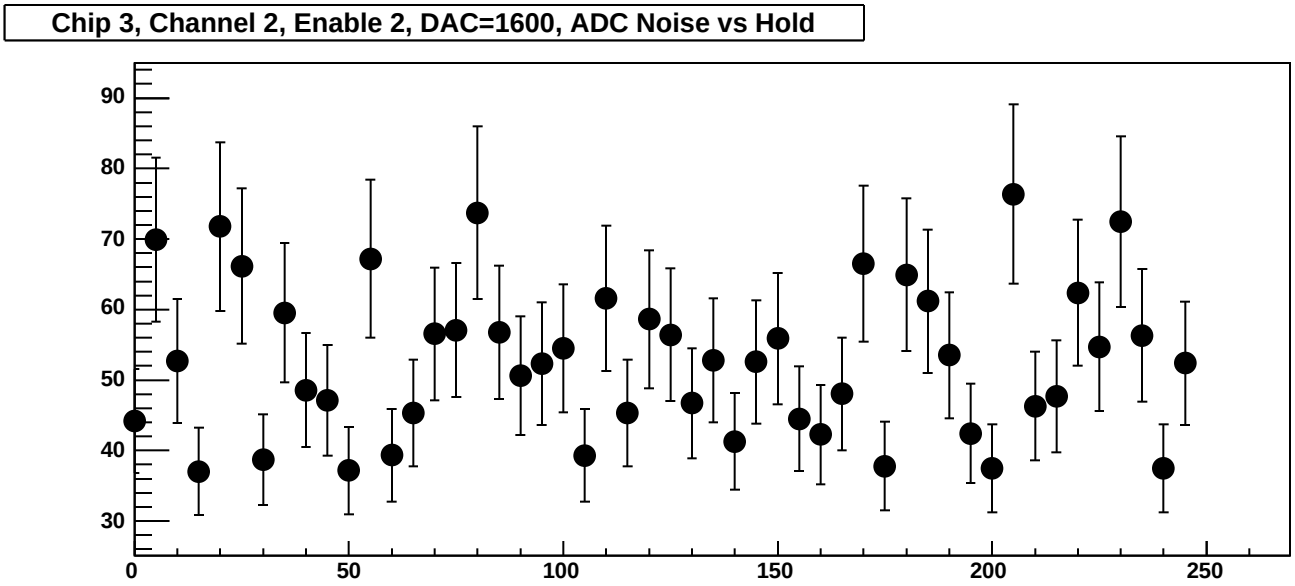
Chip 3, Channel 2, Enable 1, DAC=1600, ADC Noise vs Hold

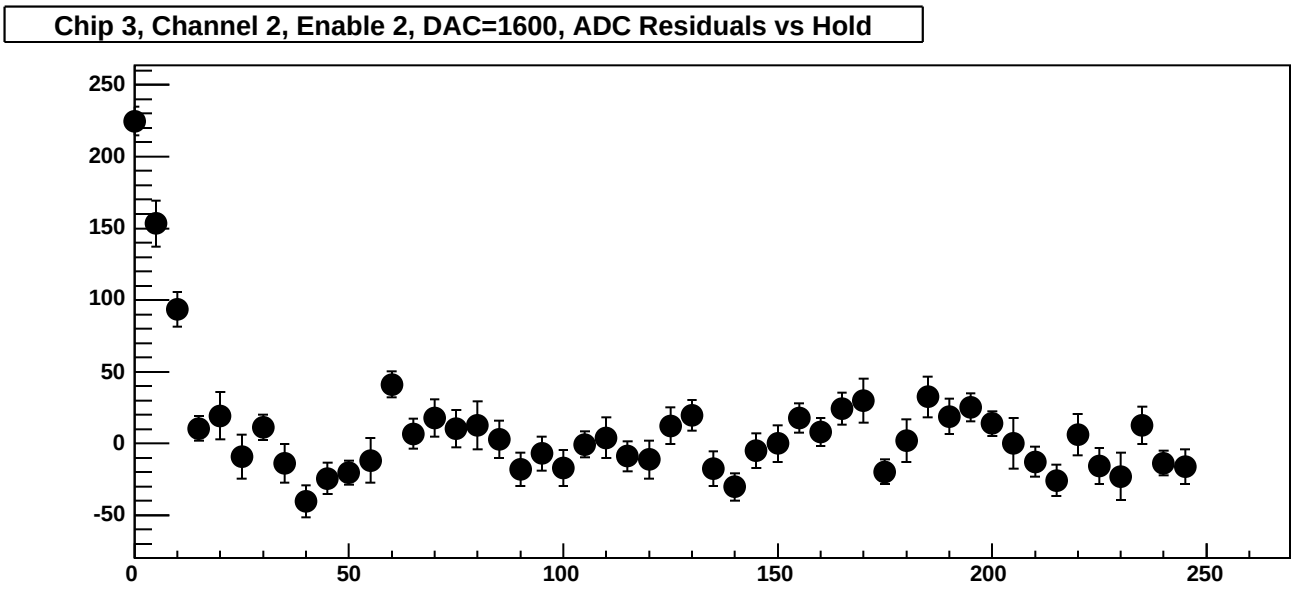


Chip 3, Channel 2, Enable 1, DAC=1600, ADC Residuals vs Hold

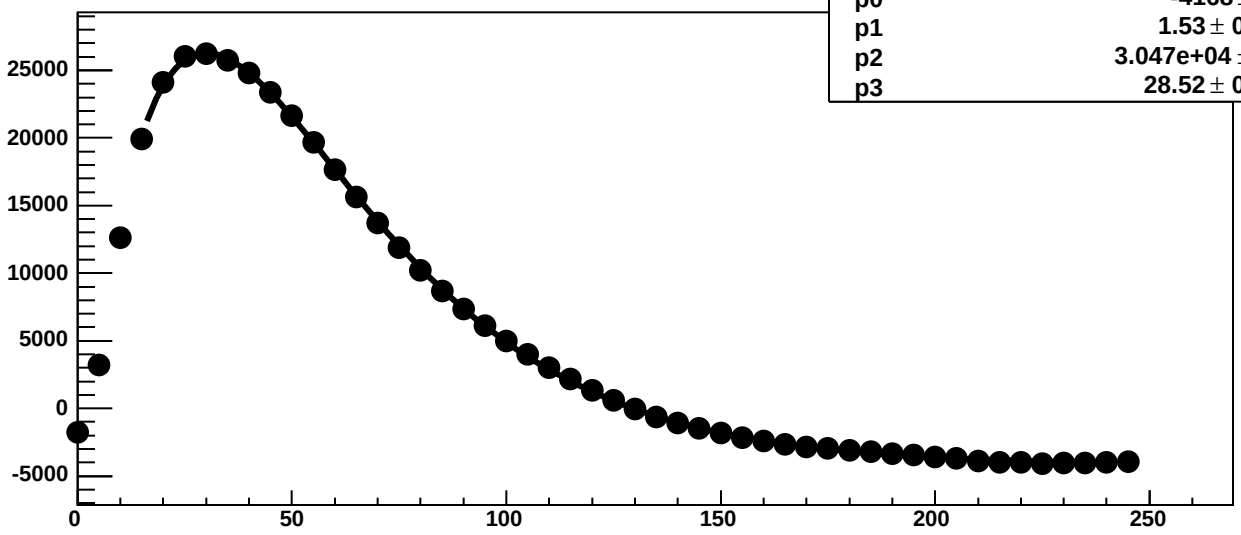



Chip 3, Channel 2, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 126.2 / 41
p0 -1218 ± 4.396
p1 97.64 ± 0.9818
p2 -1.013e+09 ± 1.78e+07
p3 1.372e+08 ± 5.448e+05
p4 7.705 ± 0.1152
-1100
-1200
-1300
-1400
-1500
-1600
-1700
-1800
-1900
0
50
100
150
200
250

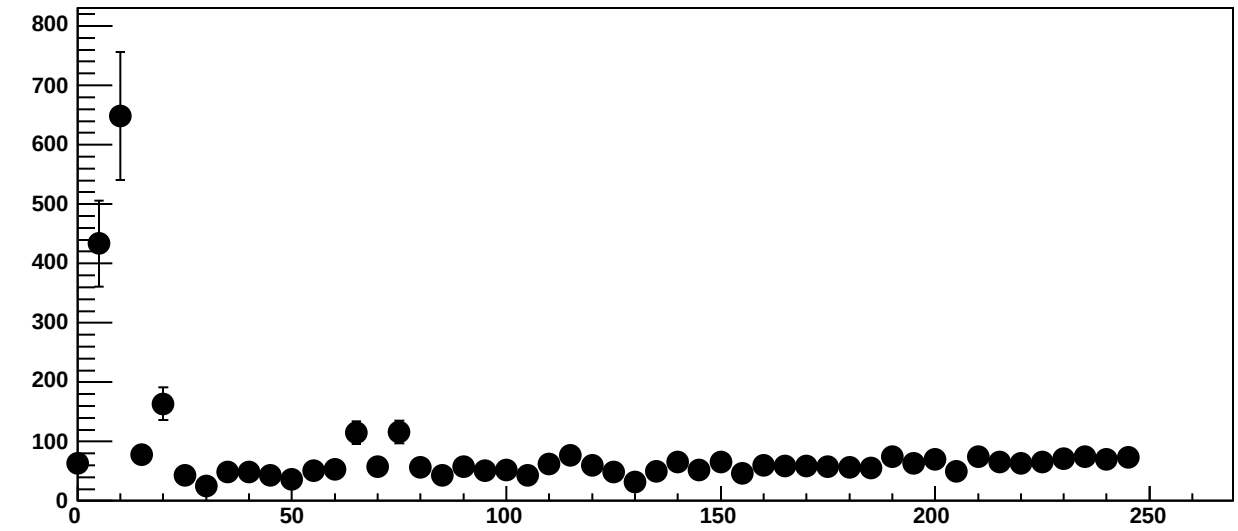

Chip 3, Channel 2, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 3, Channel 2, Enable 2, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
0
50
100
150
200
250

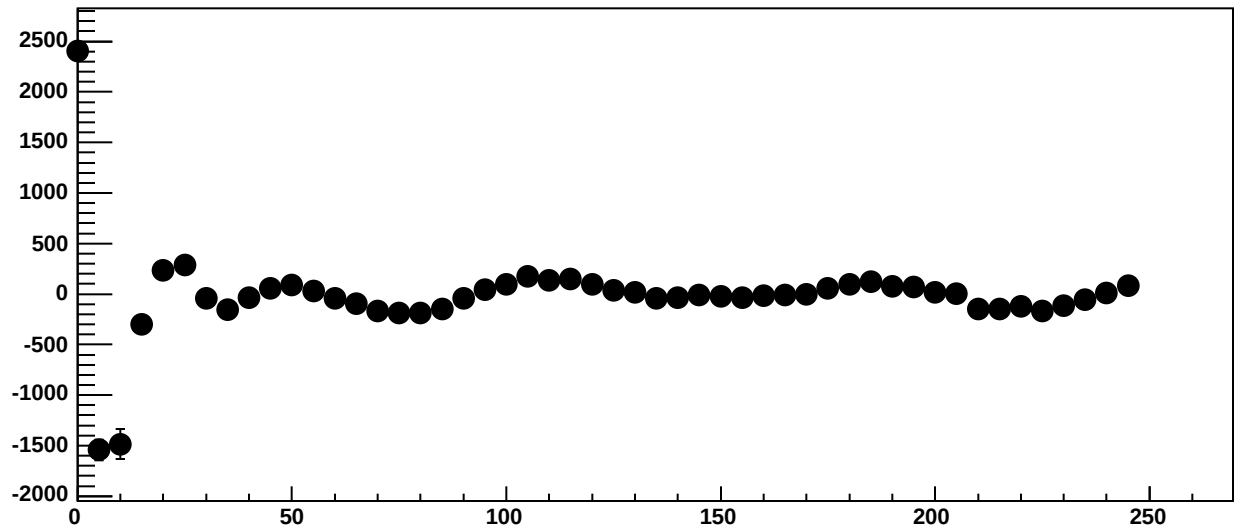
Chip 3, Channel 2, Enable 3!, DAC=1600, ADC Mean vs Hold

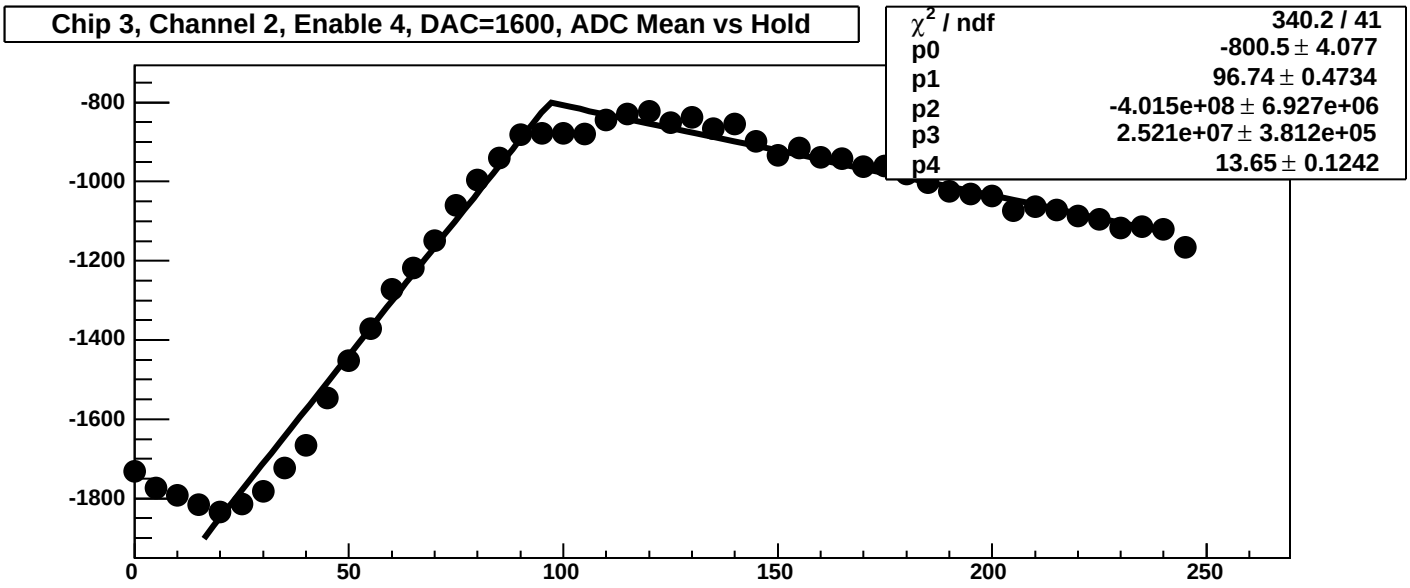


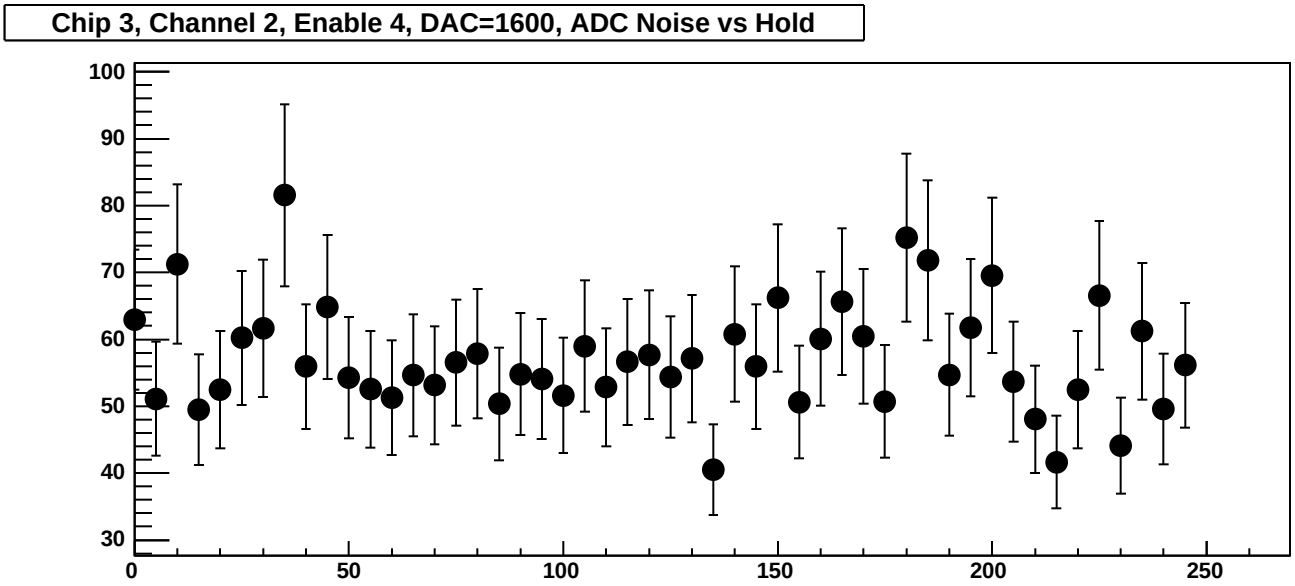
Chip 3, Channel 2, Enable 3!, DAC=1600, ADC Noise vs Hold

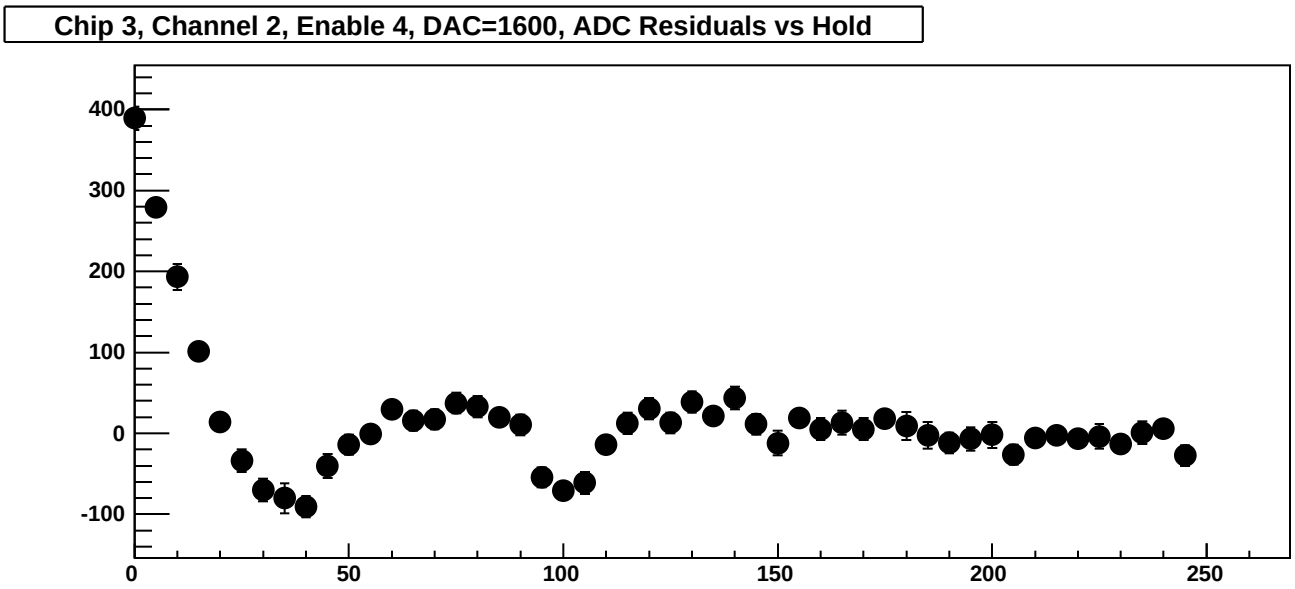


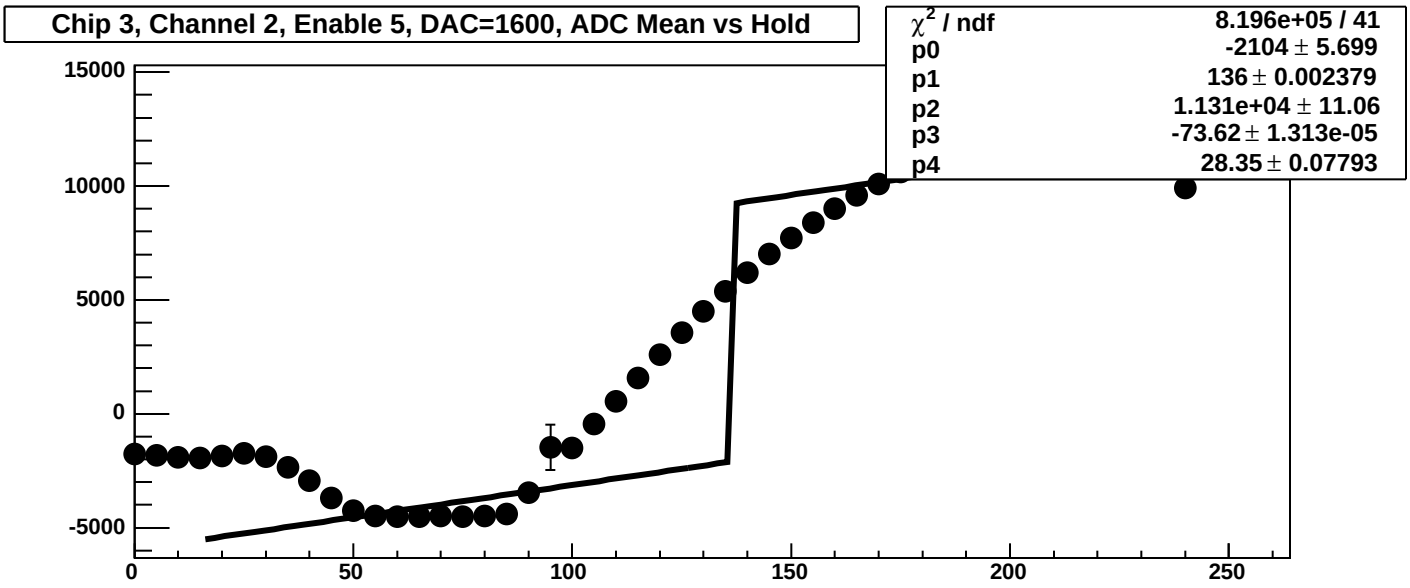
Chip 3, Channel 2, Enable 3!, DAC=1600, ADC Residuals vs Hold

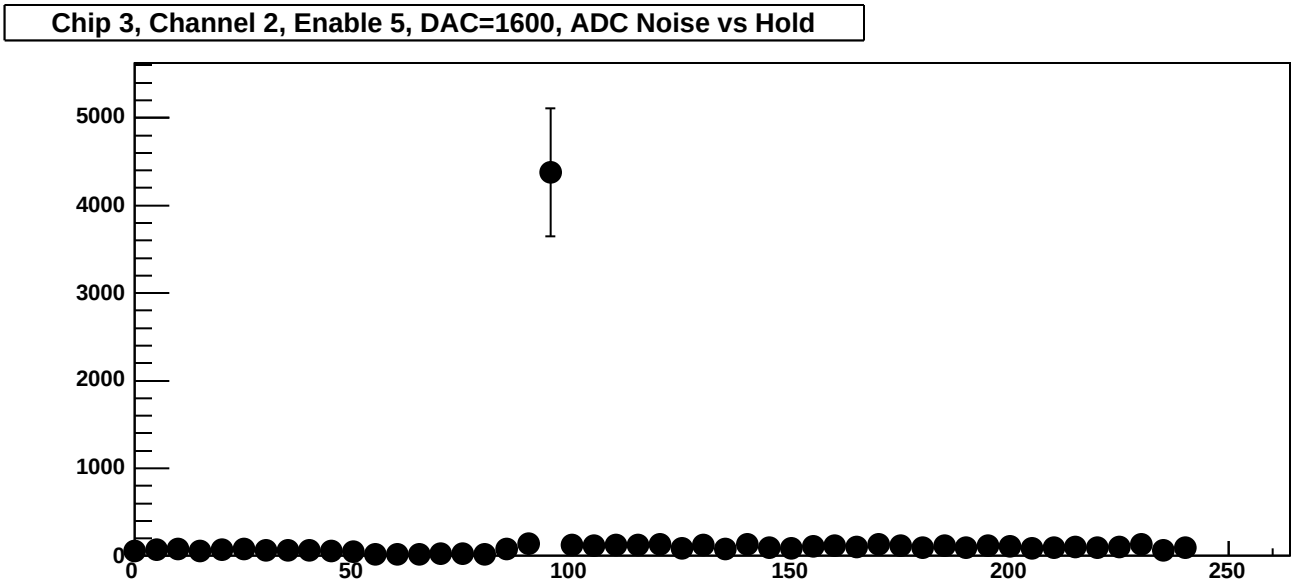


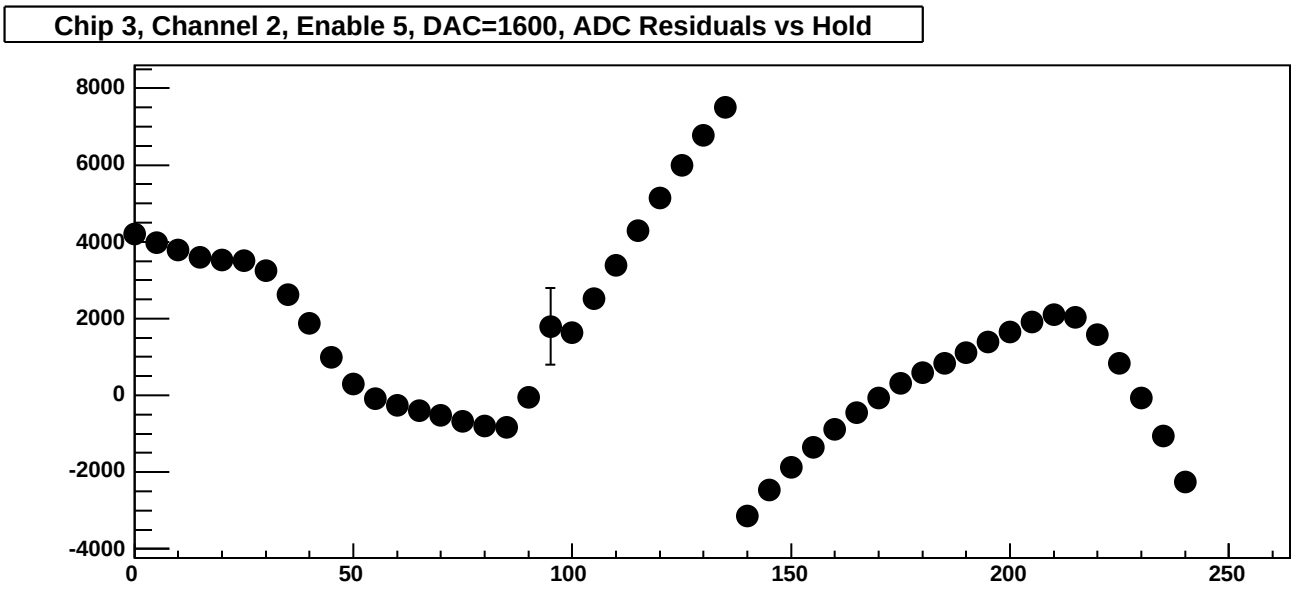

Chip 3, Channel 2, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 340.2 / 41
p0 -800.5 ± 4.077
p1 96.74 ± 0.4734
p2 -4.015e+08 ± 6.927e+06
p3 2.521e+07 ± 3.812e+05
p4 13.65 ± 0.1242
-800
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250

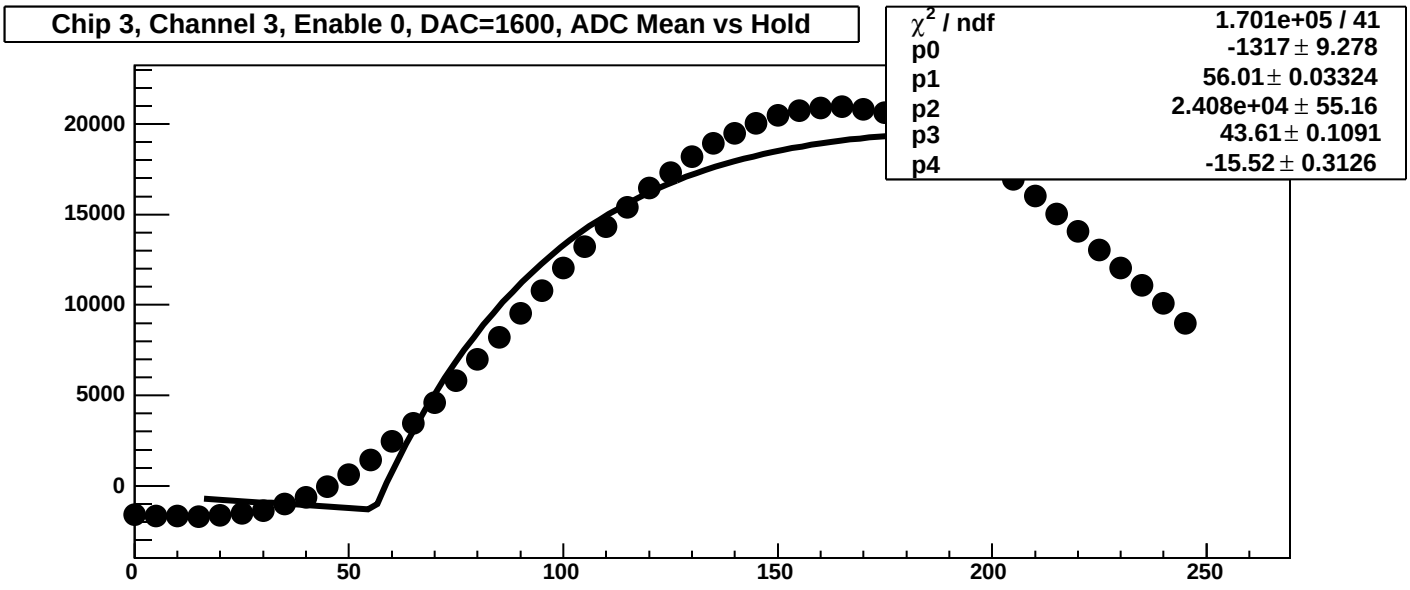

Chip 3, Channel 2, Enable 4, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250

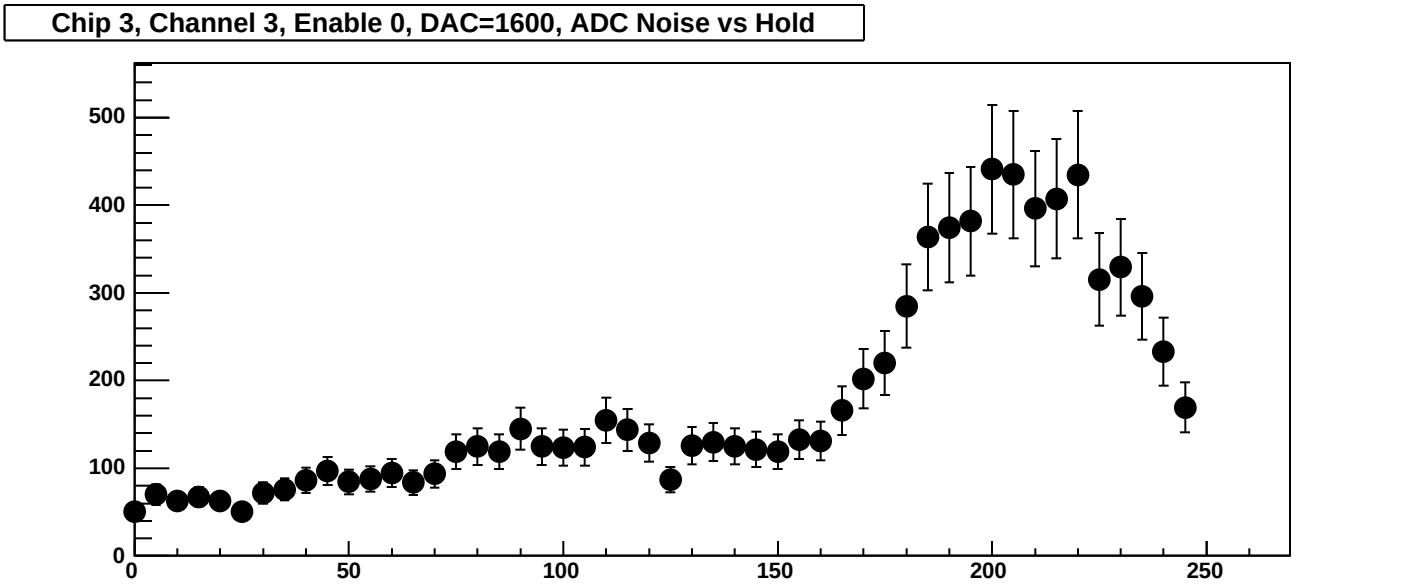

Chip 3, Channel 2, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

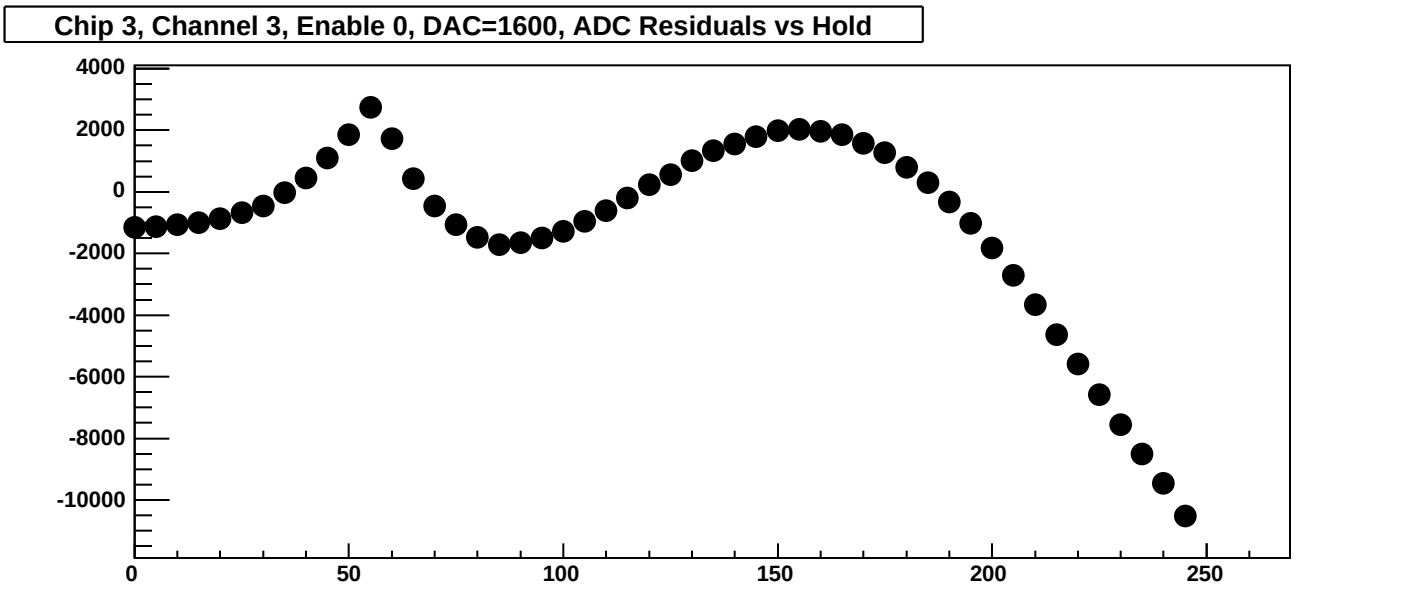

Chip 3, Channel 2, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 8.196e+05 / 41
p0 -2104 ± 5.699
p1 136 ± 0.002379
p2 1.131e+04 ± 11.06
p3 -73.62 ± 1.313e-05
p4 28.35 ± 0.07793
15000
10000
5000
0
-5000
0
50
100
150
200
250

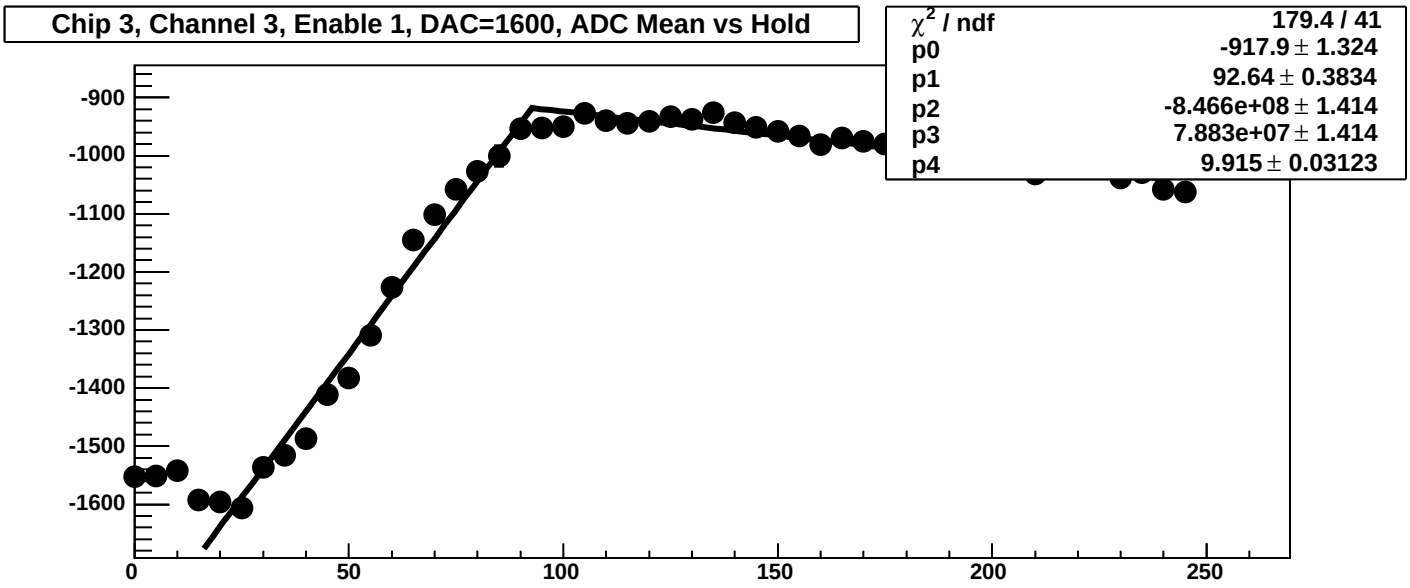

Chip 3, Channel 2, Enable 5, DAC=1600, ADC Noise vs Hold
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

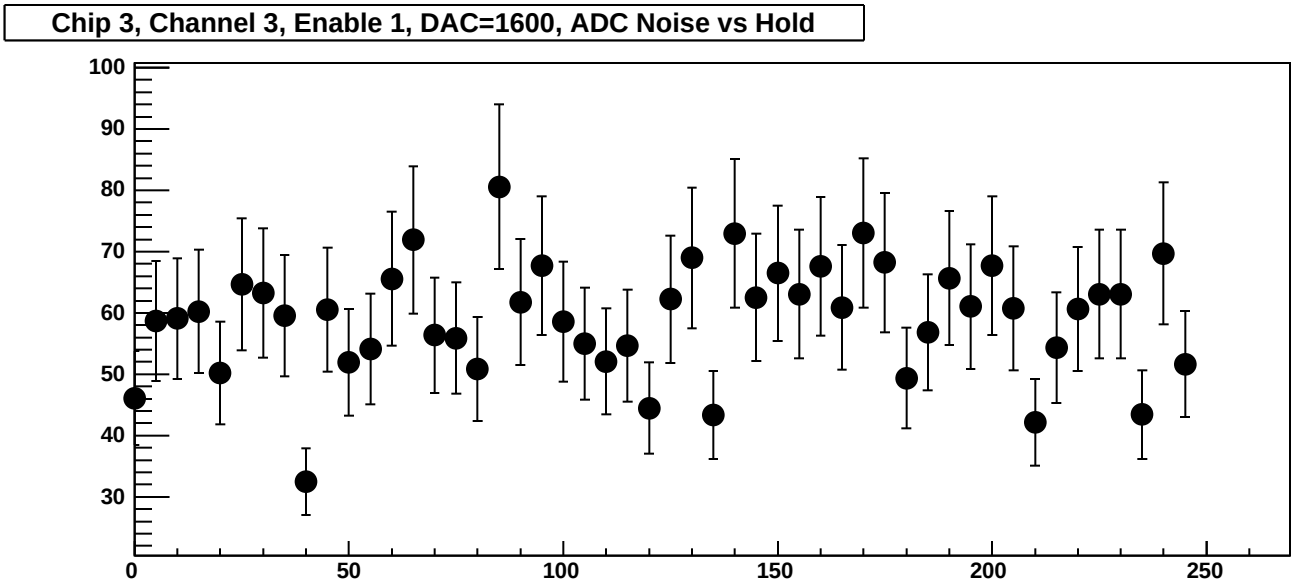

Chip 3, Channel 2, Enable 5, DAC=1600, ADC Residuals vs Hold
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

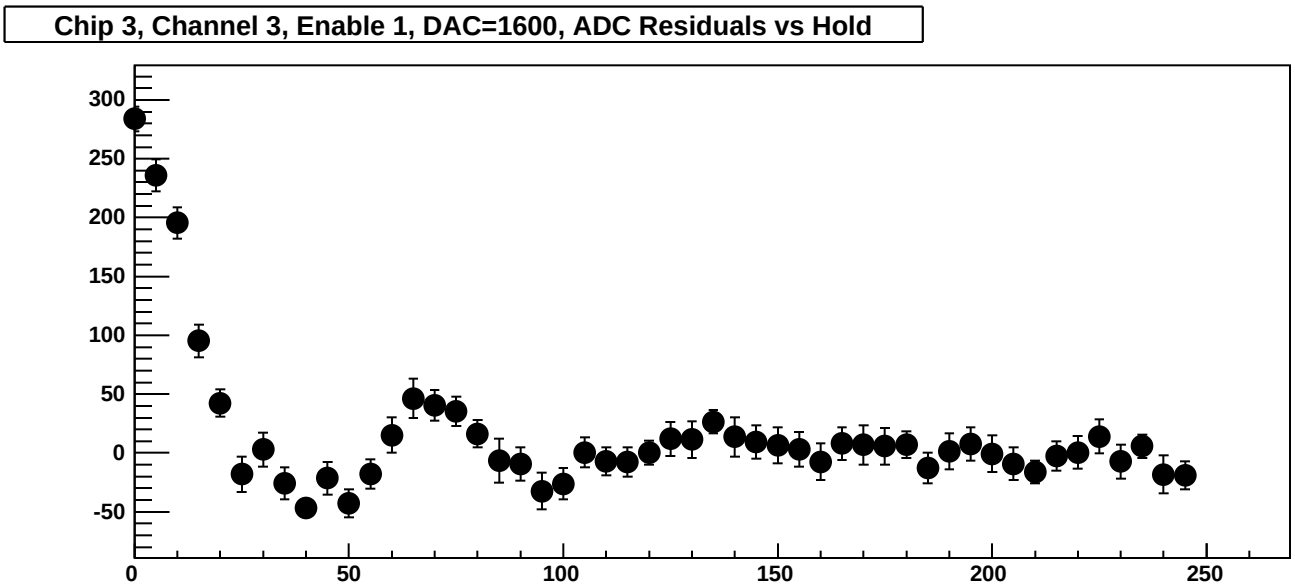

Chip 3, Channel 3, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.701e+05 / 41
p0 -1317 ± 9.278
p1 56.01 ± 0.03324
p2 2.408e+04 ± 55.16
p3 43.61 ± 0.1091
p4 -15.52 ± 0.3126
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 3, Channel 3, Enable 0, DAC=1600, ADC Noise vs Hold
500
400
300
200
100
0
0
50
100
150
200
250

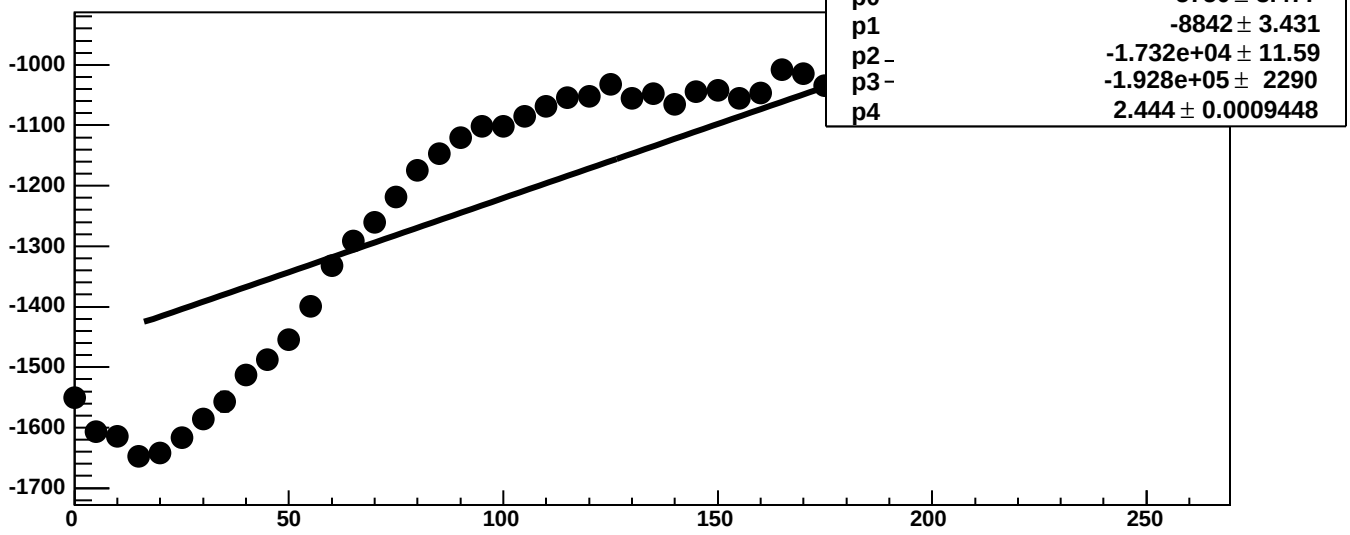

Chip 3, Channel 3, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250


Chip 3, Channel 3, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 179.4 / 41
p0 -917.9 ± 1.324
p1 92.64 ± 0.3834
p2 -8.466e+08 ± 1.414
p3 7.883e+07 ± 1.414
p4 9.915 ± 0.03123
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
50
100
150
200
250

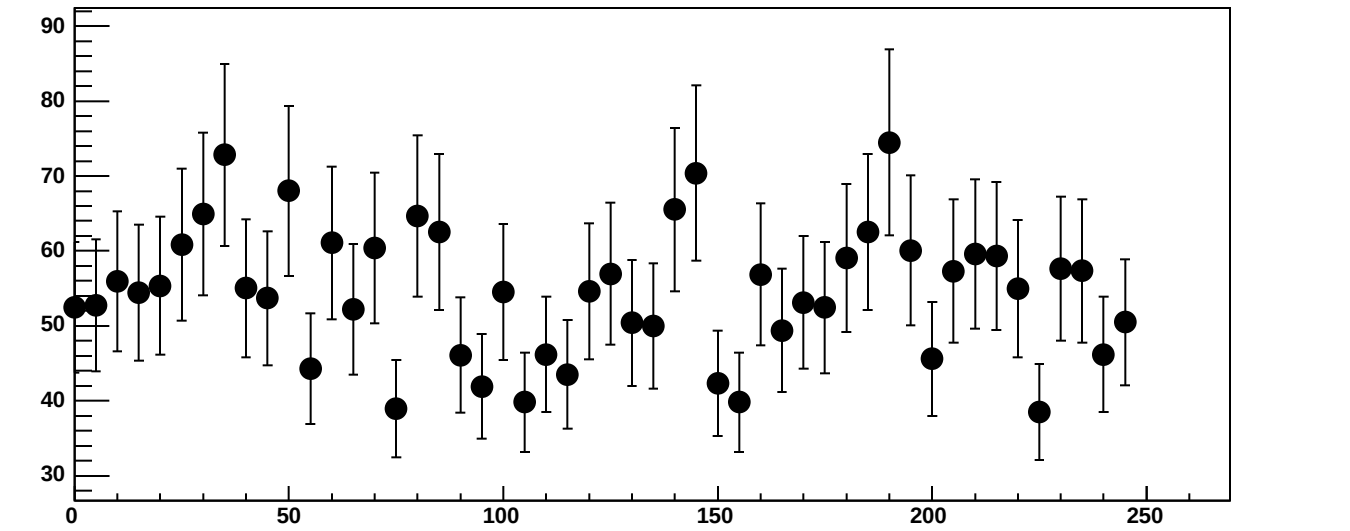

Chip 3, Channel 3, Enable 1, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 3, Channel 3, Enable 1, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
0
50
100
150
200
250

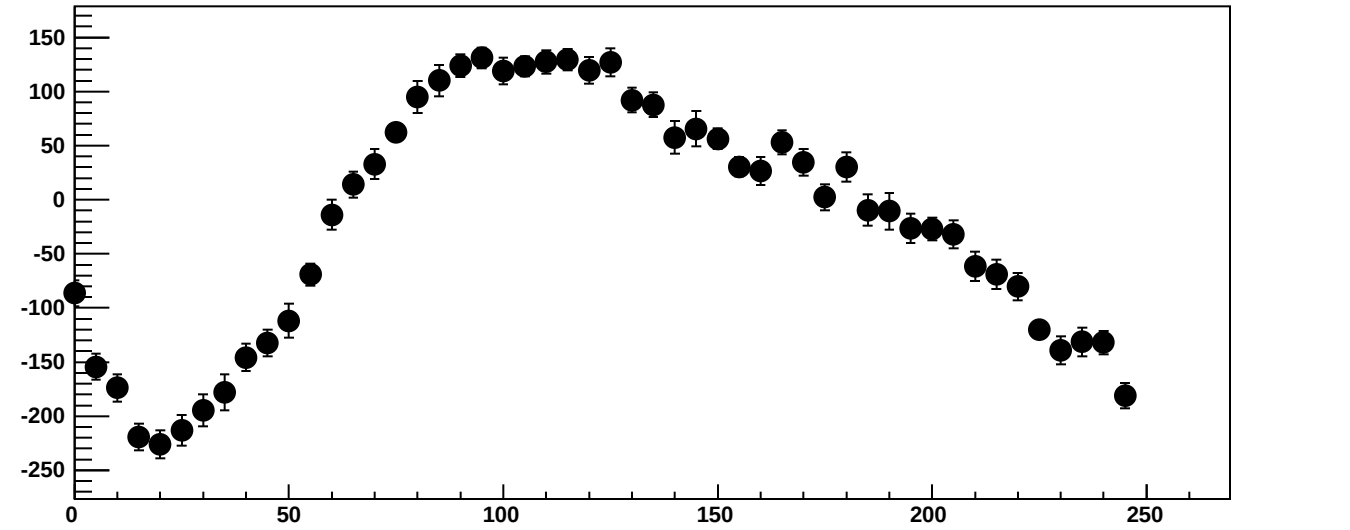
Chip 3, Channel 3, Enable 2, DAC=1600, ADC Mean vs Hold

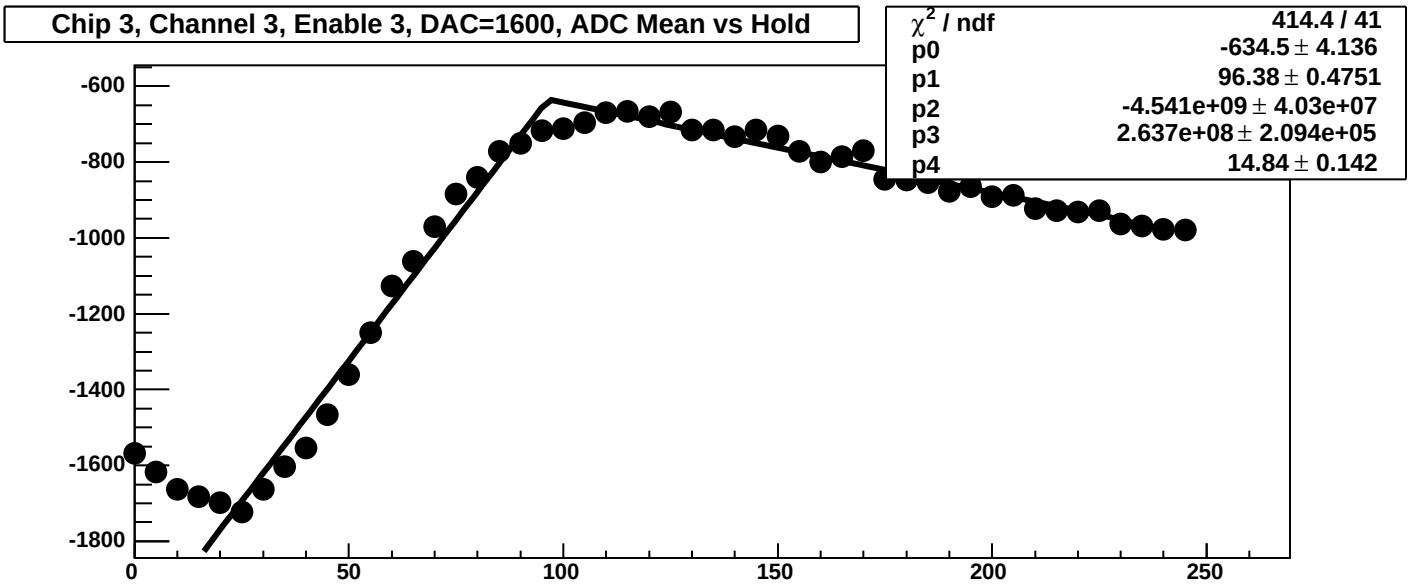


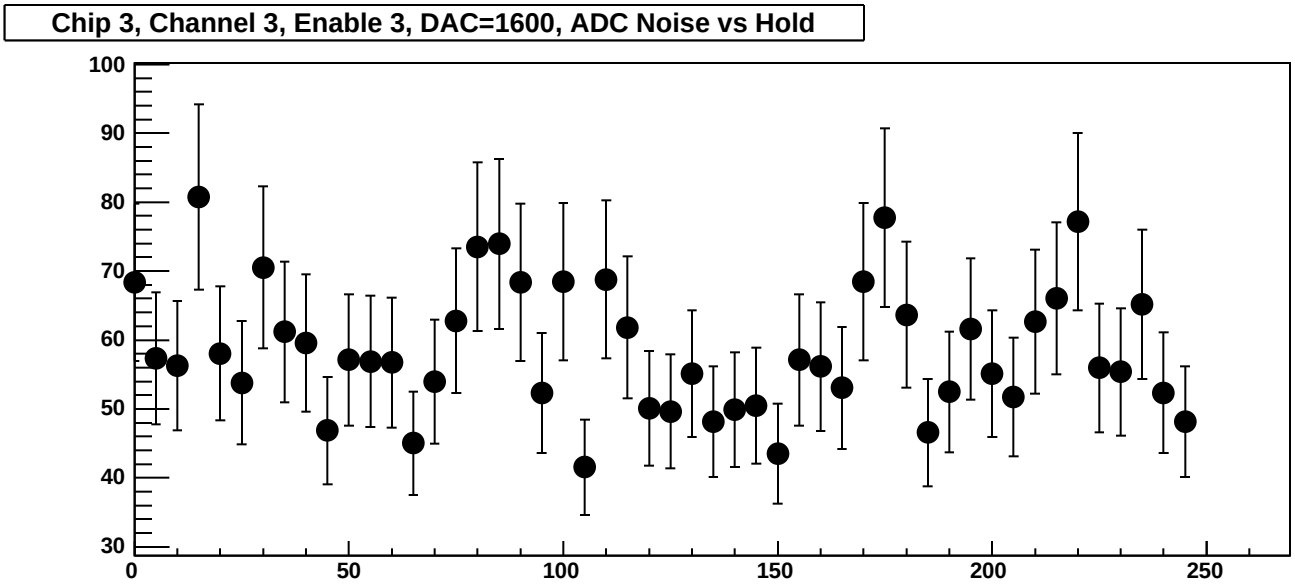
Chip 3, Channel 3, Enable 2, DAC=1600, ADC Noise vs Hold

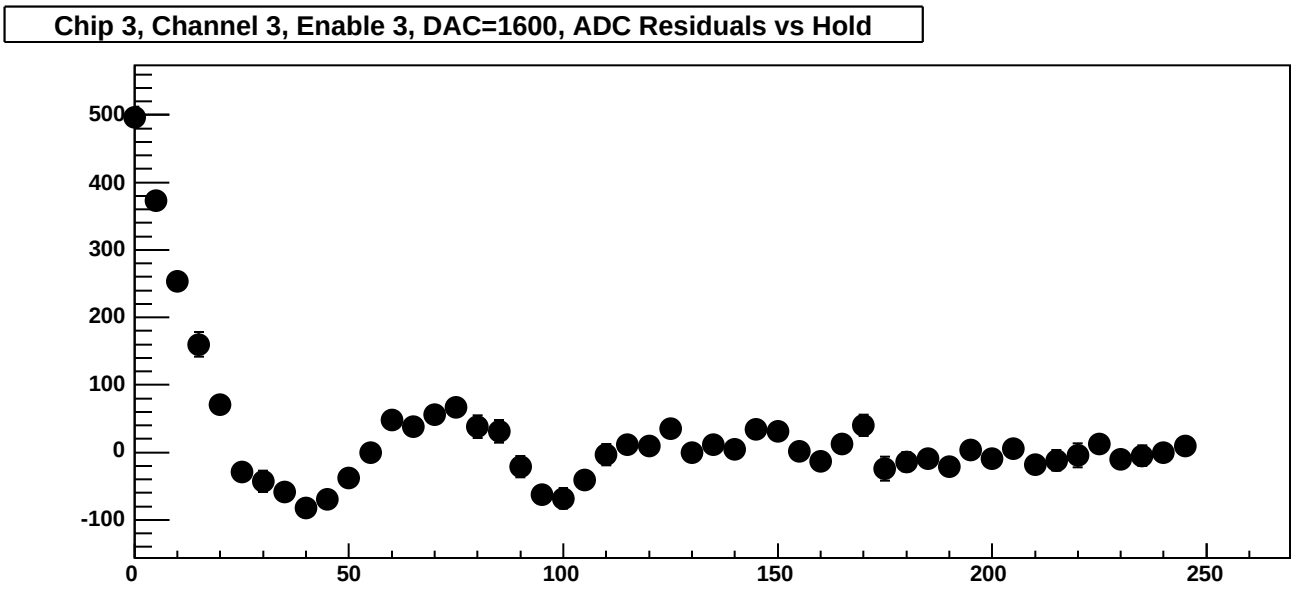


Chip 3, Channel 3, Enable 2, DAC=1600, ADC Residuals vs Hold



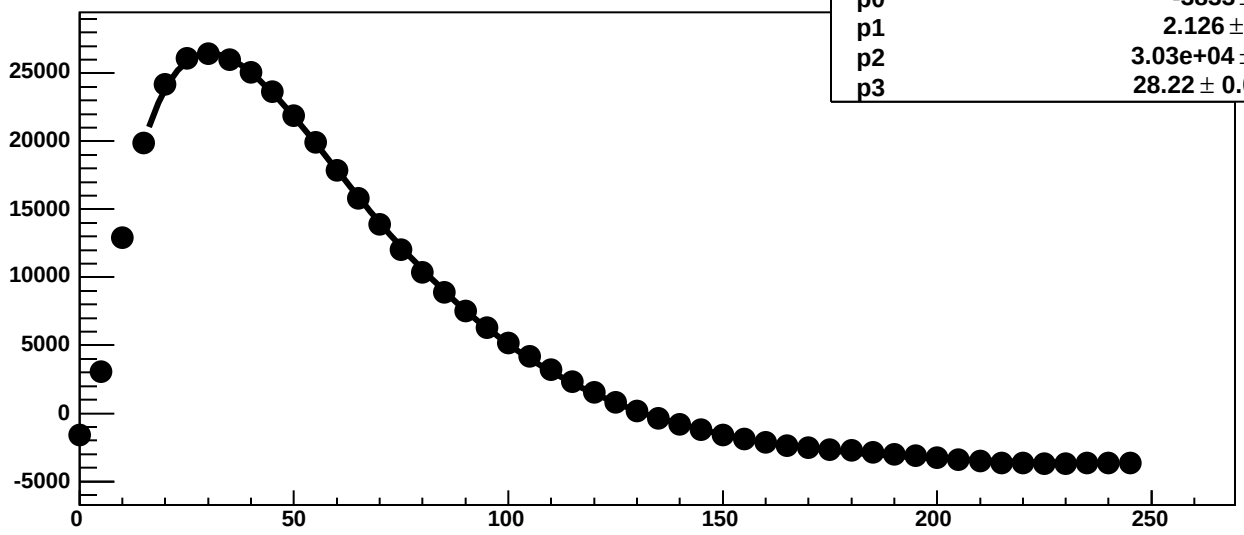

Chip 3, Channel 3, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 414.4 / 41
p0 -634.5 ± 4.136
p1 96.38 ± 0.4751
p2 -4.541e+09 ± 4.03e+07
p3 2.637e+08 ± 2.094e+05
p4 14.84 ± 0.142
-600
-800
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250


Chip 3, Channel 3, Enable 3, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250

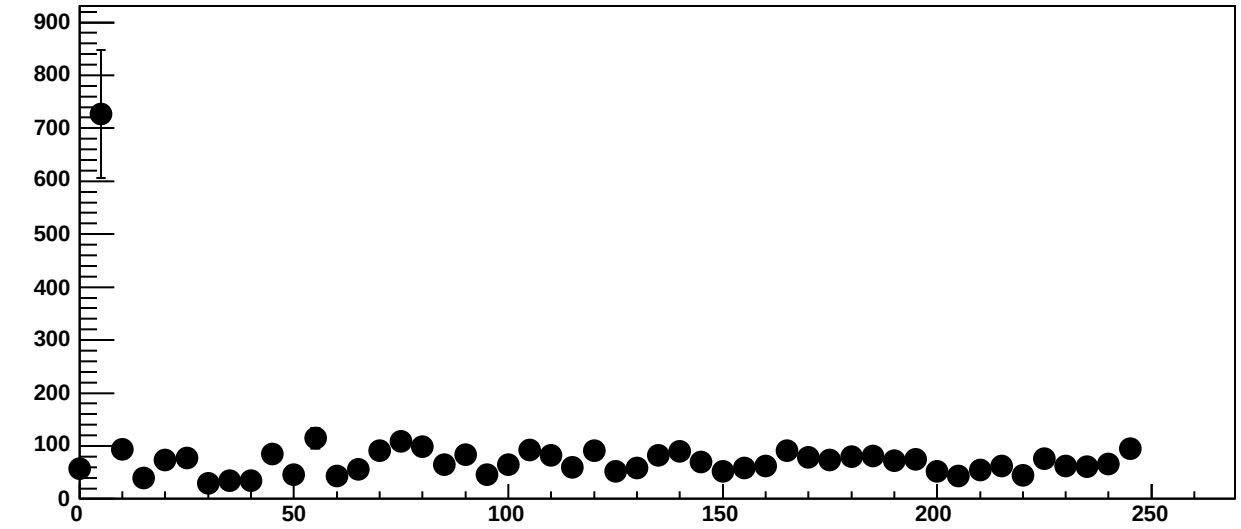

Chip 3, Channel 3, Enable 3, DAC=1600, ADC Residuals vs Hold
500
400
300
200
100
0
-100
0
50
100
150
200
250

Chip 3, Channel 3, Enable 4!, DAC=1600, ADC Mean vs Hold

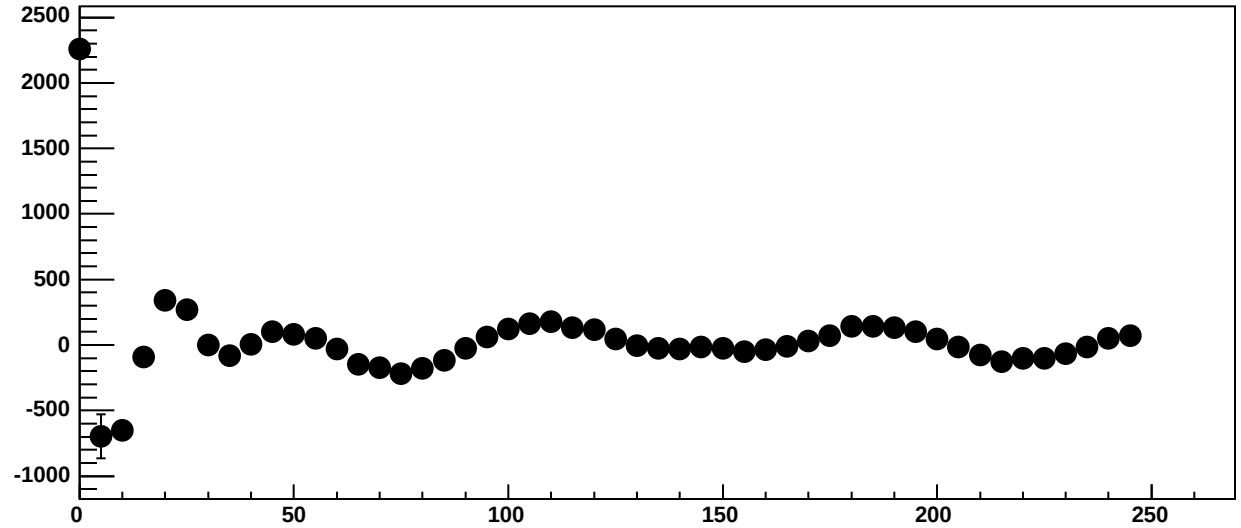
χ^2 / ndf	2313 / 42
p0	-3833 ± 3.814
p1	2.126 ± 0.0121
p2	3.03e+04 ± 4.924
p3	28.22 ± 0.009952

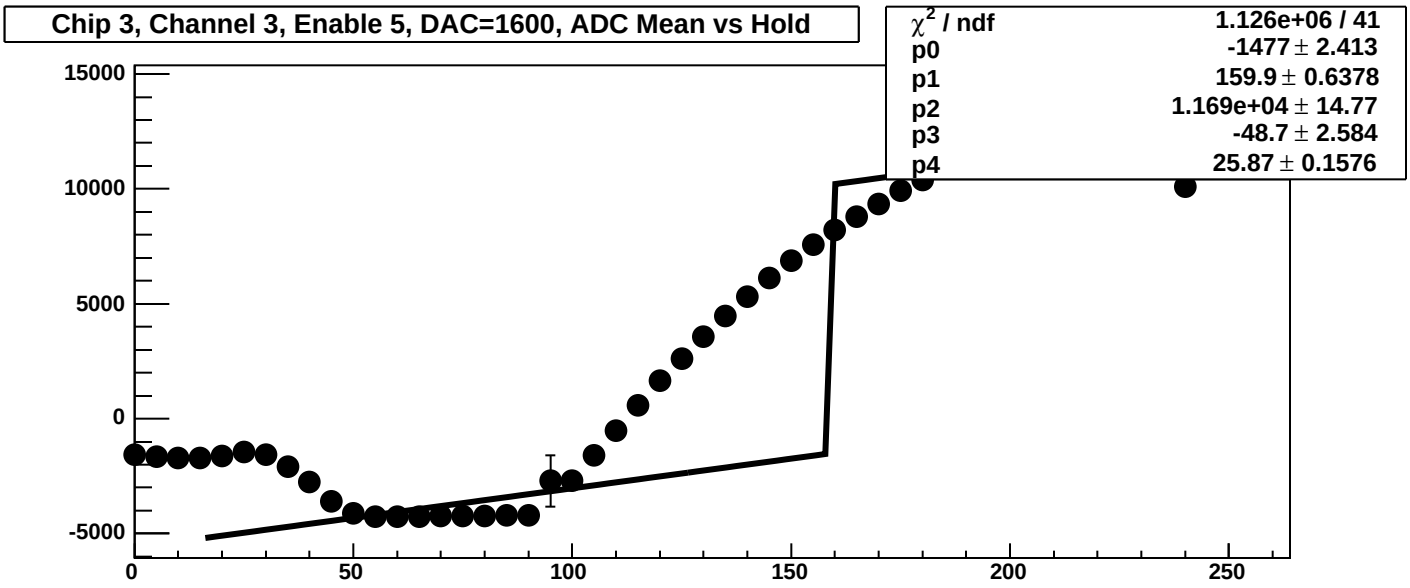


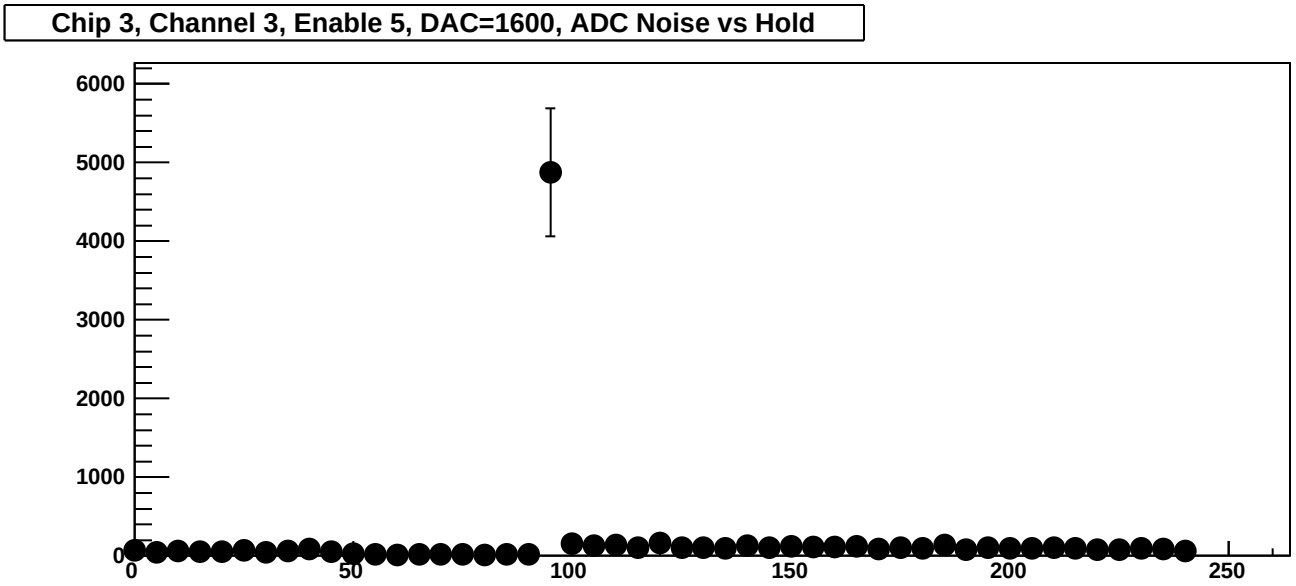
Chip 3, Channel 3, Enable 4!, DAC=1600, ADC Noise vs Hold

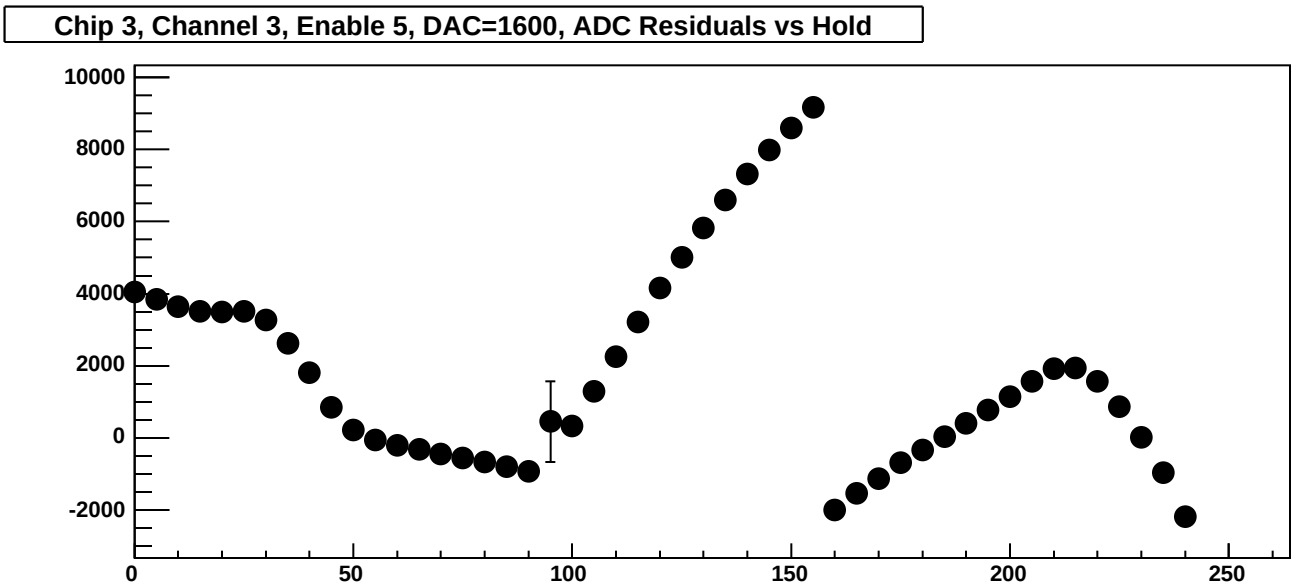


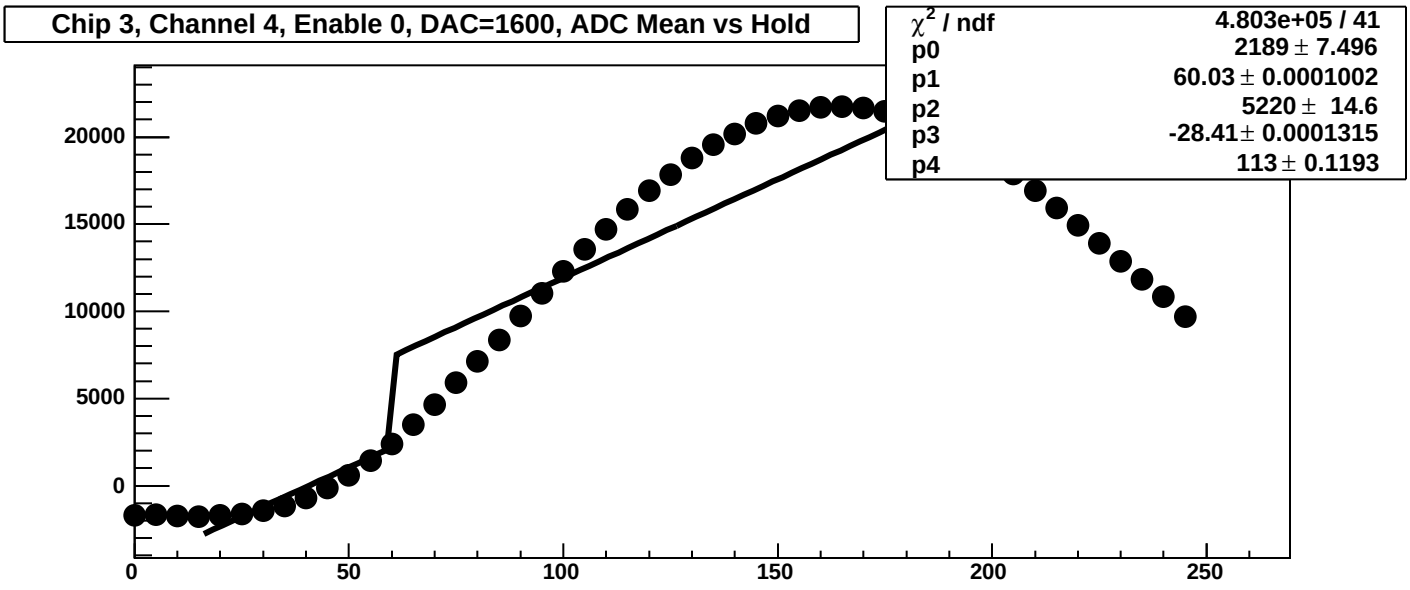
Chip 3, Channel 3, Enable 4!, DAC=1600, ADC Residuals vs Hold

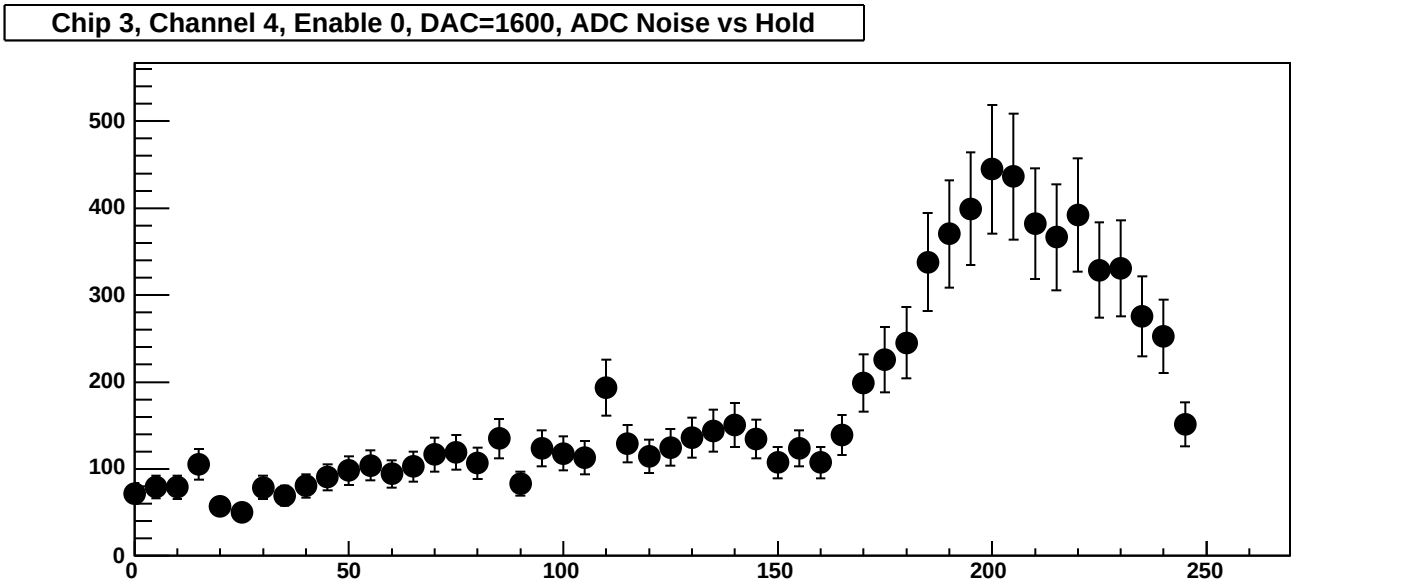


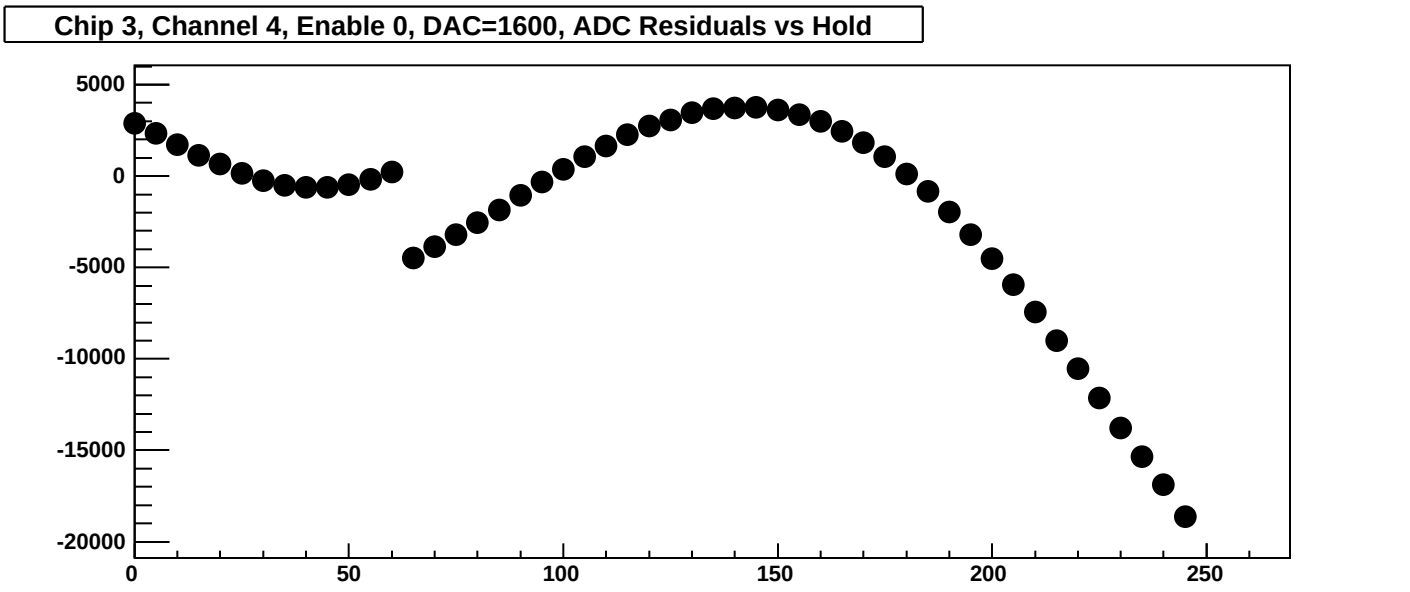

Chip 3, Channel 3, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.126e+06 / 41
p0 -1477 ± 2.413
p1 159.9 ± 0.6378
p2 1.169e+04 ± 14.77
p3 -48.7 ± 2.584
p4 25.87 ± 0.1576
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 3, Channel 3, Enable 5, DAC=1600, ADC Noise vs Hold
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

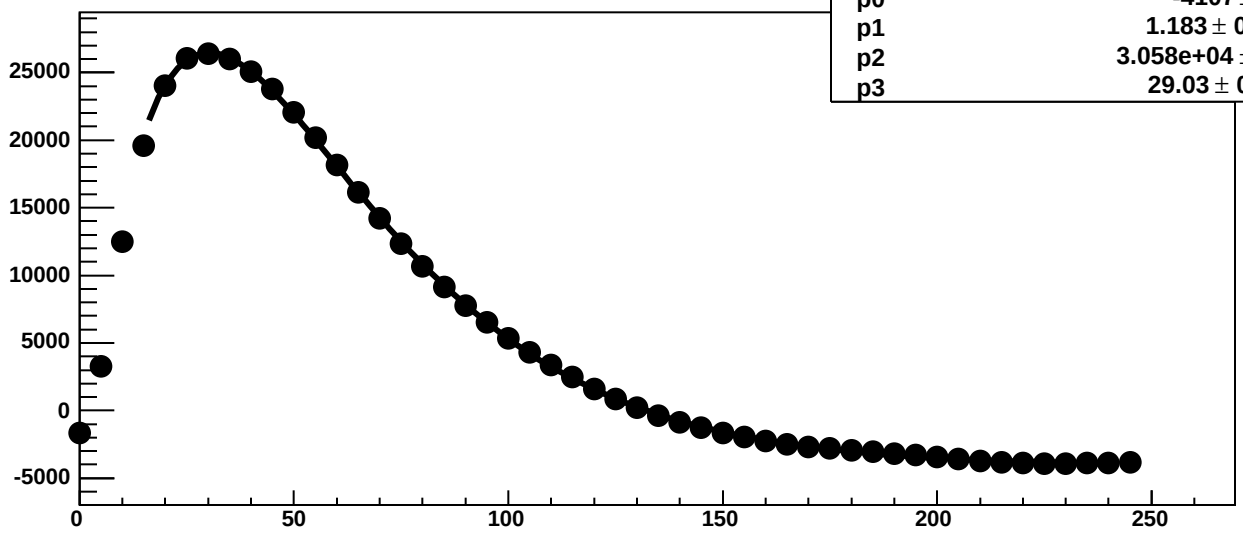

Chip 3, Channel 3, Enable 5, DAC=1600, ADC Residuals vs Hold
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250


Chip 3, Channel 4, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.803e+05 / 41
p0 2189 ± 7.496
p1 60.03 ± 0.0001002
p2 5220 ± 14.6
p3 -28.41 ± 0.0001315
p4 113 ± 0.1193
20000
15000
10000
5000
0
0
50
100
150
200
250

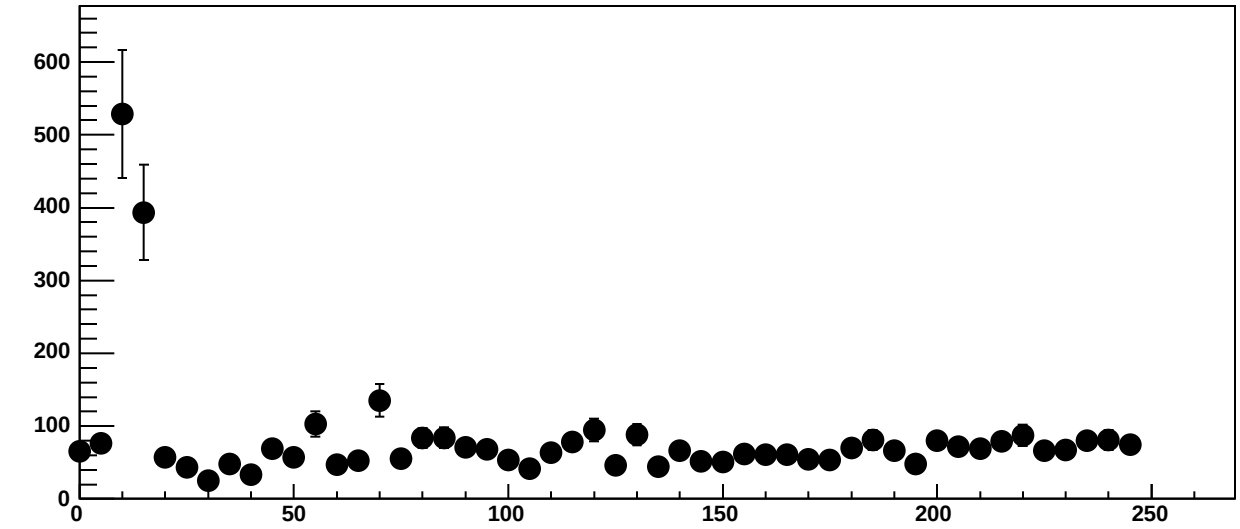

Chip 3, Channel 4, Enable 0, DAC=1600, ADC Noise vs Hold
500
400
300
200
100
0
0
50
100
150
200
250


Chip 3, Channel 4, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

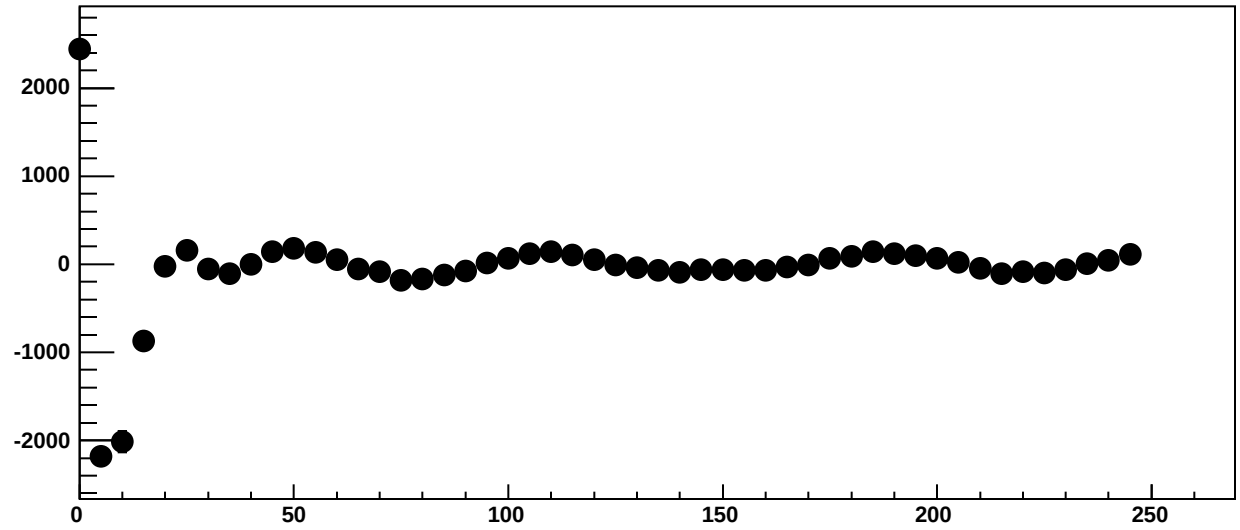
Chip 3, Channel 4, Enable 1!, DAC=1600, ADC Mean vs Hold



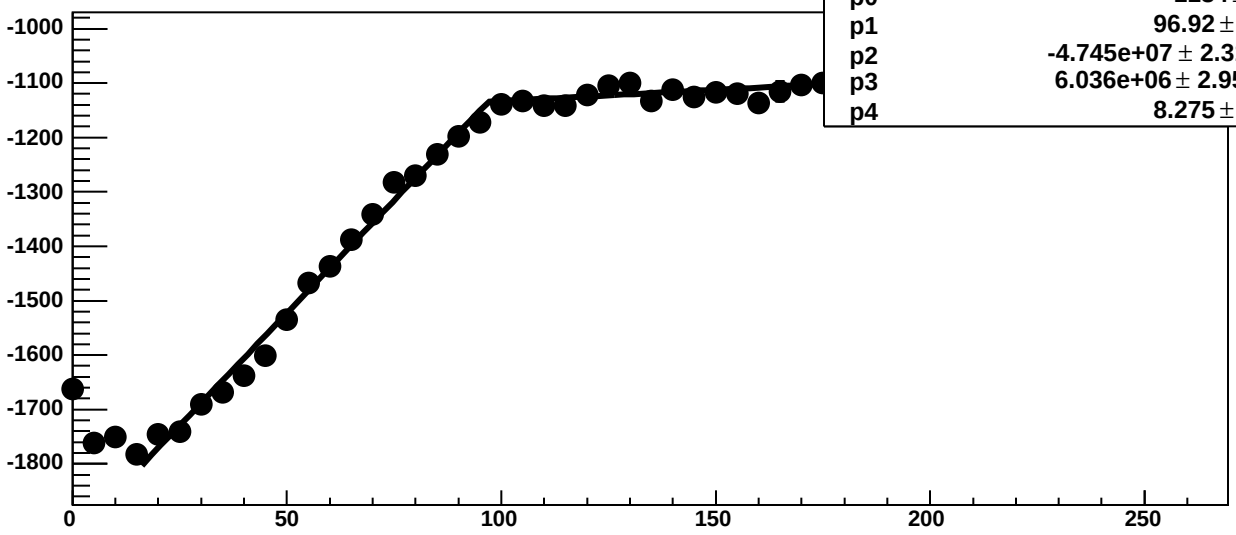
Chip 3, Channel 4, Enable 1!, DAC=1600, ADC Noise vs Hold



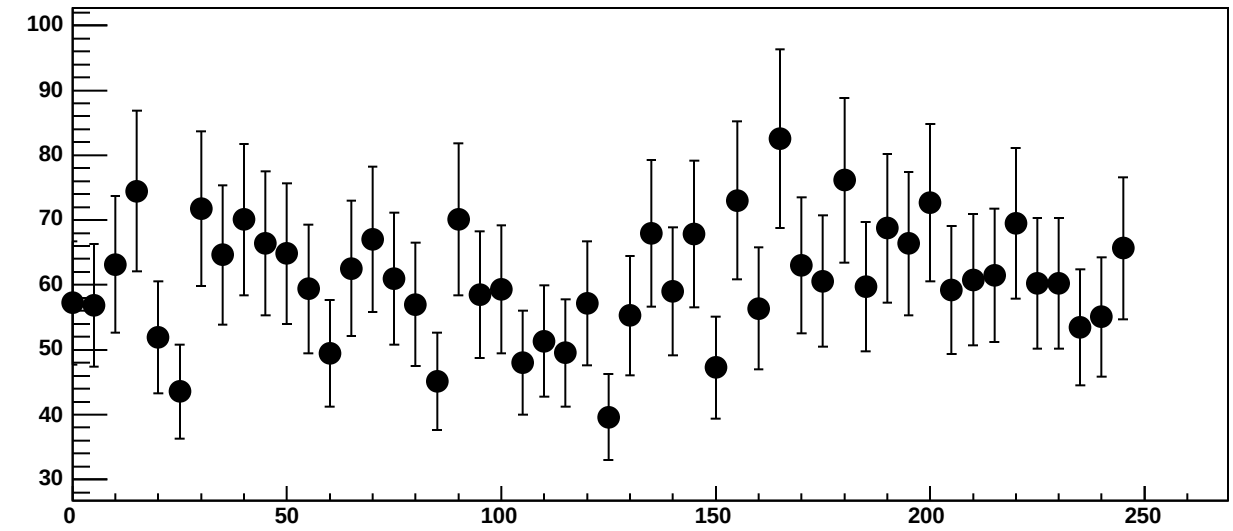
Chip 3, Channel 4, Enable 1!, DAC=1600, ADC Residuals vs Hold



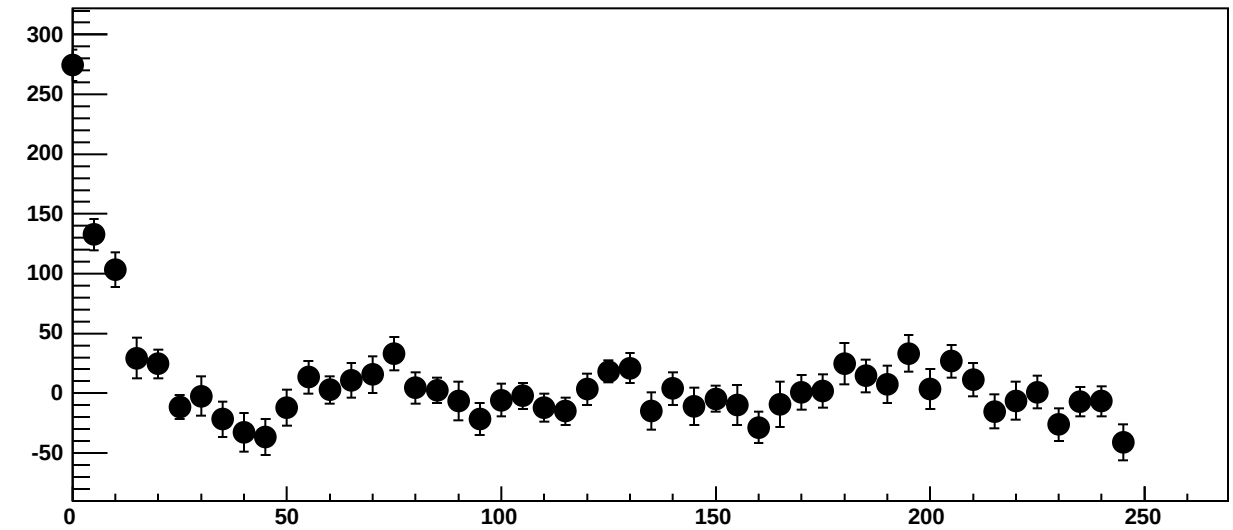
Chip 3, Channel 4, Enable 2, DAC=1600, ADC Mean vs Hold



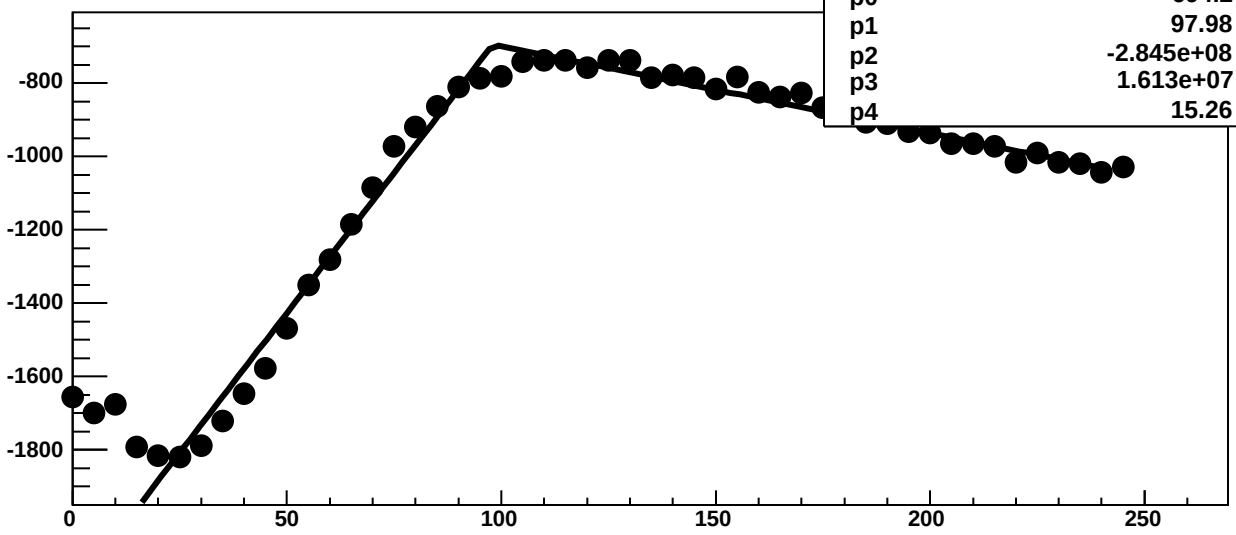
Chip 3, Channel 4, Enable 2, DAC=1600, ADC Noise vs Hold



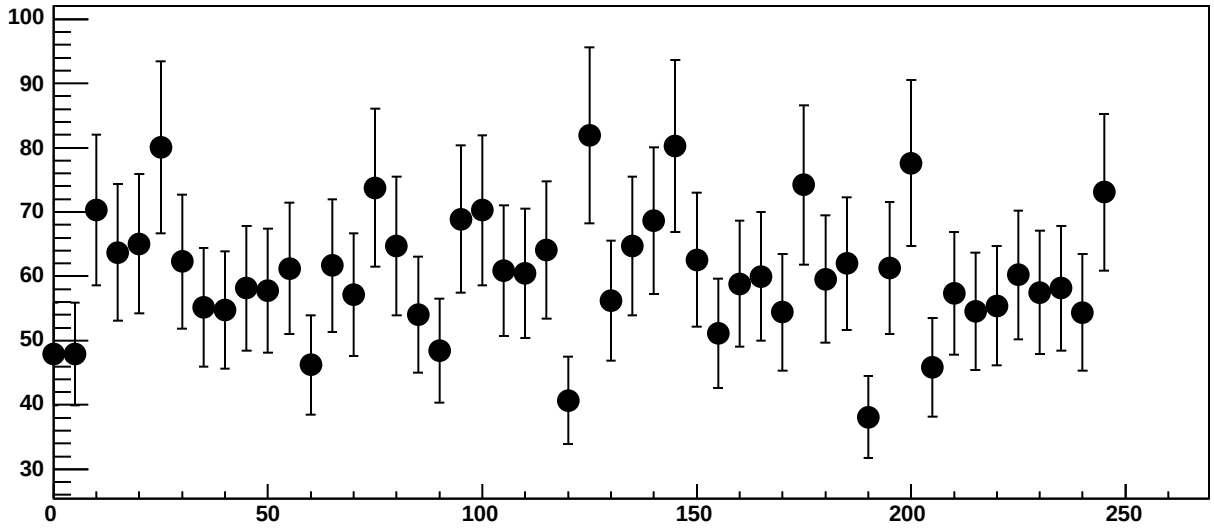
Chip 3, Channel 4, Enable 2, DAC=1600, ADC Residuals vs Hold



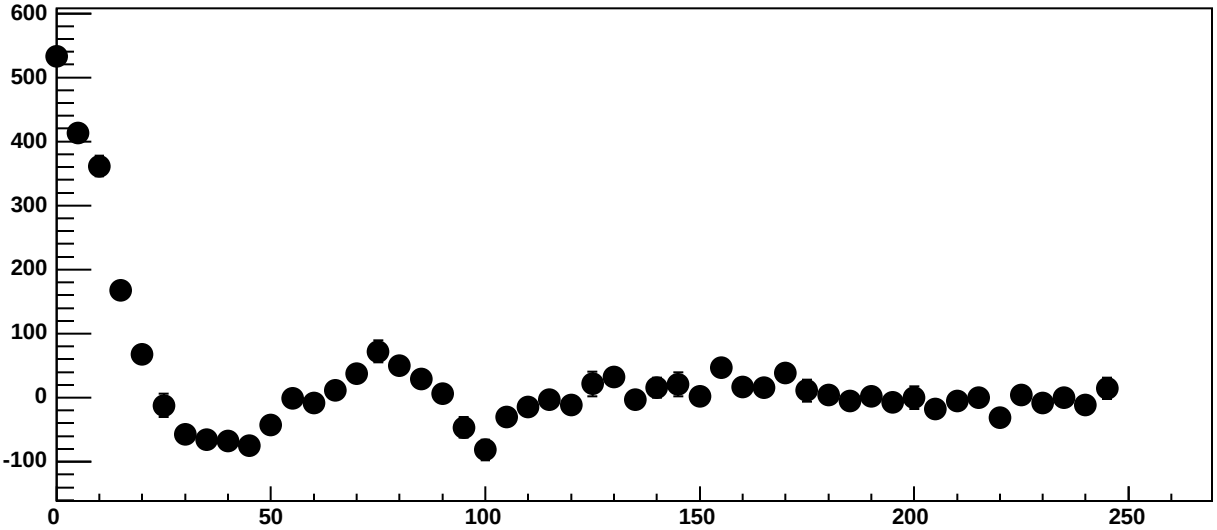
Chip 3, Channel 4, Enable 3, DAC=1600, ADC Mean vs Hold



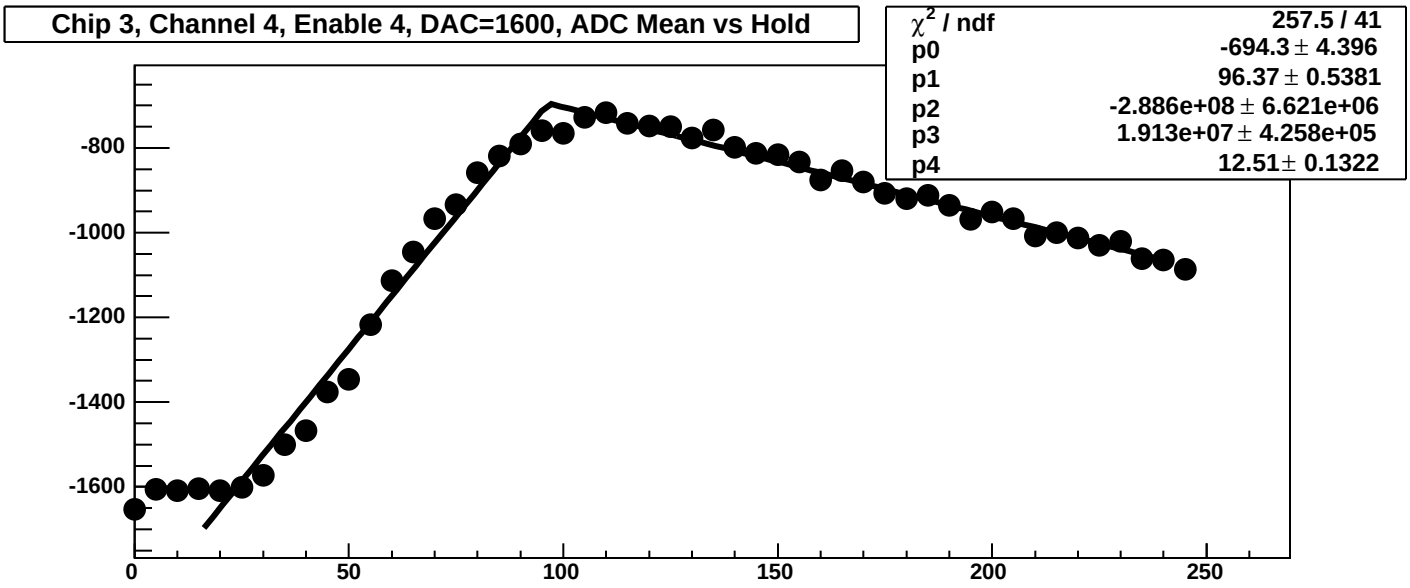
Chip 3, Channel 4, Enable 3, DAC=1600, ADC Noise vs Hold



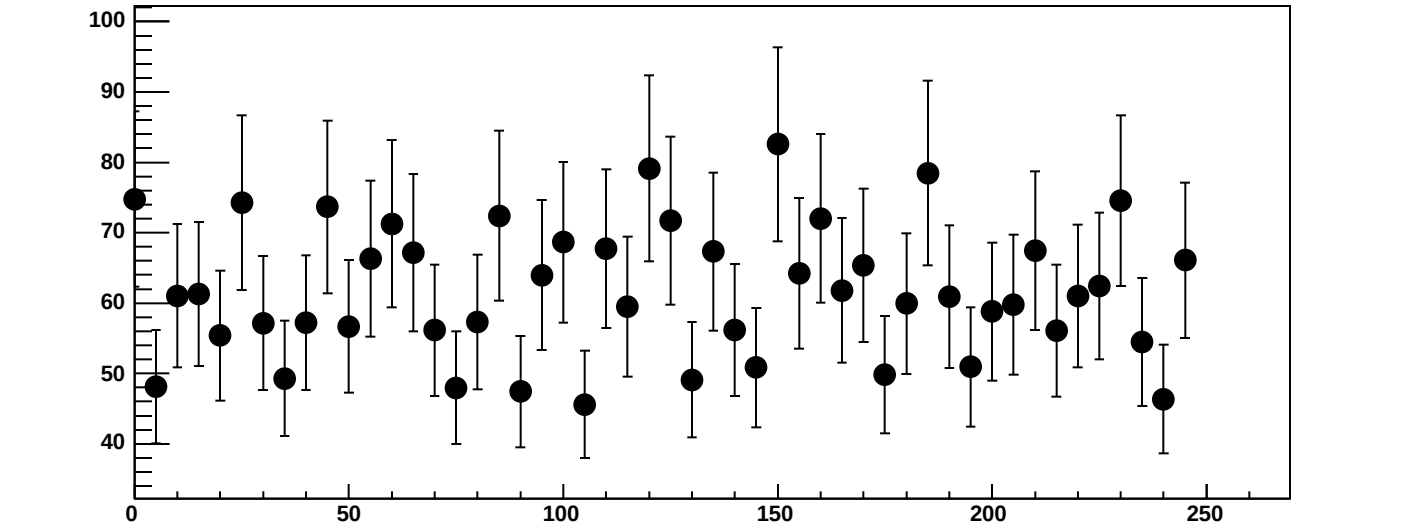
Chip 3, Channel 4, Enable 3, DAC=1600, ADC Residuals vs Hold



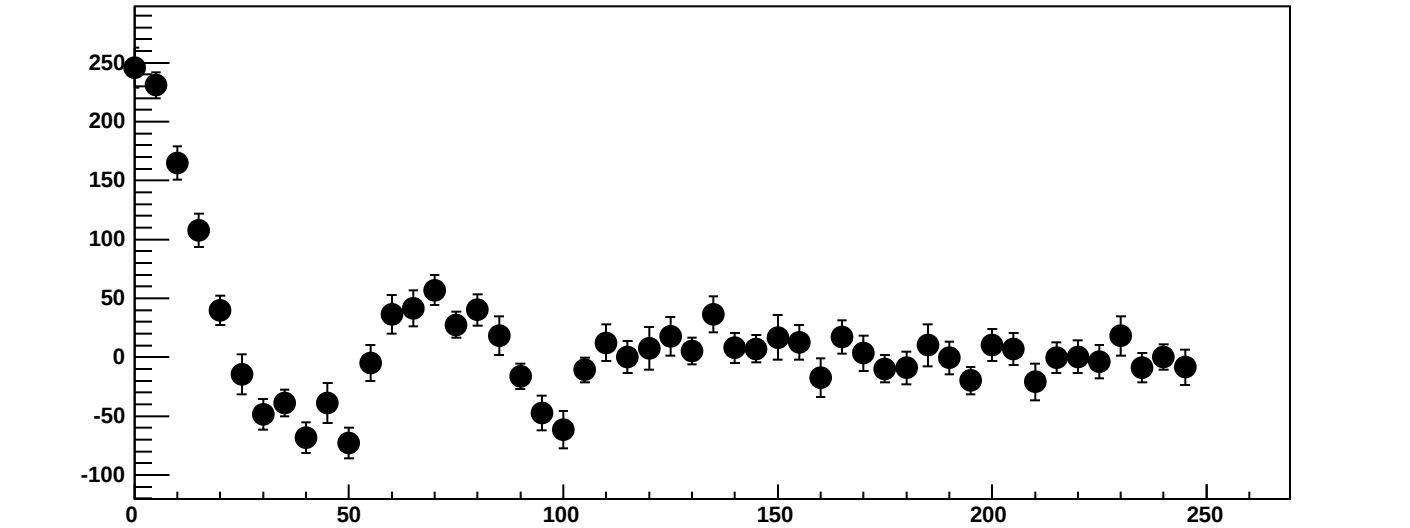
Chip 3, Channel 4, Enable 4, DAC=1600, ADC Mean vs Hold



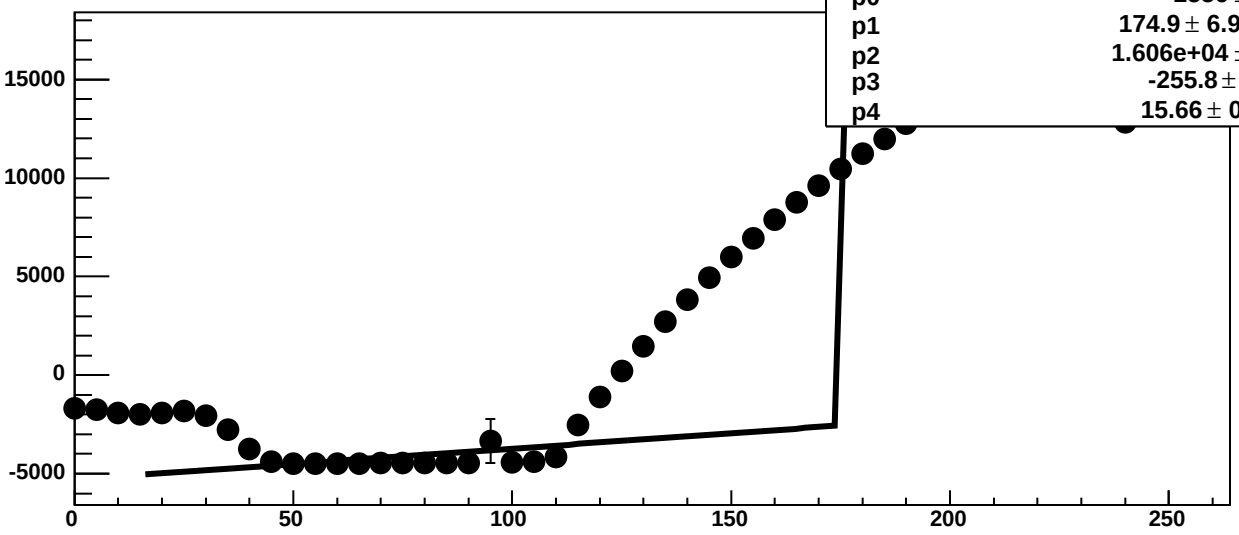
Chip 3, Channel 4, Enable 4, DAC=1600, ADC Noise vs Hold



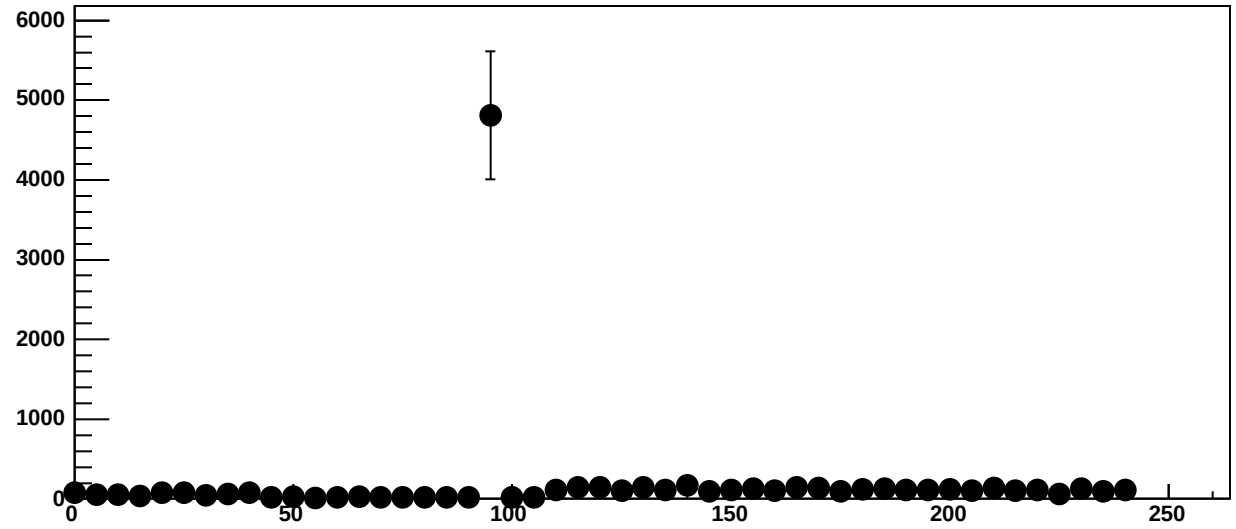
Chip 3, Channel 4, Enable 4, DAC=1600, ADC Residuals vs Hold



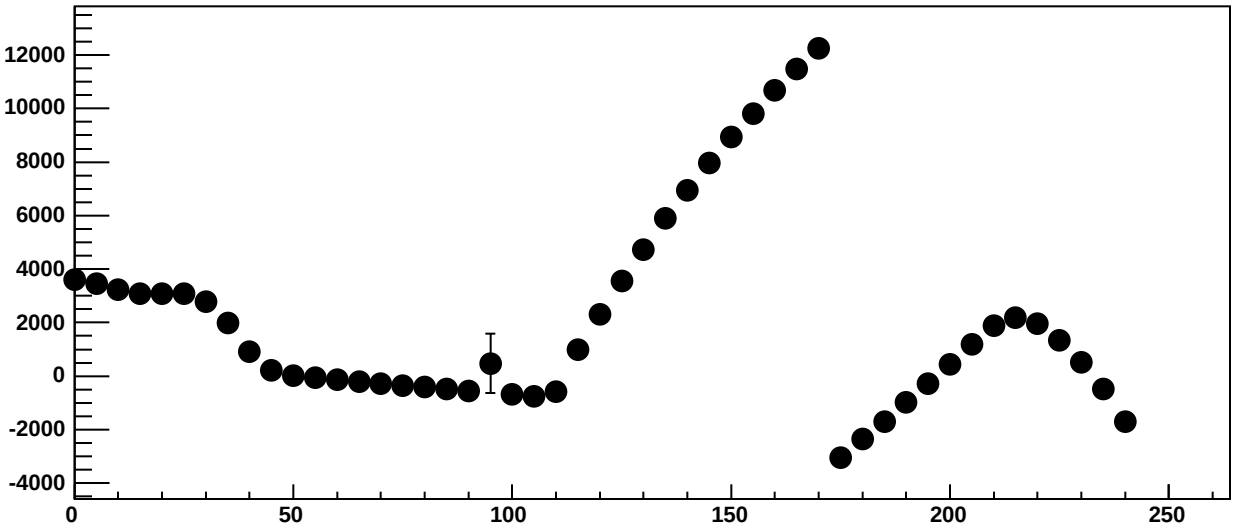
Chip 3, Channel 4, Enable 5, DAC=1600, ADC Mean vs Hold



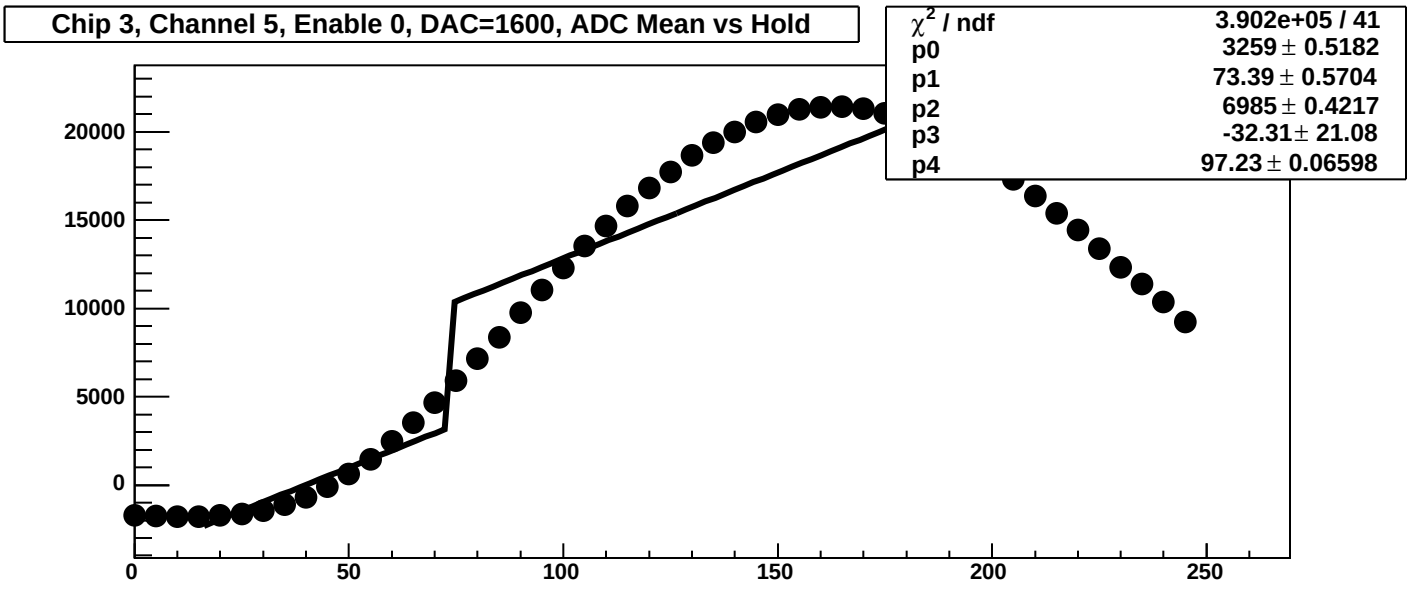
Chip 3, Channel 4, Enable 5, DAC=1600, ADC Noise vs Hold



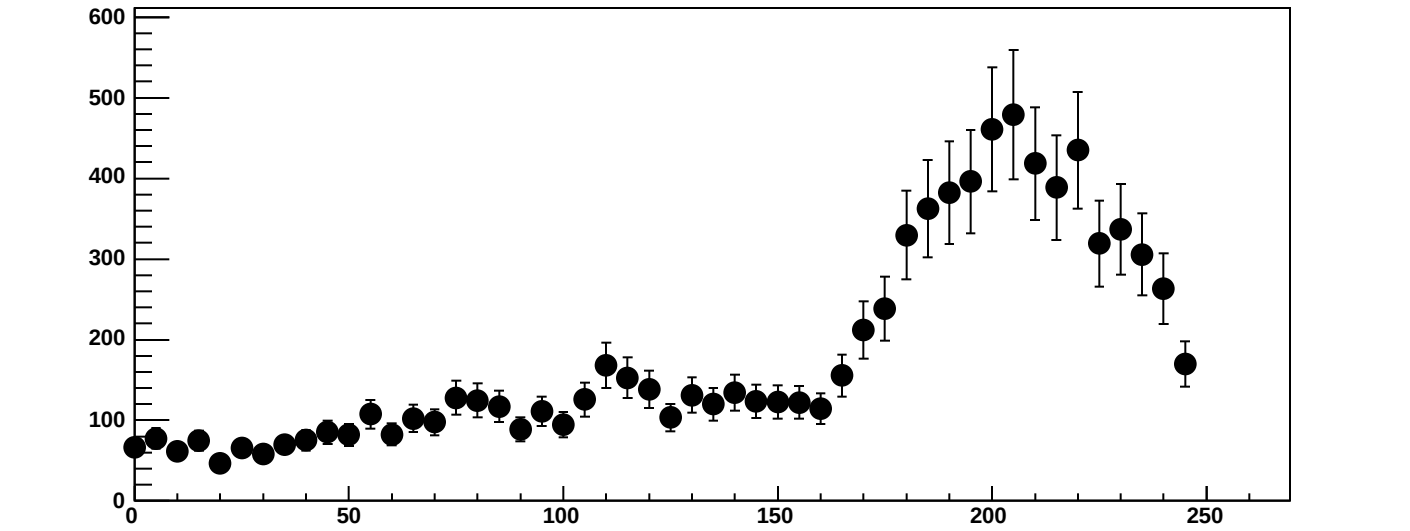
Chip 3, Channel 4, Enable 5, DAC=1600, ADC Residuals vs Hold



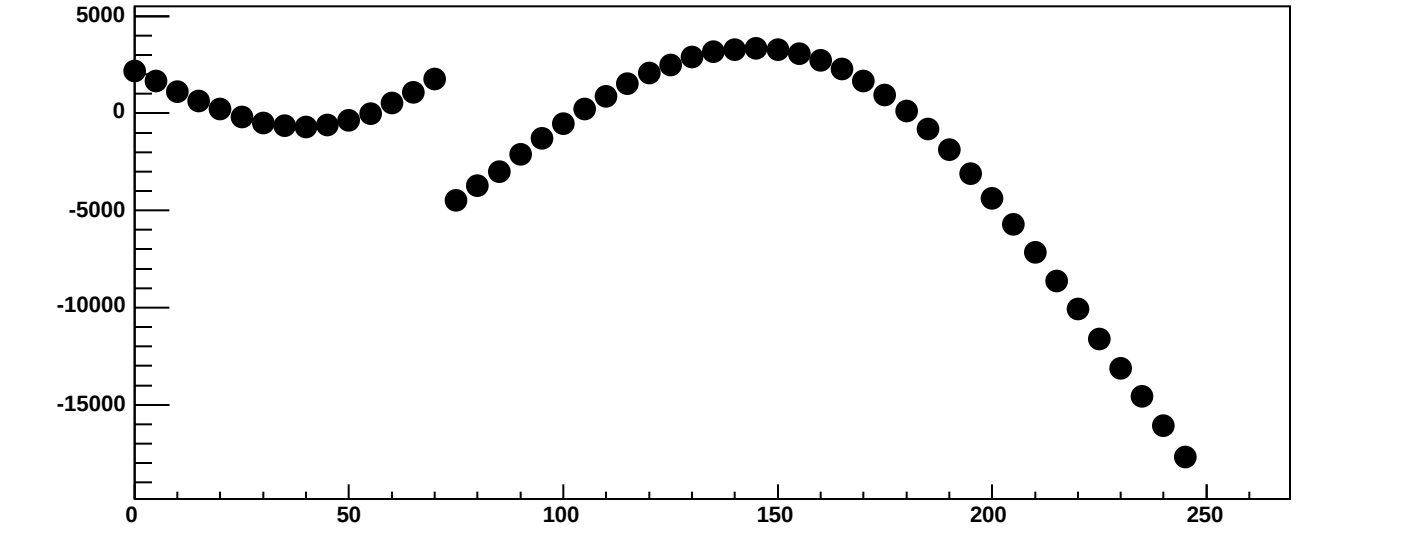
Chip 3, Channel 5, Enable 0, DAC=1600, ADC Mean vs Hold



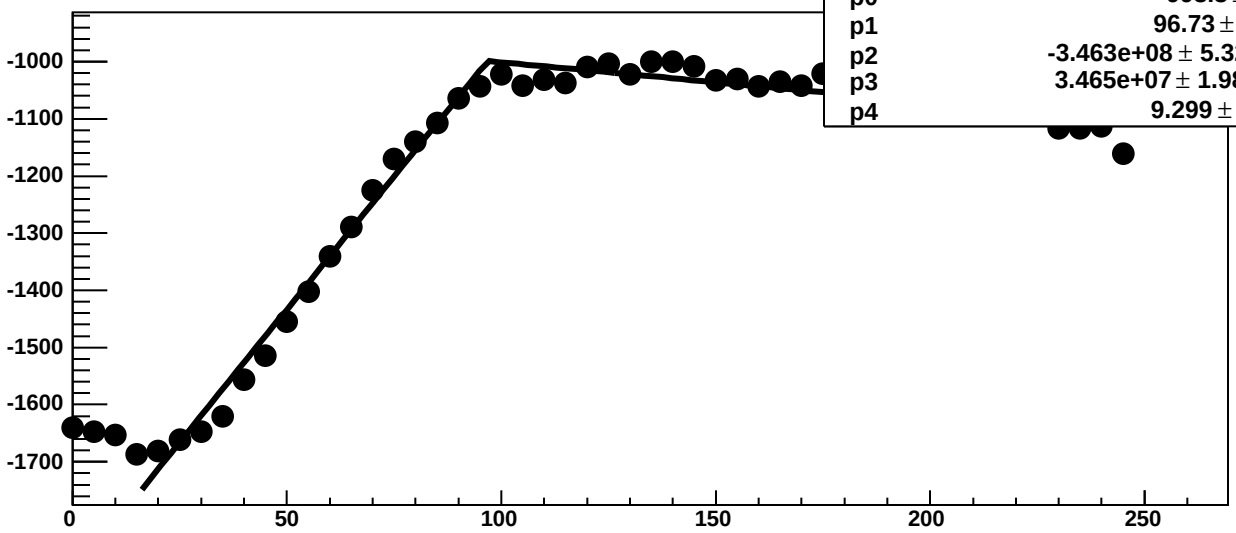
Chip 3, Channel 5, Enable 0, DAC=1600, ADC Noise vs Hold



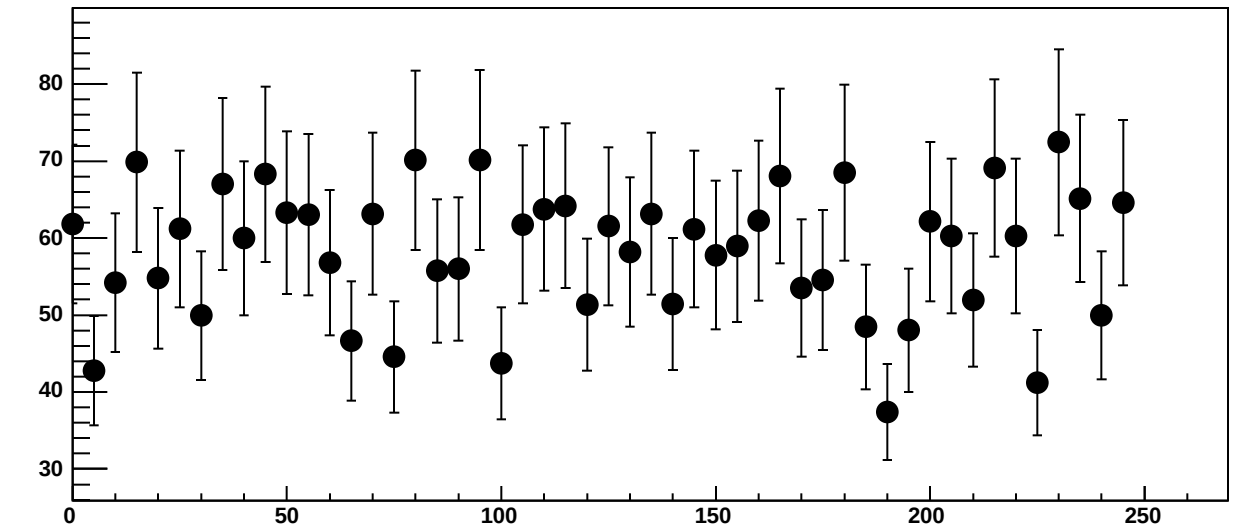
Chip 3, Channel 5, Enable 0, DAC=1600, ADC Residuals vs Hold



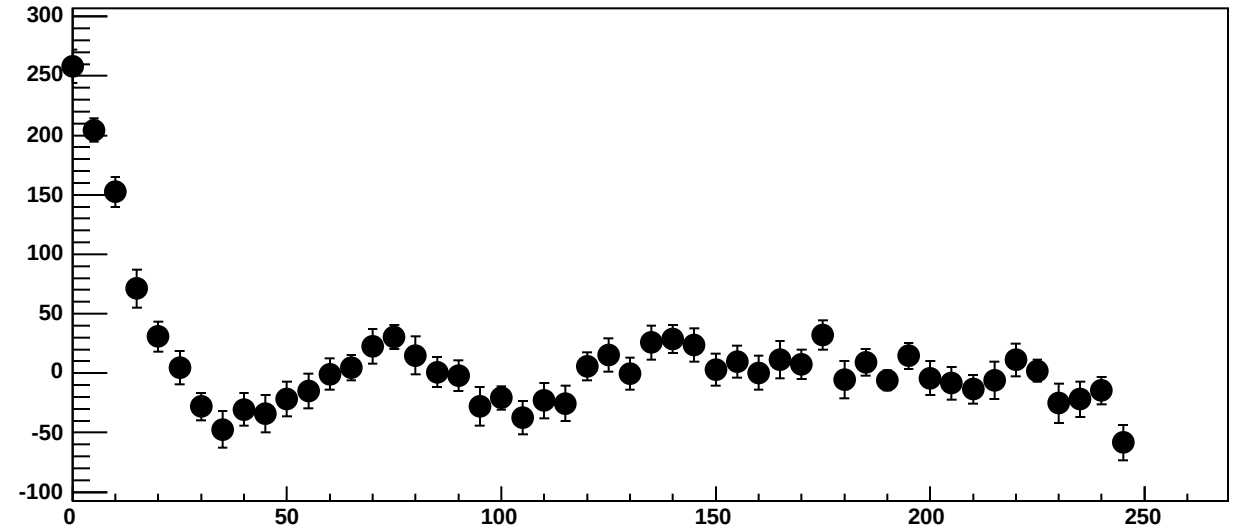
Chip 3, Channel 5, Enable 1, DAC=1600, ADC Mean vs Hold



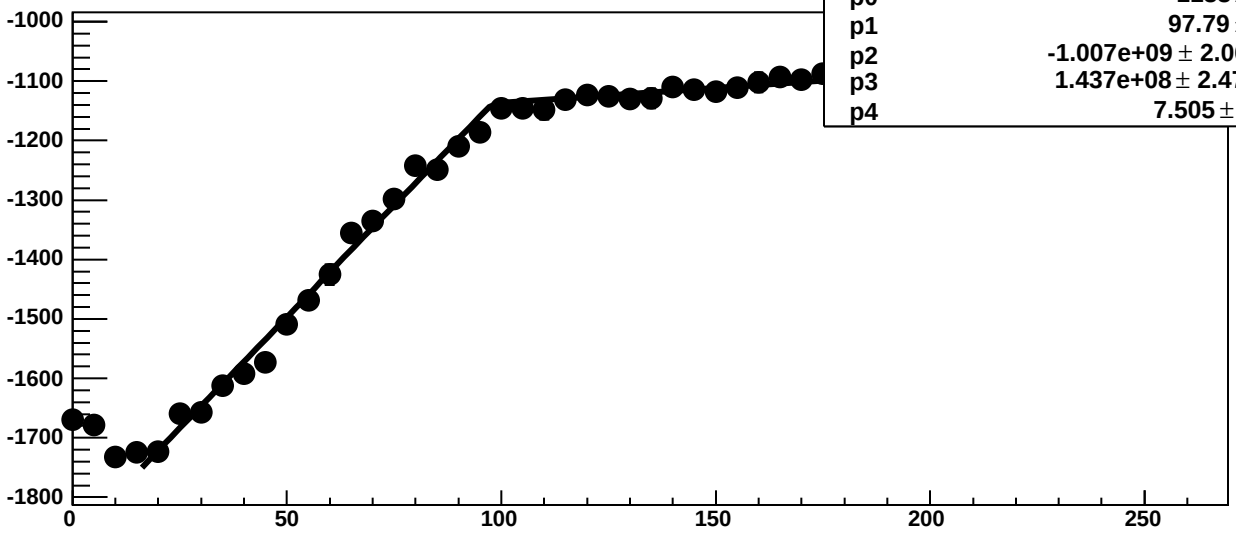
Chip 3, Channel 5, Enable 1, DAC=1600, ADC Noise vs Hold



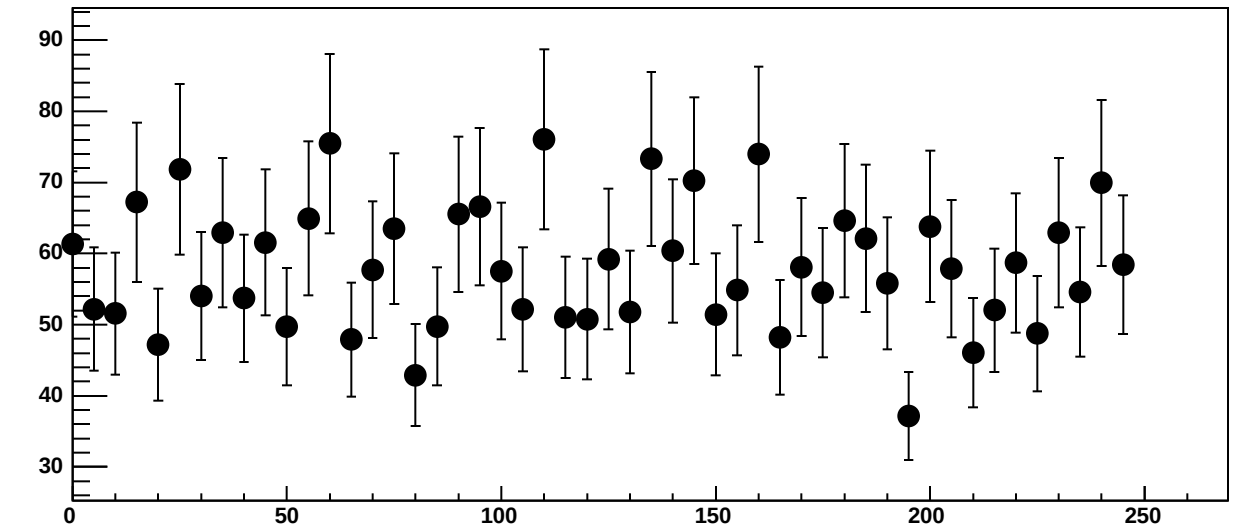
Chip 3, Channel 5, Enable 1, DAC=1600, ADC Residuals vs Hold



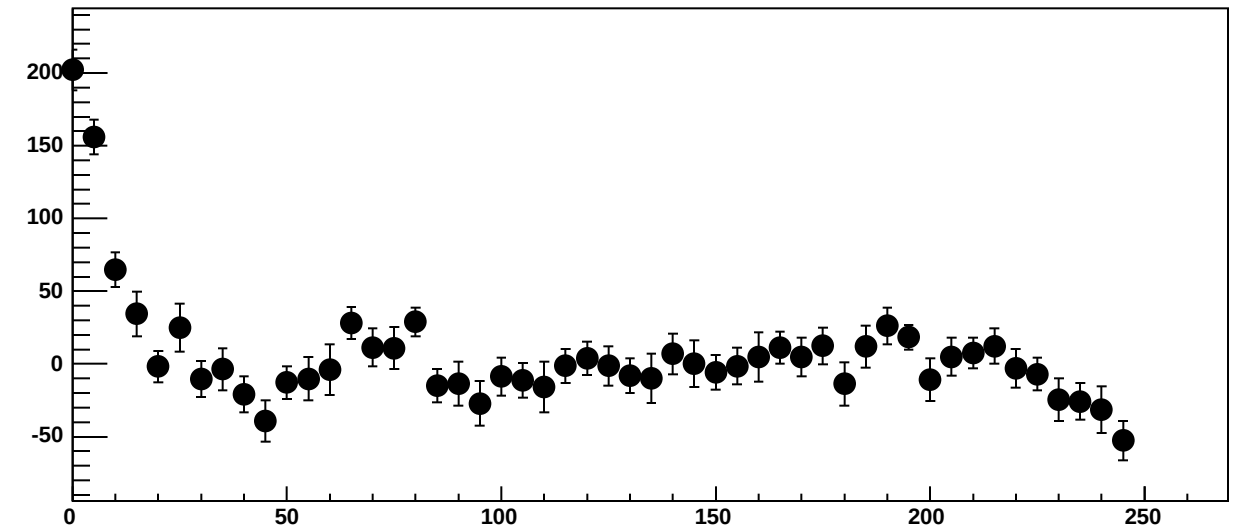
Chip 3, Channel 5, Enable 2, DAC=1600, ADC Mean vs Hold



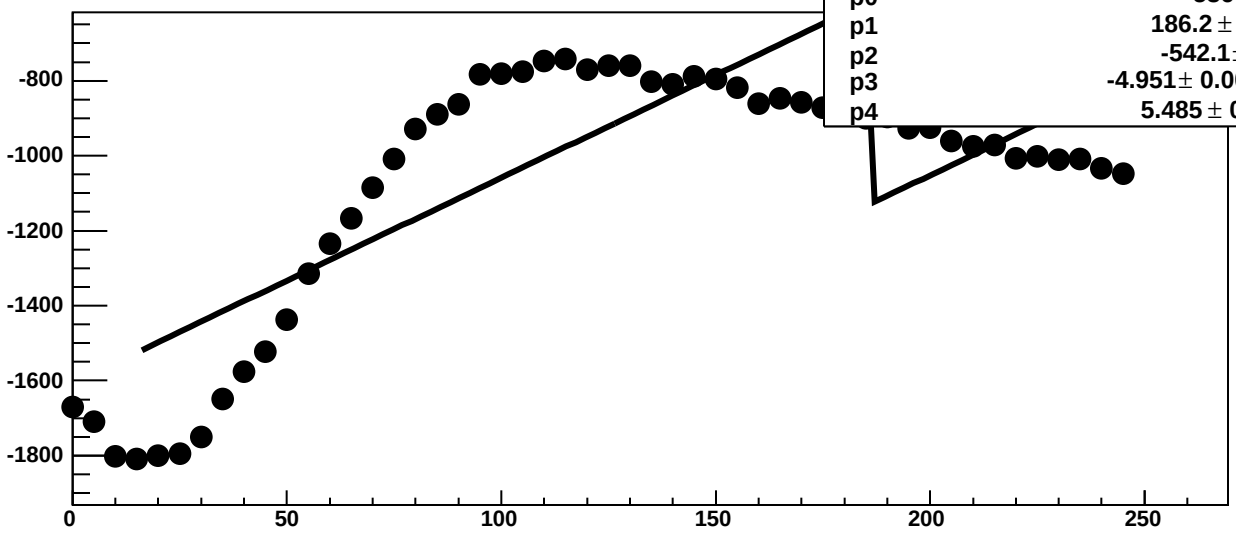
Chip 3, Channel 5, Enable 2, DAC=1600, ADC Noise vs Hold



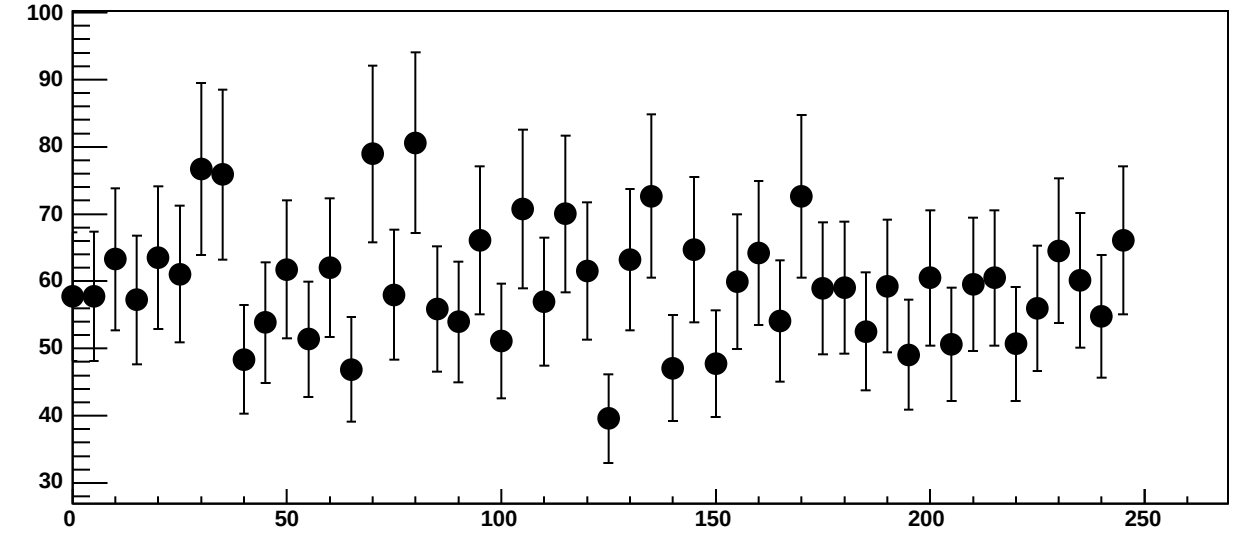
Chip 3, Channel 5, Enable 2, DAC=1600, ADC Residuals vs Hold



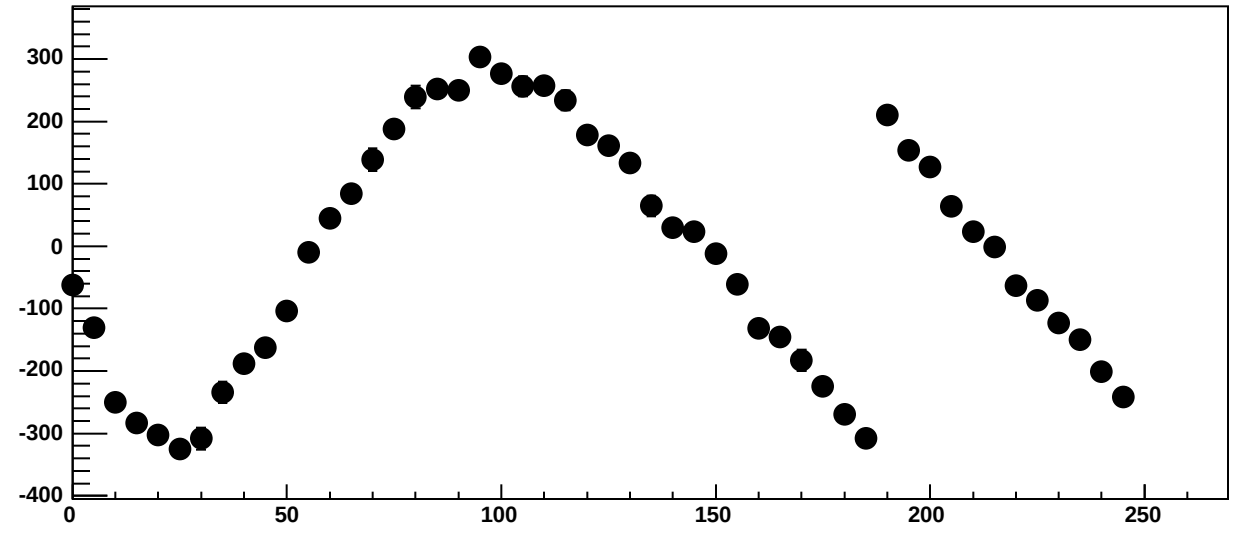
Chip 3, Channel 5, Enable 3, DAC=1600, ADC Mean vs Hold



Chip 3, Channel 5, Enable 3, DAC=1600, ADC Noise vs Hold

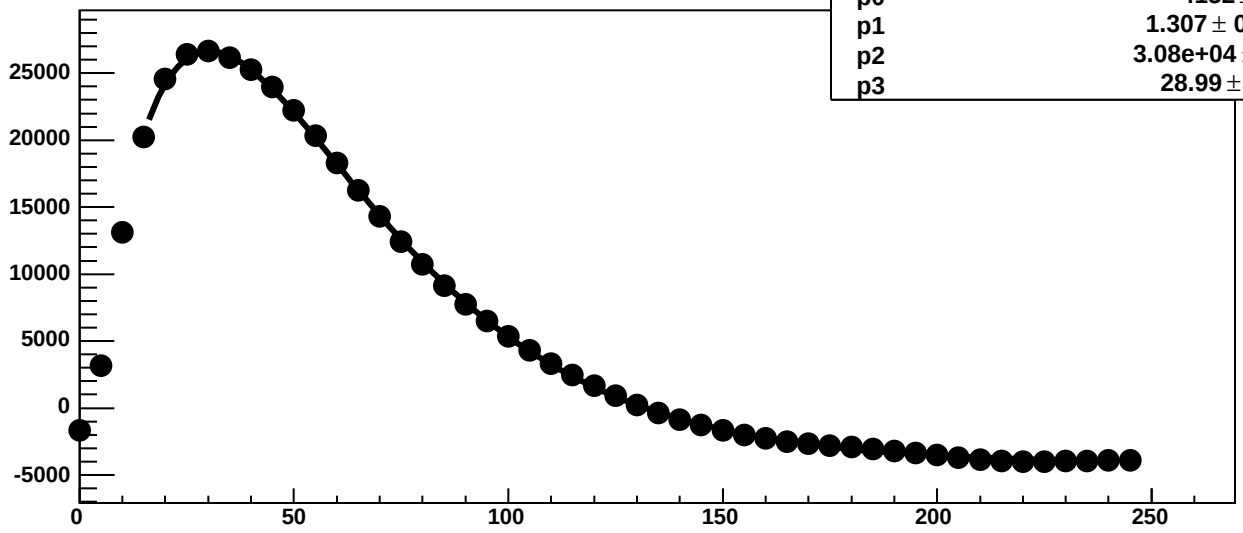


Chip 3, Channel 5, Enable 3, DAC=1600, ADC Residuals vs Hold

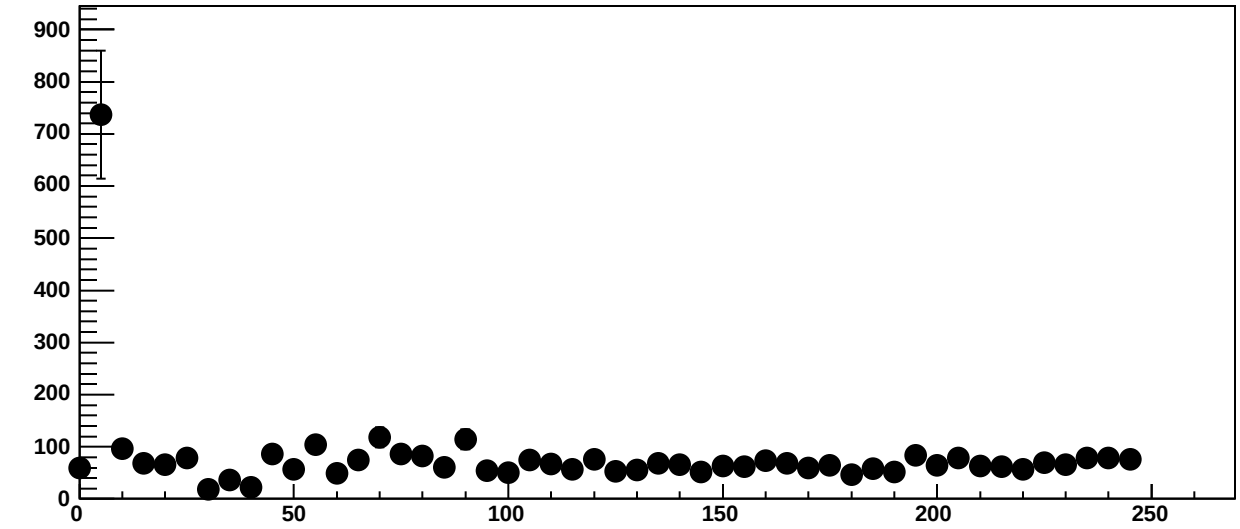


Chip 3, Channel 5, Enable 4!, DAC=1600, ADC Mean vs Hold

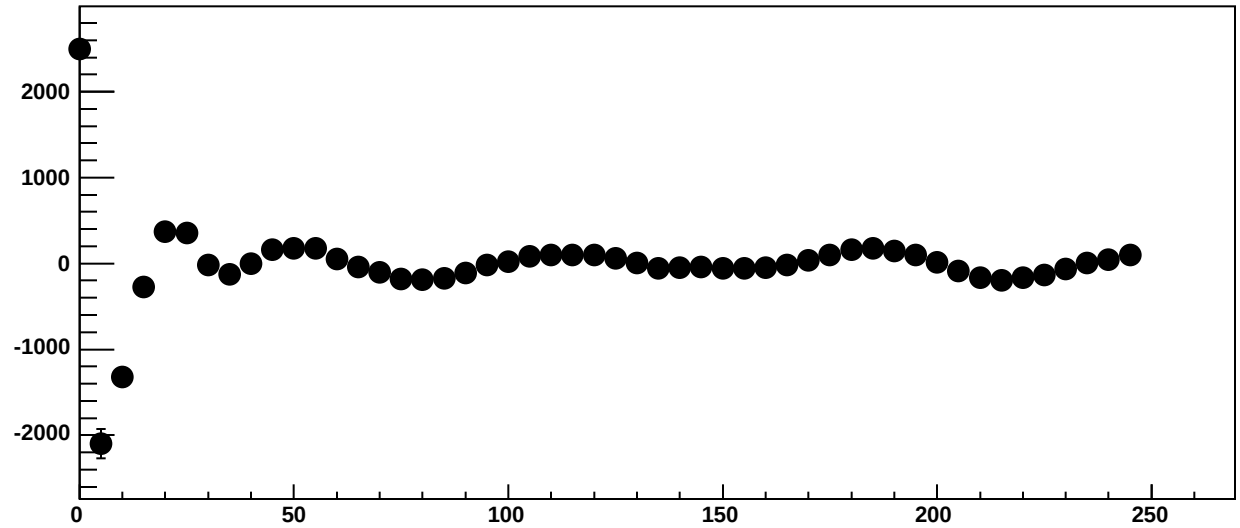
χ^2 / ndf	3690 / 42
p0	-4152 ± 4.142
p1	1.307 ± 0.01668
p2	3.08e+04 ± 4.721
p3	28.99 ± 0.0109

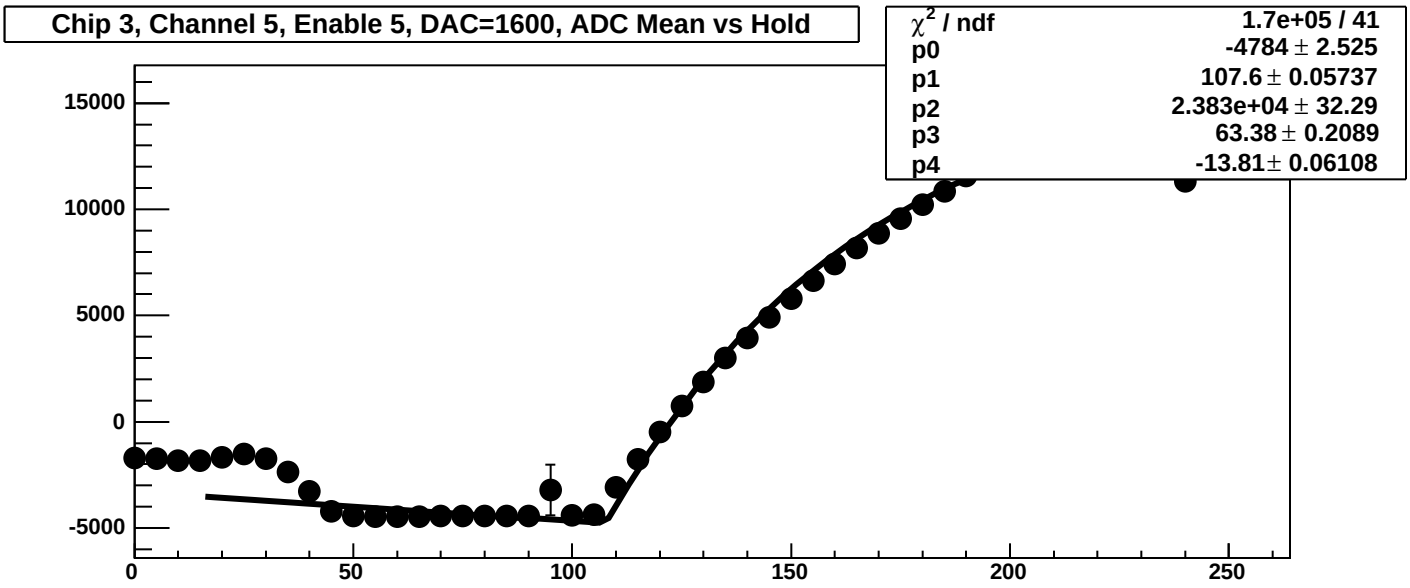


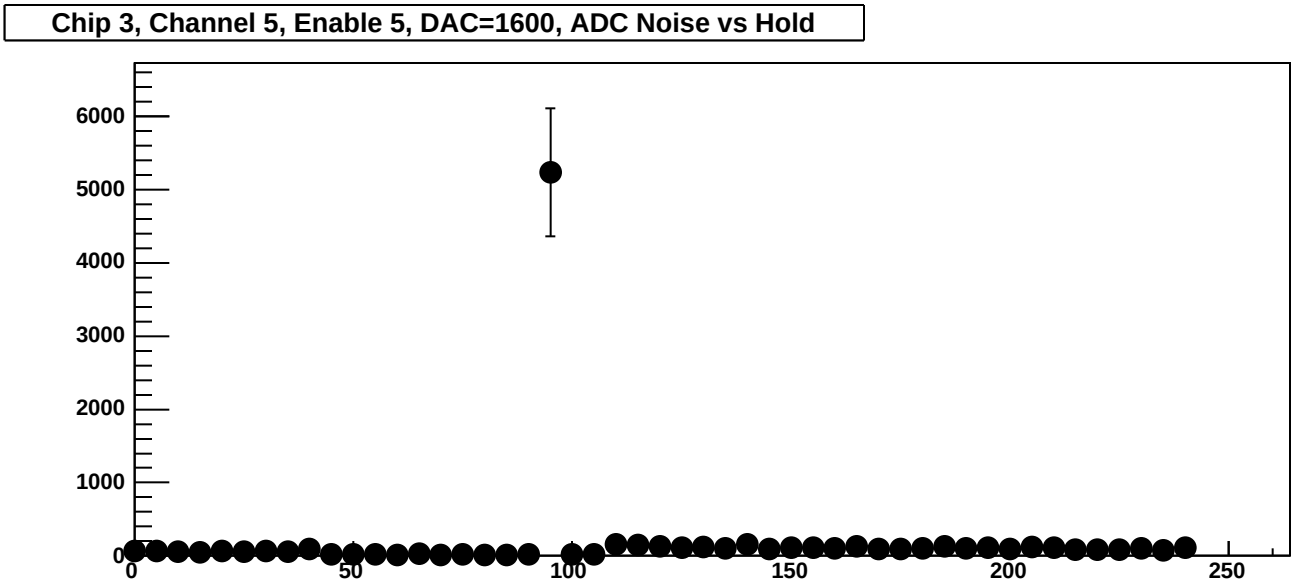
Chip 3, Channel 5, Enable 4!, DAC=1600, ADC Noise vs Hold

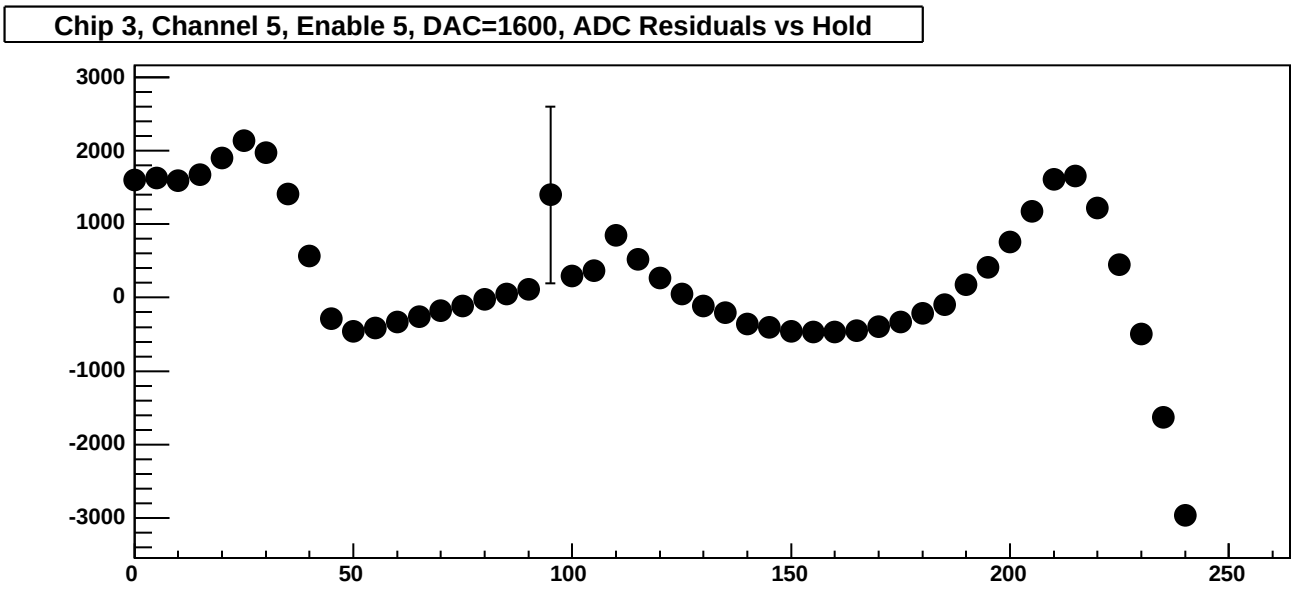


Chip 3, Channel 5, Enable 4!, DAC=1600, ADC Residuals vs Hold

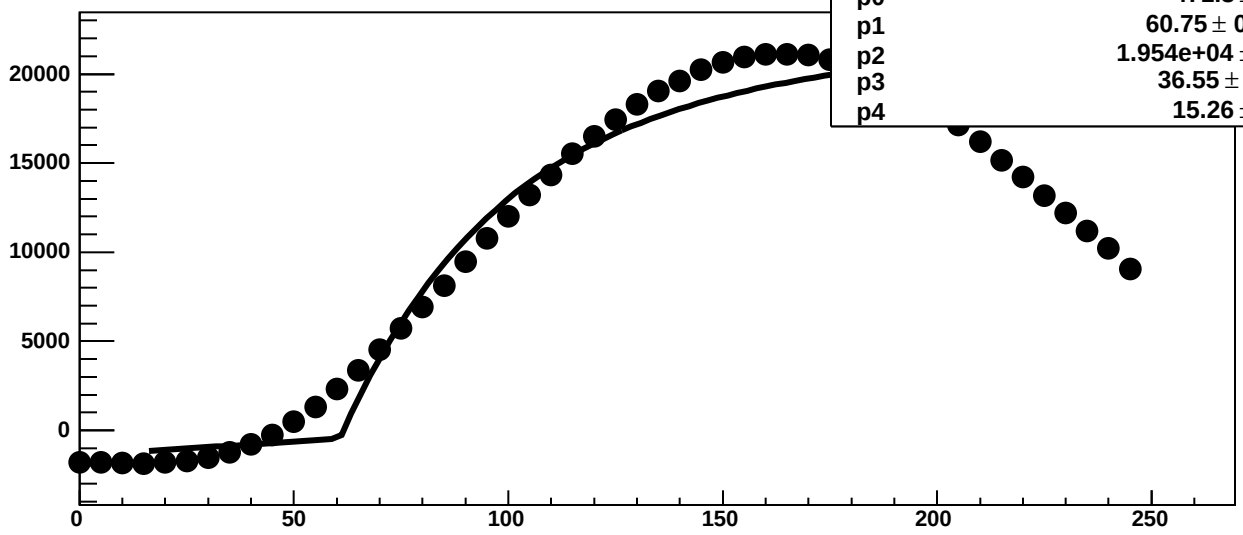



Chip 3, Channel 5, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.7e+05 / 41
p0 -4784 ± 2.525
p1 107.6 ± 0.05737
p2 2.383e+04 ± 32.29
p3 63.38 ± 0.2089
p4 -13.81 ± 0.06108
15000
10000
5000
0
-5000
0
50
100
150
200
250

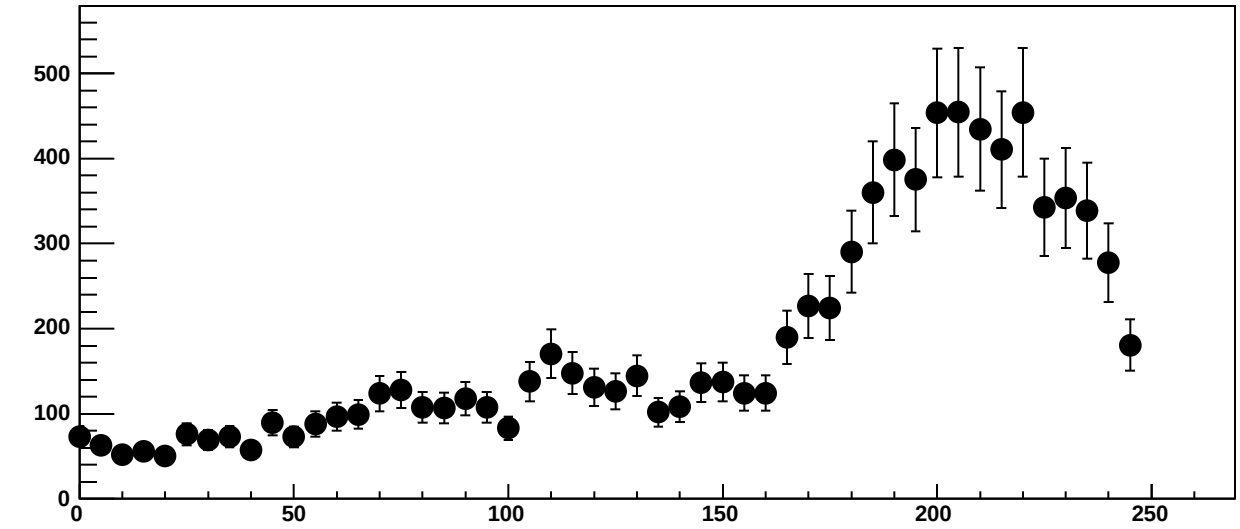

Chip 3, Channel 5, Enable 5, DAC=1600, ADC Noise vs Hold
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 3, Channel 5, Enable 5, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

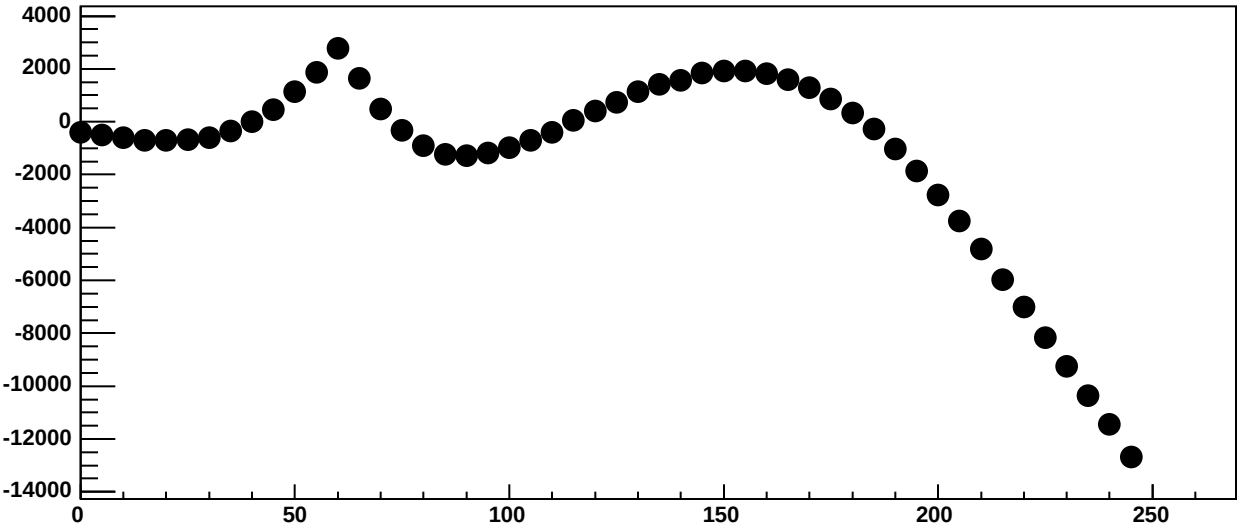
Chip 3, Channel 6, Enable 0, DAC=1600, ADC Mean vs Hold



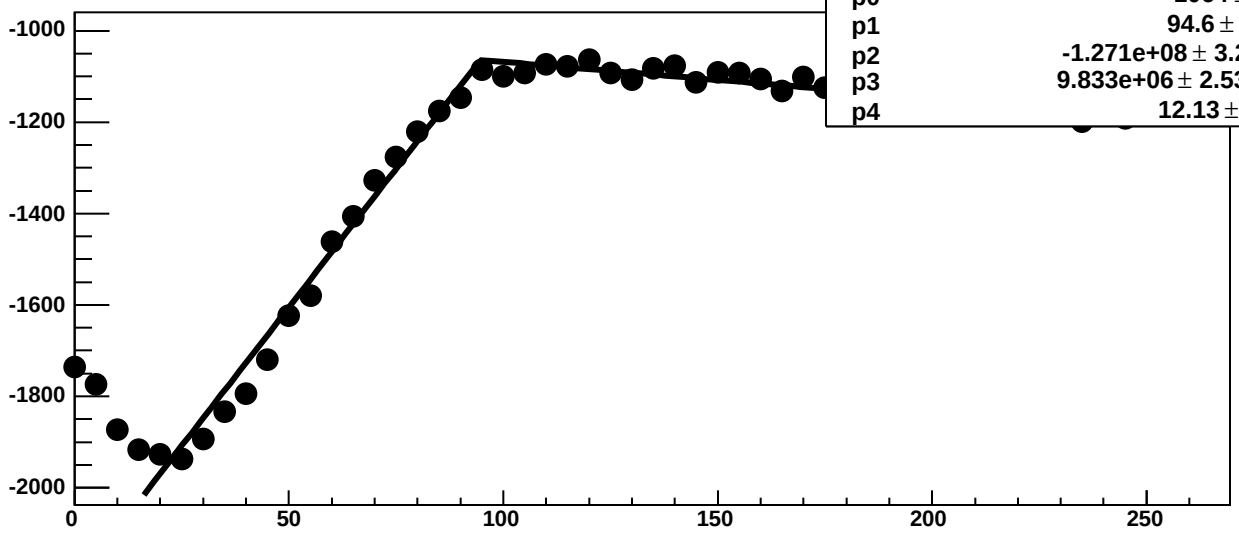
Chip 3, Channel 6, Enable 0, DAC=1600, ADC Noise vs Hold



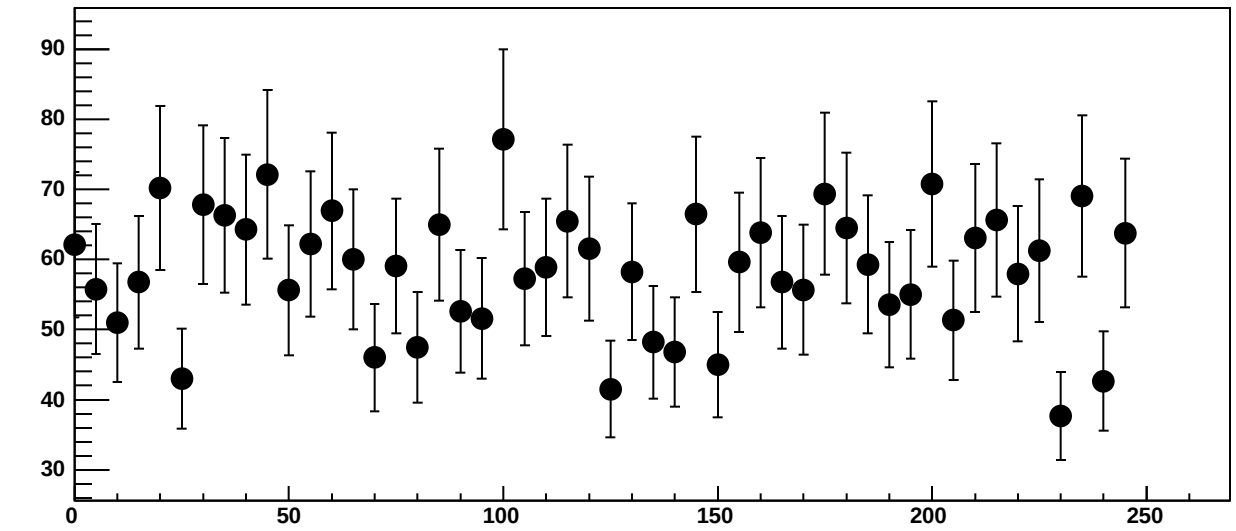
Chip 3, Channel 6, Enable 0, DAC=1600, ADC Residuals vs Hold



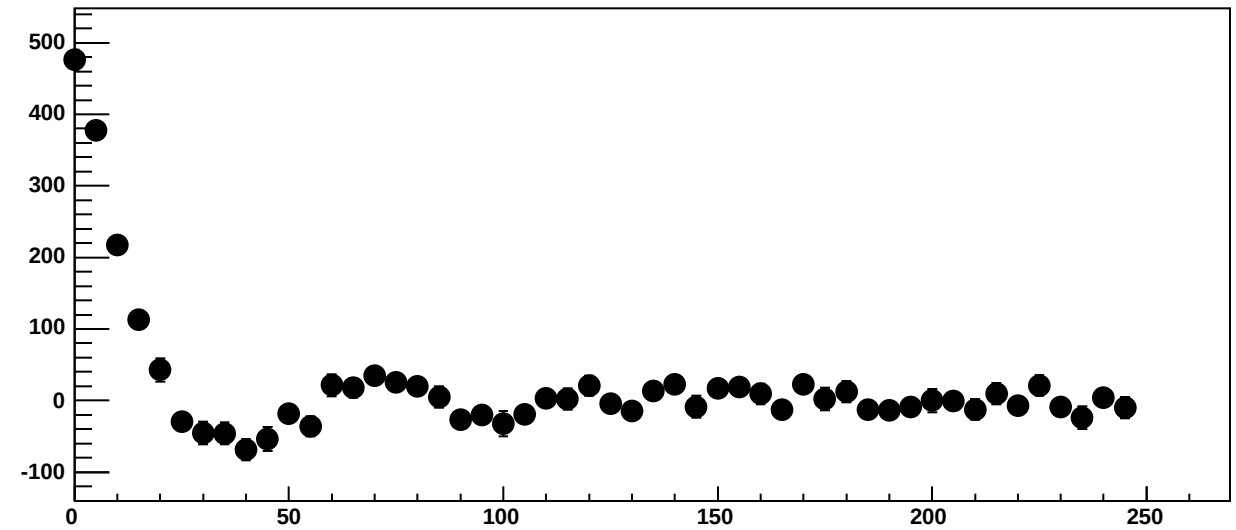
Chip 3, Channel 6, Enable 1, DAC=1600, ADC Mean vs Hold



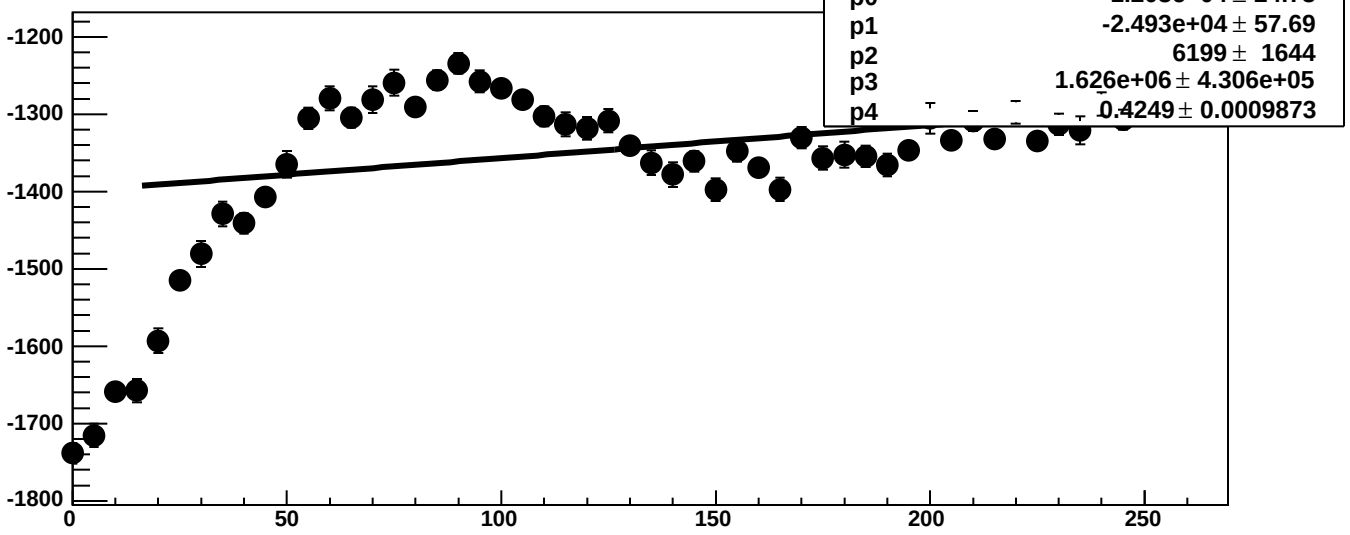
Chip 3, Channel 6, Enable 1, DAC=1600, ADC Noise vs Hold



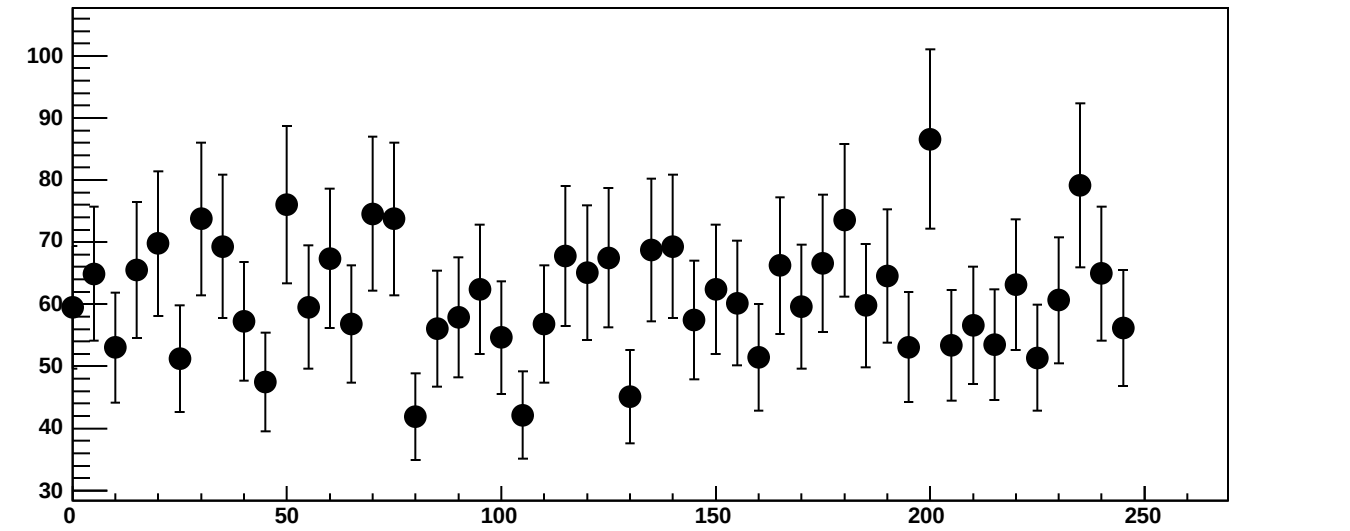
Chip 3, Channel 6, Enable 1, DAC=1600, ADC Residuals vs Hold



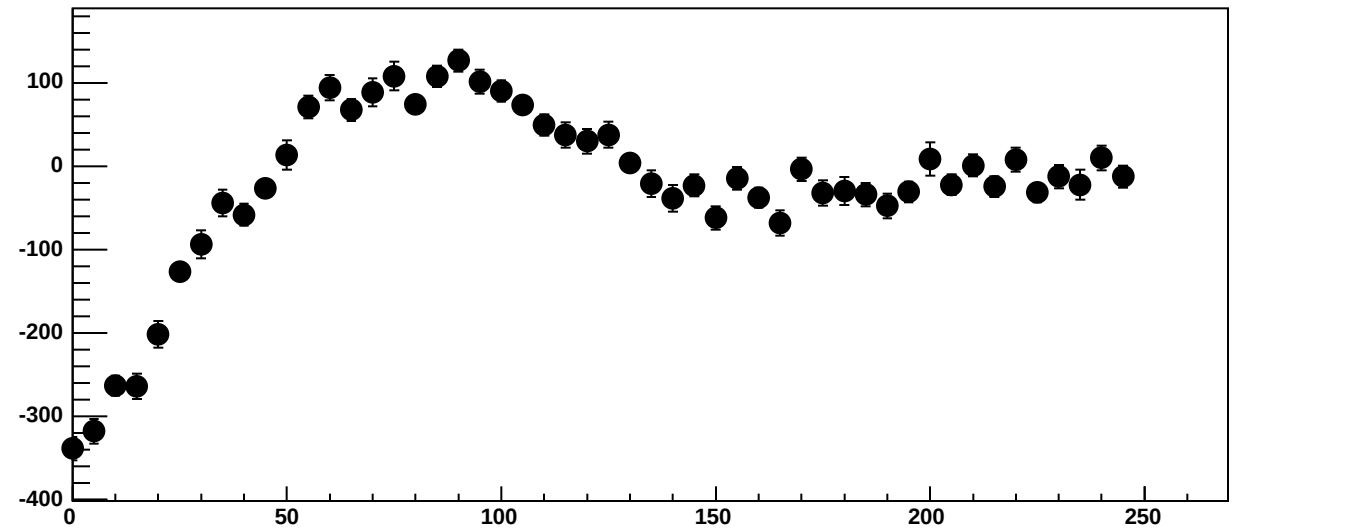
Chip 3, Channel 6, Enable 2, DAC=1600, ADC Mean vs Hold



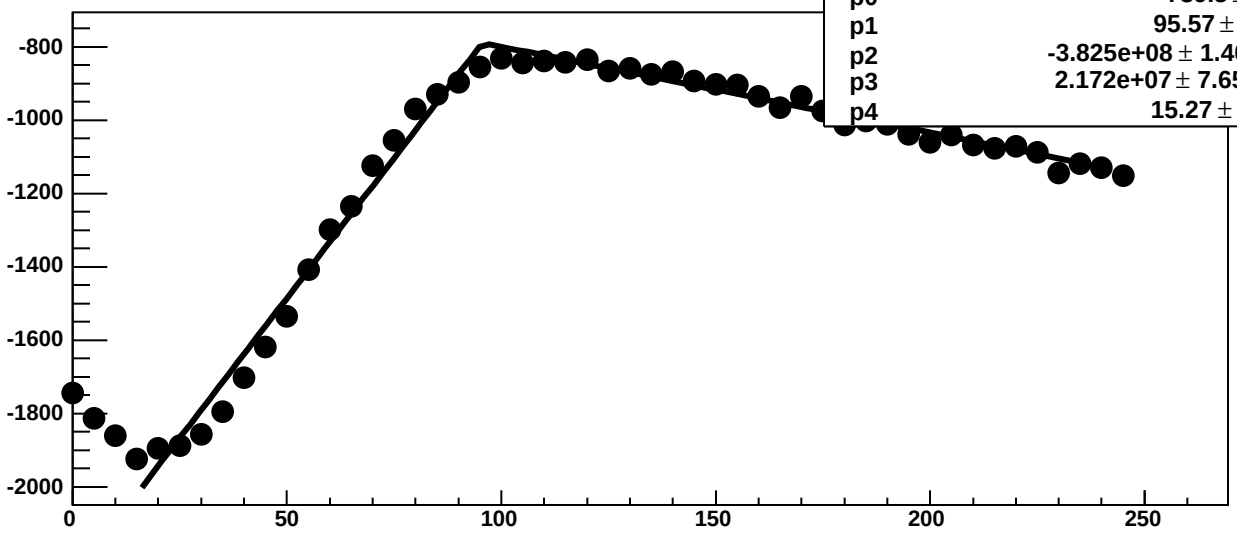
Chip 3, Channel 6, Enable 2, DAC=1600, ADC Noise vs Hold



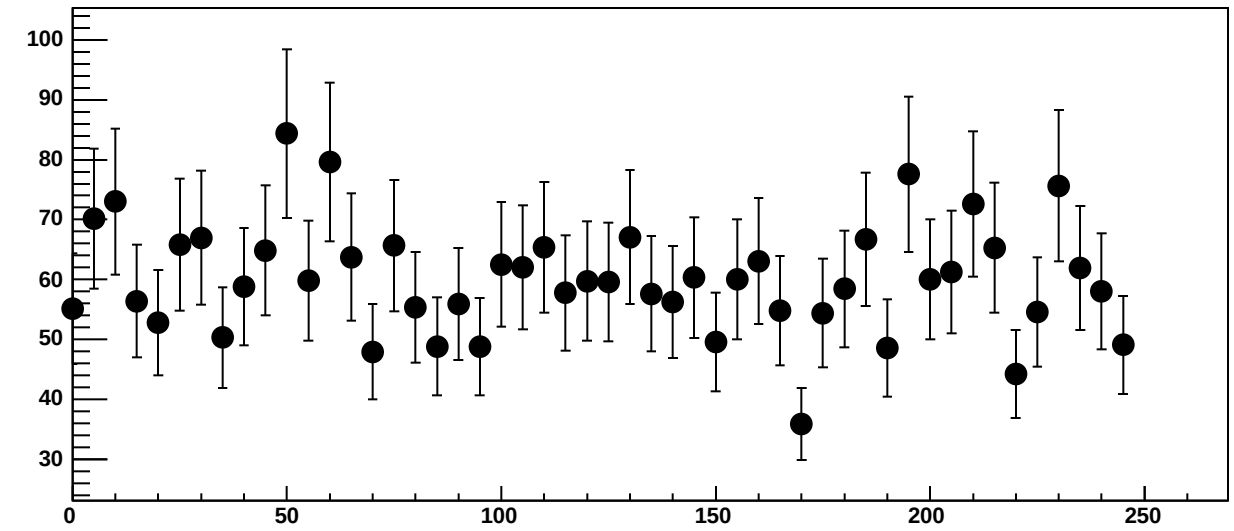
Chip 3, Channel 6, Enable 2, DAC=1600, ADC Residuals vs Hold



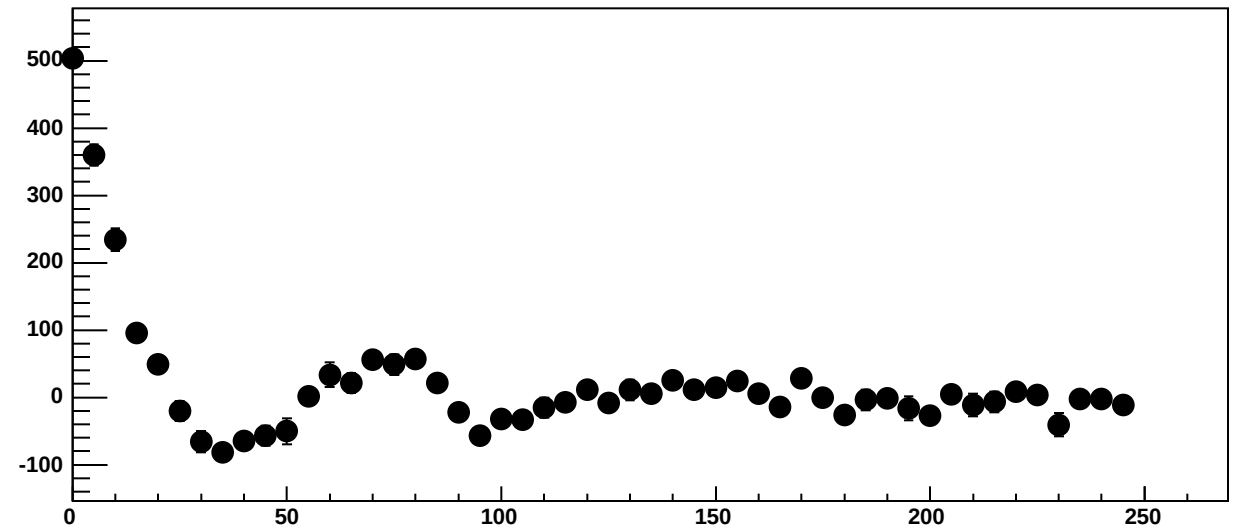
Chip 3, Channel 6, Enable 3, DAC=1600, ADC Mean vs Hold



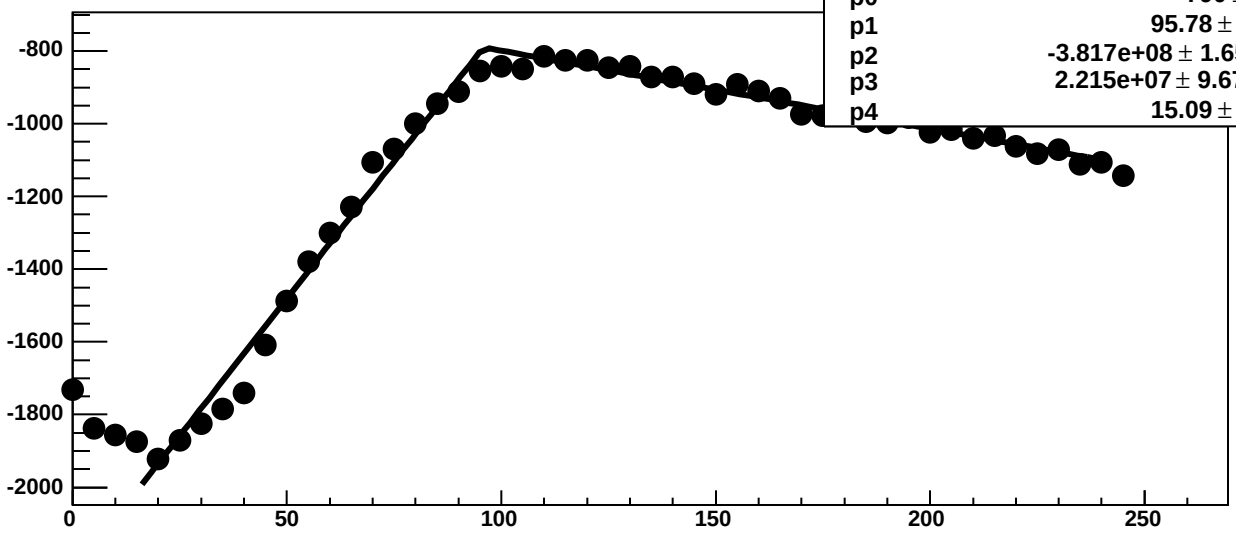
Chip 3, Channel 6, Enable 3, DAC=1600, ADC Noise vs Hold



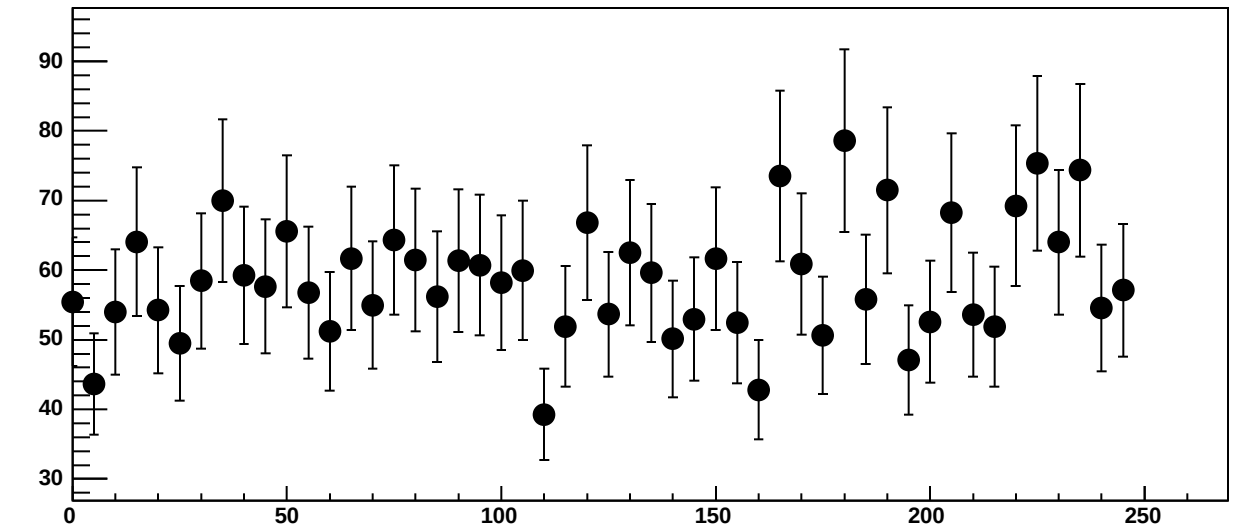
Chip 3, Channel 6, Enable 3, DAC=1600, ADC Residuals vs Hold



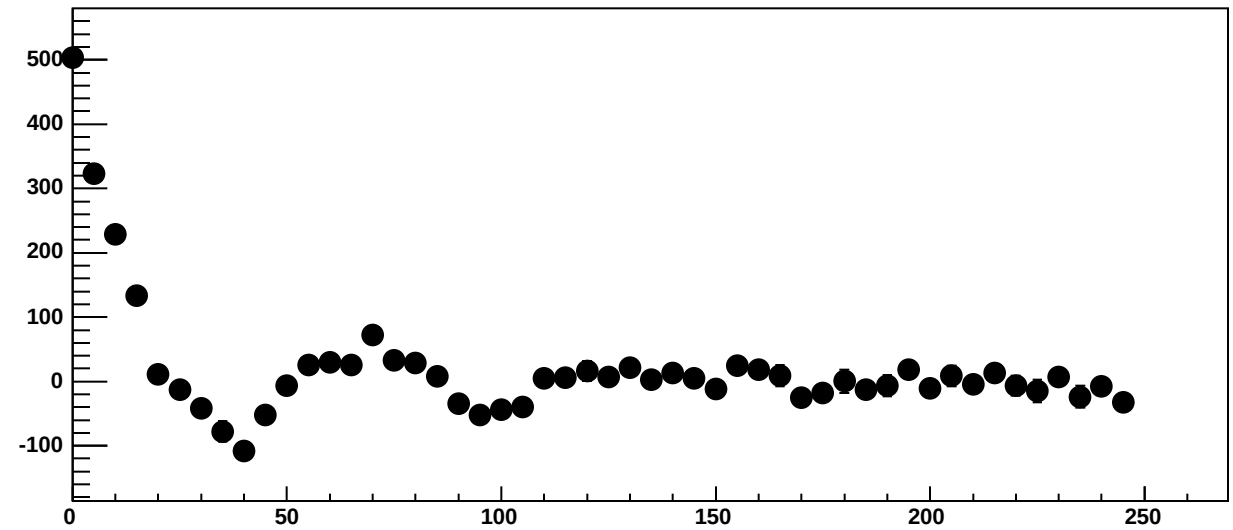
Chip 3, Channel 6, Enable 4, DAC=1600, ADC Mean vs Hold



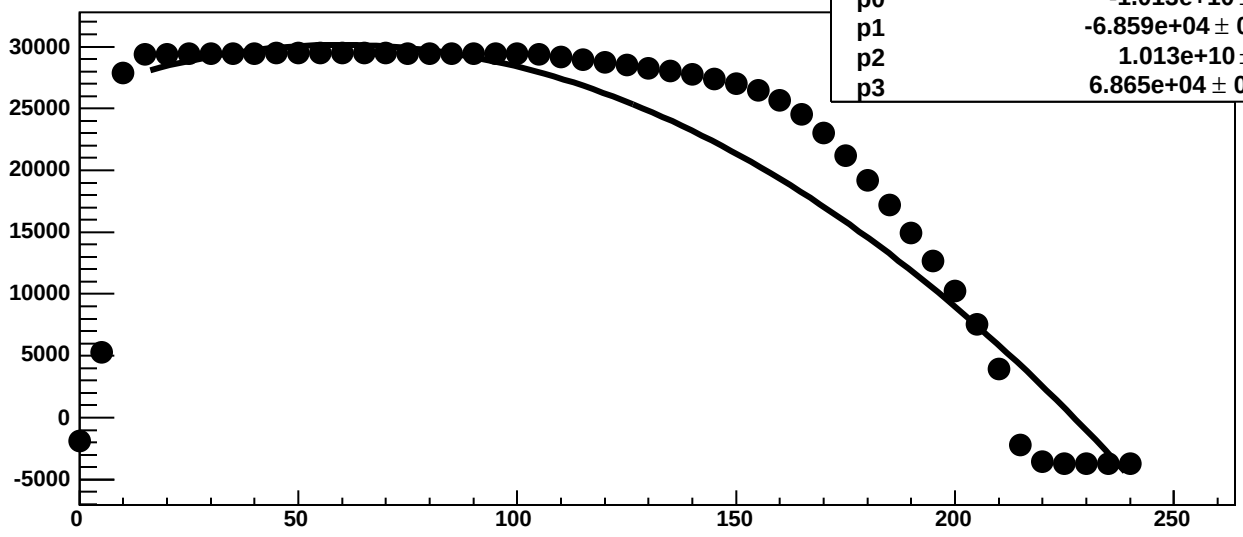
Chip 3, Channel 6, Enable 4, DAC=1600, ADC Noise vs Hold



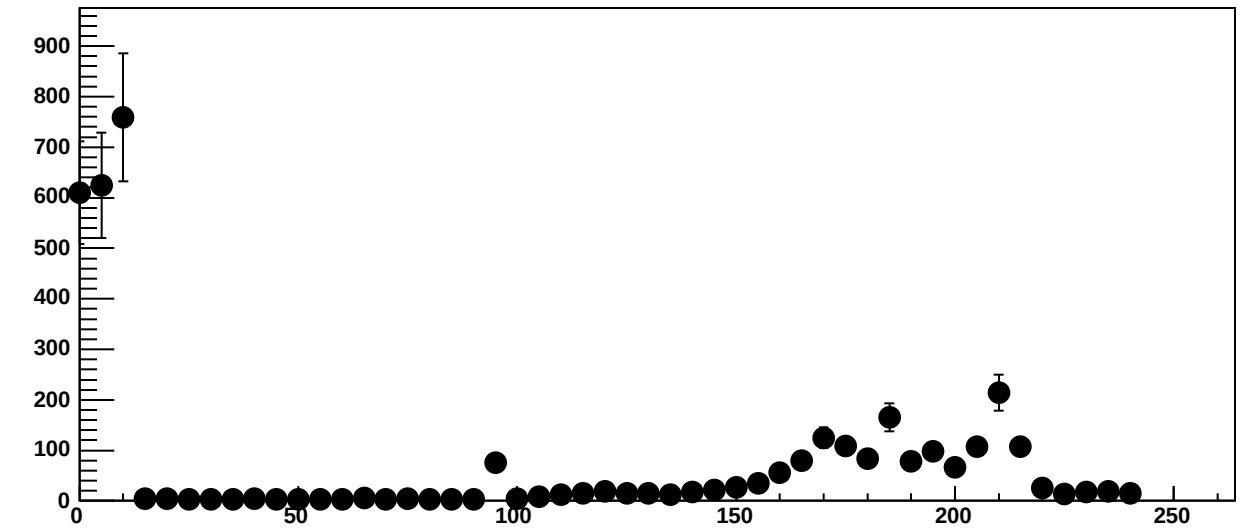
Chip 3, Channel 6, Enable 4, DAC=1600, ADC Residuals vs Hold



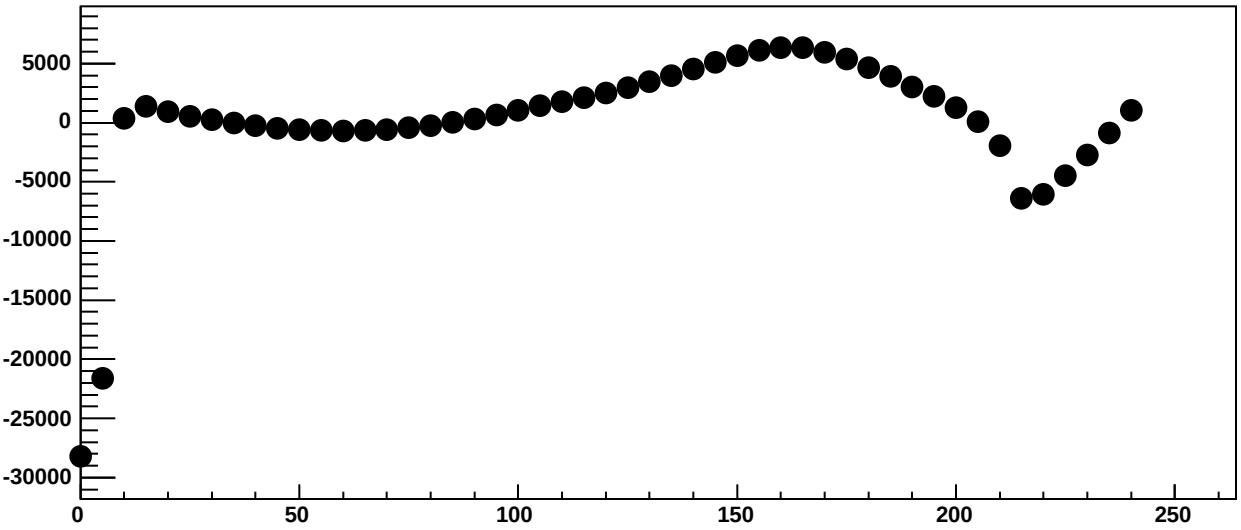
Chip 3, Channel 6, Enable 5!, DAC=1600, ADC Mean vs Hold

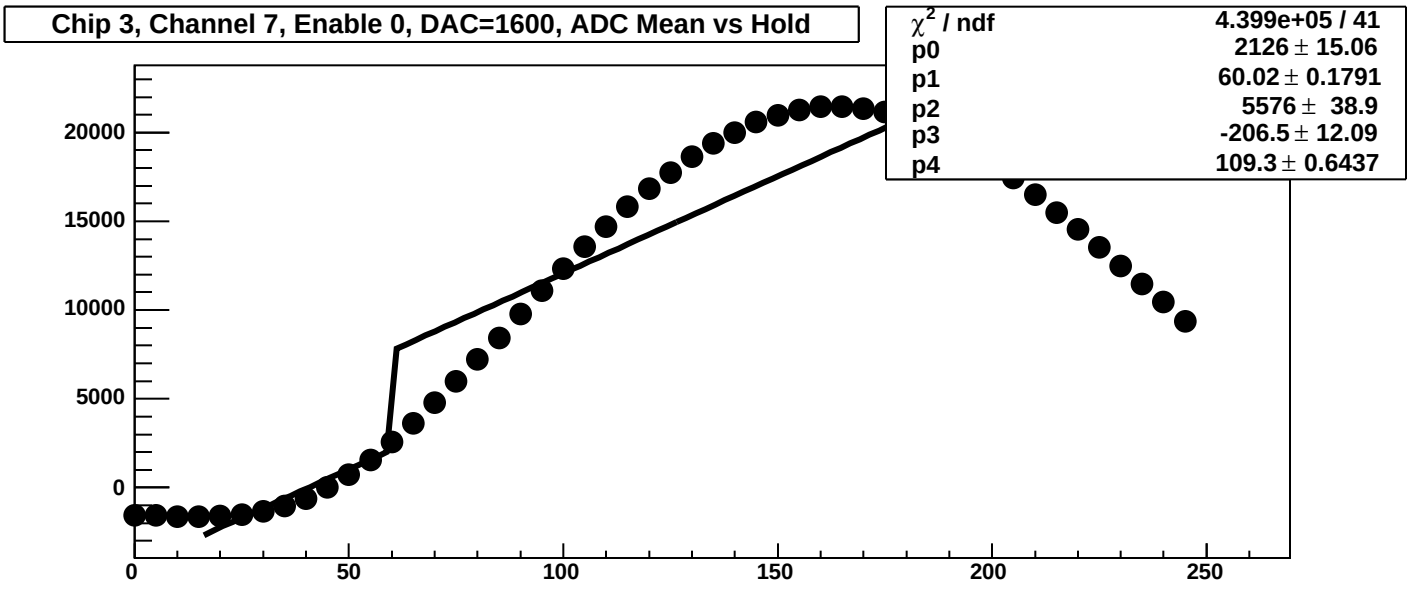


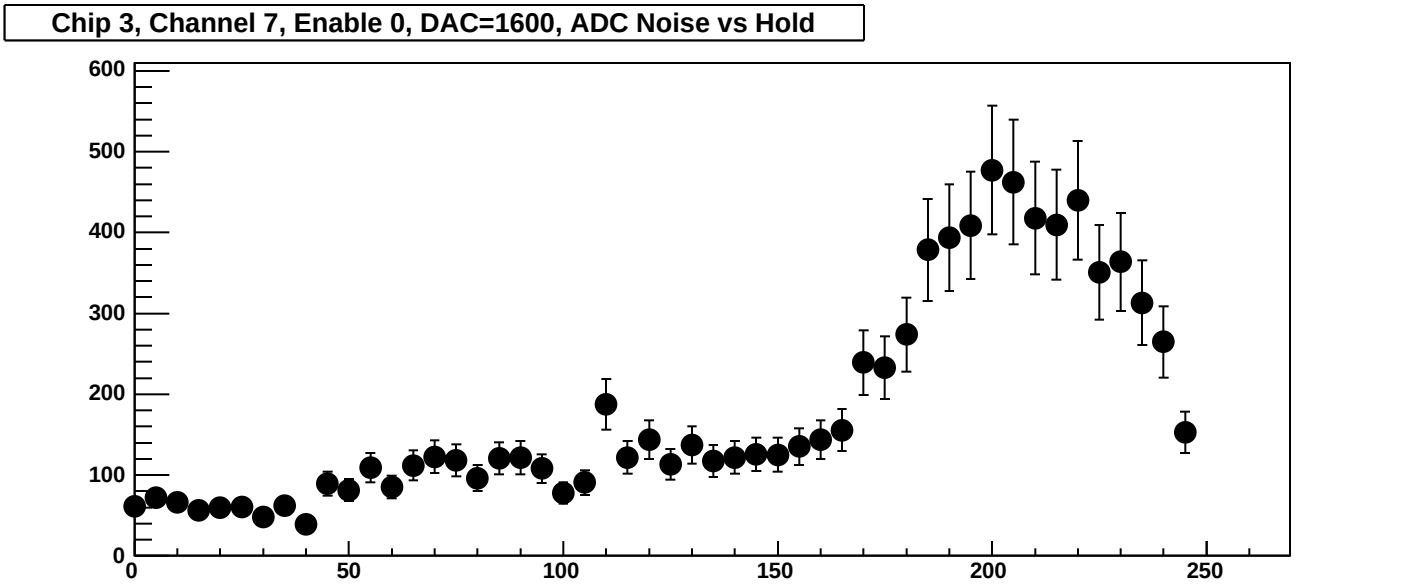
Chip 3, Channel 6, Enable 5!, DAC=1600, ADC Noise vs Hold

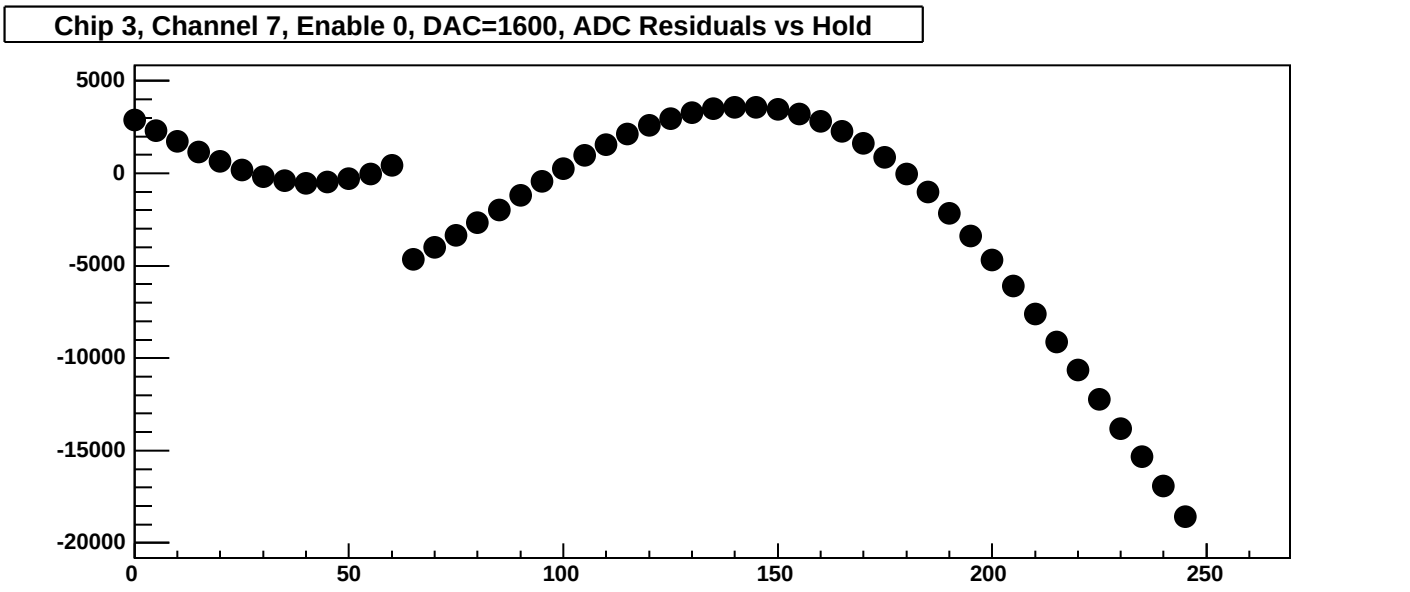


Chip 3, Channel 6, Enable 5!, DAC=1600, ADC Residuals vs Hold

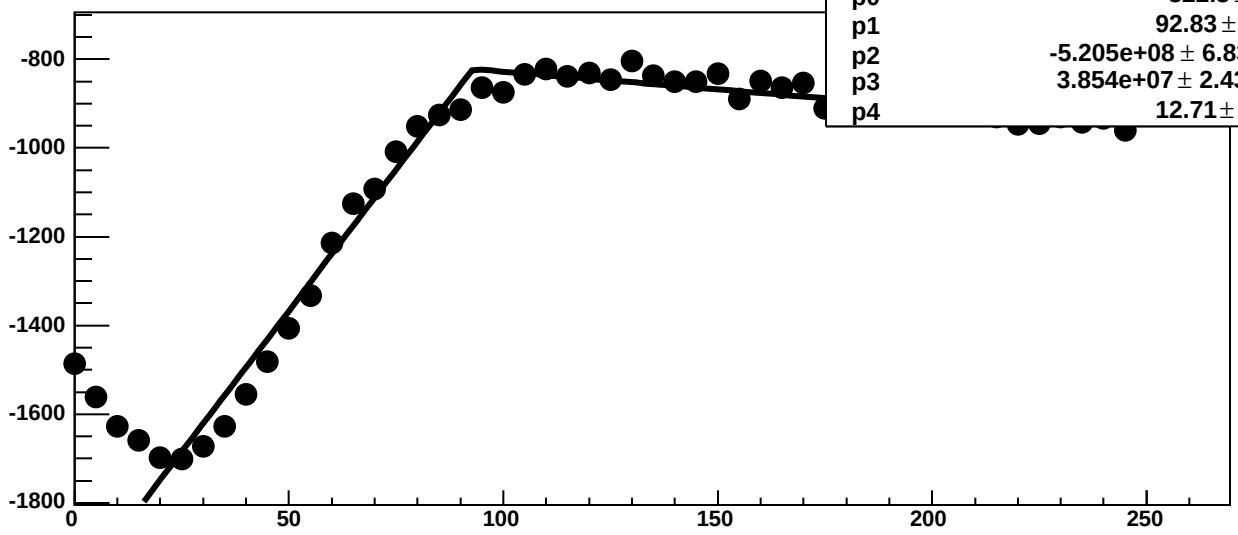



Chip 3, Channel 7, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.399e+05 / 41
p0 2126 ± 15.06
p1 60.02 ± 0.1791
p2 5576 ± 38.9
p3 -206.5 ± 12.09
p4 109.3 ± 0.6437
20000
15000
10000
5000
0
0
50
100
150
200
250

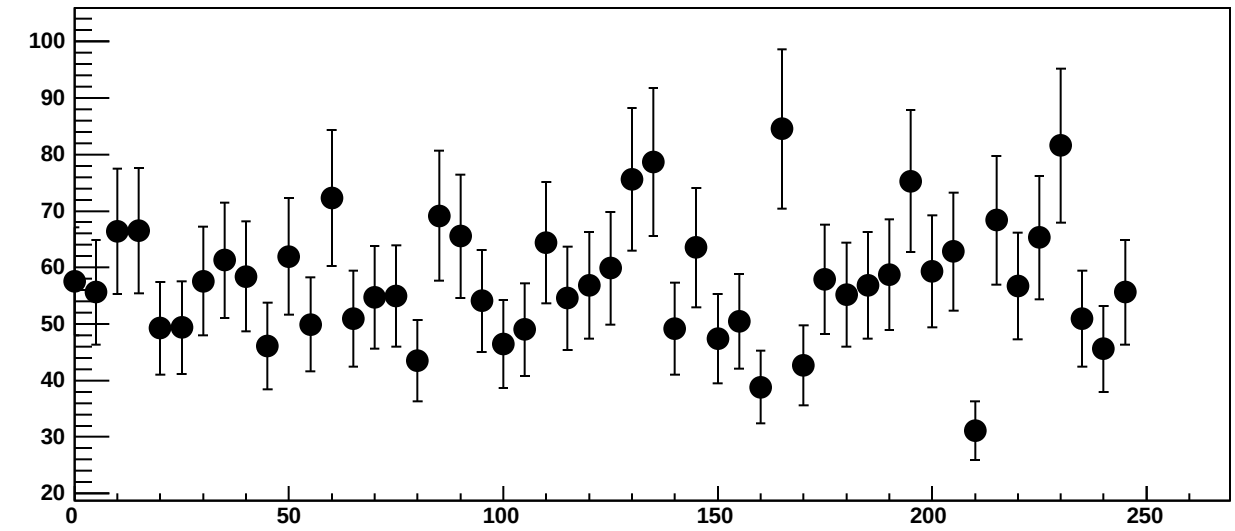

Chip 3, Channel 7, Enable 0, DAC=1600, ADC Noise vs Hold
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 3, Channel 7, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

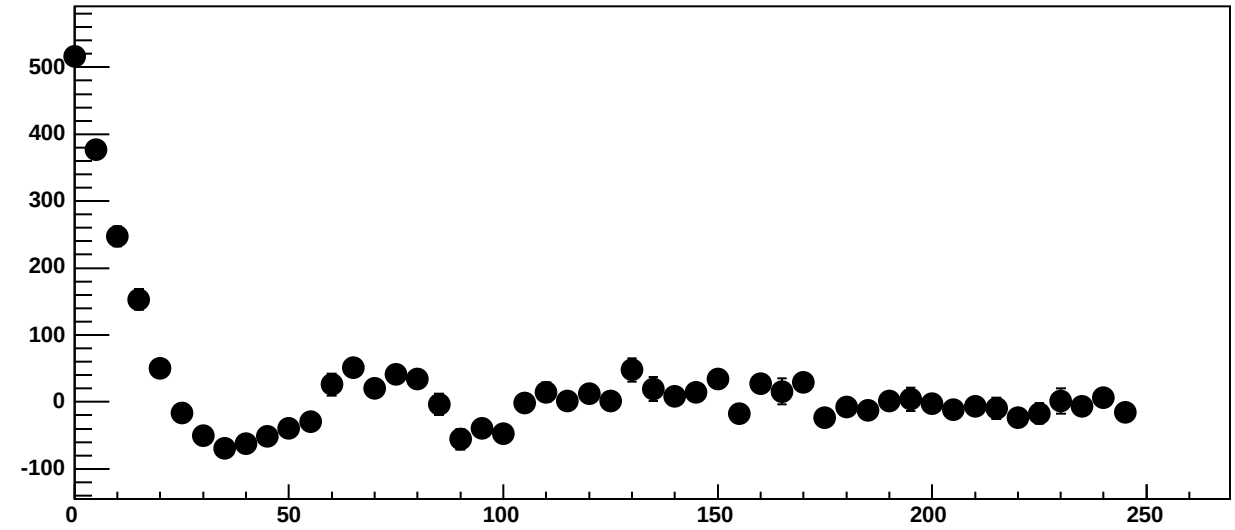
Chip 3, Channel 7, Enable 1, DAC=1600, ADC Mean vs Hold



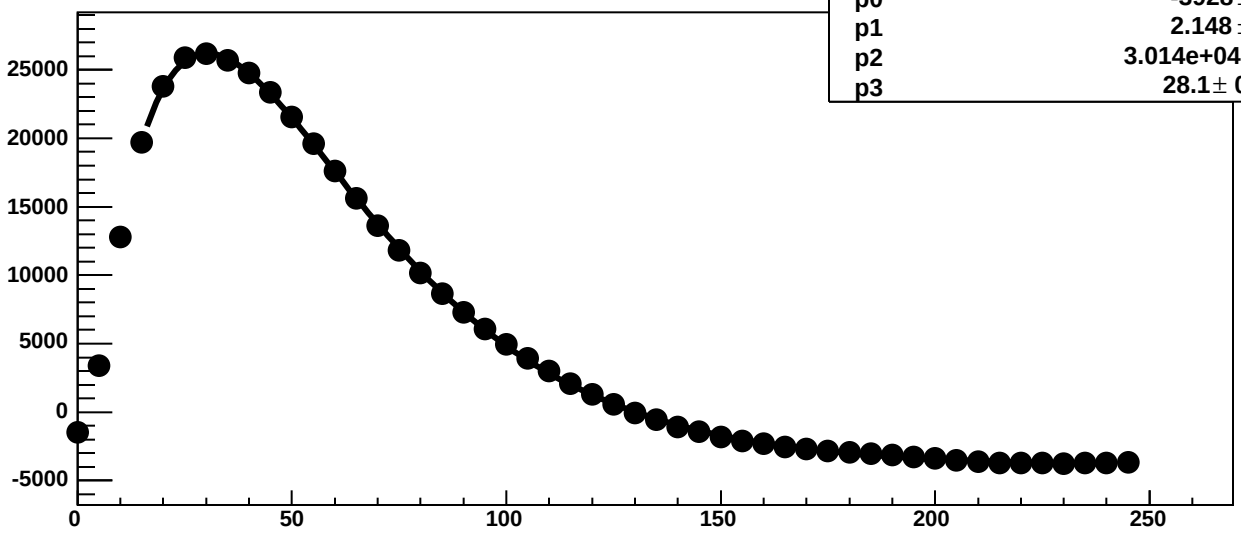
Chip 3, Channel 7, Enable 1, DAC=1600, ADC Noise vs Hold



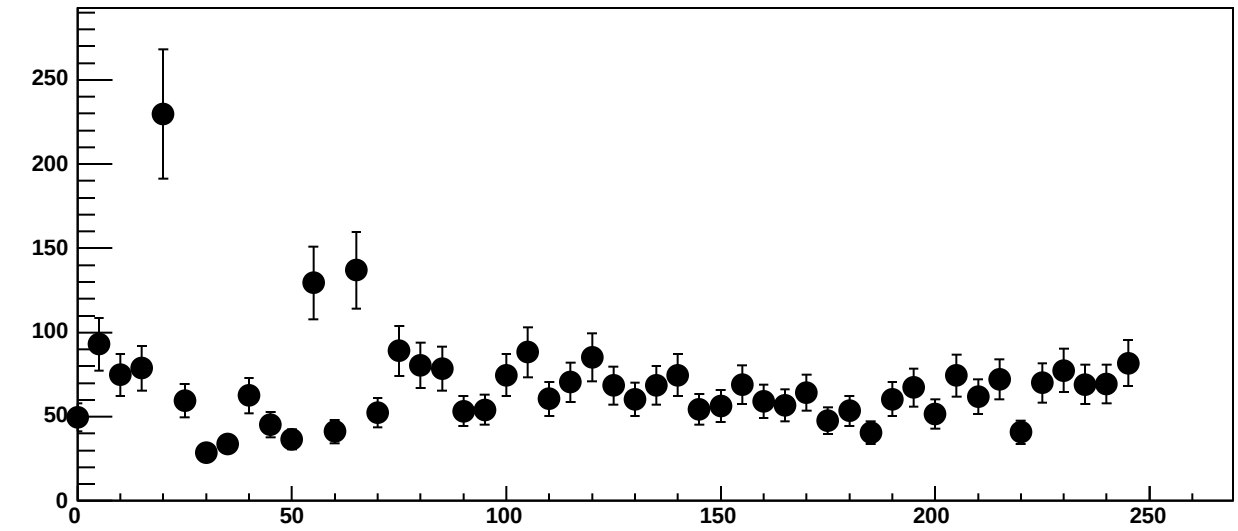
Chip 3, Channel 7, Enable 1, DAC=1600, ADC Residuals vs Hold



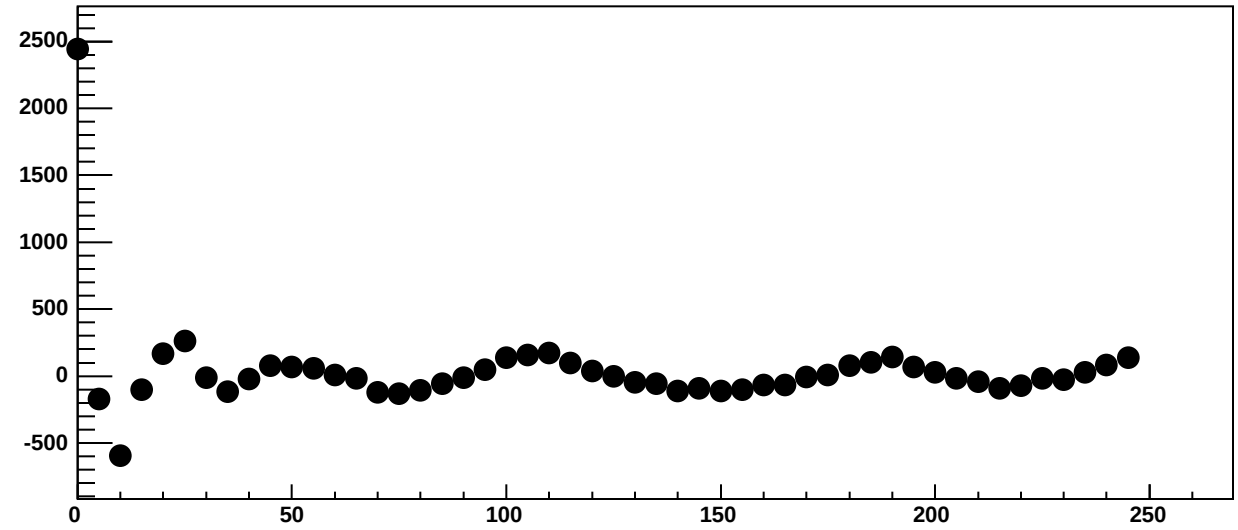
Chip 3, Channel 7, Enable 2!, DAC=1600, ADC Mean vs Hold



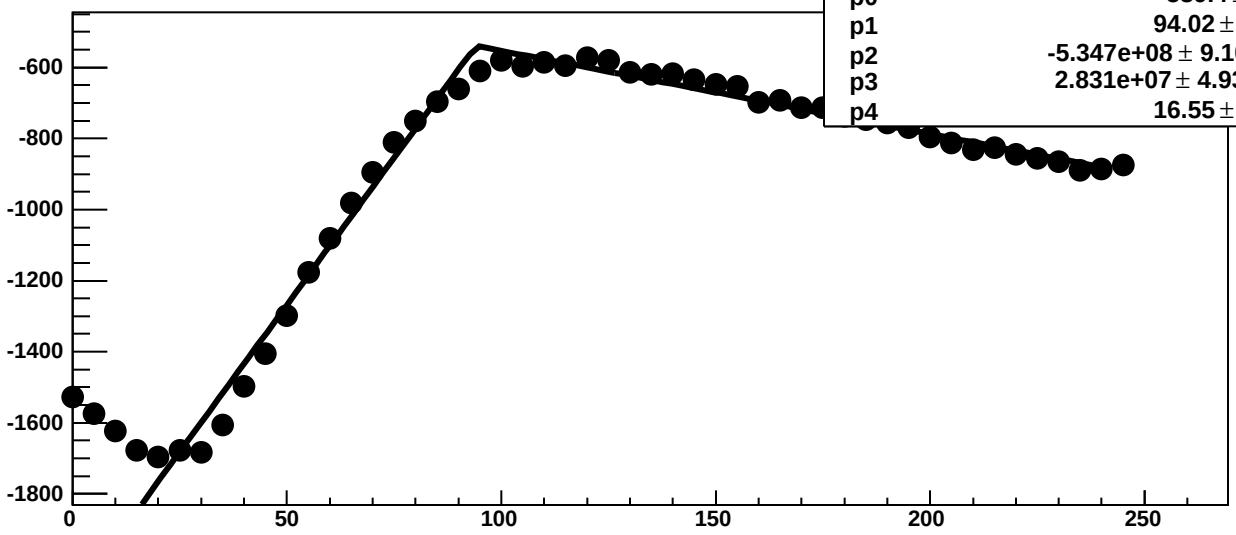
Chip 3, Channel 7, Enable 2!, DAC=1600, ADC Noise vs Hold



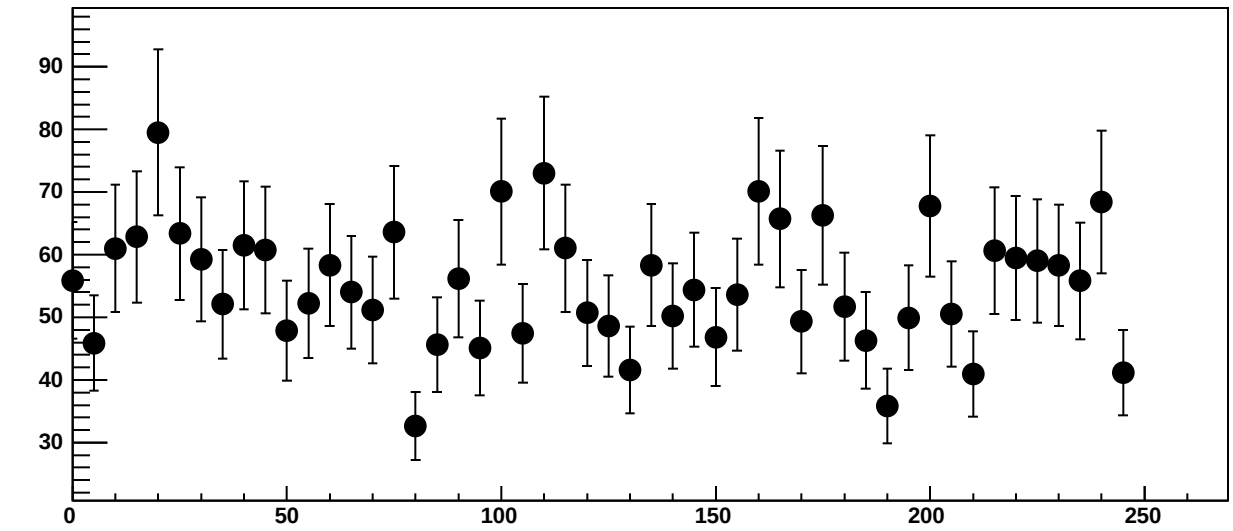
Chip 3, Channel 7, Enable 2!, DAC=1600, ADC Residuals vs Hold



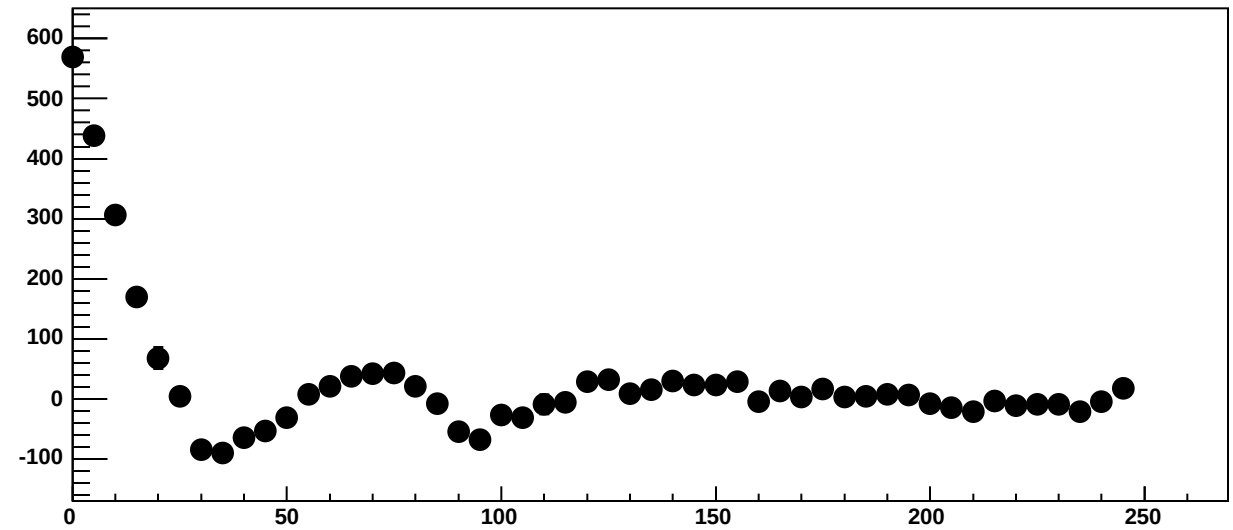
Chip 3, Channel 7, Enable 3, DAC=1600, ADC Mean vs Hold



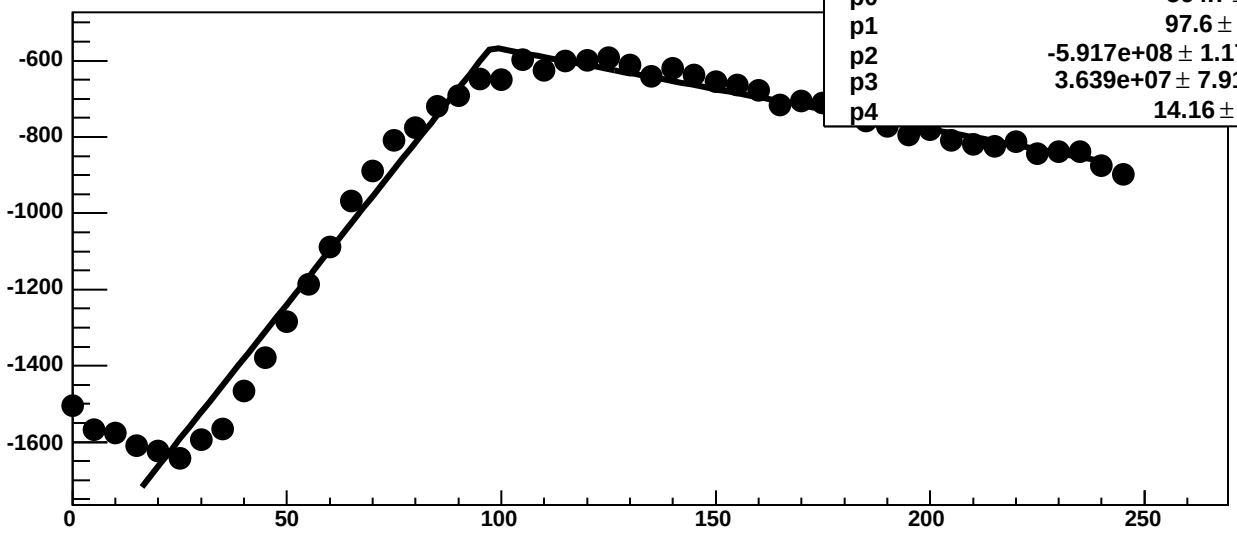
Chip 3, Channel 7, Enable 3, DAC=1600, ADC Noise vs Hold



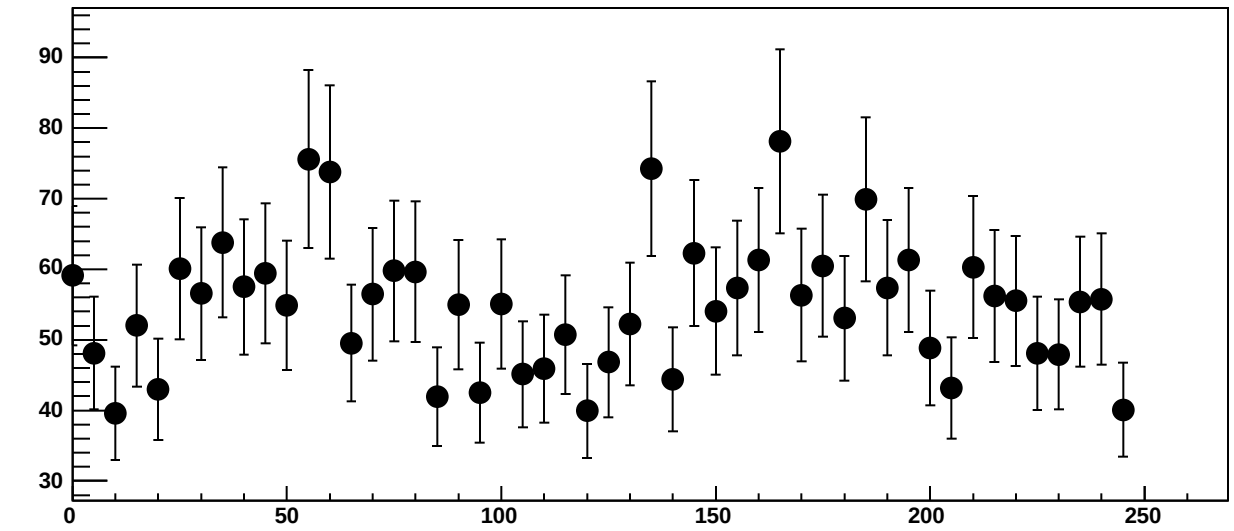
Chip 3, Channel 7, Enable 3, DAC=1600, ADC Residuals vs Hold



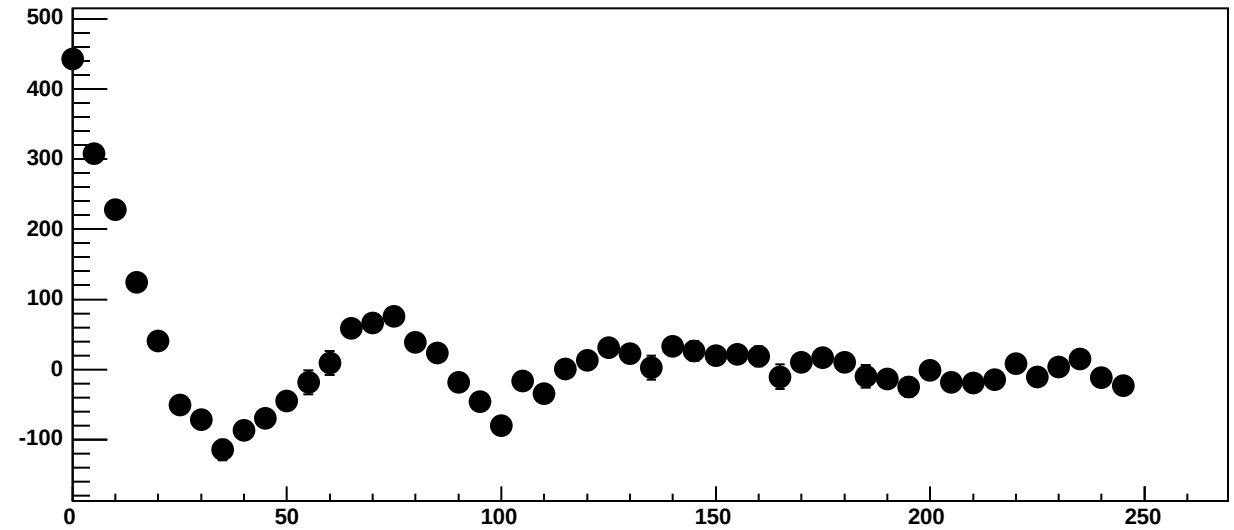
Chip 3, Channel 7, Enable 4, DAC=1600, ADC Mean vs Hold



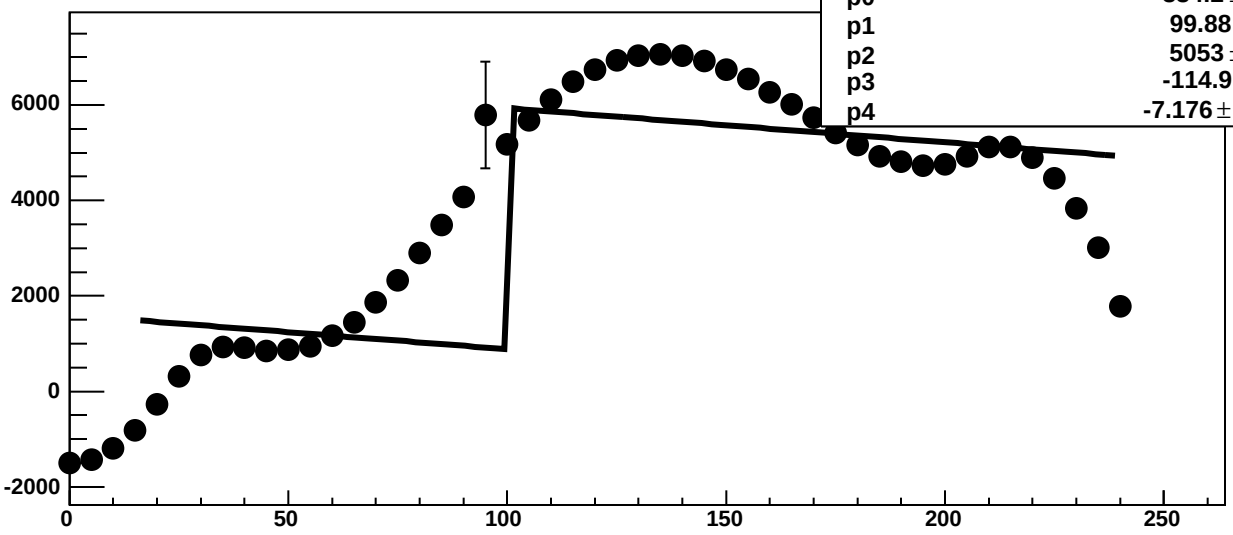
Chip 3, Channel 7, Enable 4, DAC=1600, ADC Noise vs Hold



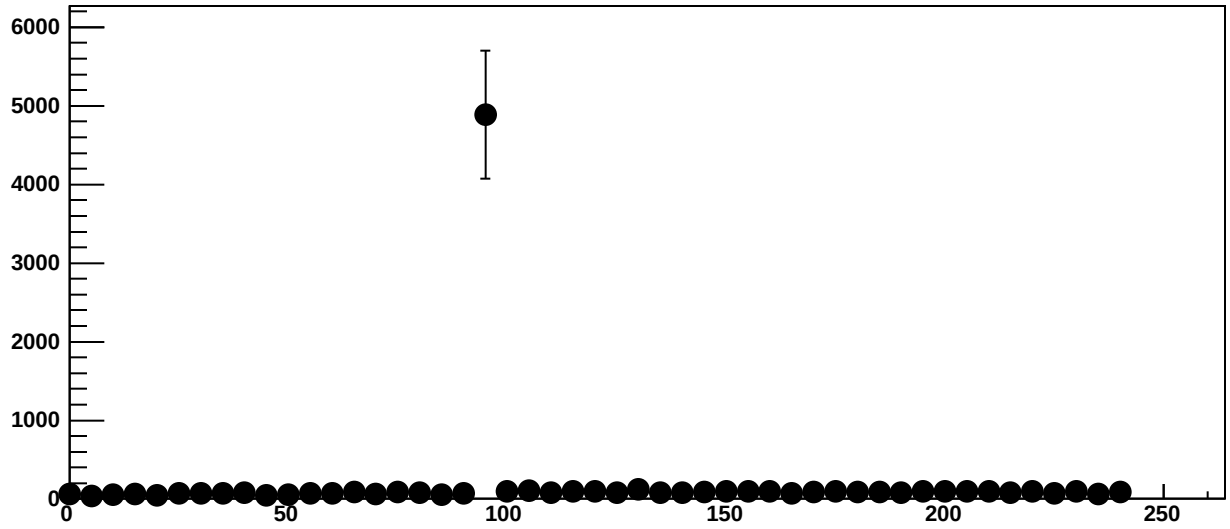
Chip 3, Channel 7, Enable 4, DAC=1600, ADC Residuals vs Hold



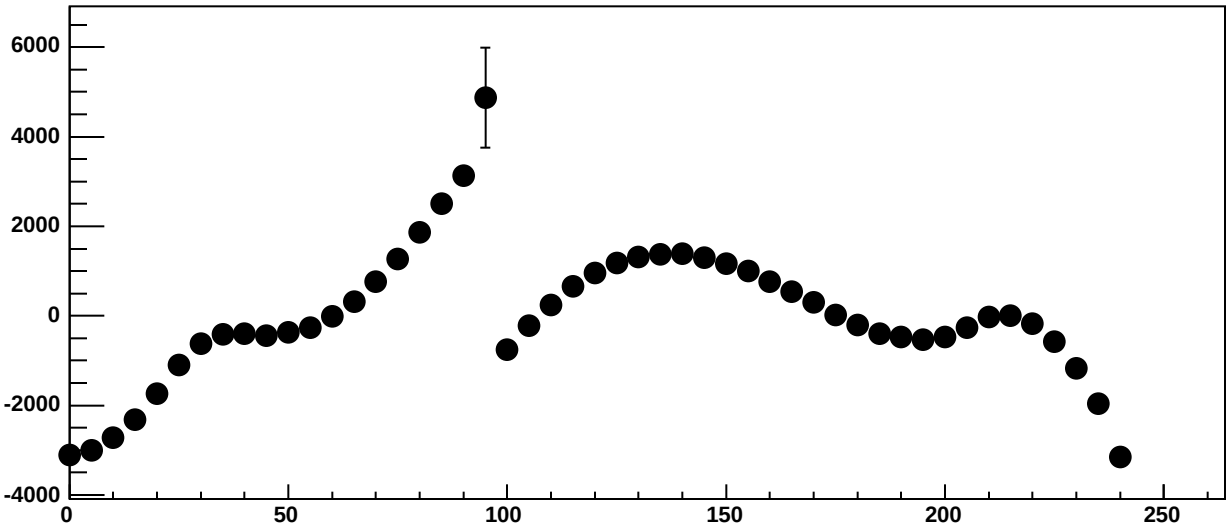
Chip 3, Channel 7, Enable 5, DAC=1600, ADC Mean vs Hold

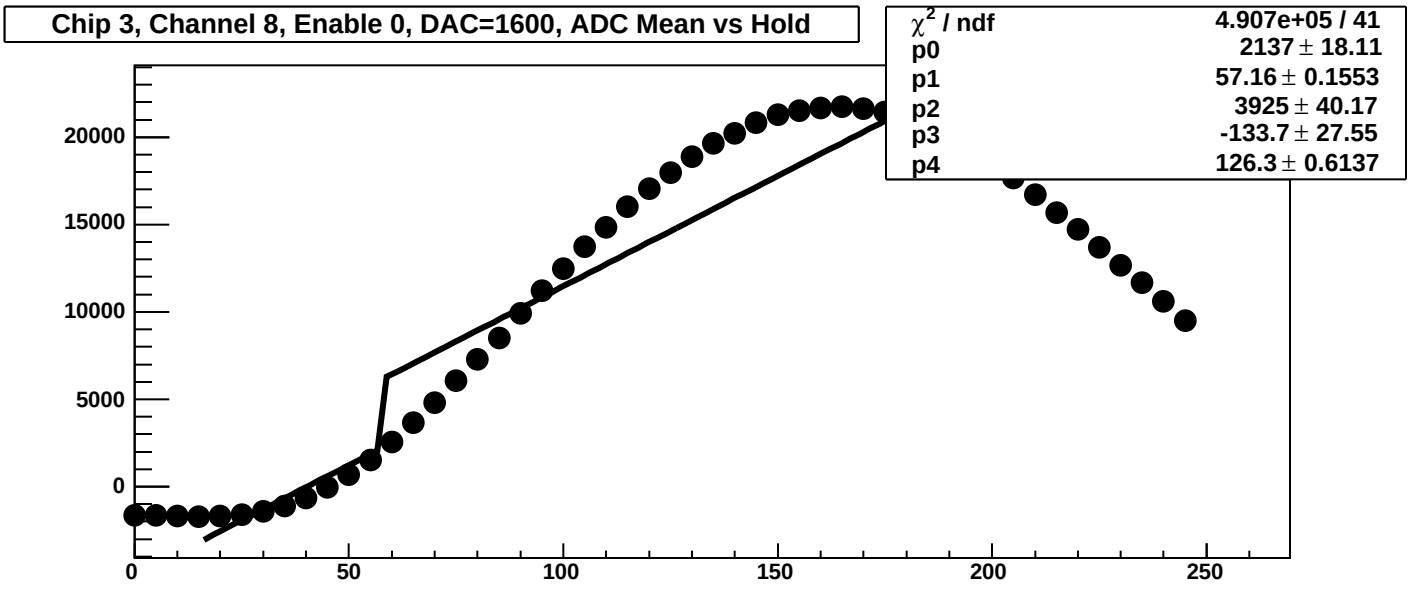


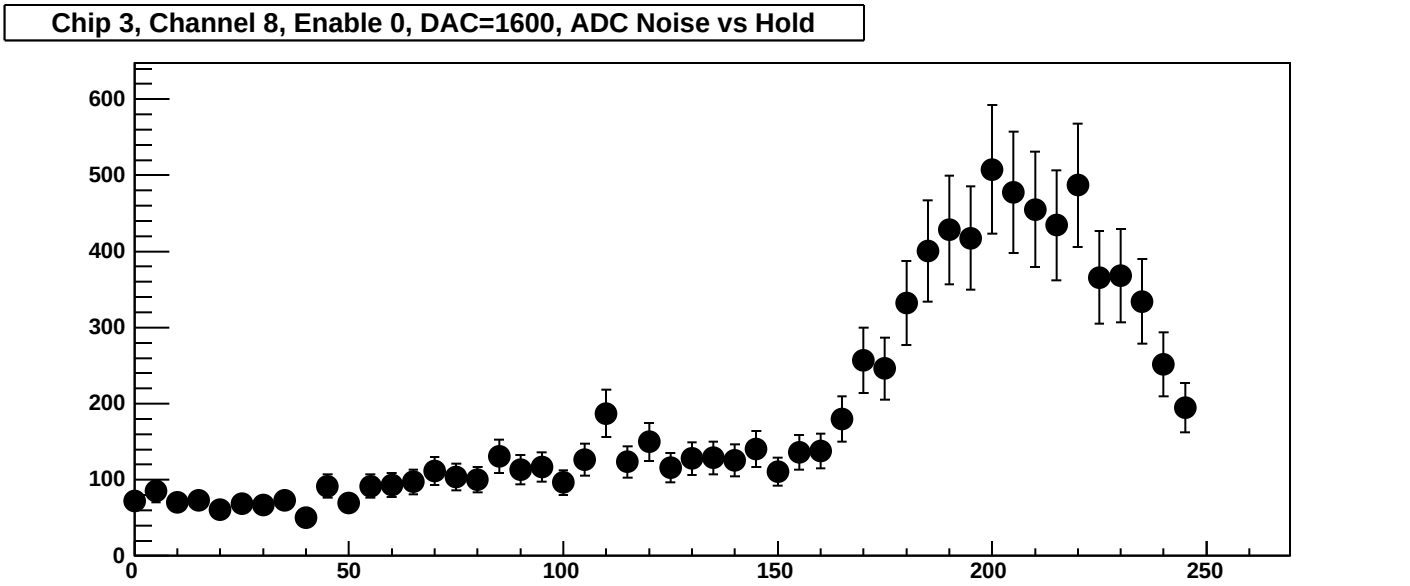
Chip 3, Channel 7, Enable 5, DAC=1600, ADC Noise vs Hold

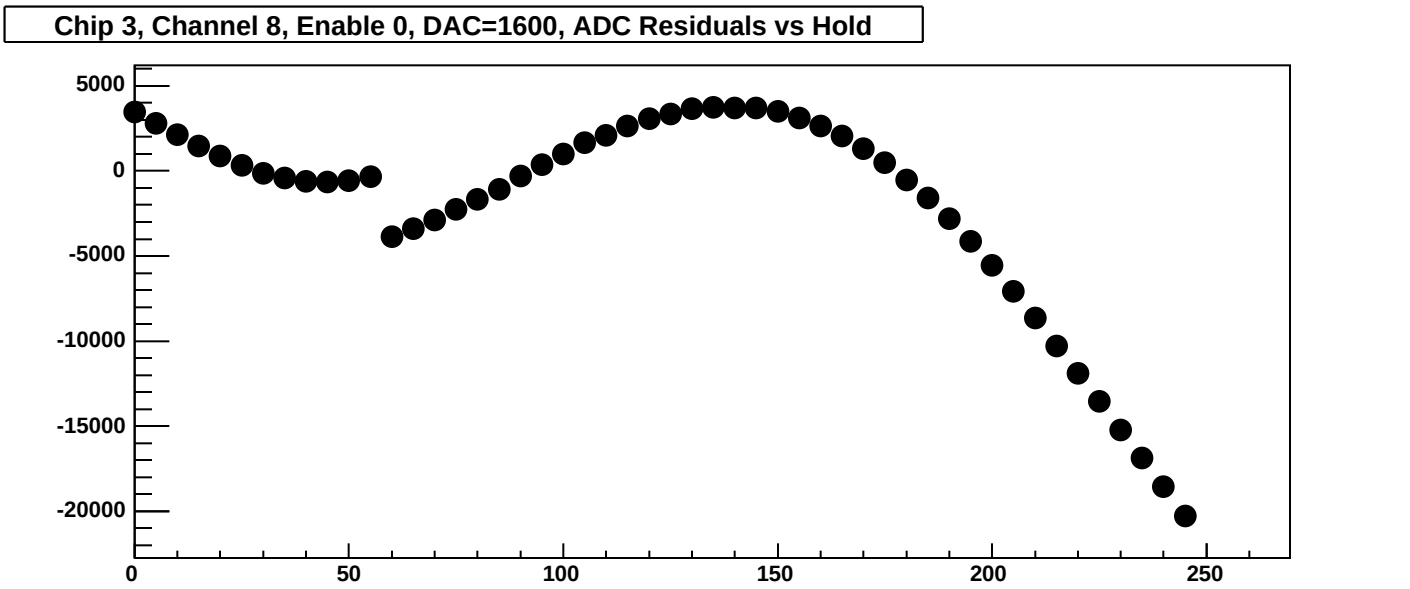


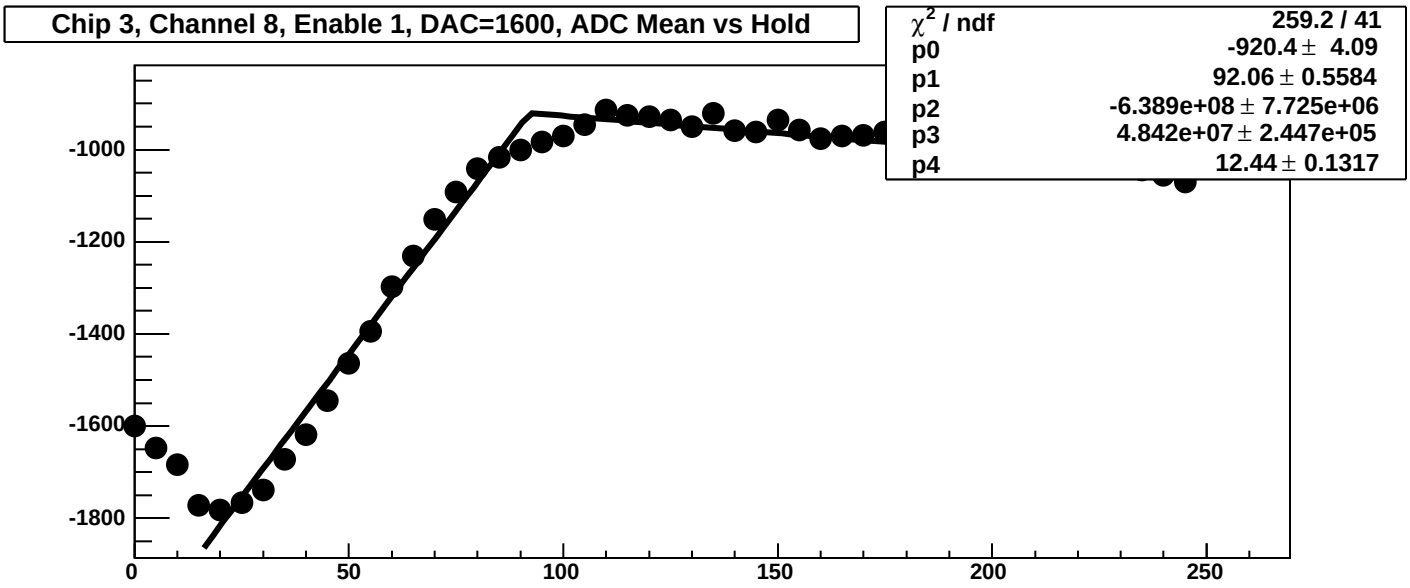
Chip 3, Channel 7, Enable 5, DAC=1600, ADC Residuals vs Hold

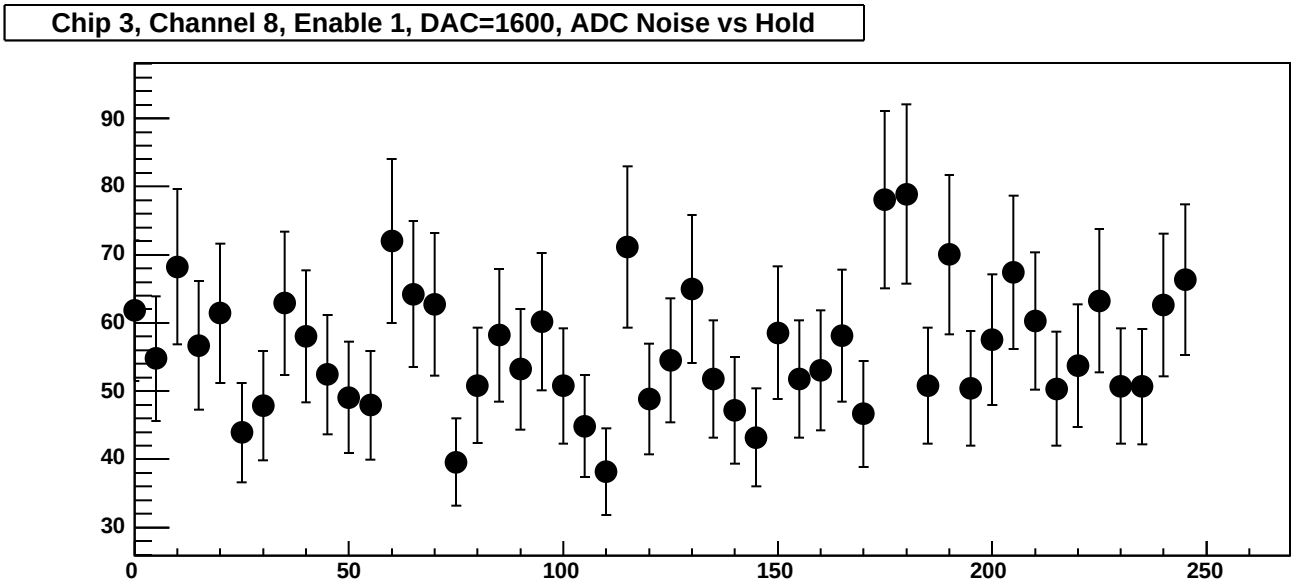


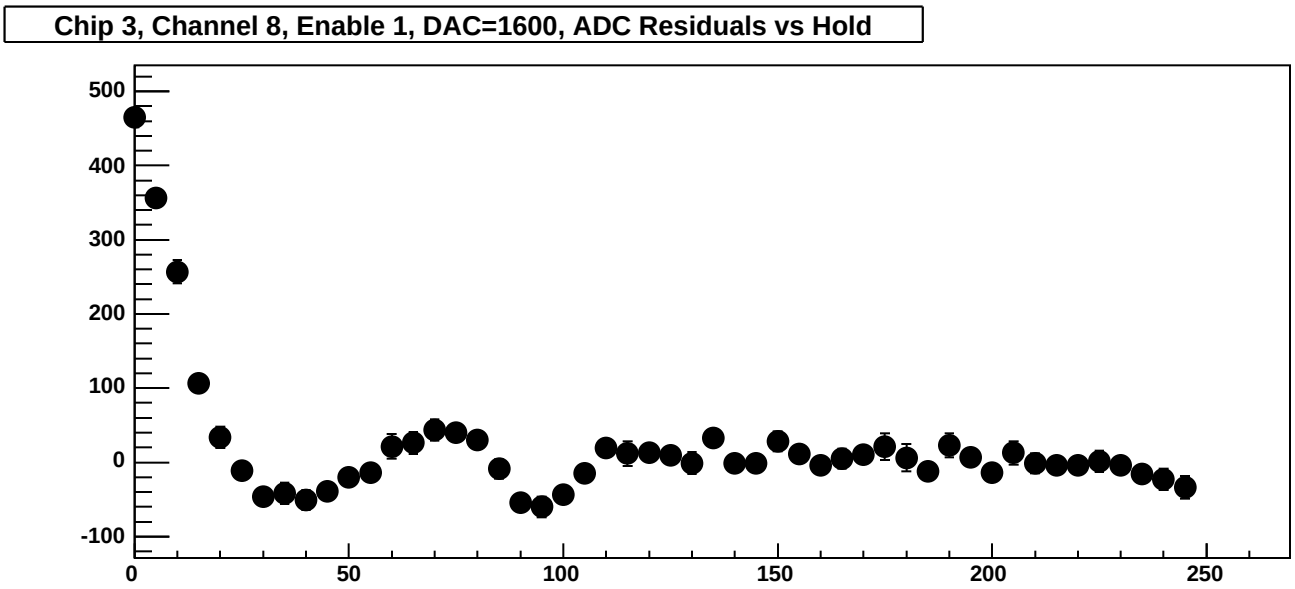

Chip 3, Channel 8, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.907e+05 / 41
p0 2137 ± 18.11
p1 57.16 ± 0.1553
p2 3925 ± 40.17
p3 -133.7 ± 27.55
p4 126.3 ± 0.6137
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 3, Channel 8, Enable 0, DAC=1600, ADC Noise vs Hold
600
500
400
300
200
100
0
0
50
100
150
200
250

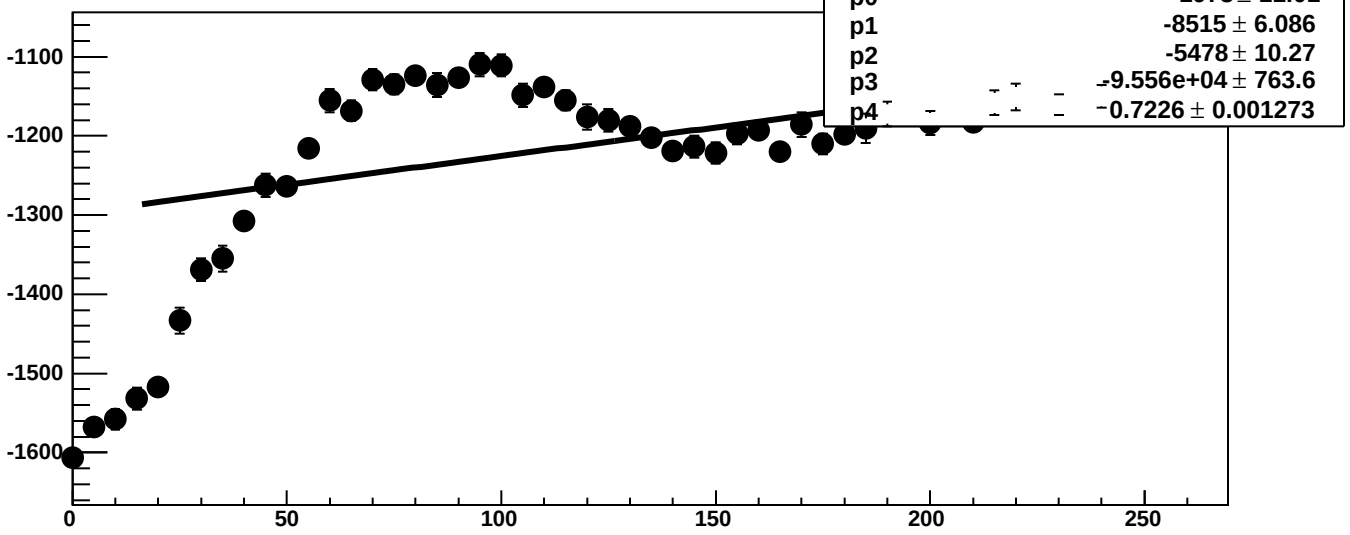

Chip 3, Channel 8, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250


Chip 3, Channel 8, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 259.2 / 41
p0 -920.4 ± 4.09
p1 92.06 ± 0.5584
p2 -6.389e+08 ± 7.725e+06
p3 4.842e+07 ± 2.447e+05
p4 12.44 ± 0.1317
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250

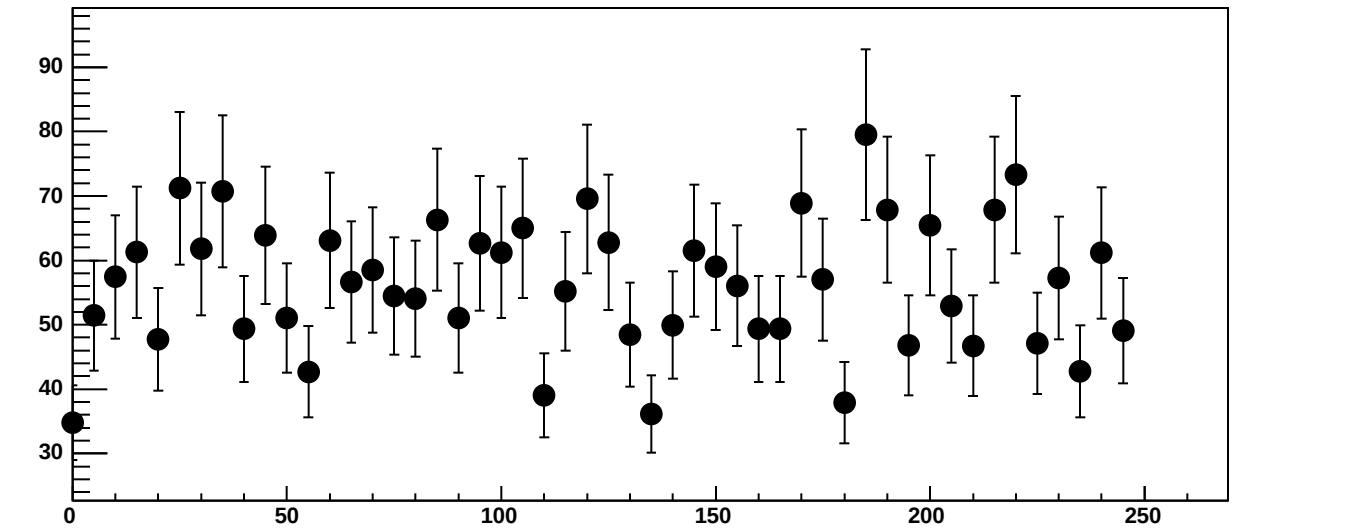

Chip 3, Channel 8, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 3, Channel 8, Enable 1, DAC=1600, ADC Residuals vs Hold
500
400
300
200
100
0
-100
0
50
100
150
200
250

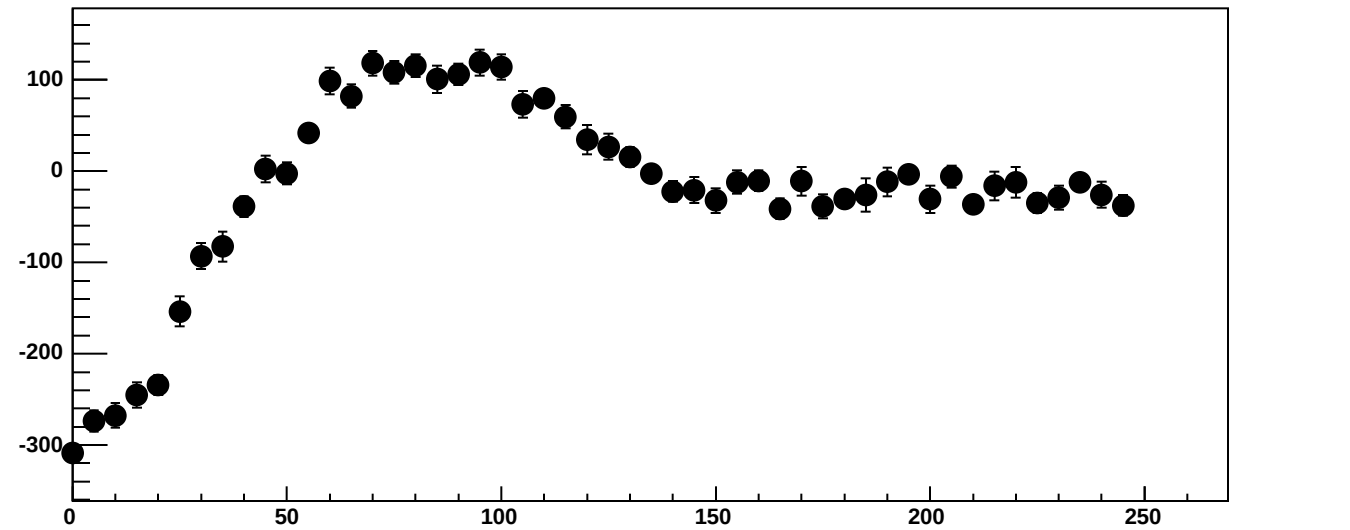
Chip 3, Channel 8, Enable 2, DAC=1600, ADC Mean vs Hold



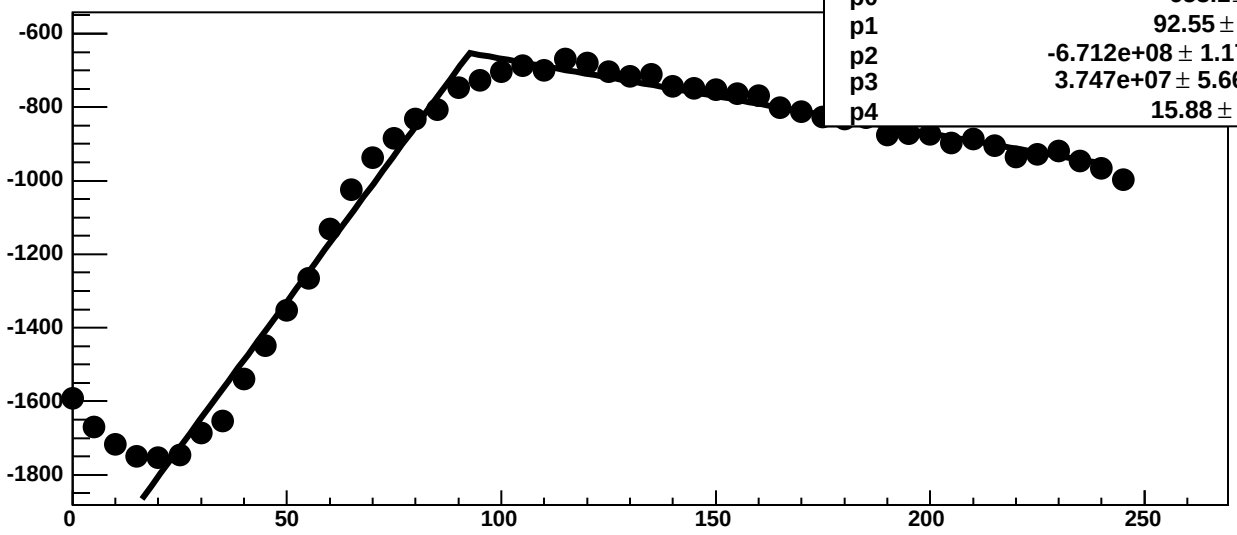
Chip 3, Channel 8, Enable 2, DAC=1600, ADC Noise vs Hold



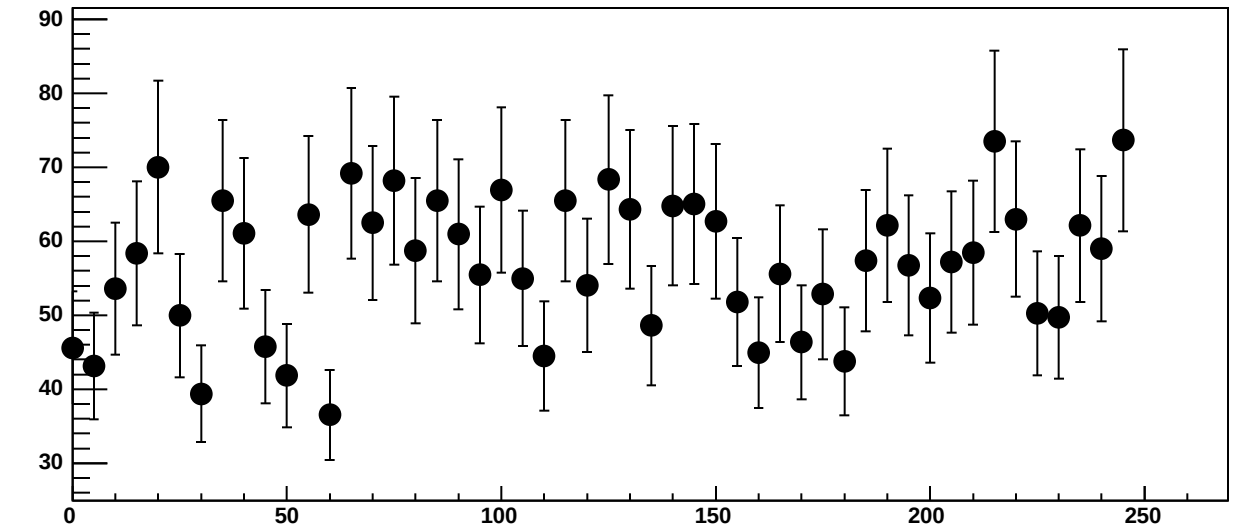
Chip 3, Channel 8, Enable 2, DAC=1600, ADC Residuals vs Hold



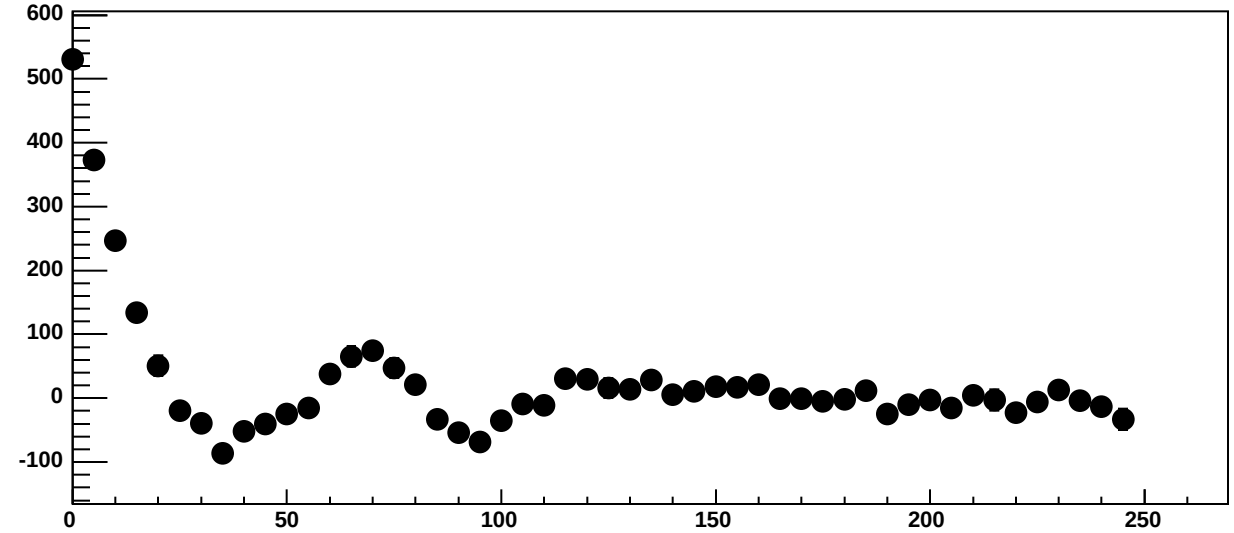
Chip 3, Channel 8, Enable 3, DAC=1600, ADC Mean vs Hold



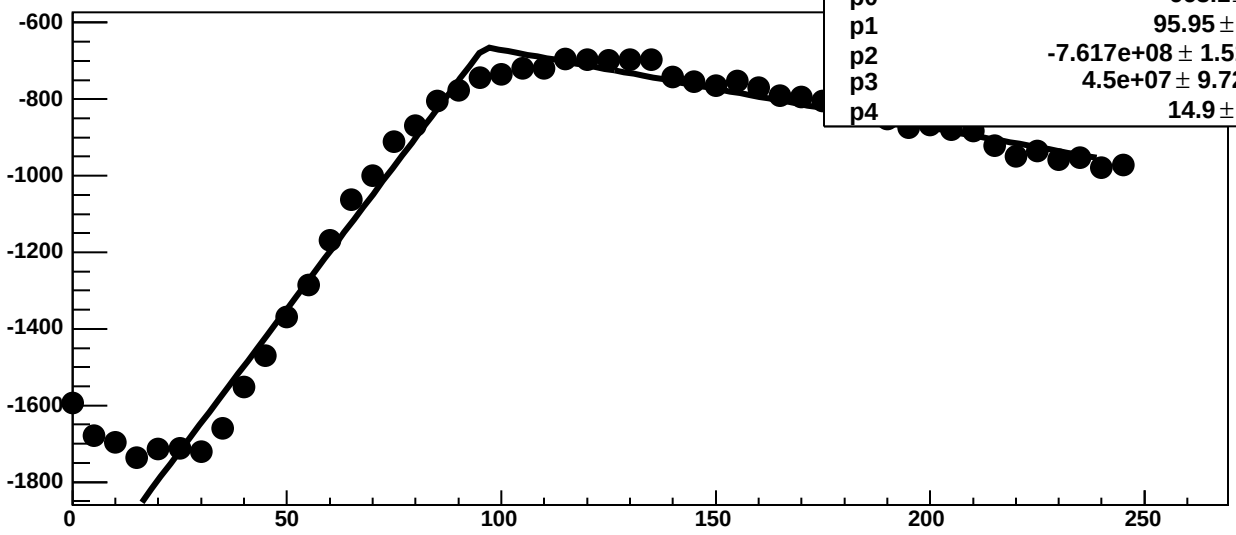
Chip 3, Channel 8, Enable 3, DAC=1600, ADC Noise vs Hold



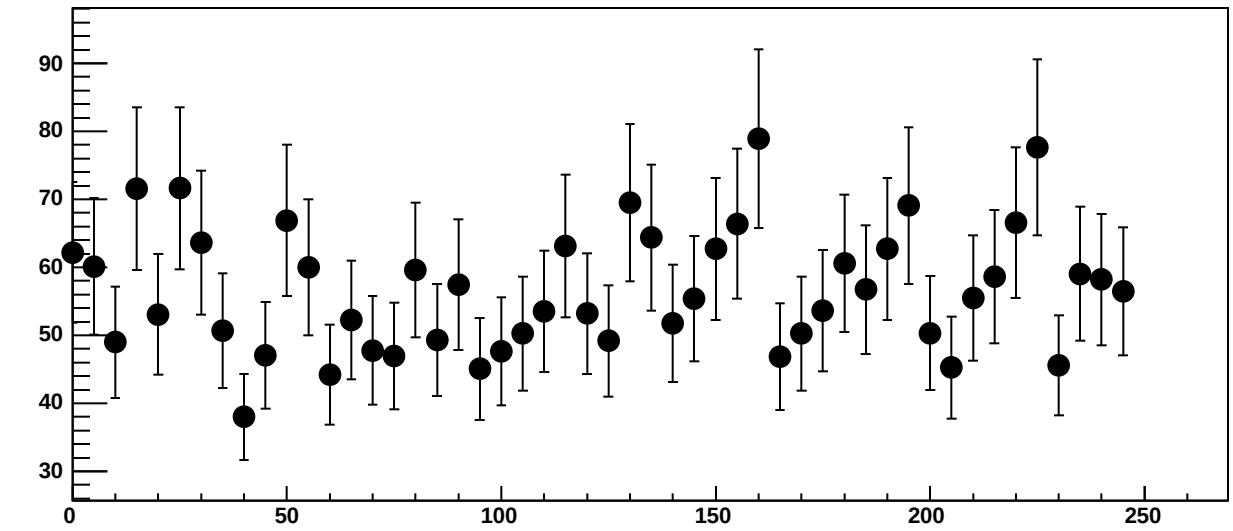
Chip 3, Channel 8, Enable 3, DAC=1600, ADC Residuals vs Hold



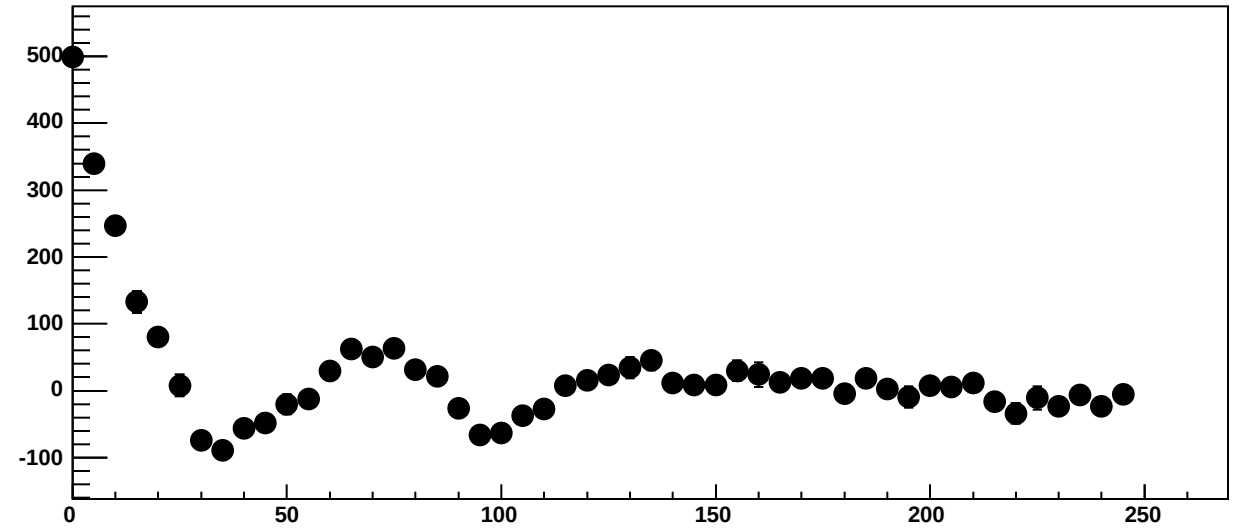
Chip 3, Channel 8, Enable 4, DAC=1600, ADC Mean vs Hold



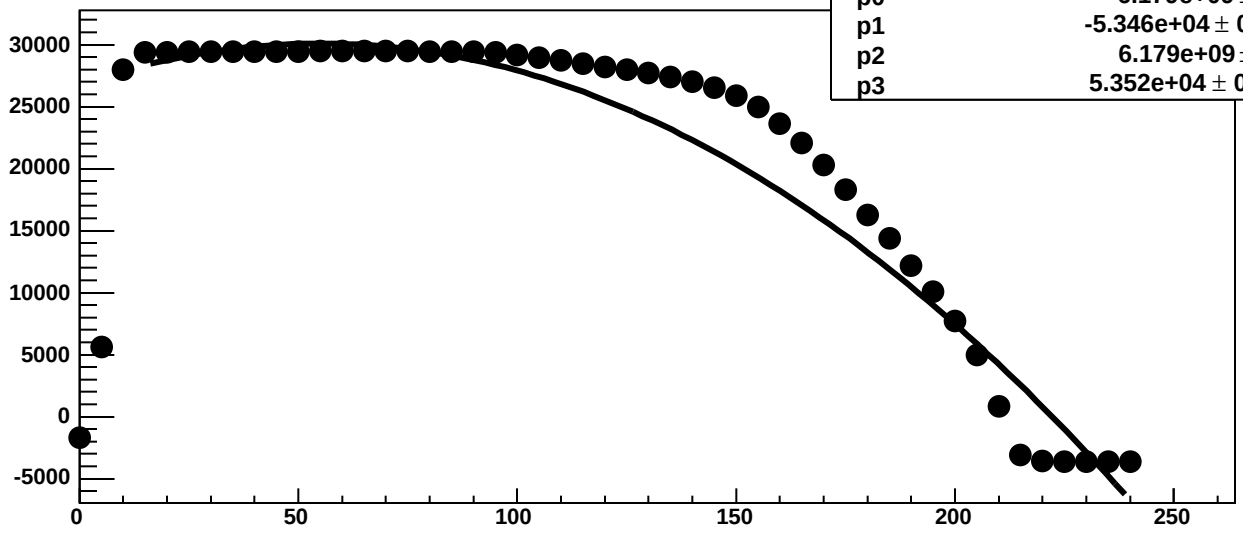
Chip 3, Channel 8, Enable 4, DAC=1600, ADC Noise vs Hold



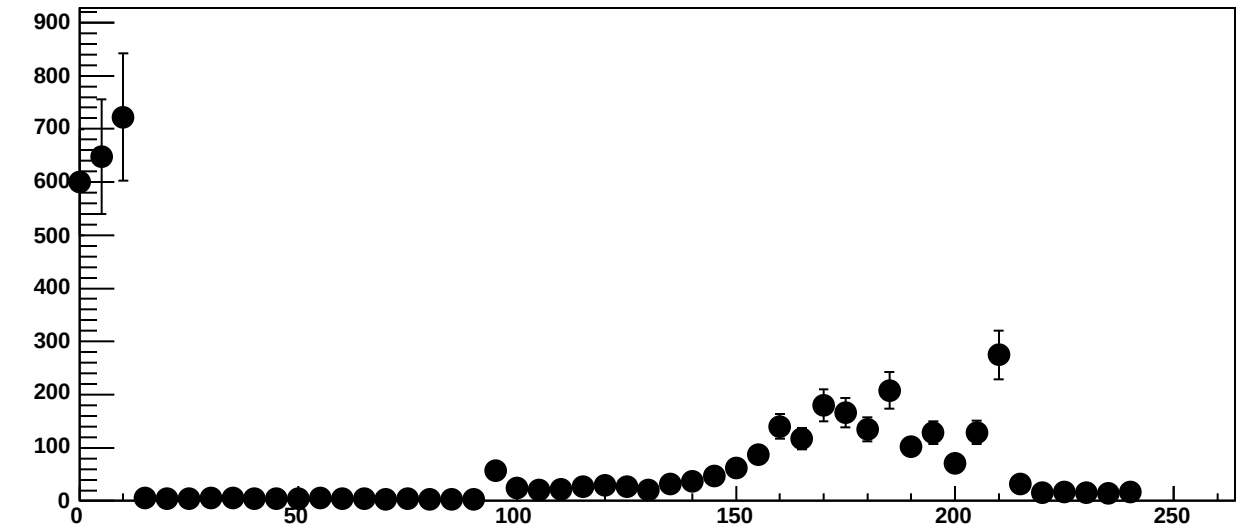
Chip 3, Channel 8, Enable 4, DAC=1600, ADC Residuals vs Hold



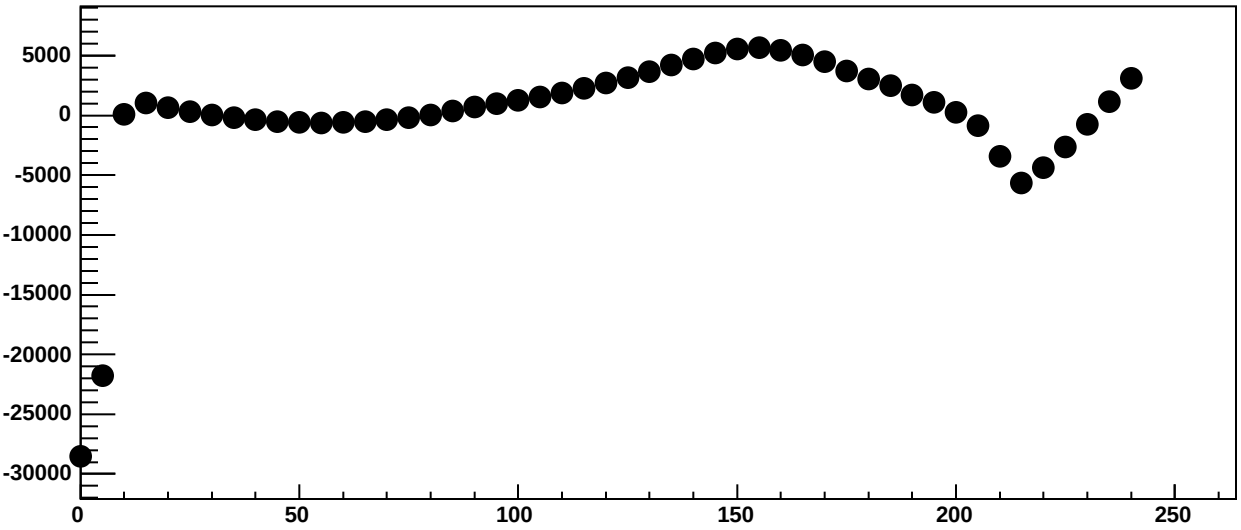
Chip 3, Channel 8, Enable 5!, DAC=1600, ADC Mean vs Hold



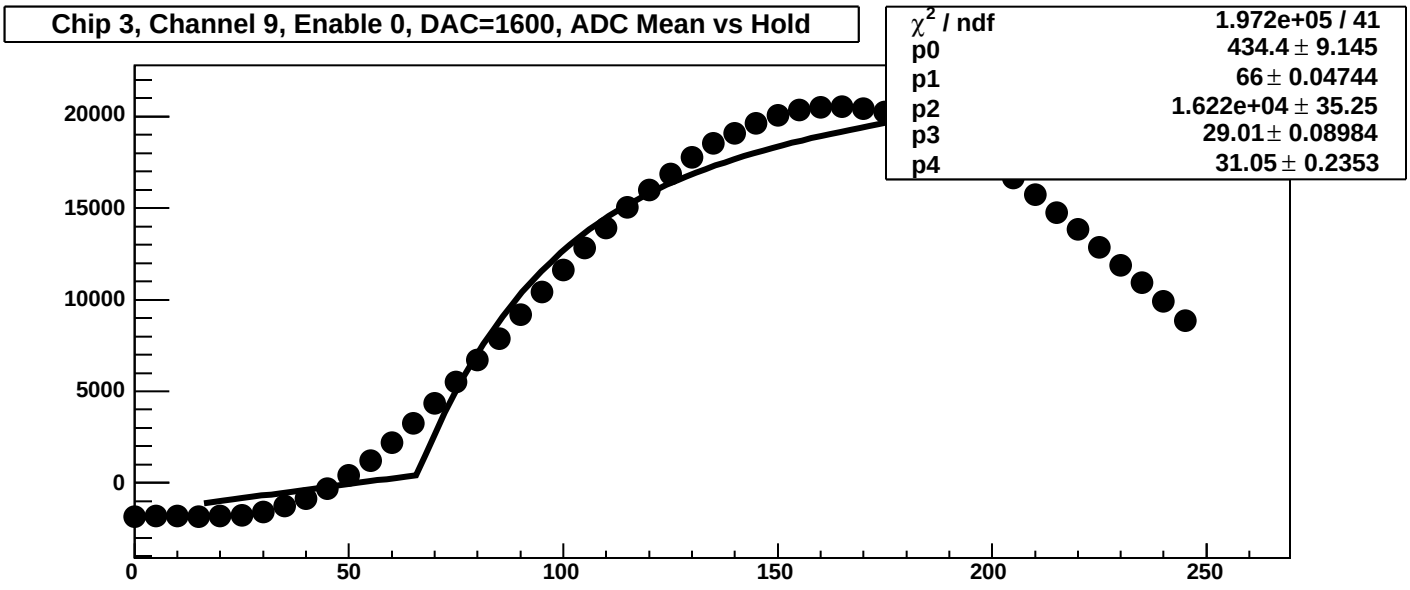
Chip 3, Channel 8, Enable 5!, DAC=1600, ADC Noise vs Hold



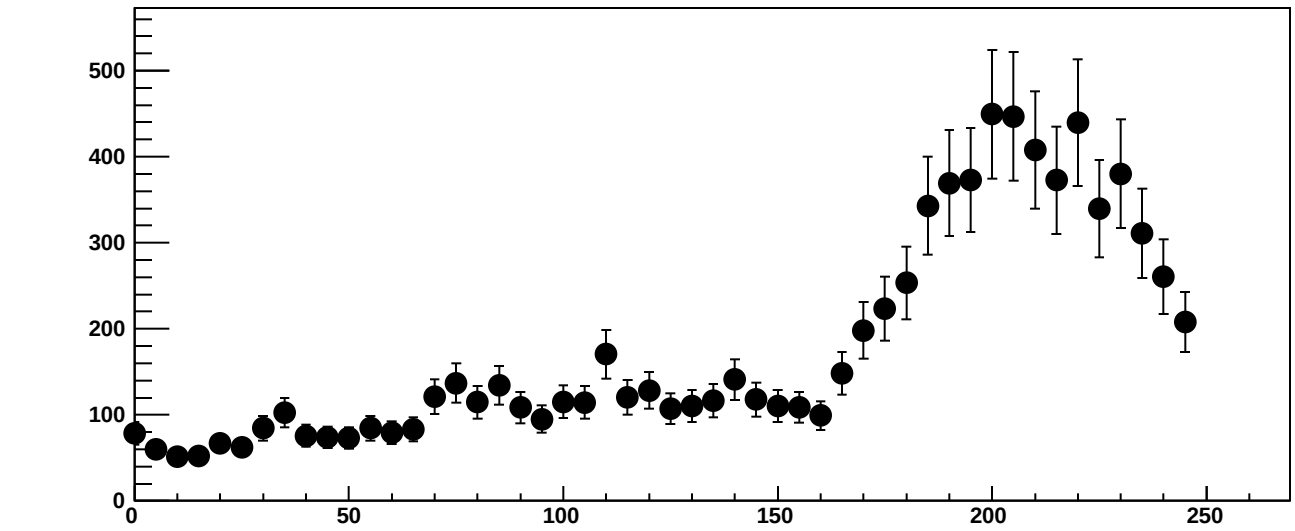
Chip 3, Channel 8, Enable 5!, DAC=1600, ADC Residuals vs Hold



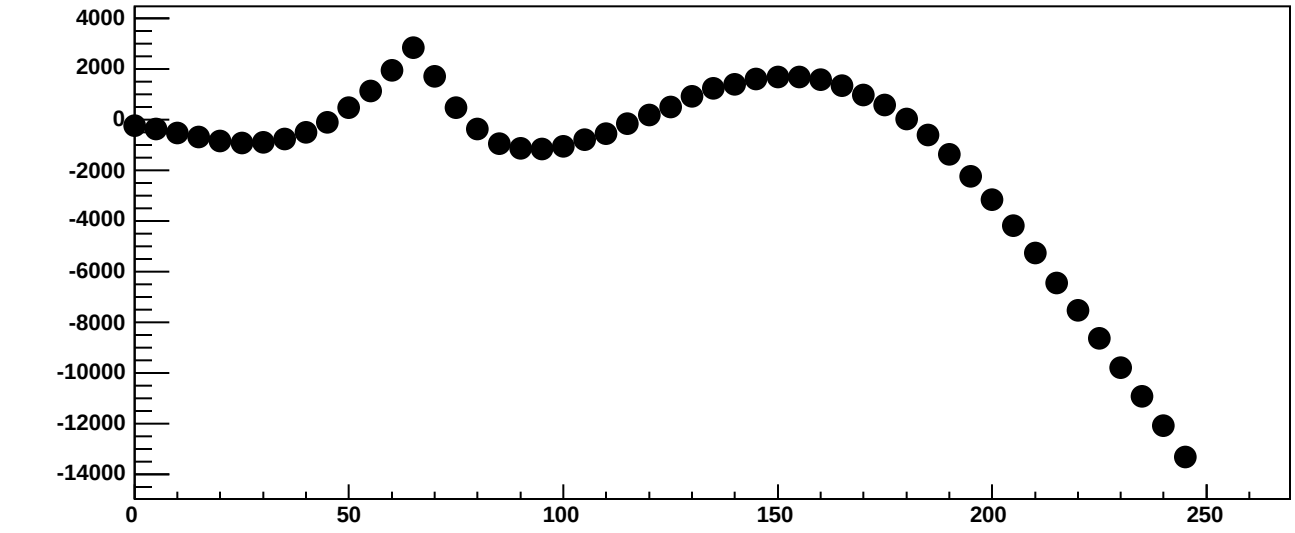
Chip 3, Channel 9, Enable 0, DAC=1600, ADC Mean vs Hold



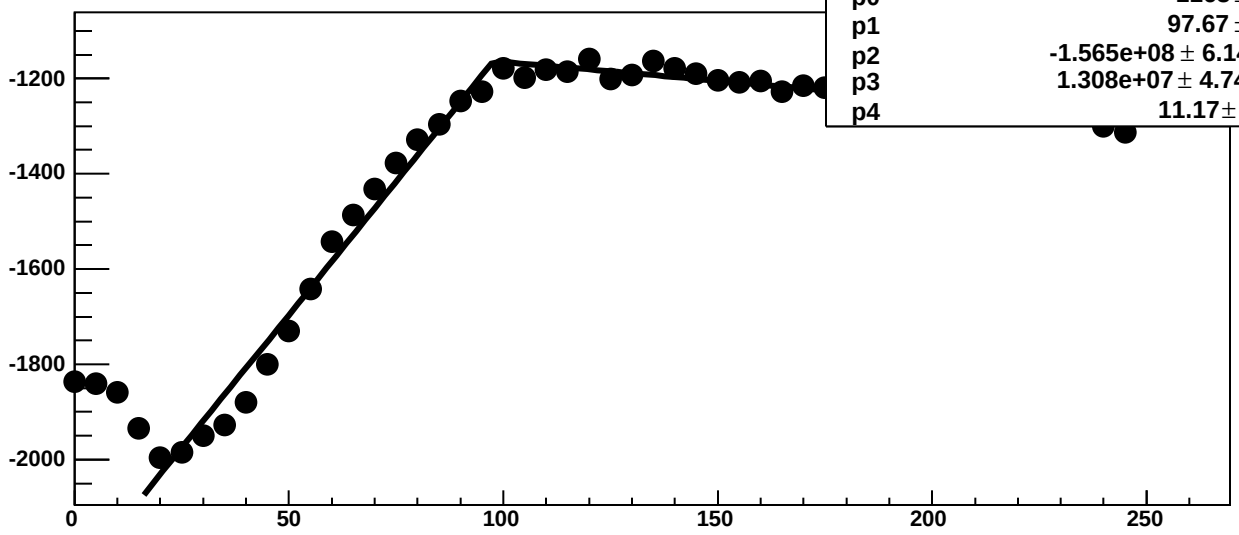
Chip 3, Channel 9, Enable 0, DAC=1600, ADC Noise vs Hold



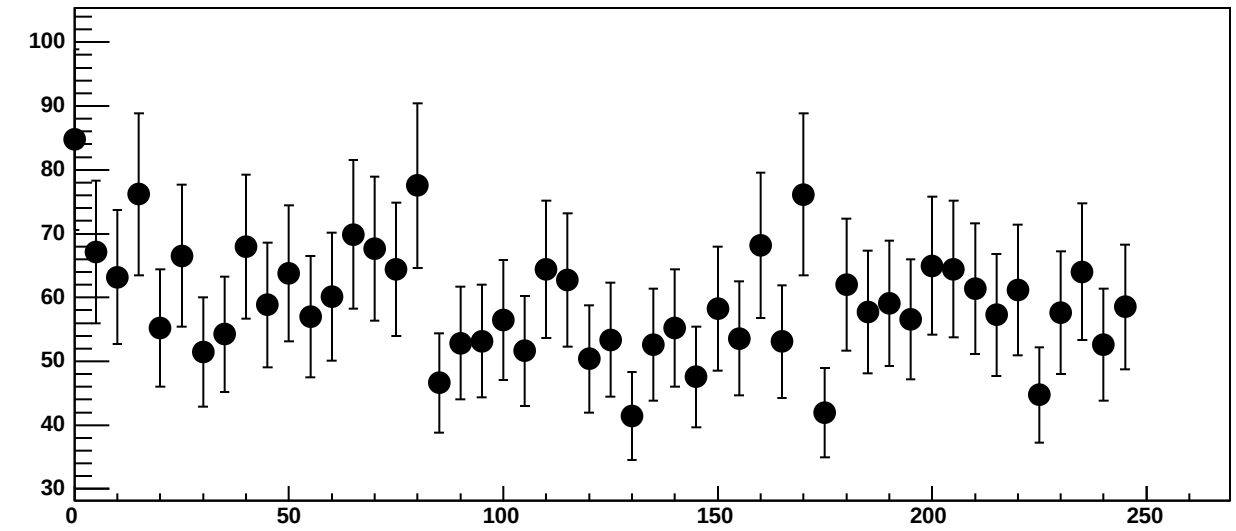
Chip 3, Channel 9, Enable 0, DAC=1600, ADC Residuals vs Hold



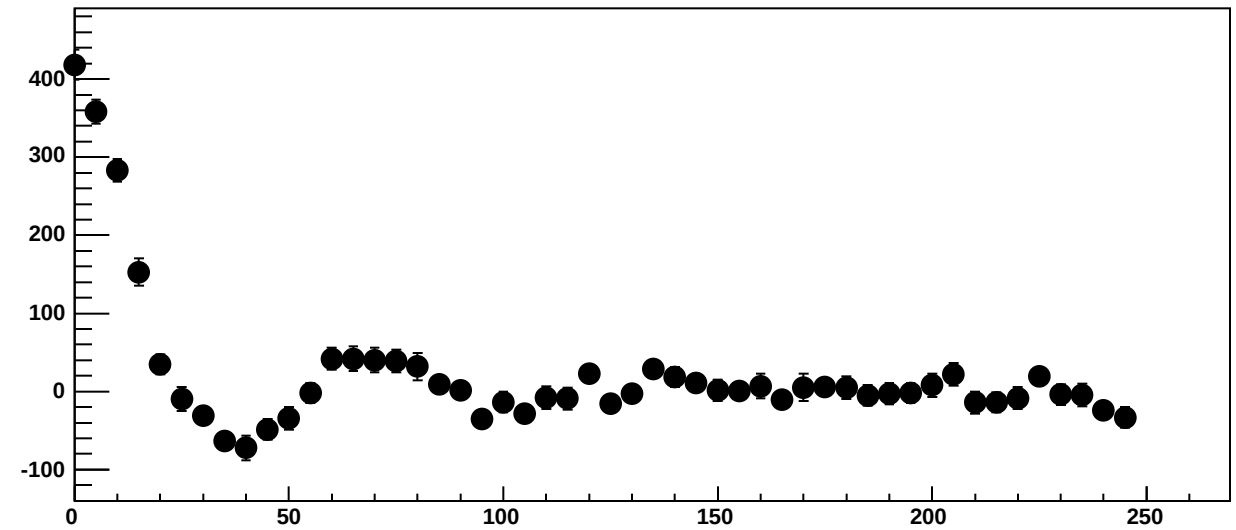
Chip 3, Channel 9, Enable 1, DAC=1600, ADC Mean vs Hold



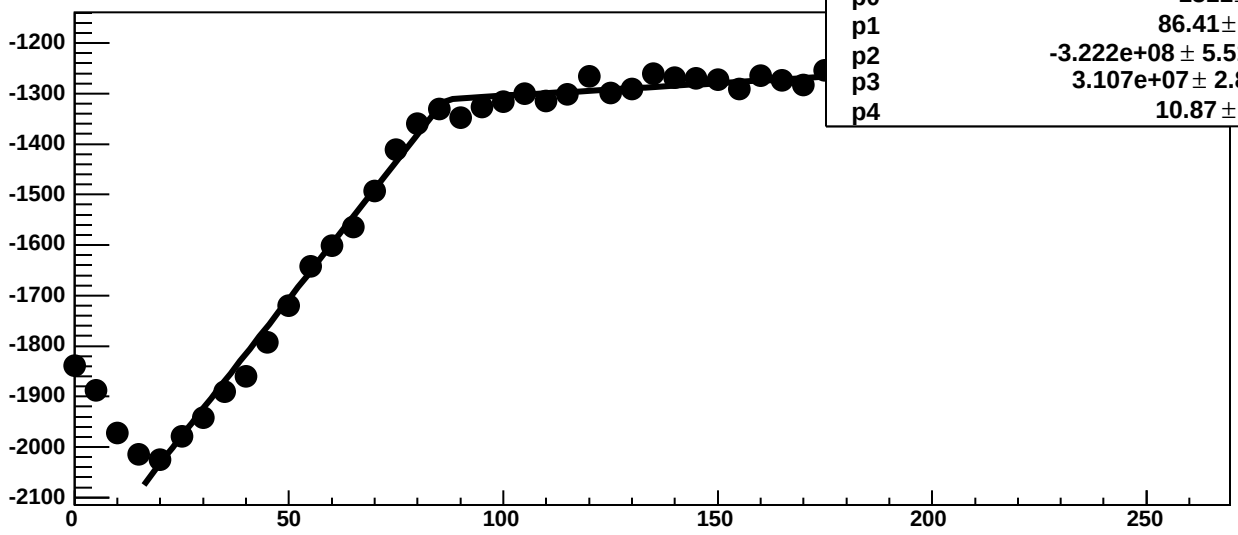
Chip 3, Channel 9, Enable 1, DAC=1600, ADC Noise vs Hold



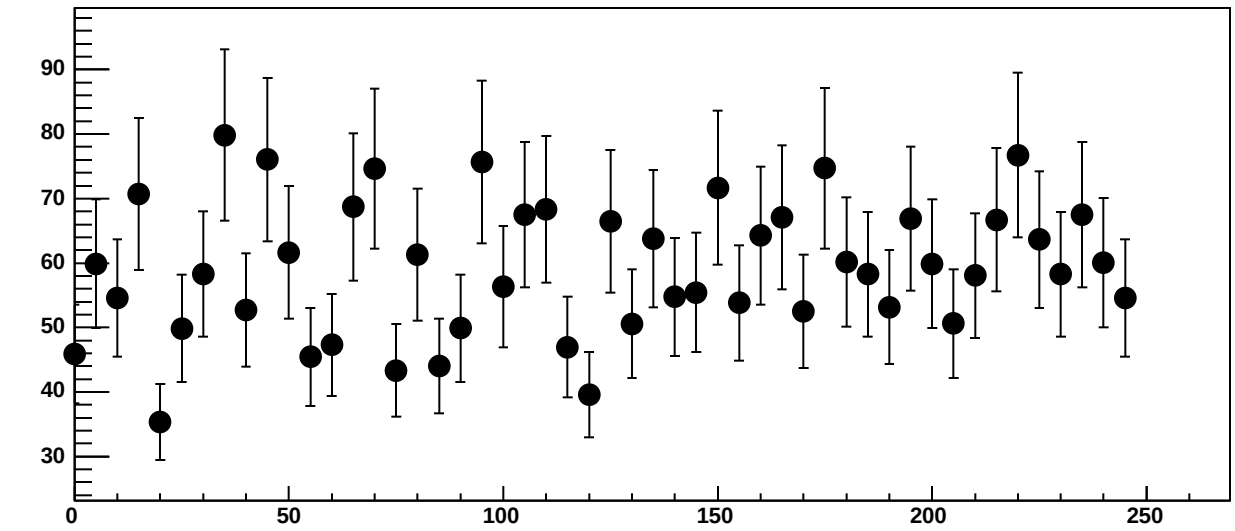
Chip 3, Channel 9, Enable 1, DAC=1600, ADC Residuals vs Hold



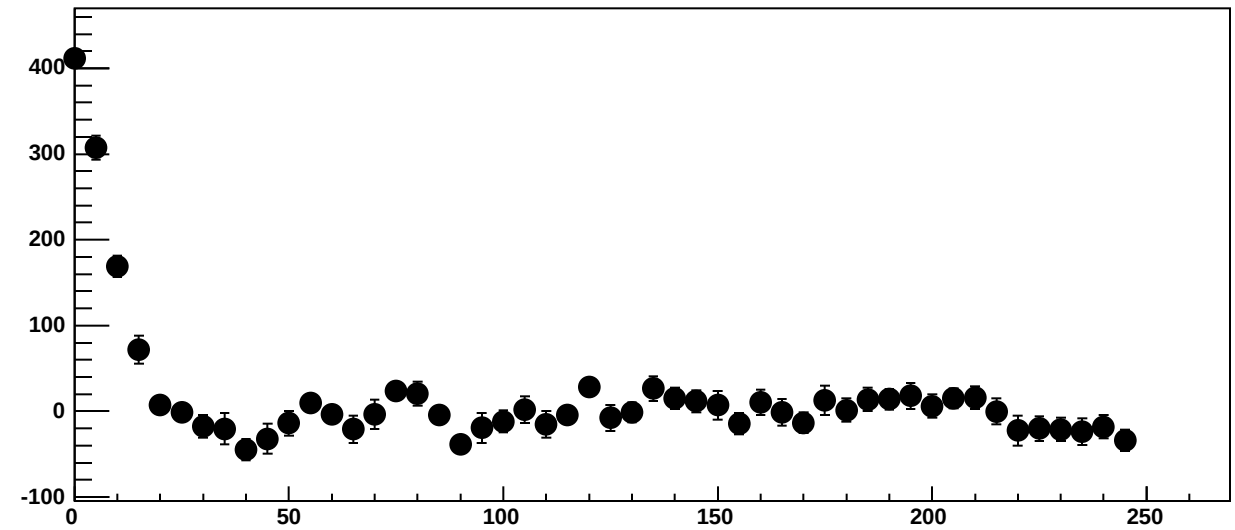
Chip 3, Channel 9, Enable 2, DAC=1600, ADC Mean vs Hold



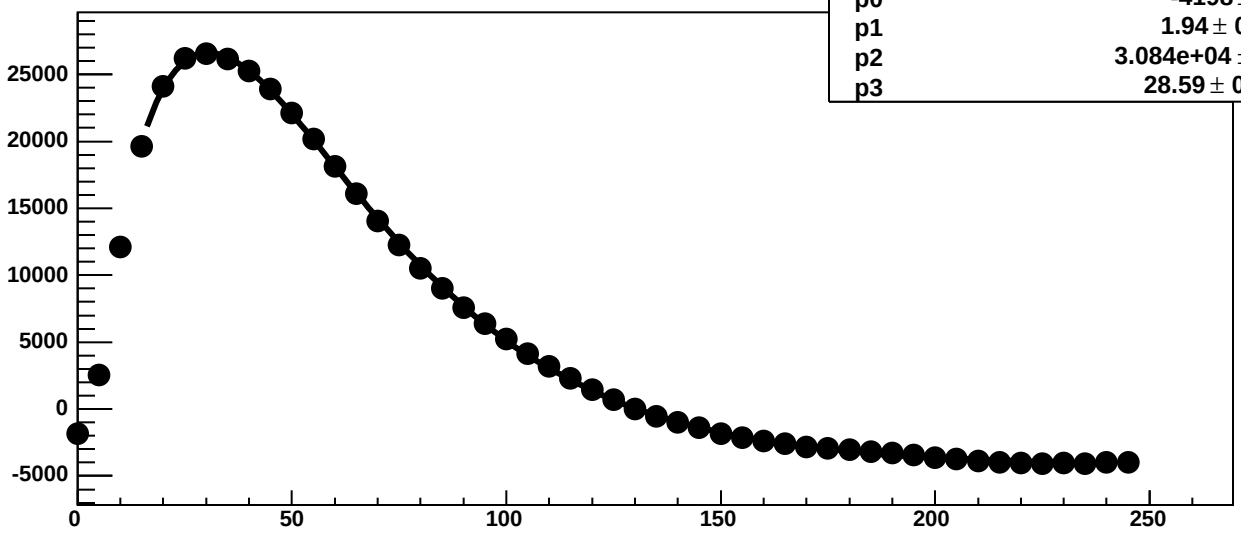
Chip 3, Channel 9, Enable 2, DAC=1600, ADC Noise vs Hold



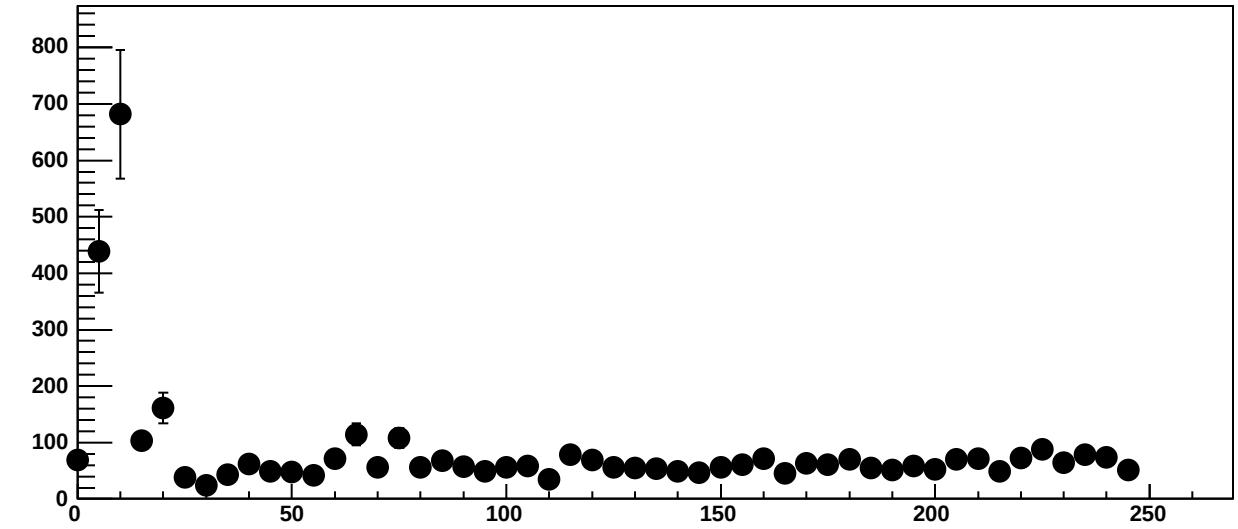
Chip 3, Channel 9, Enable 2, DAC=1600, ADC Residuals vs Hold



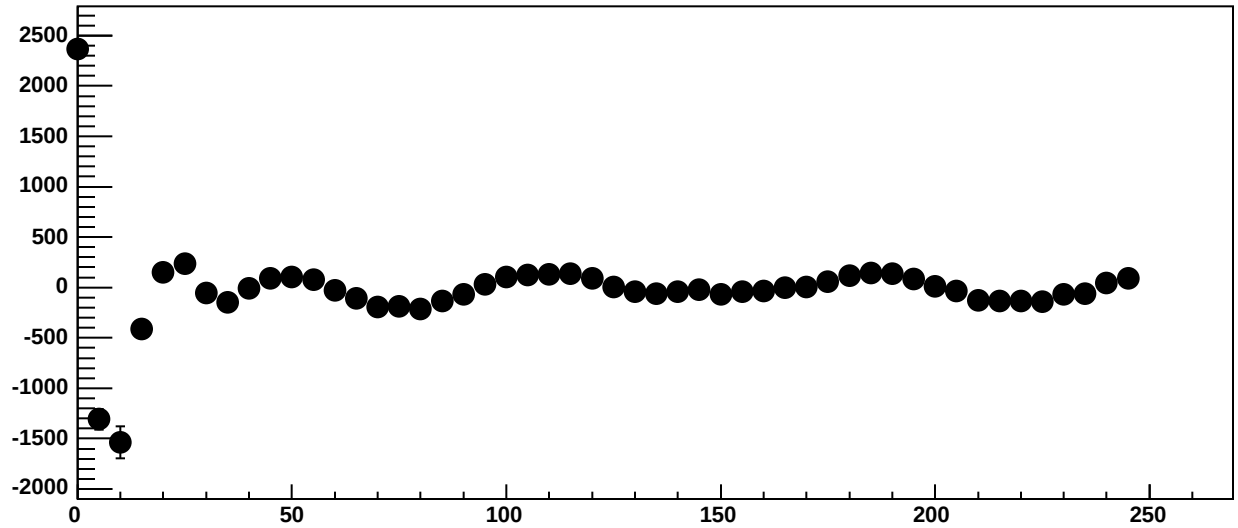
Chip 3, Channel 9, Enable 3!, DAC=1600, ADC Mean vs Hold

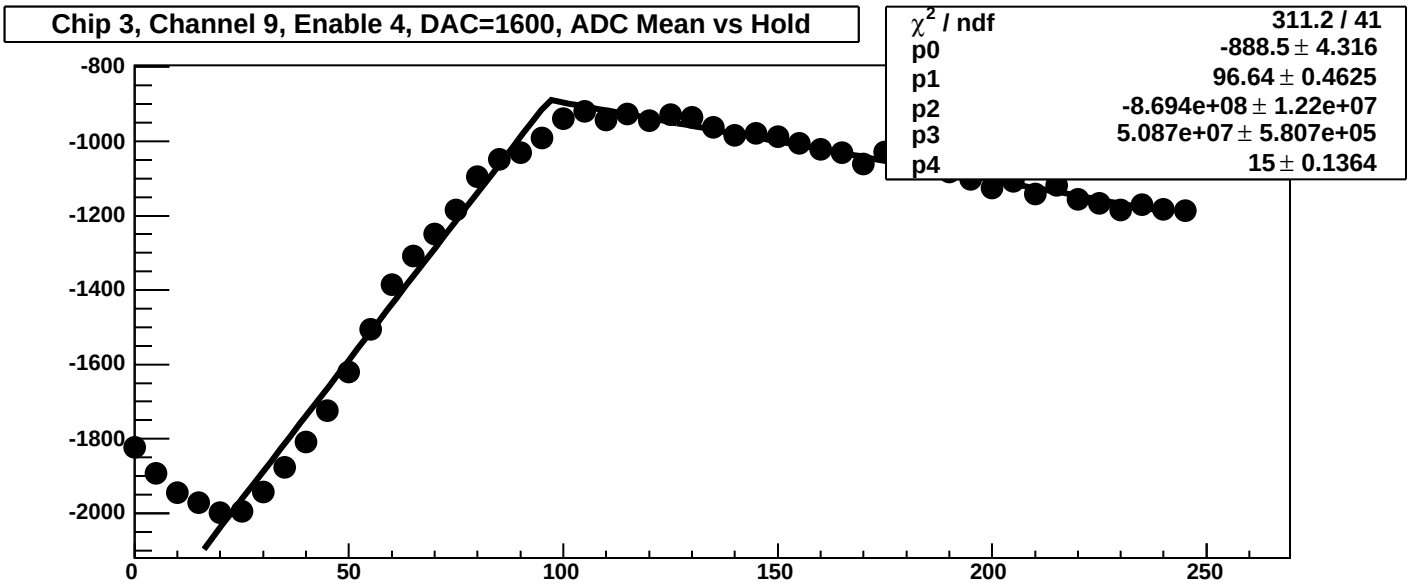


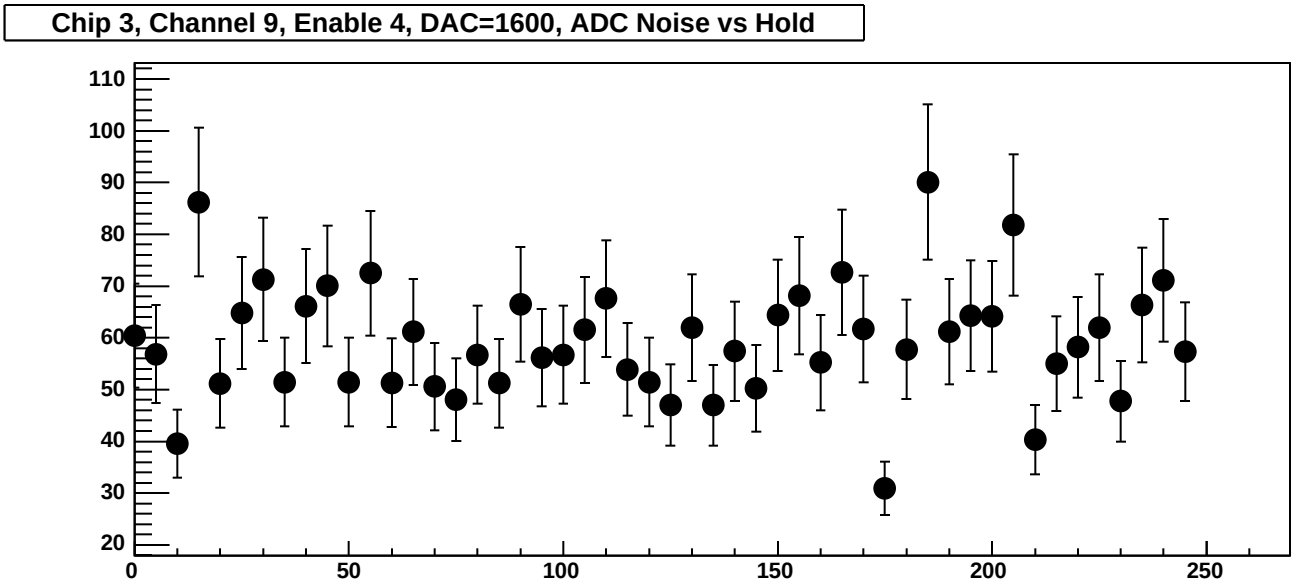
Chip 3, Channel 9, Enable 3!, DAC=1600, ADC Noise vs Hold

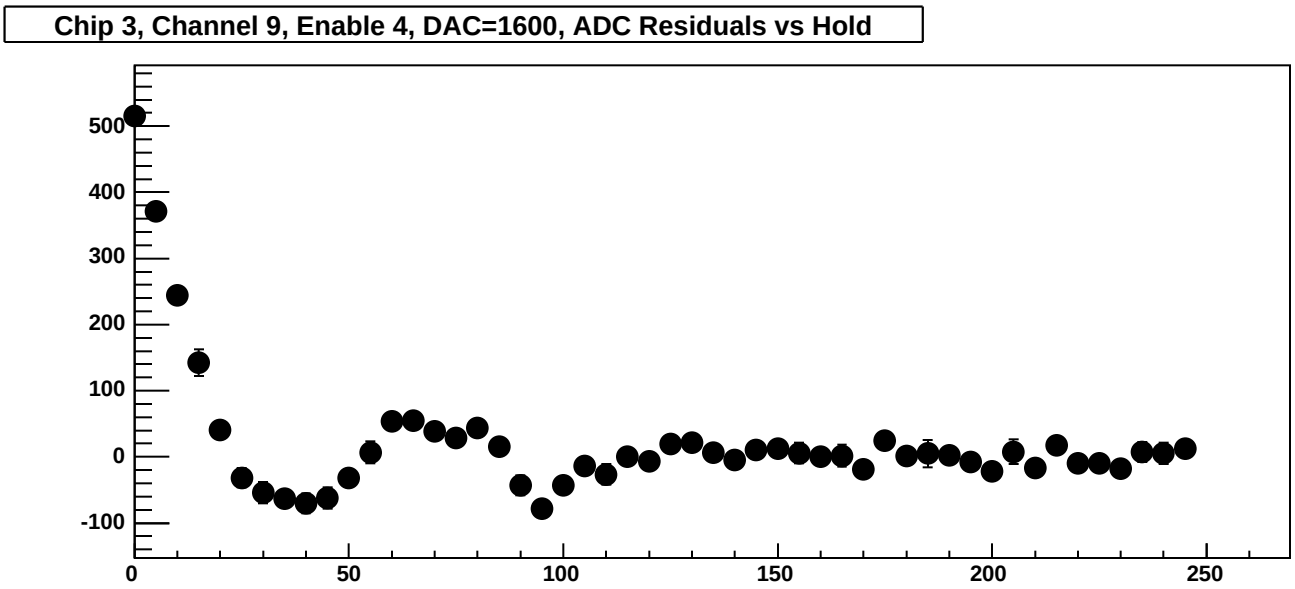


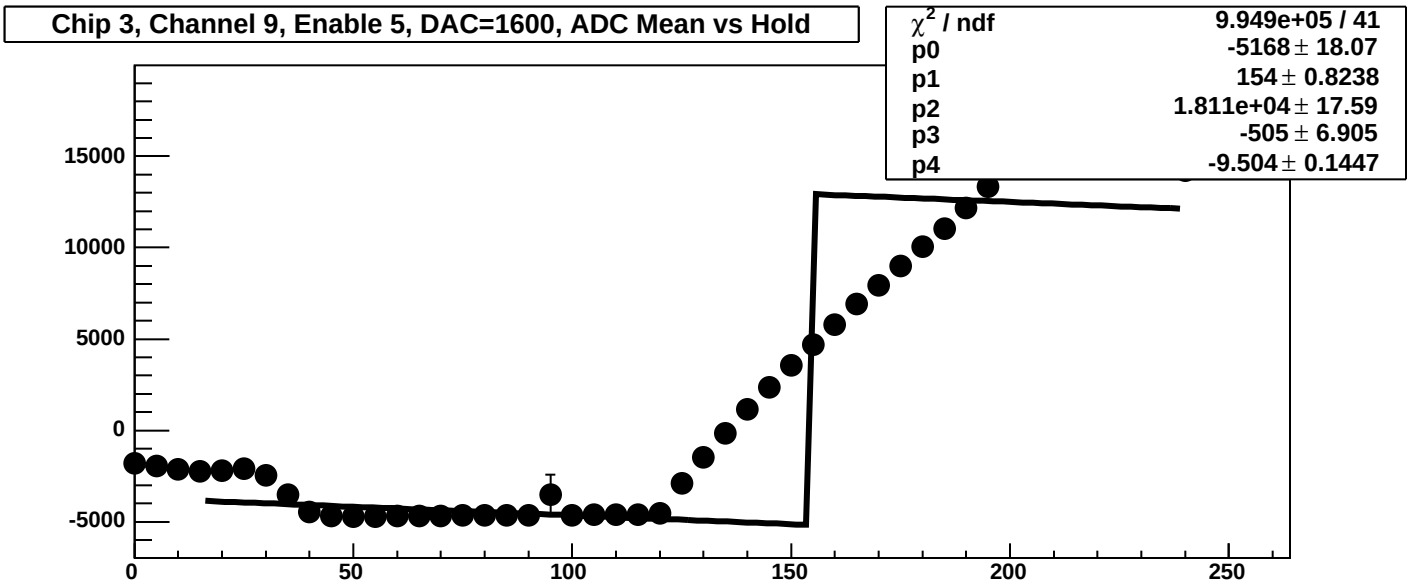
Chip 3, Channel 9, Enable 3!, DAC=1600, ADC Residuals vs Hold

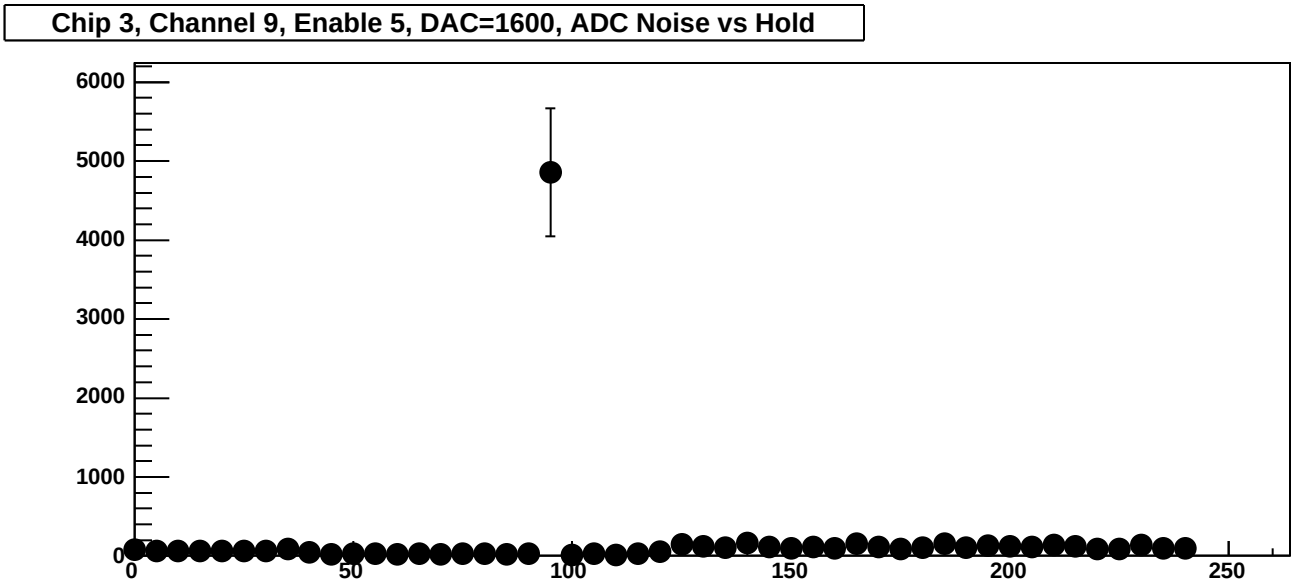


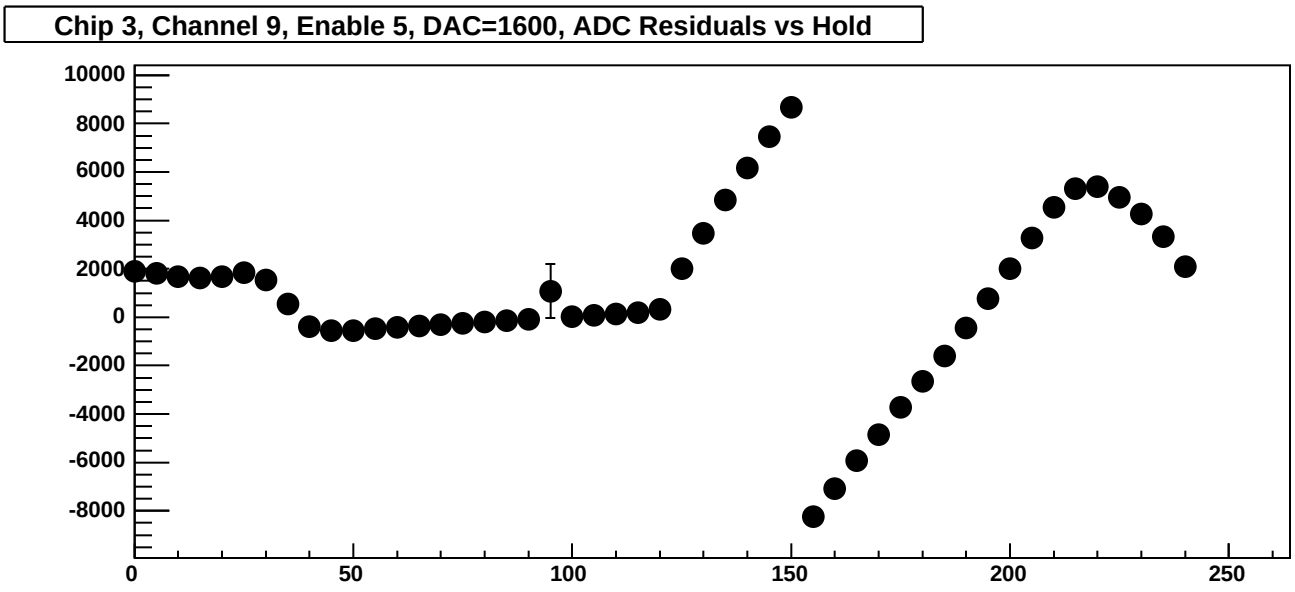

Chip 3, Channel 9, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 311.2 / 41
p0 -888.5 ± 4.316
p1 96.64 ± 0.4625
p2 -8.694e+08 ± 1.22e+07
p3 5.087e+07 ± 5.807e+05
p4 15 ± 0.1364
-800
-1000
-1200
-1400
-1600
-1800
-2000
0
50
100
150
200
250


Chip 3, Channel 9, Enable 4, DAC=1600, ADC Noise vs Hold
110
100
90
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 3, Channel 9, Enable 4, DAC=1600, ADC Residuals vs Hold
500
400
300
200
100
0
-100
0
50
100
150
200
250

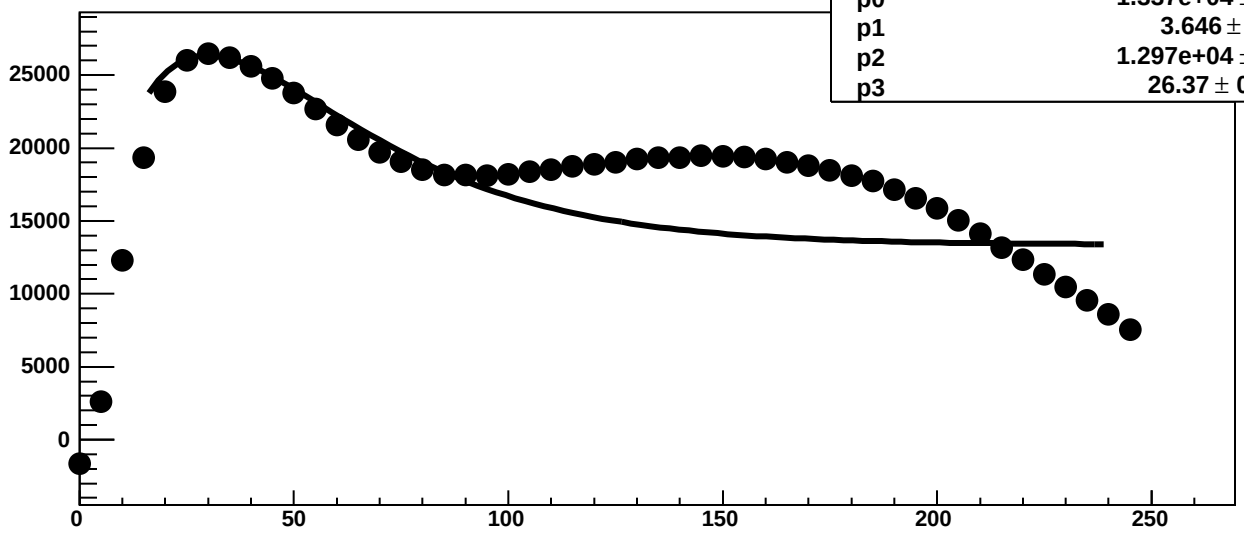

Chip 3, Channel 9, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 9.949e+05 / 41
p0 -5168 ± 18.07
p1 154 ± 0.8238
p2 1.811e+04 ± 17.59
p3 -505 ± 6.905
p4 -9.504 ± 0.1447
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 3, Channel 9, Enable 5, DAC=1600, ADC Noise vs Hold
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

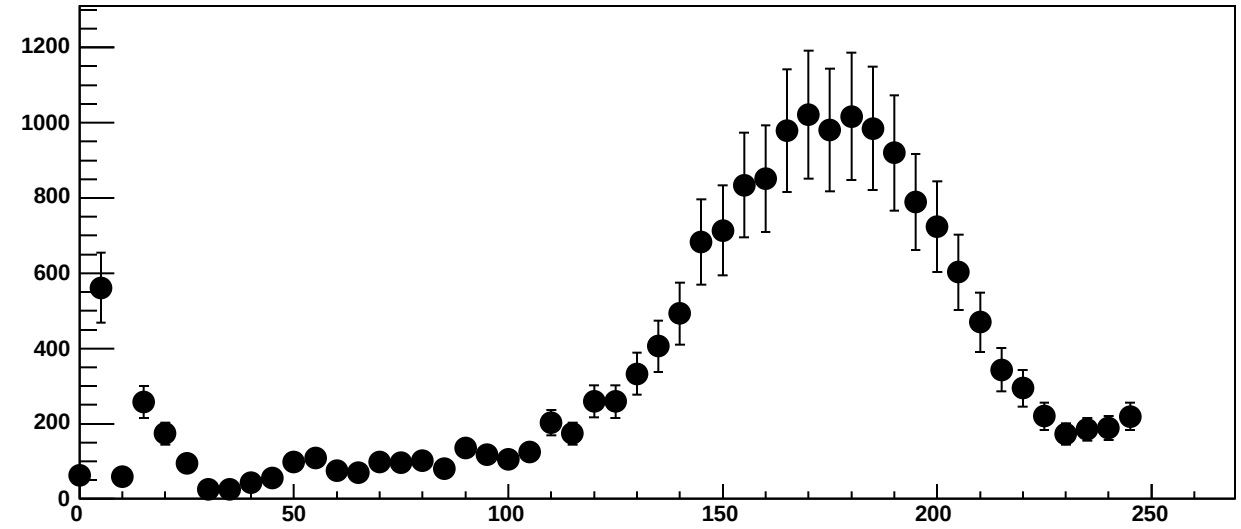

Chip 3, Channel 9, Enable 5, DAC=1600, ADC Residuals vs Hold
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250

Chip 3, Channel 10, Enable 0!, DAC=1600, ADC Mean vs Hold

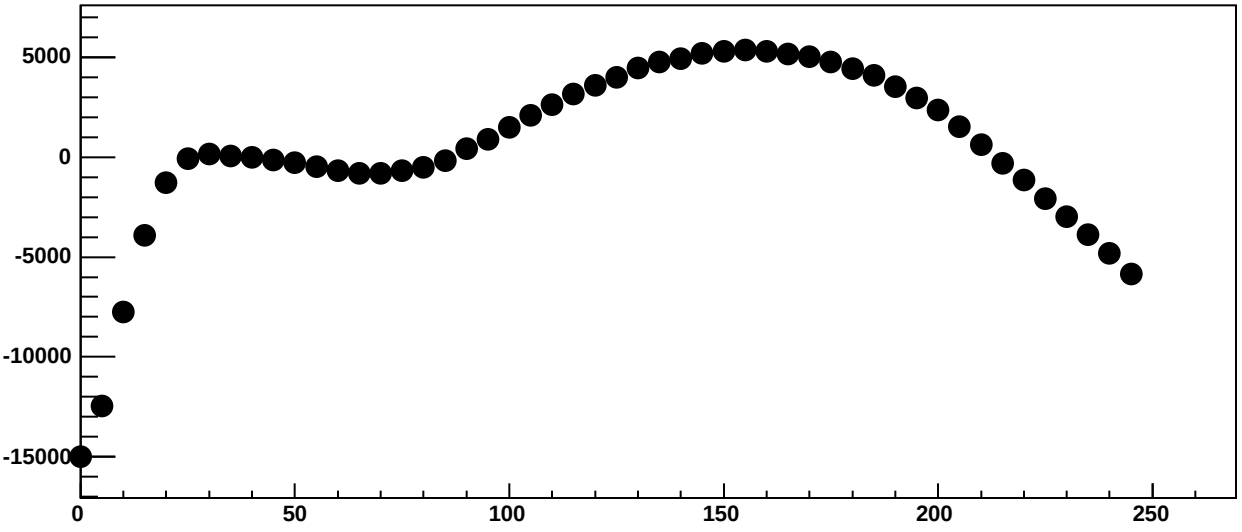
χ^2 / ndf	8.502e+04 / 42
p0	1.337e+04 ± 23.05
p1	3.646 ± 0.1303
p2	1.297e+04 ± 23.04
p3	26.37 ± 0.08701



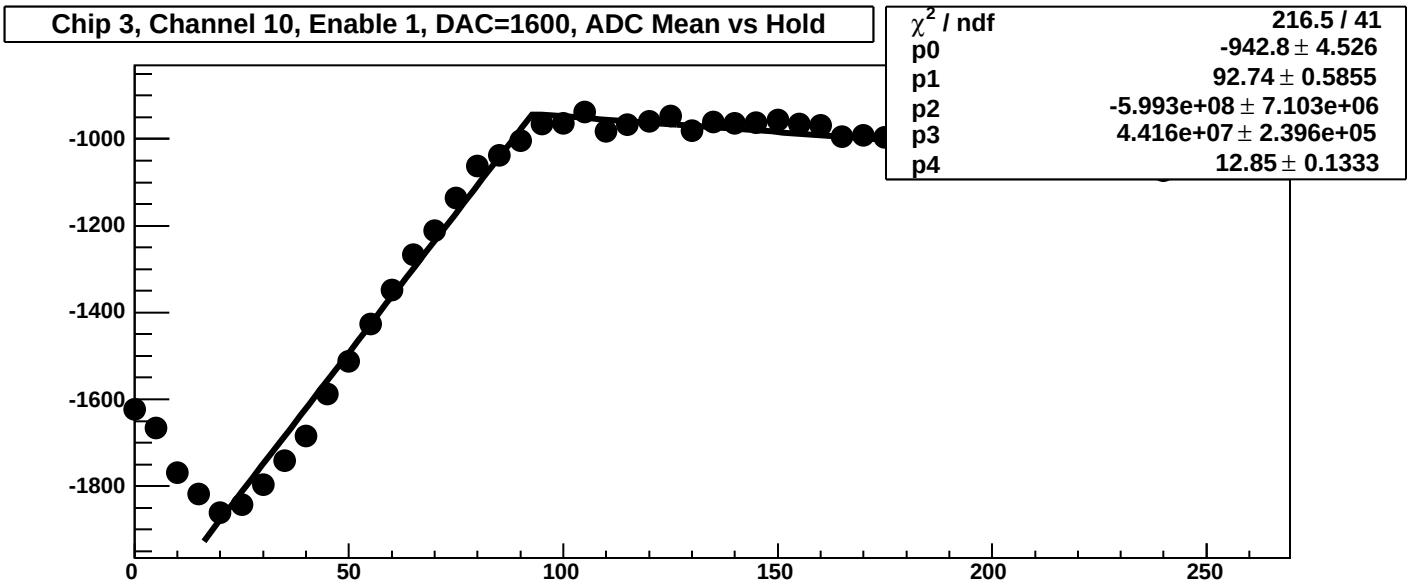
Chip 3, Channel 10, Enable 0!, DAC=1600, ADC Noise vs Hold



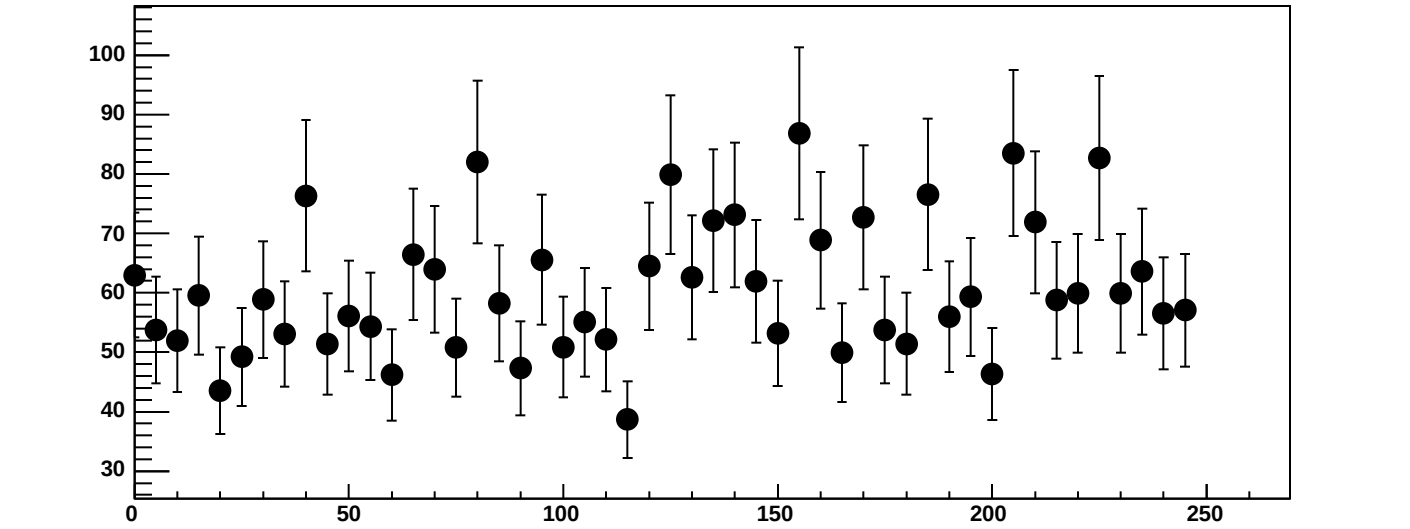
Chip 3, Channel 10, Enable 0!, DAC=1600, ADC Residuals vs Hold



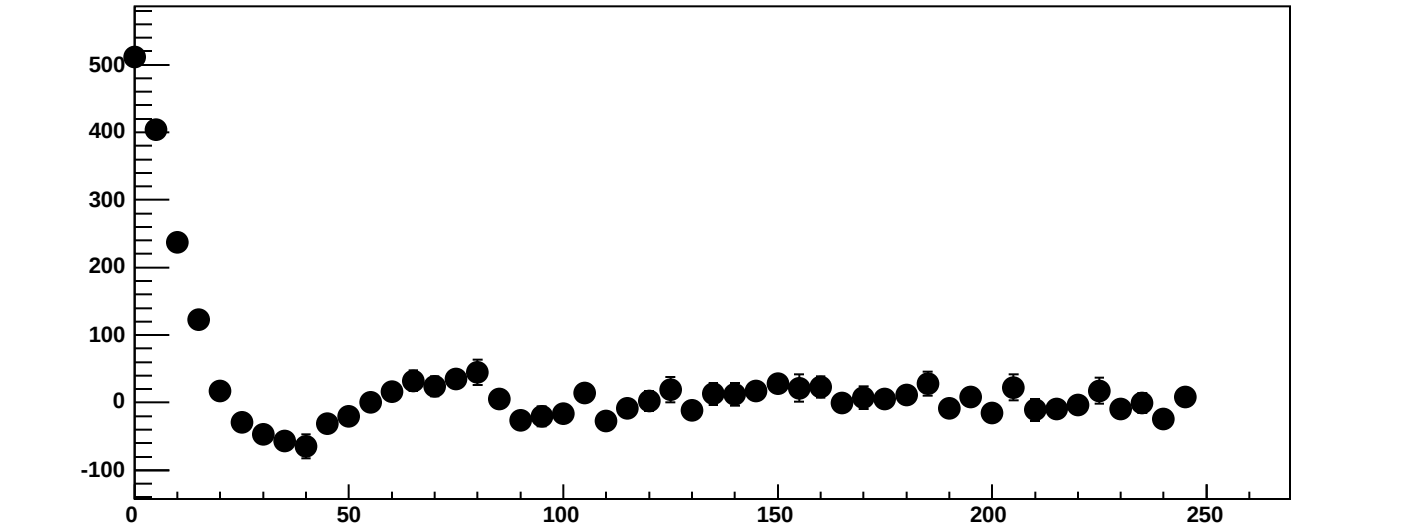
Chip 3, Channel 10, Enable 1, DAC=1600, ADC Mean vs Hold



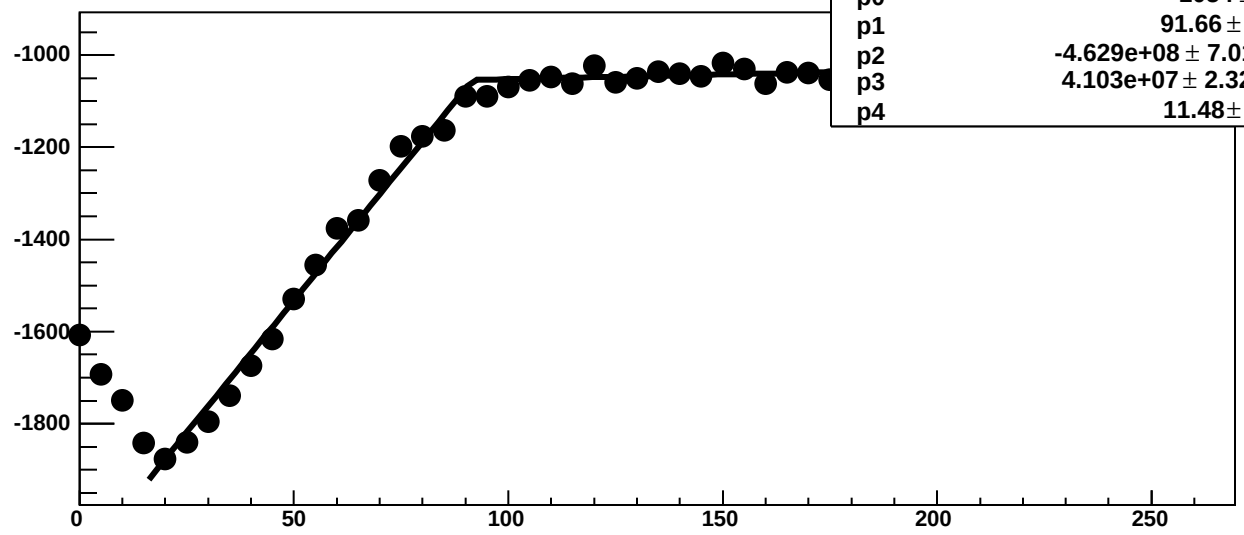
Chip 3, Channel 10, Enable 1, DAC=1600, ADC Noise vs Hold



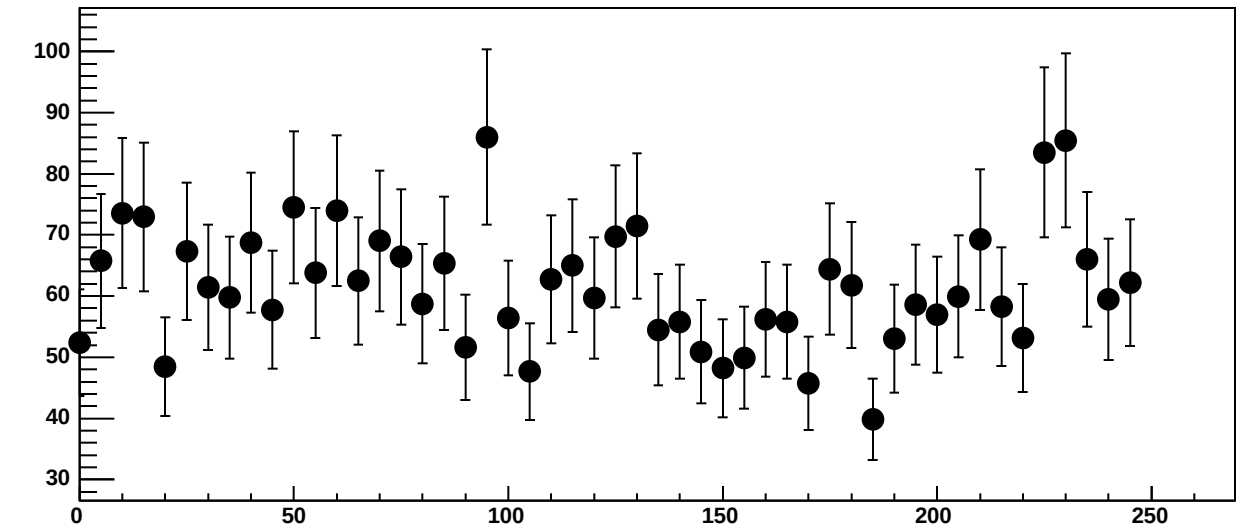
Chip 3, Channel 10, Enable 1, DAC=1600, ADC Residuals vs Hold



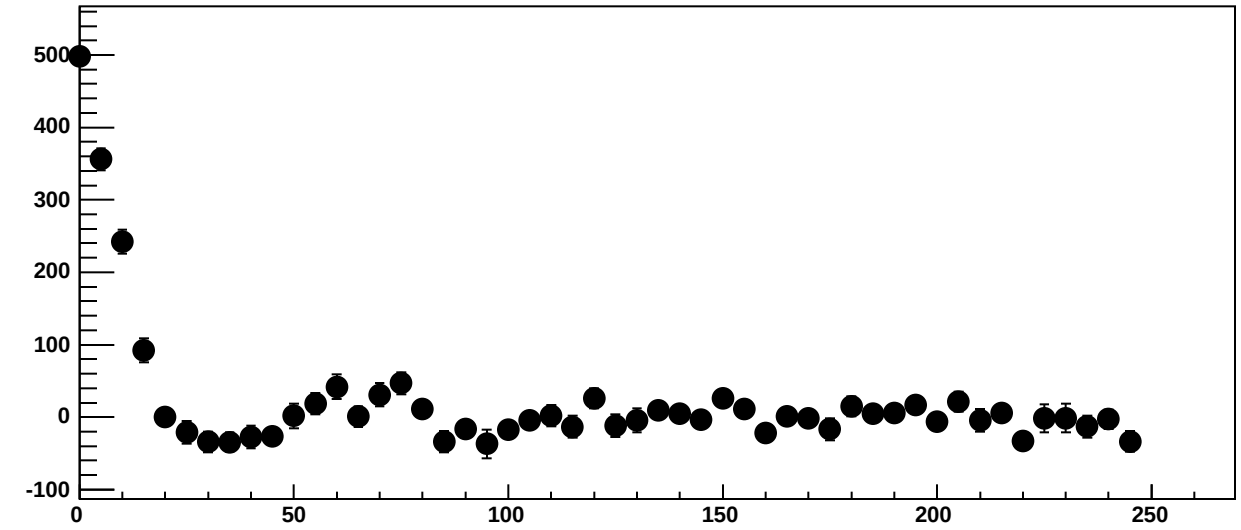
Chip 3, Channel 10, Enable 2, DAC=1600, ADC Mean vs Hold

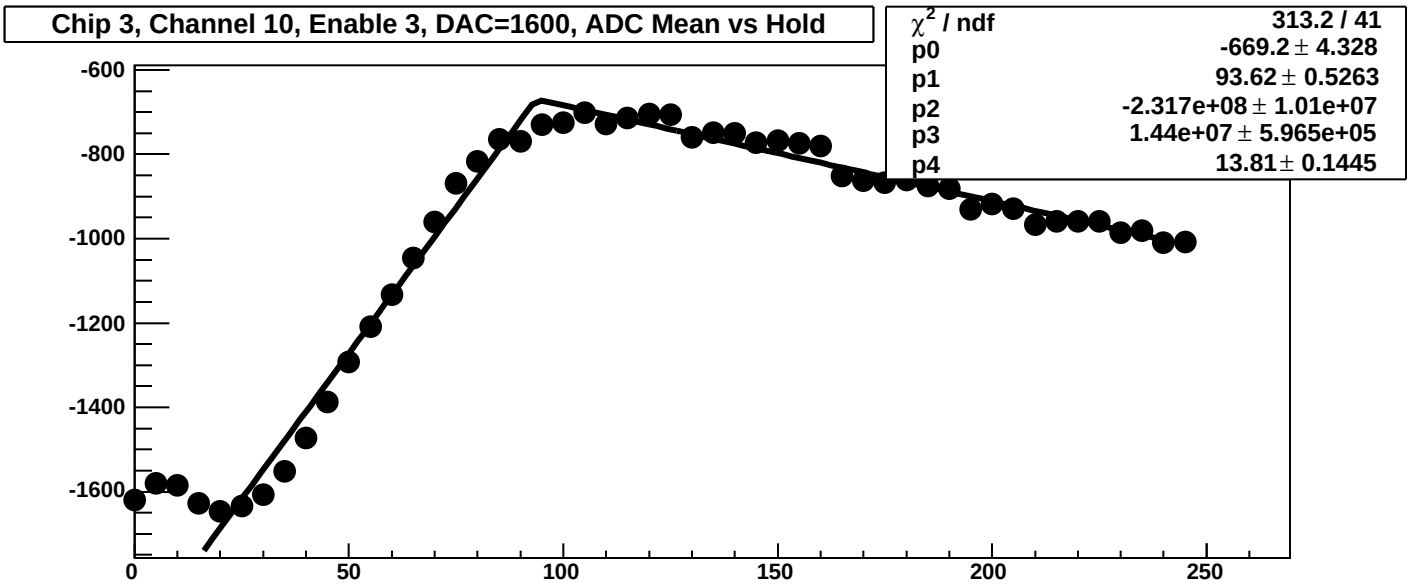


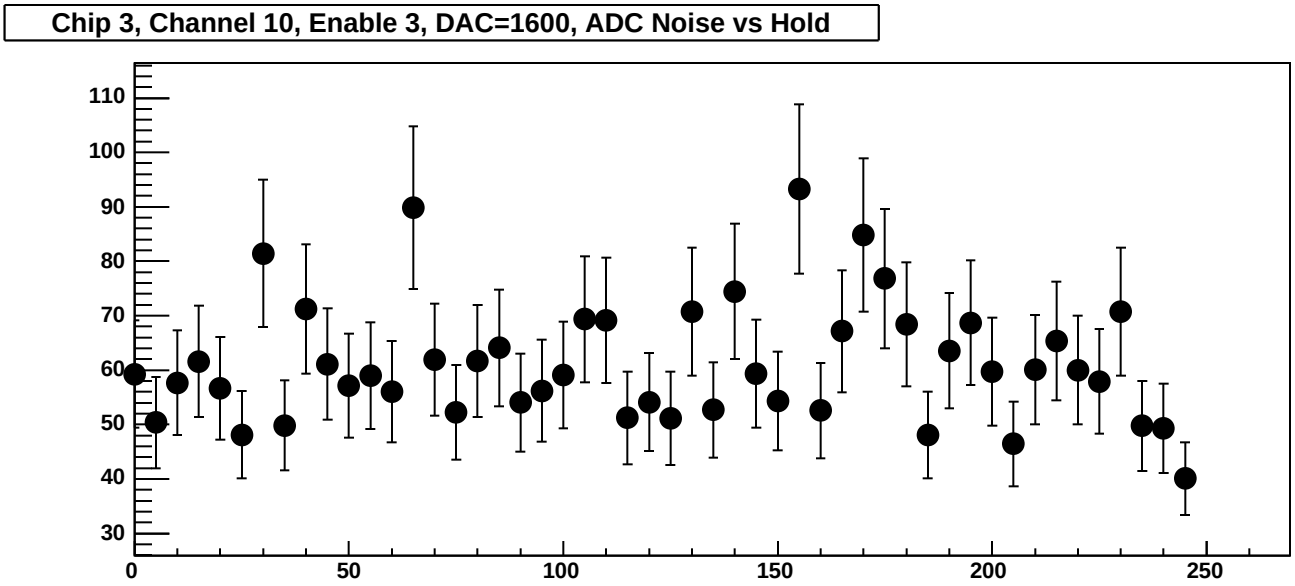
Chip 3, Channel 10, Enable 2, DAC=1600, ADC Noise vs Hold

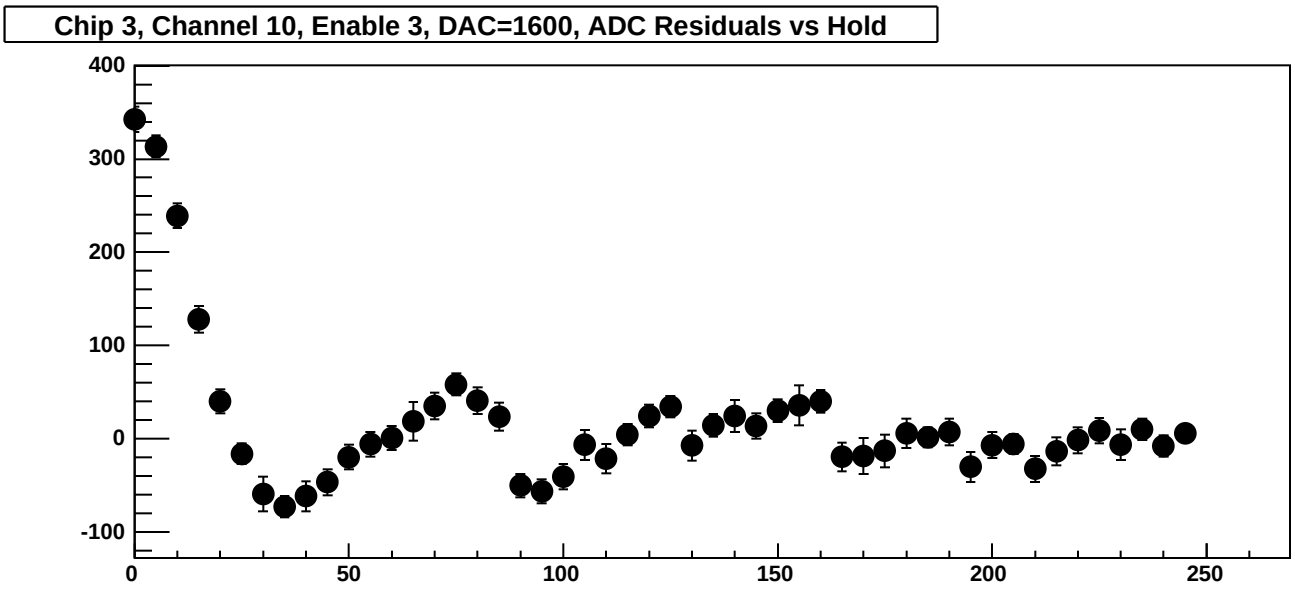


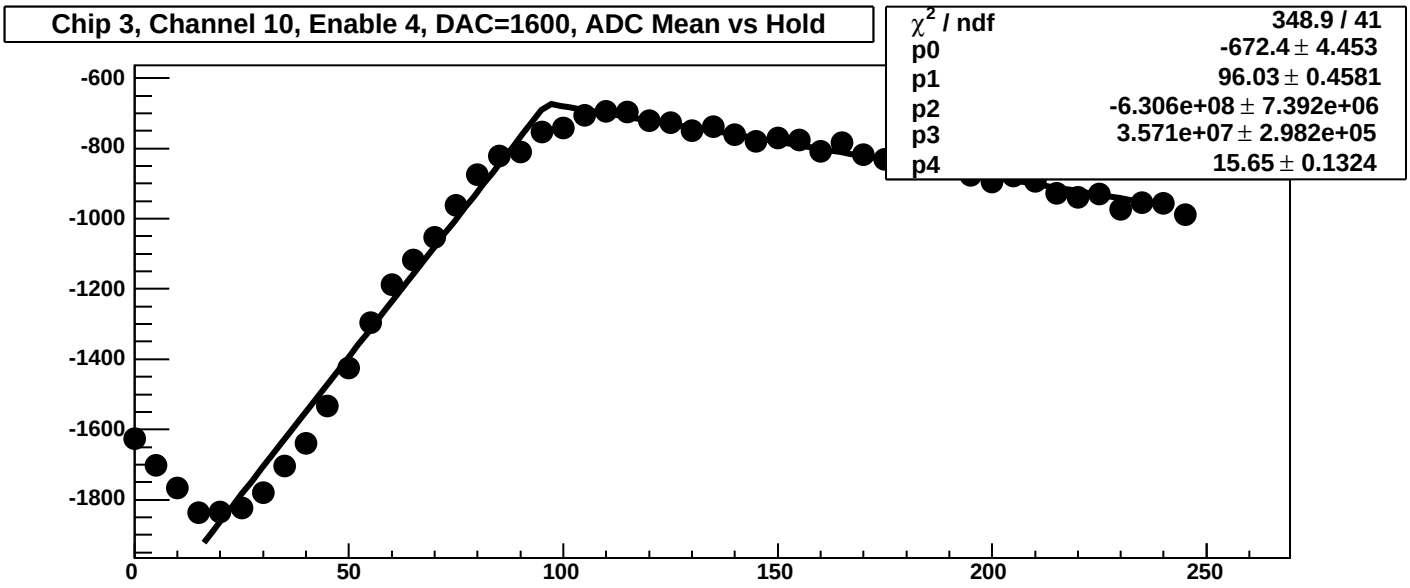
Chip 3, Channel 10, Enable 2, DAC=1600, ADC Residuals vs Hold

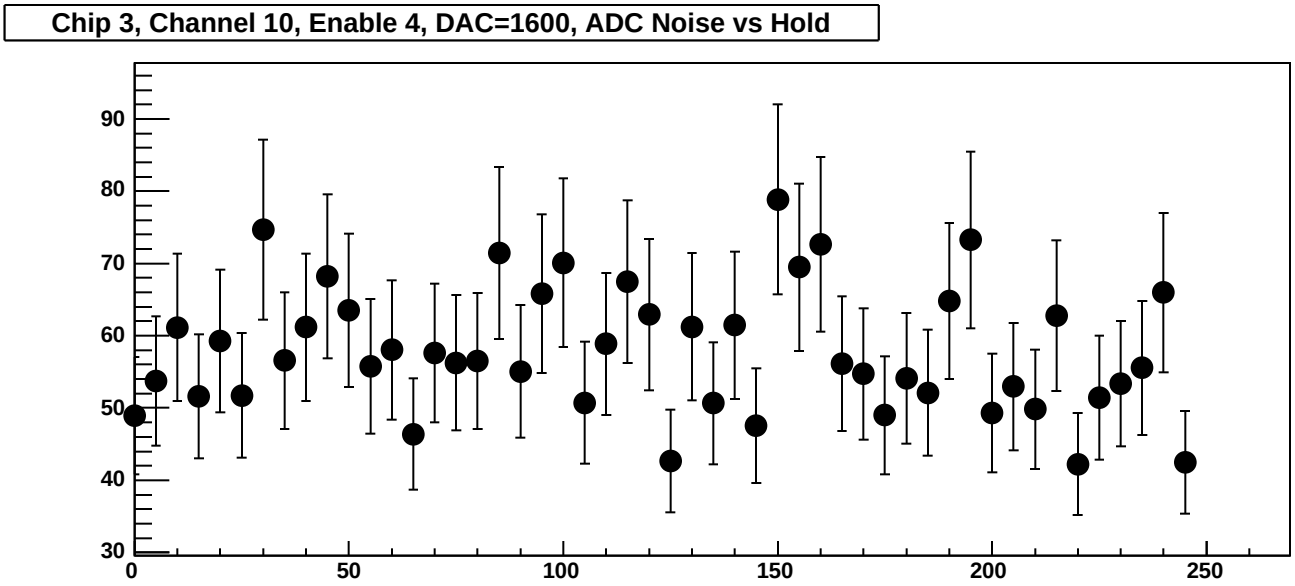


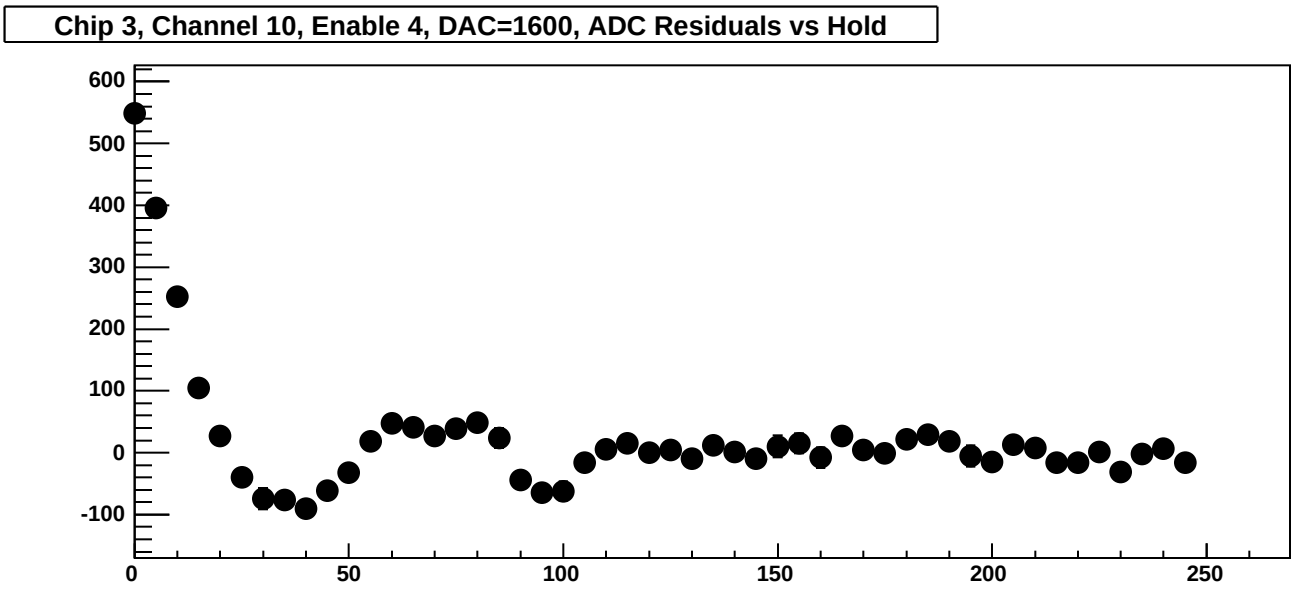

Chip 3, Channel 10, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 313.2 / 41
p0 -669.2 ± 4.328
p1 93.62 ± 0.5263
p2 -2.317e+08 ± 1.01e+07
p3 1.44e+07 ± 5.965e+05
p4 13.81 ± 0.1445
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250


Chip 3, Channel 10, Enable 3, DAC=1600, ADC Noise vs Hold
110
100
90
80
70
60
50
40
30
0
50
100
150
200
250

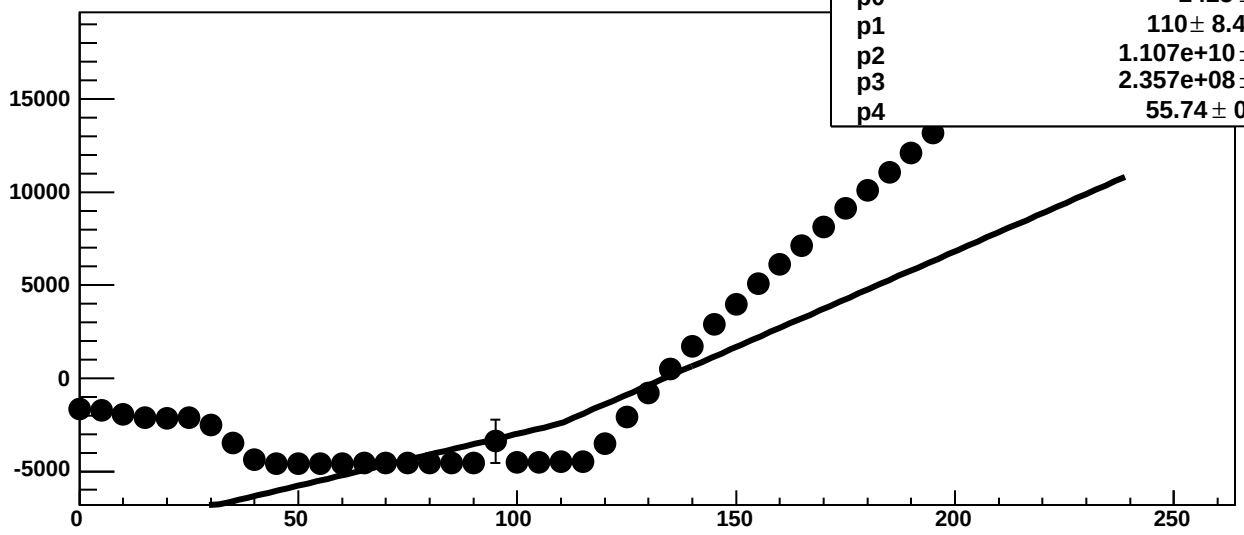

Chip 3, Channel 10, Enable 3, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 3, Channel 10, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 348.9 / 41
p0 -672.4 ± 4.453
p1 96.03 ± 0.4581
p2 -6.306e+08 ± 7.392e+06
p3 3.571e+07 ± 2.982e+05
p4 15.65 ± 0.1324
-600
-800
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250

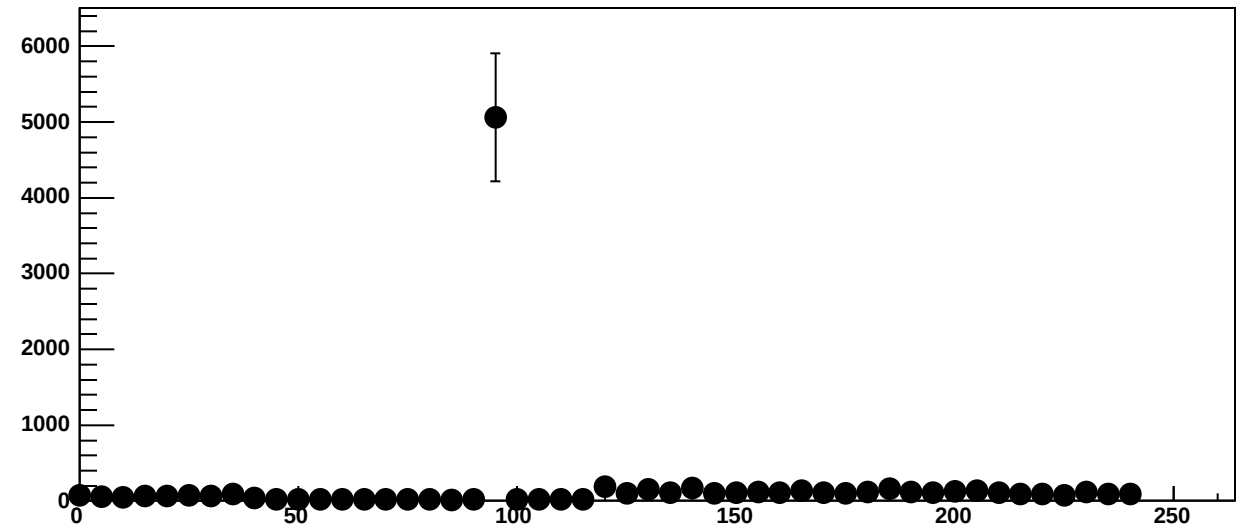

Chip 3, Channel 10, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 3, Channel 10, Enable 4, DAC=1600, ADC Residuals vs Hold
600
500
400
300
200
100
0
-100
0
50
100
150
200
250

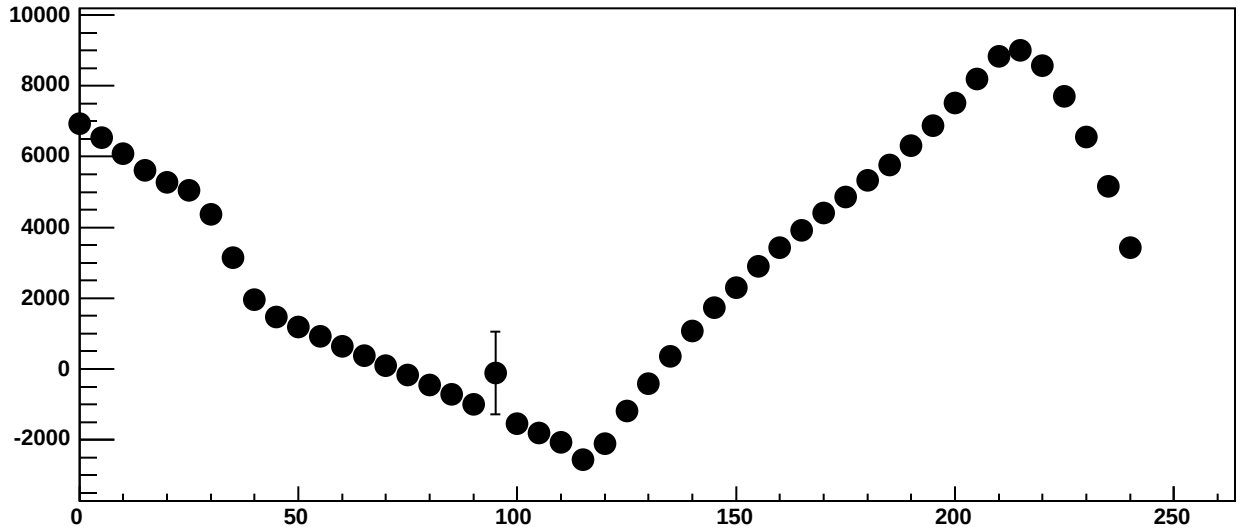
Chip 3, Channel 10, Enable 5, DAC=1600, ADC Mean vs Hold

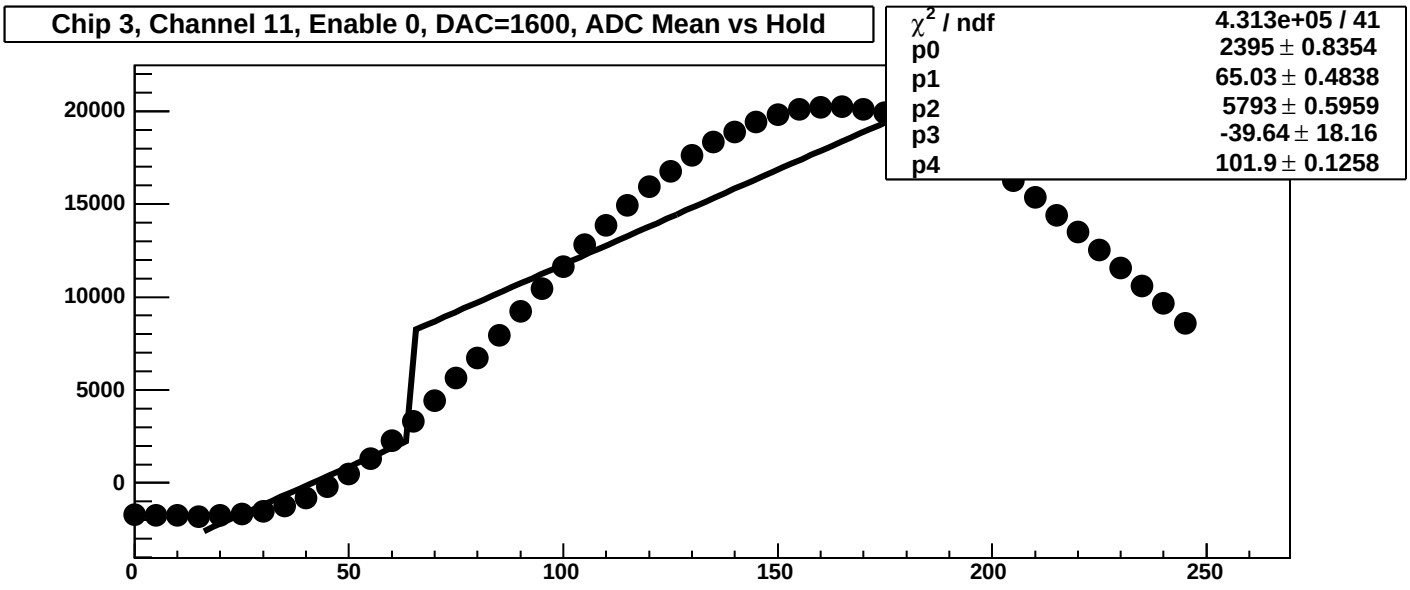


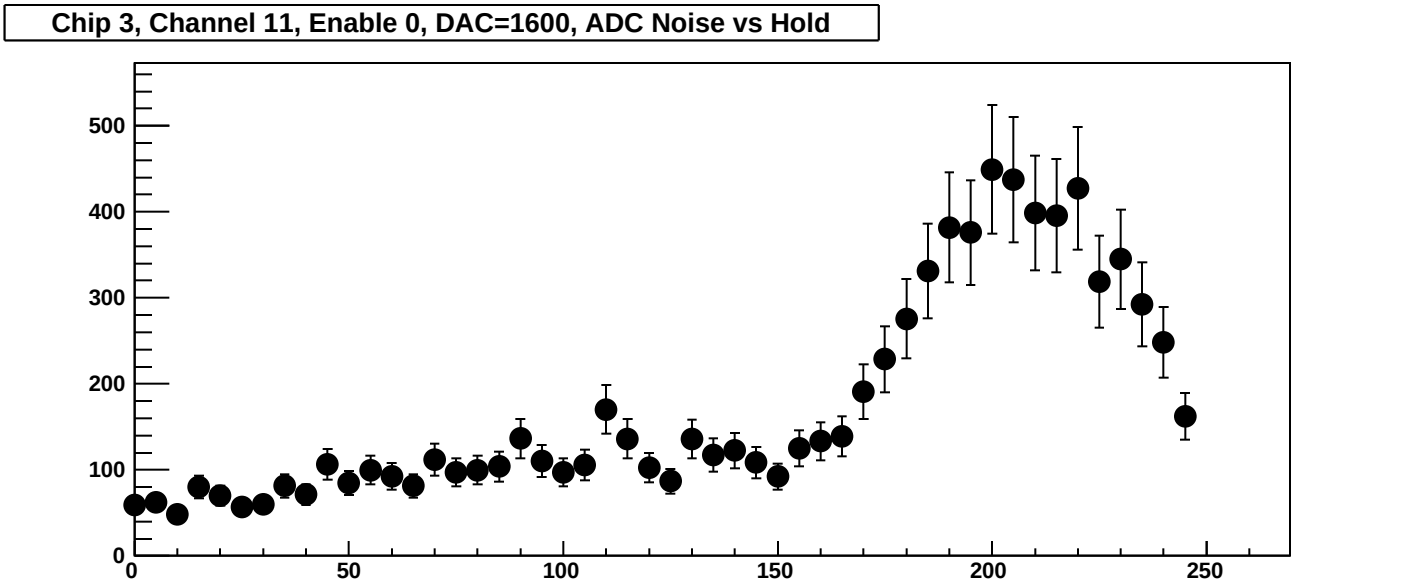
Chip 3, Channel 10, Enable 5, DAC=1600, ADC Noise vs Hold

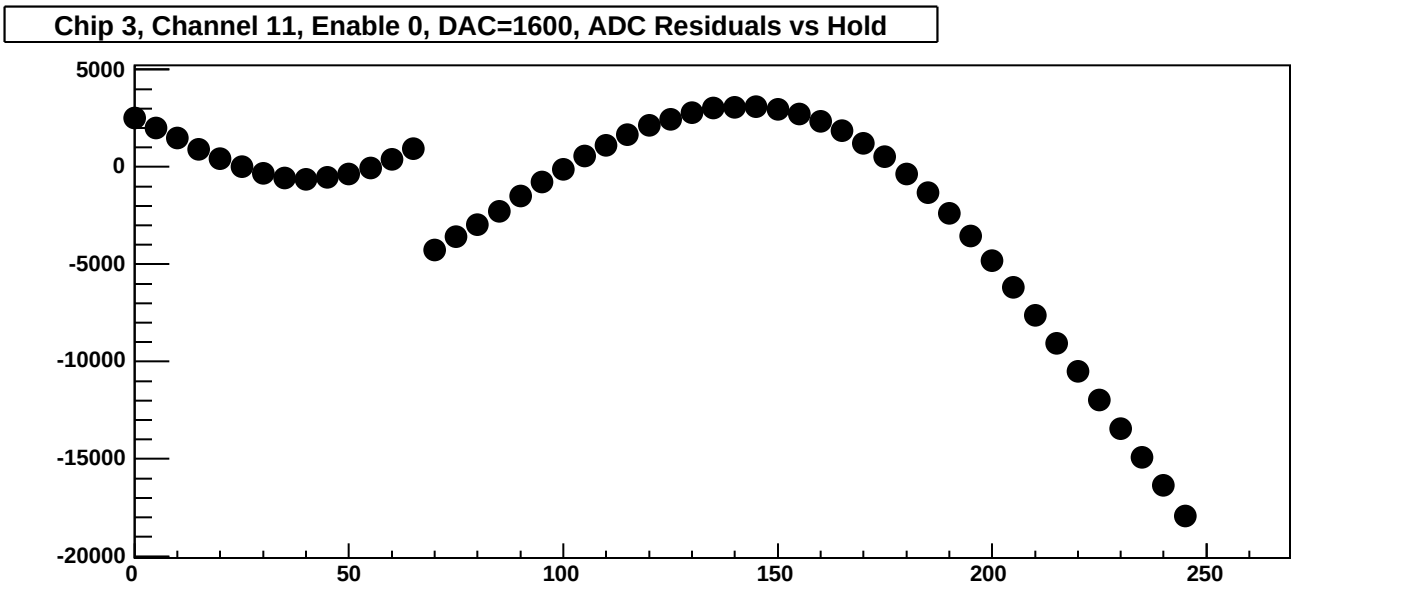


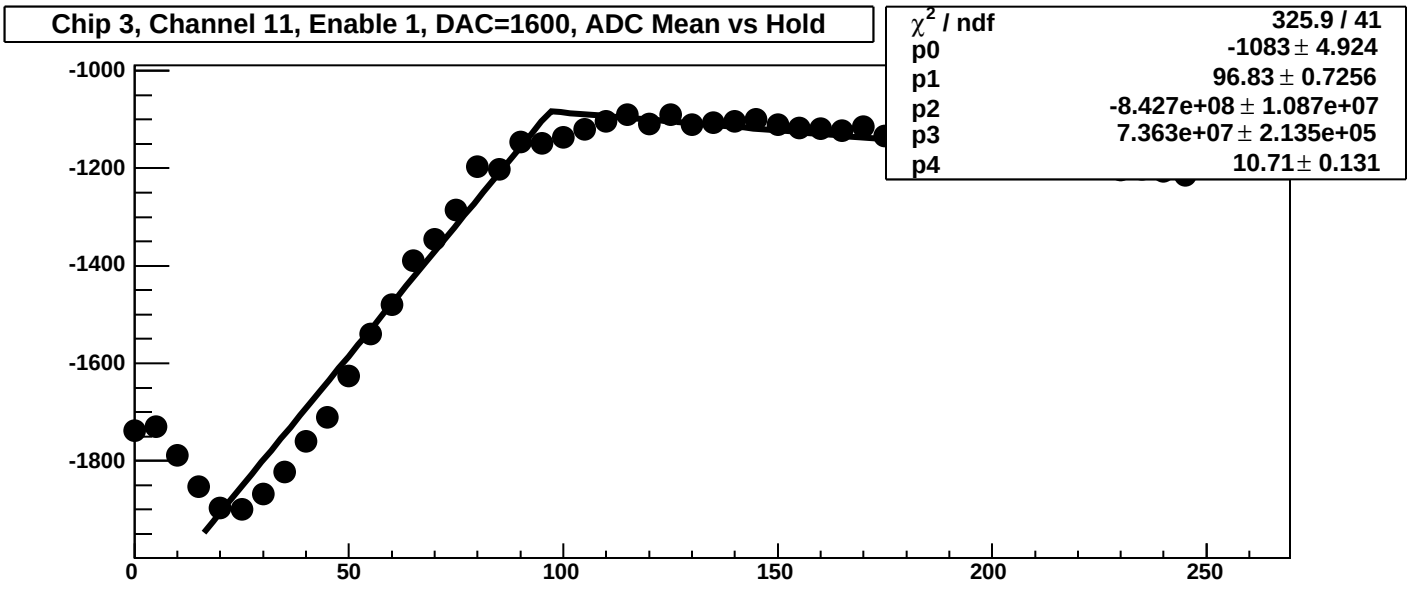
Chip 3, Channel 10, Enable 5, DAC=1600, ADC Residuals vs Hold

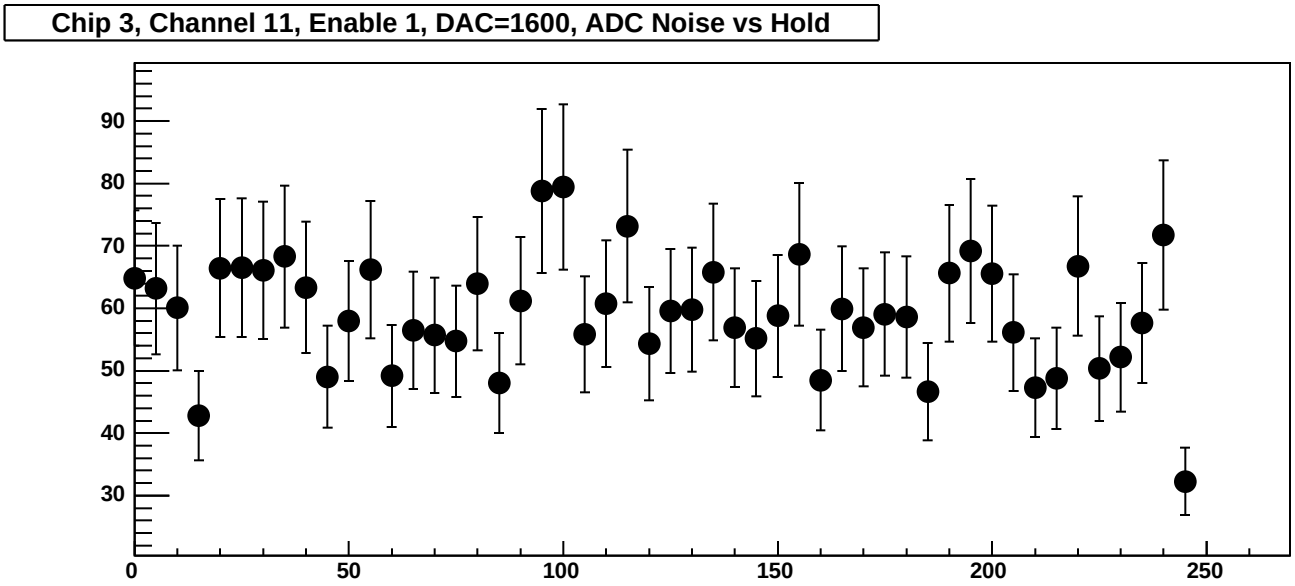


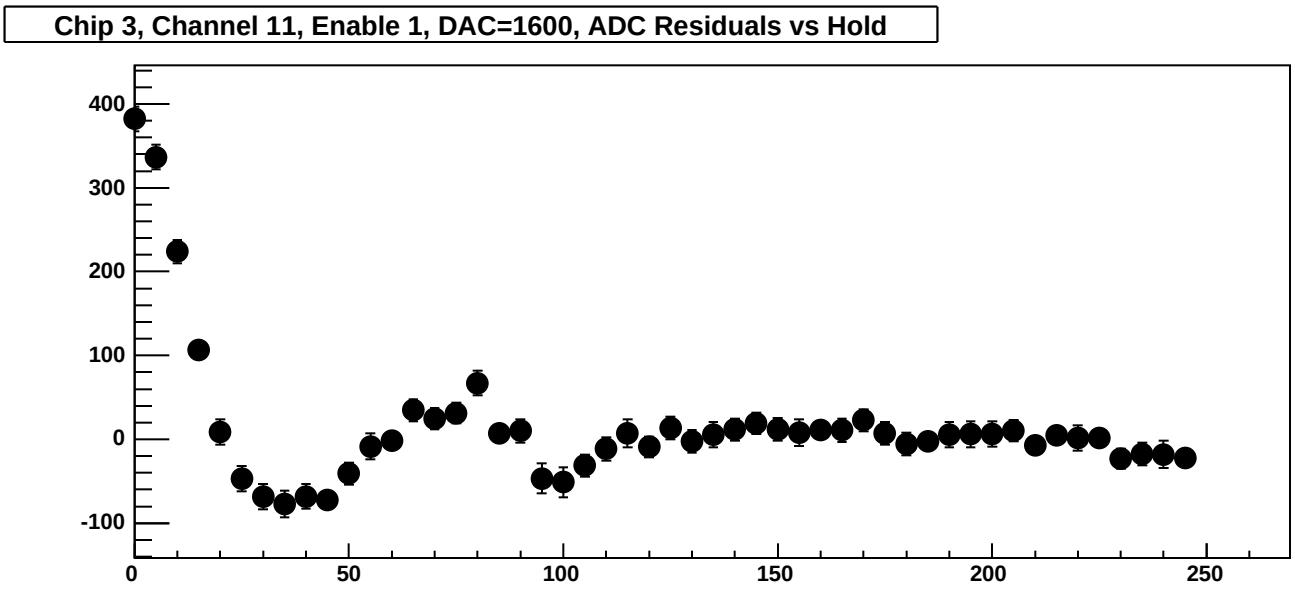

Chip 3, Channel 11, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.313e+05 / 41
p0 2395 ± 0.8354
p1 65.03 ± 0.4838
p2 5793 ± 0.5959
p3 -39.64 ± 18.16
p4 101.9 ± 0.1258
20000
15000
10000
5000
0
0
50
100
150
200
250

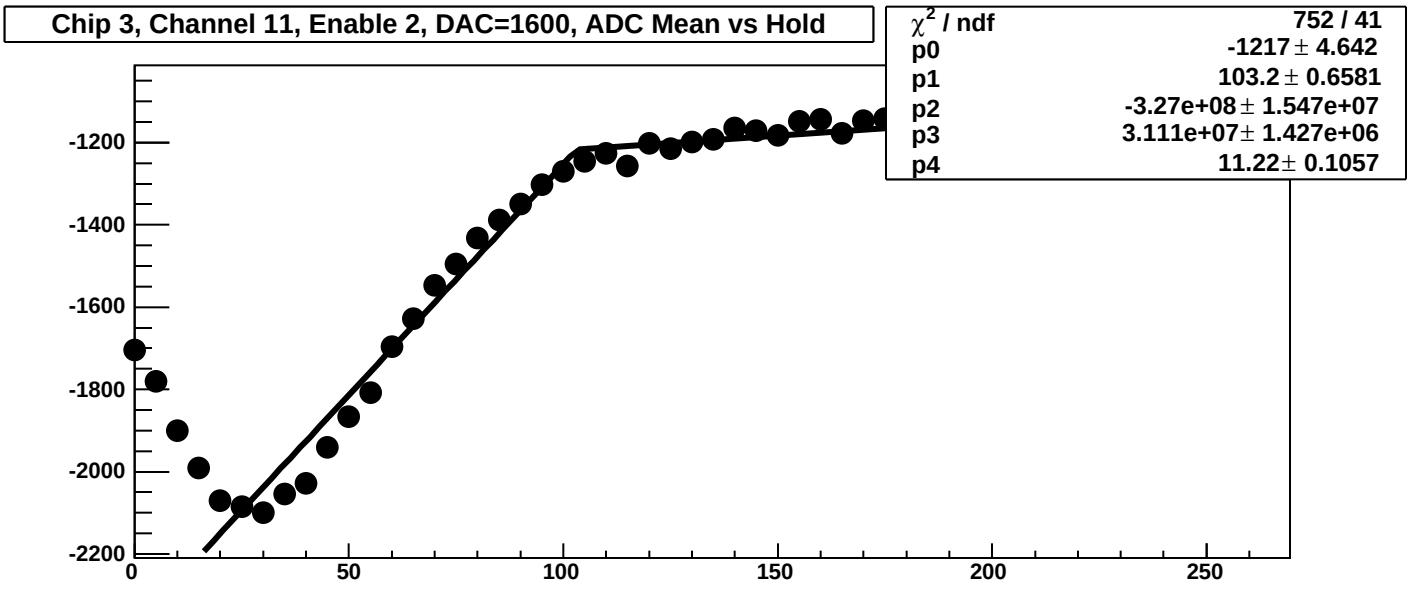

Chip 3, Channel 11, Enable 0, DAC=1600, ADC Noise vs Hold
500
400
300
200
100
0
0
50
100
150
200
250

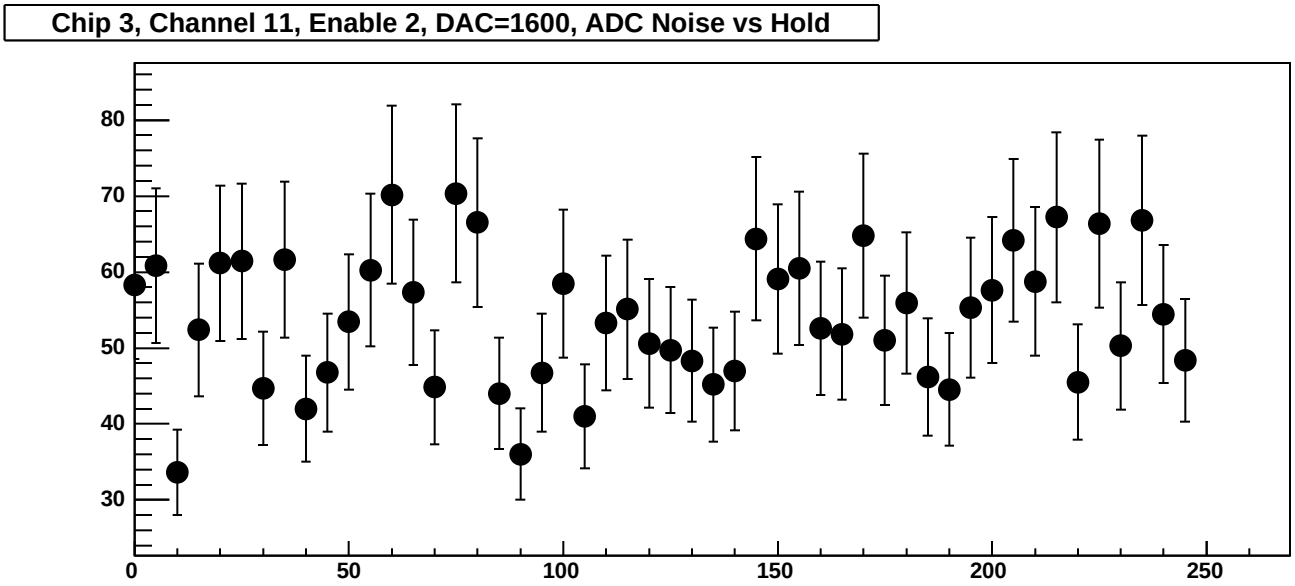

Chip 3, Channel 11, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

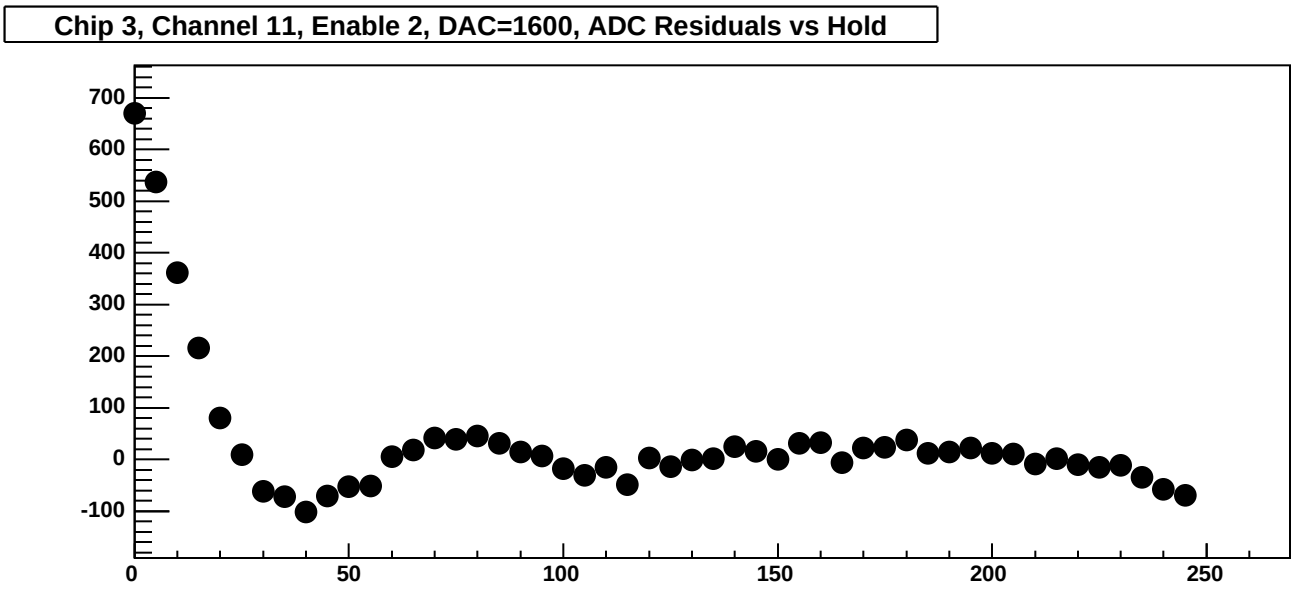

Chip 3, Channel 11, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 325.9 / 41
p0 -1083 ± 4.924
p1 96.83 ± 0.7256
p2 -8.427e+08 ± 1.087e+07
p3 7.363e+07 ± 2.135e+05
p4 10.71 ± 0.131
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250


Chip 3, Channel 11, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

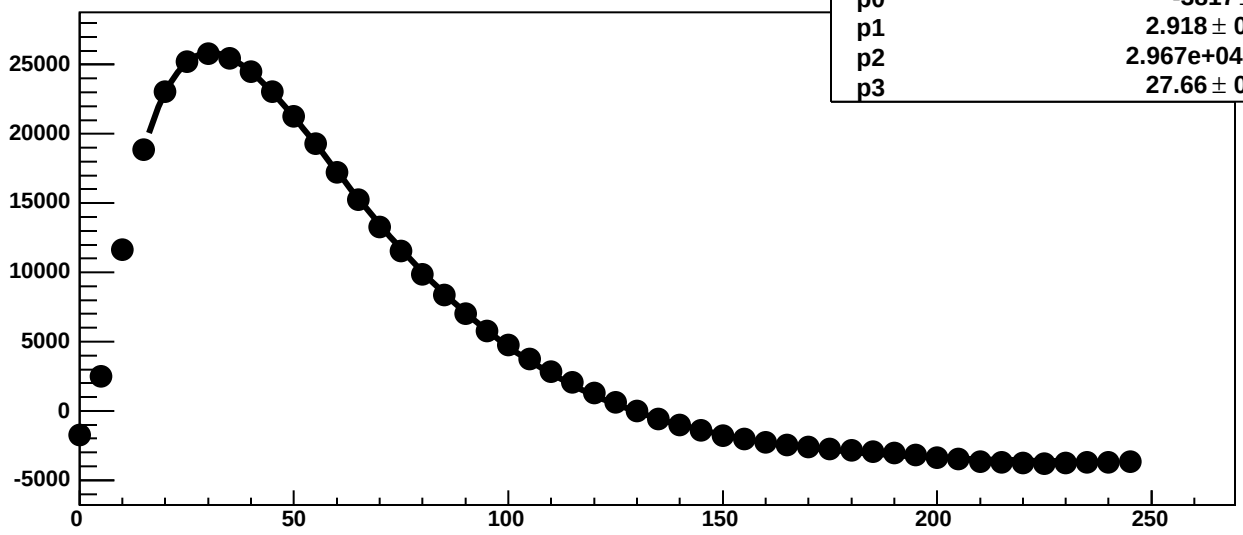

Chip 3, Channel 11, Enable 1, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 3, Channel 11, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 752 / 41
p0 -1217 ± 4.642
p1 103.2 ± 0.6581
p2 -3.27e+08 ± 1.547e+07
p3 3.111e+07 ± 1.427e+06
p4 11.22 ± 0.1057
-1200
-1400
-1600
-1800
-2000
-2200
0
50
100
150
200
250

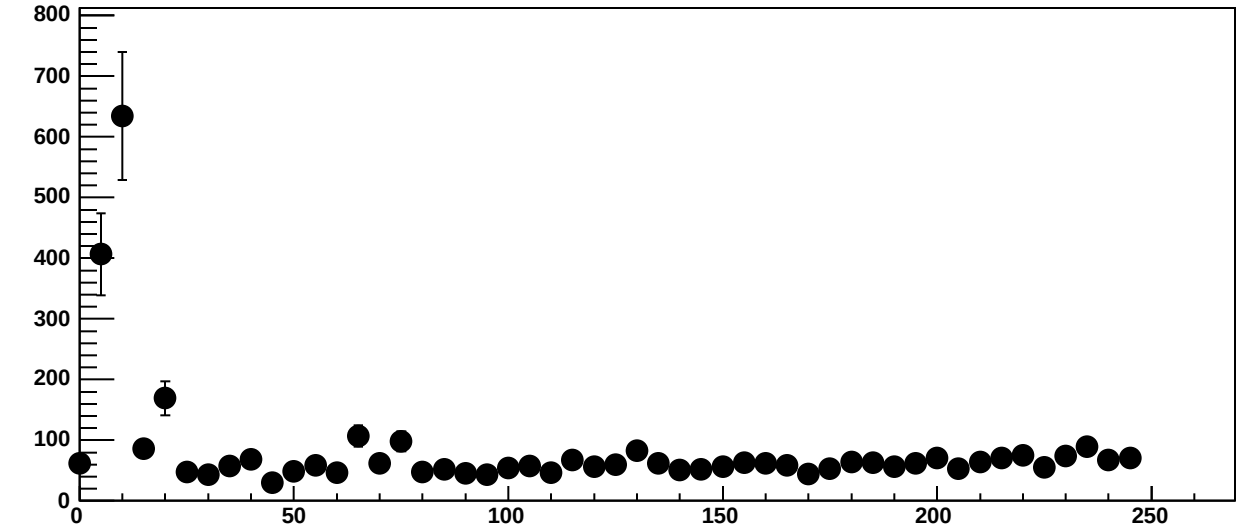

Chip 3, Channel 11, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 3, Channel 11, Enable 2, DAC=1600, ADC Residuals vs Hold
700
600
500
400
300
200
100
0
-100
0
50
100
150
200
250

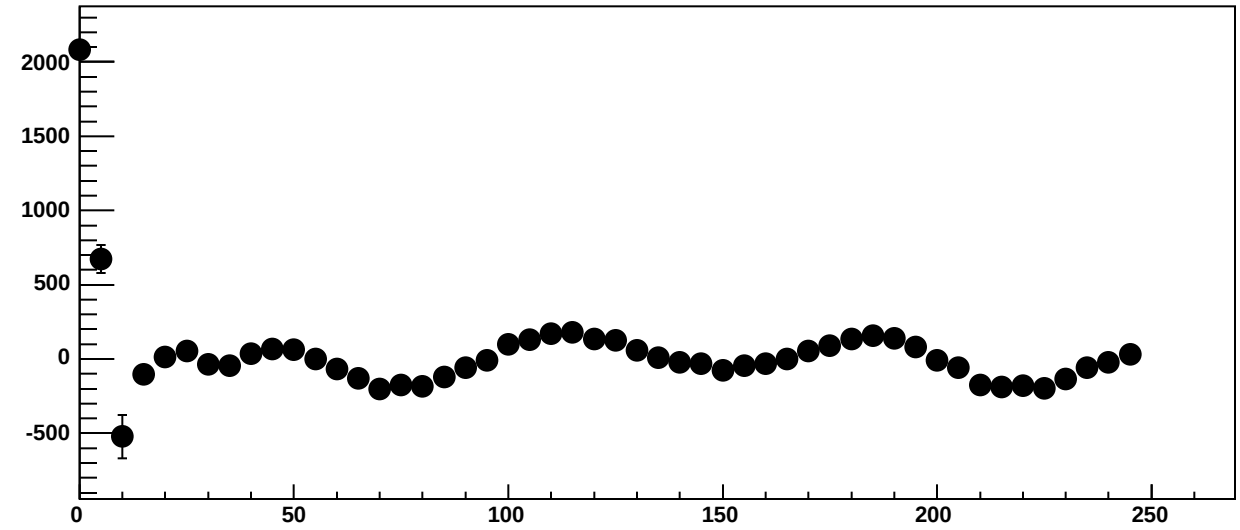
Chip 3, Channel 11, Enable 3!, DAC=1600, ADC Mean vs Hold



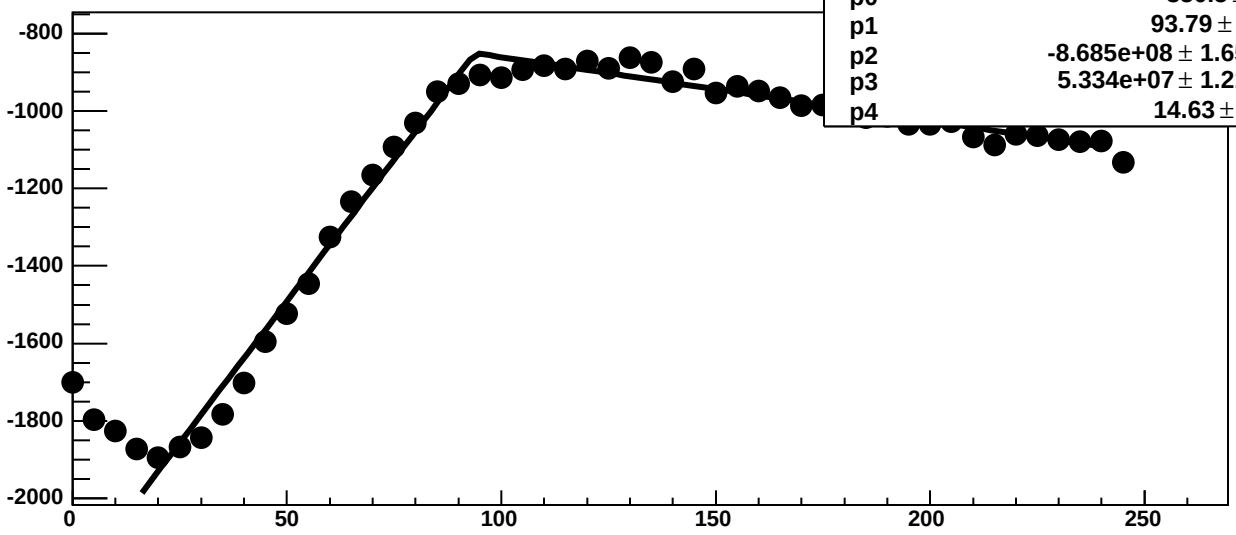
Chip 3, Channel 11, Enable 3!, DAC=1600, ADC Noise vs Hold



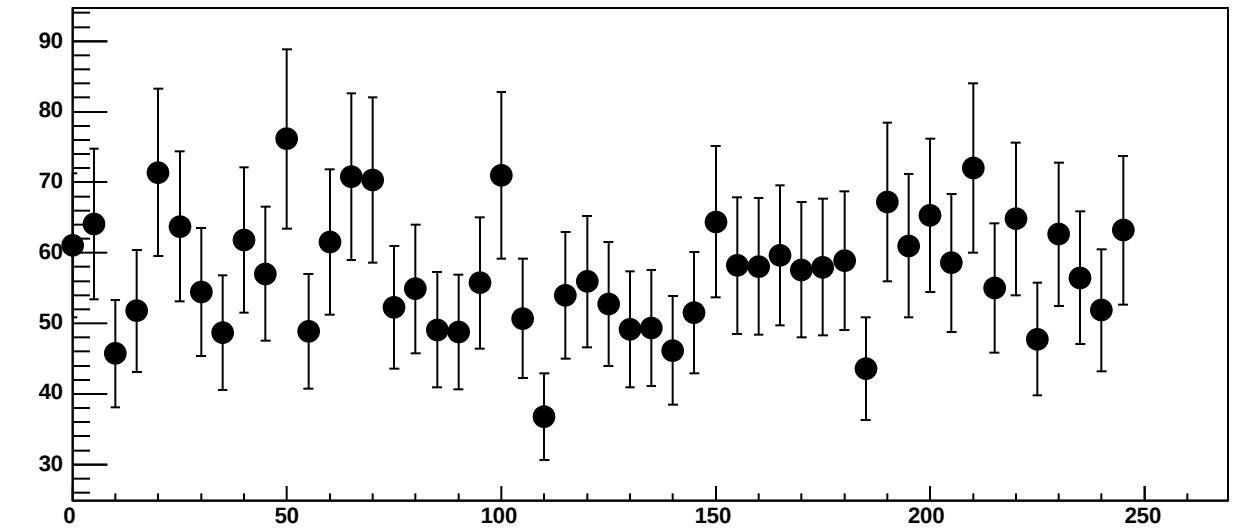
Chip 3, Channel 11, Enable 3!, DAC=1600, ADC Residuals vs Hold



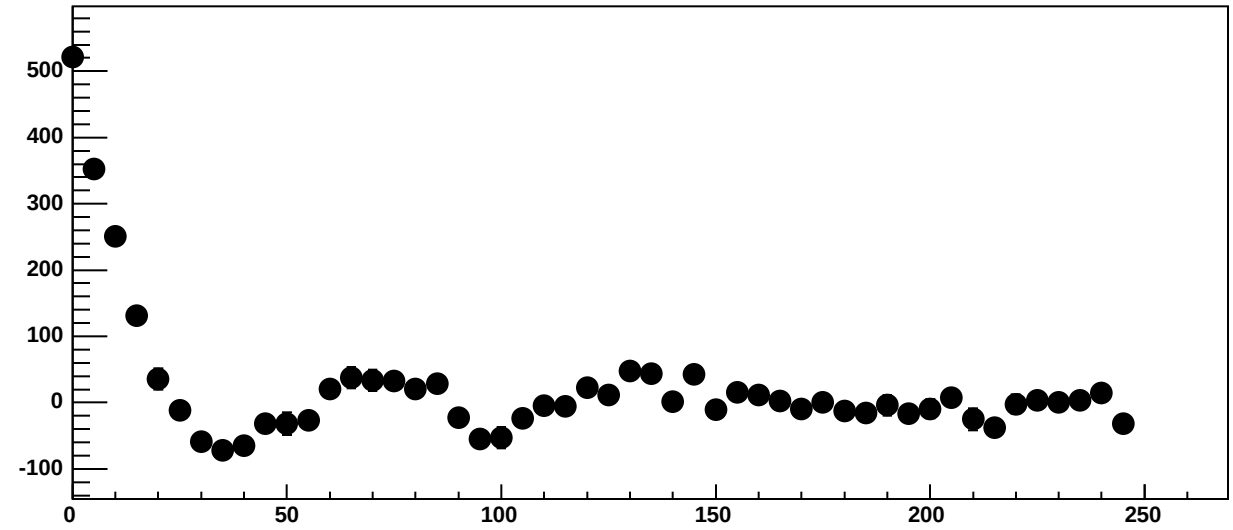
Chip 3, Channel 11, Enable 4, DAC=1600, ADC Mean vs Hold

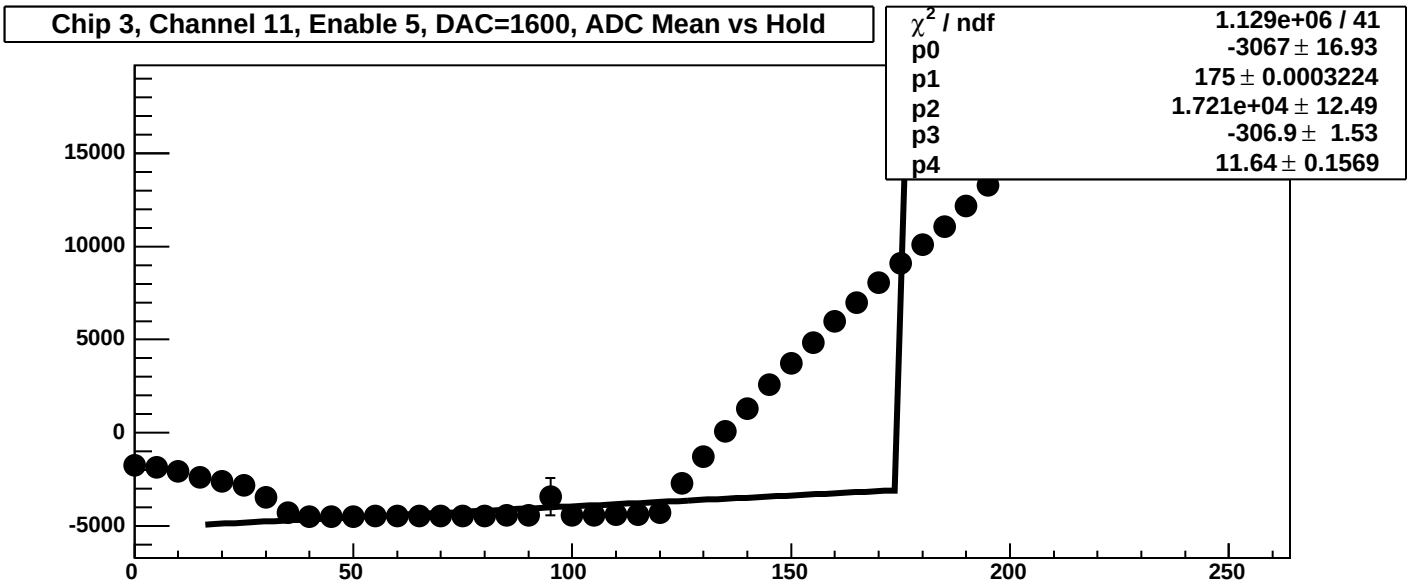


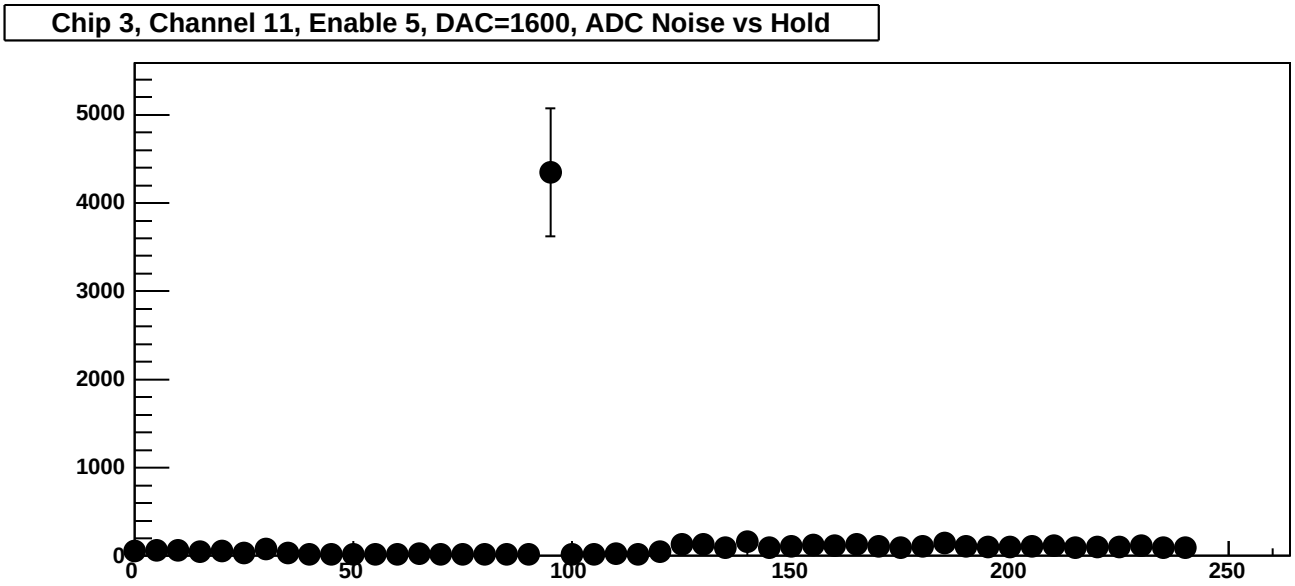
Chip 3, Channel 11, Enable 4, DAC=1600, ADC Noise vs Hold

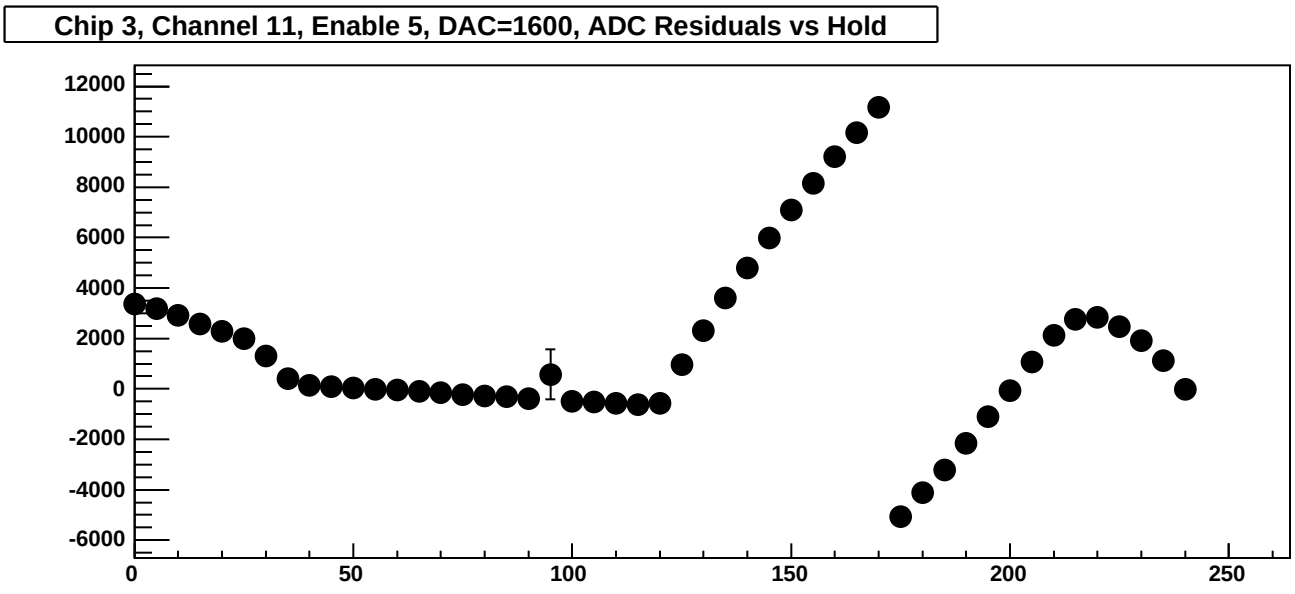


Chip 3, Channel 11, Enable 4, DAC=1600, ADC Residuals vs Hold

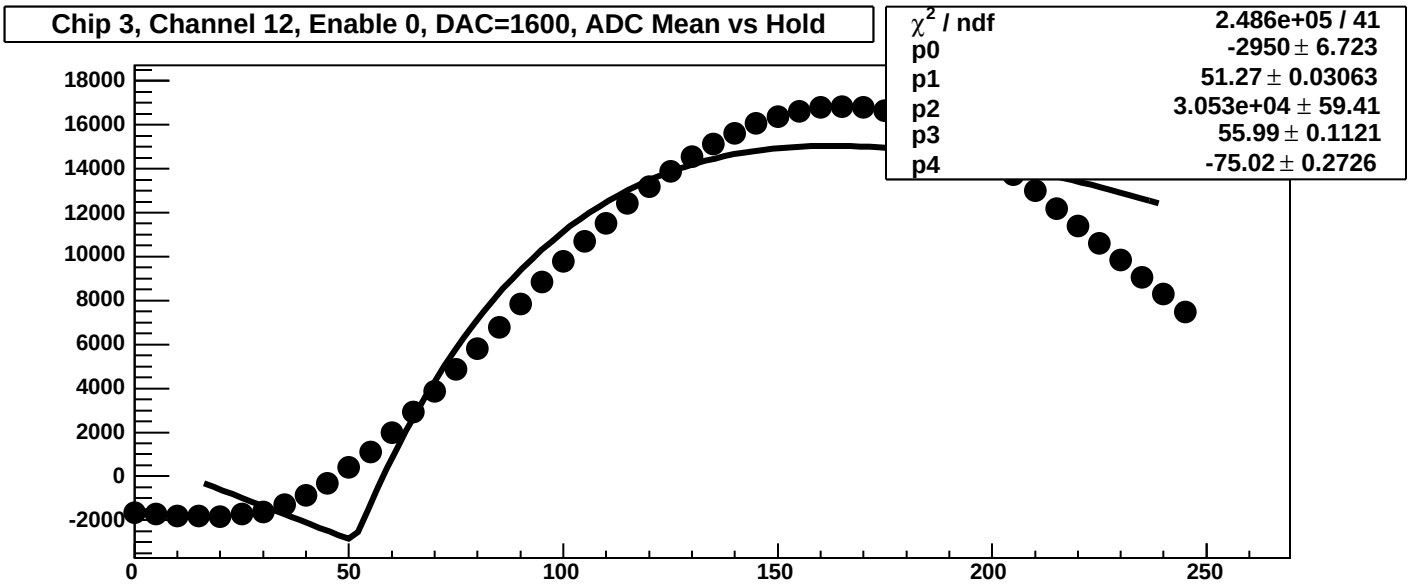



Chip 3, Channel 11, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.129e+06 / 41
p0 -3067 ± 16.93
p1 175 ± 0.0003224
p2 1.721e+04 ± 12.49
p3 -306.9 ± 1.53
p4 11.64 ± 0.1569
15000
10000
5000
0
-5000
0
50
100
150
200
250

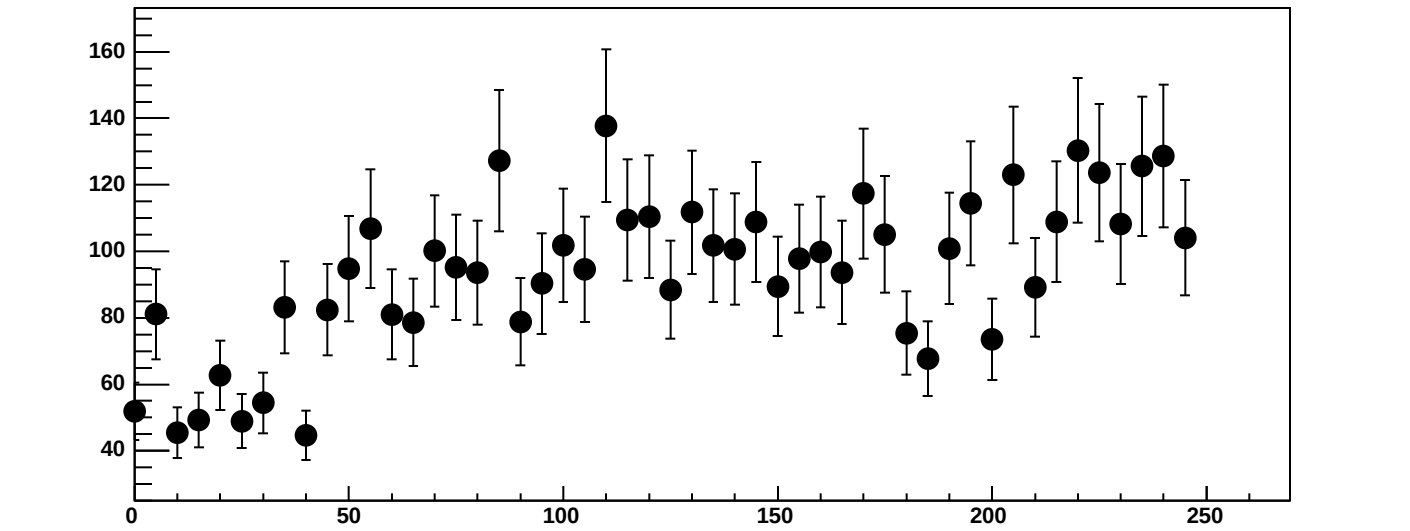

Chip 3, Channel 11, Enable 5, DAC=1600, ADC Noise vs Hold
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 3, Channel 11, Enable 5, DAC=1600, ADC Residuals vs Hold
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

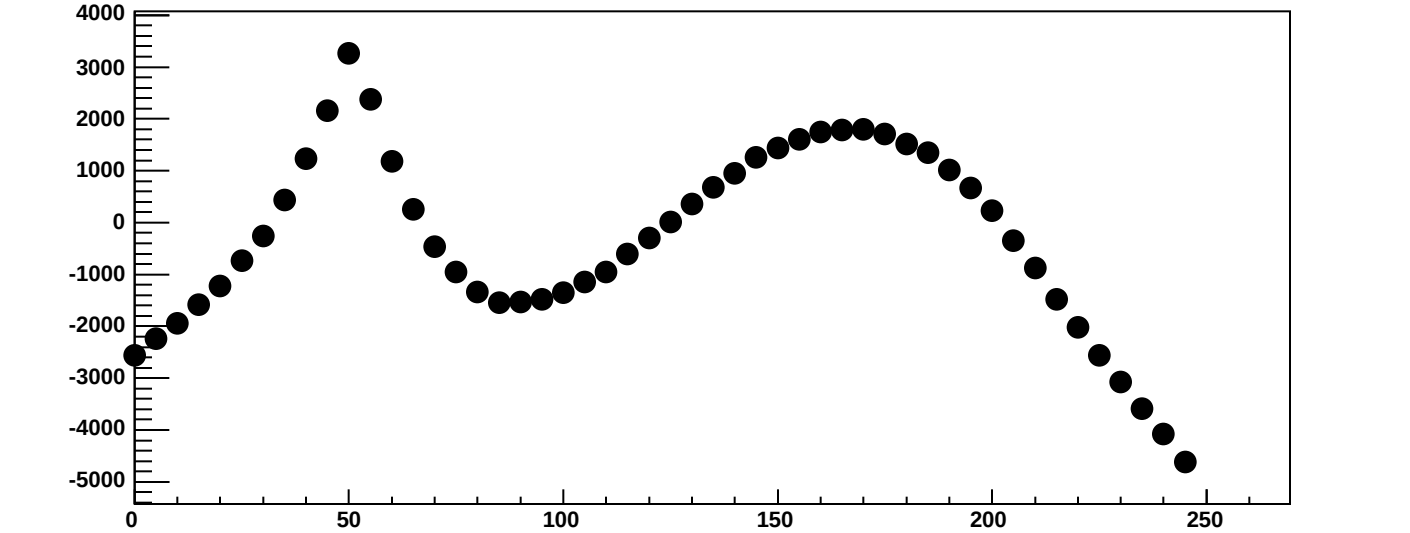
Chip 3, Channel 12, Enable 0, DAC=1600, ADC Mean vs Hold

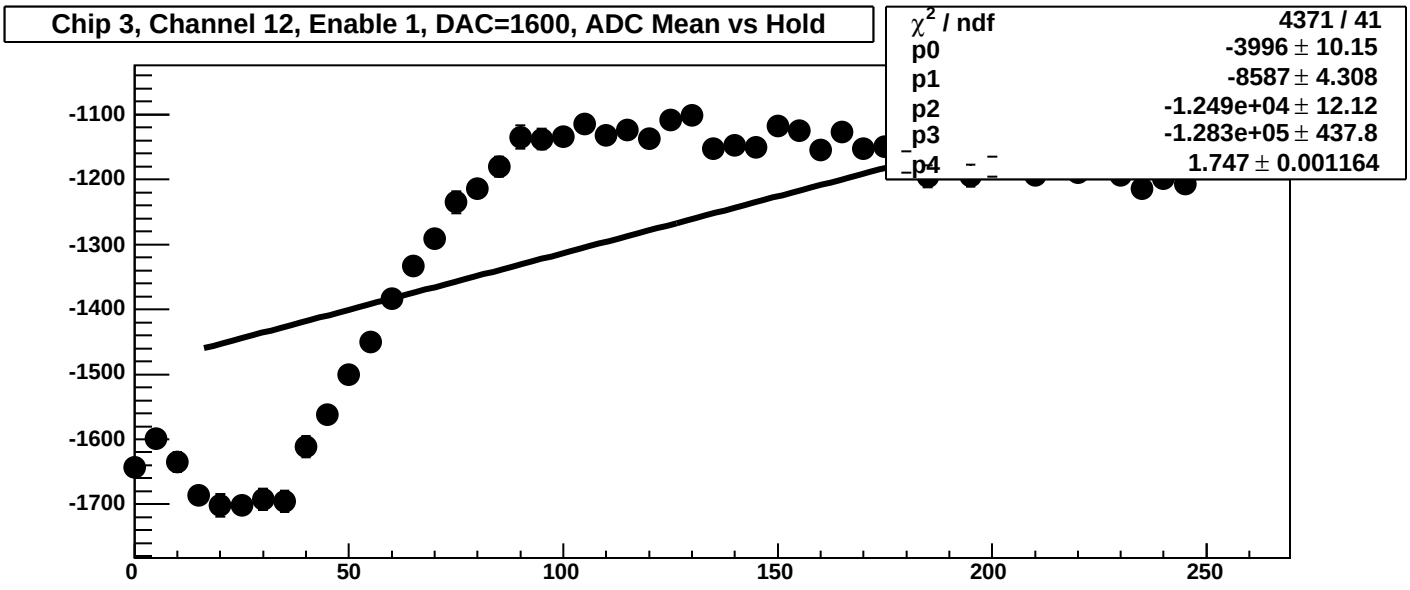


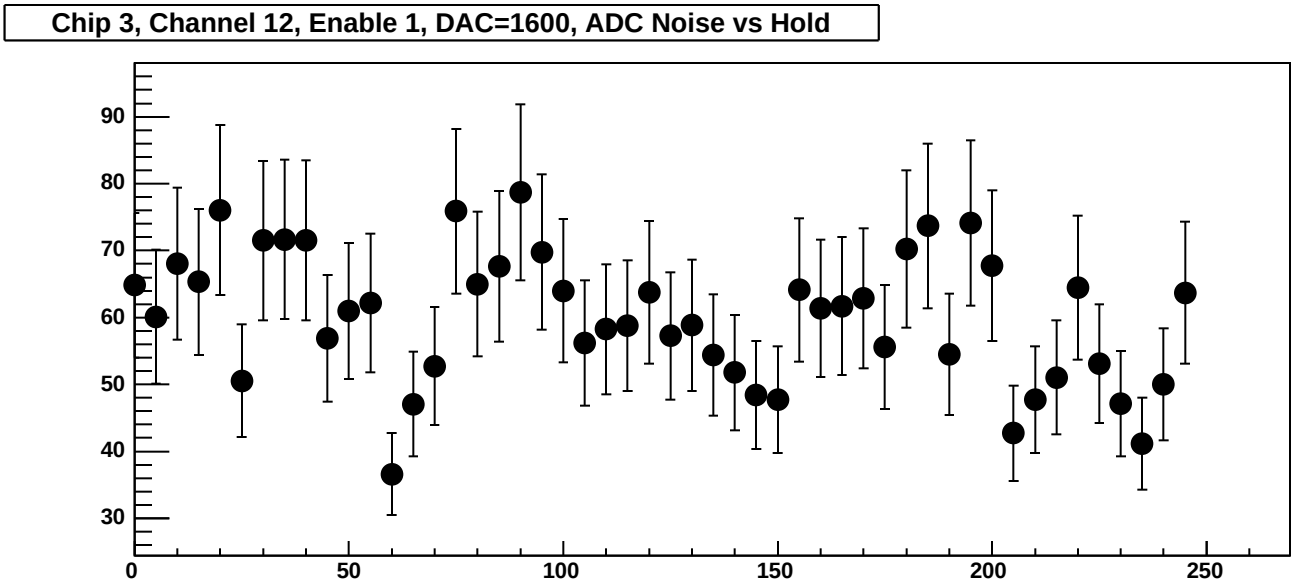
Chip 3, Channel 12, Enable 0, DAC=1600, ADC Noise vs Hold

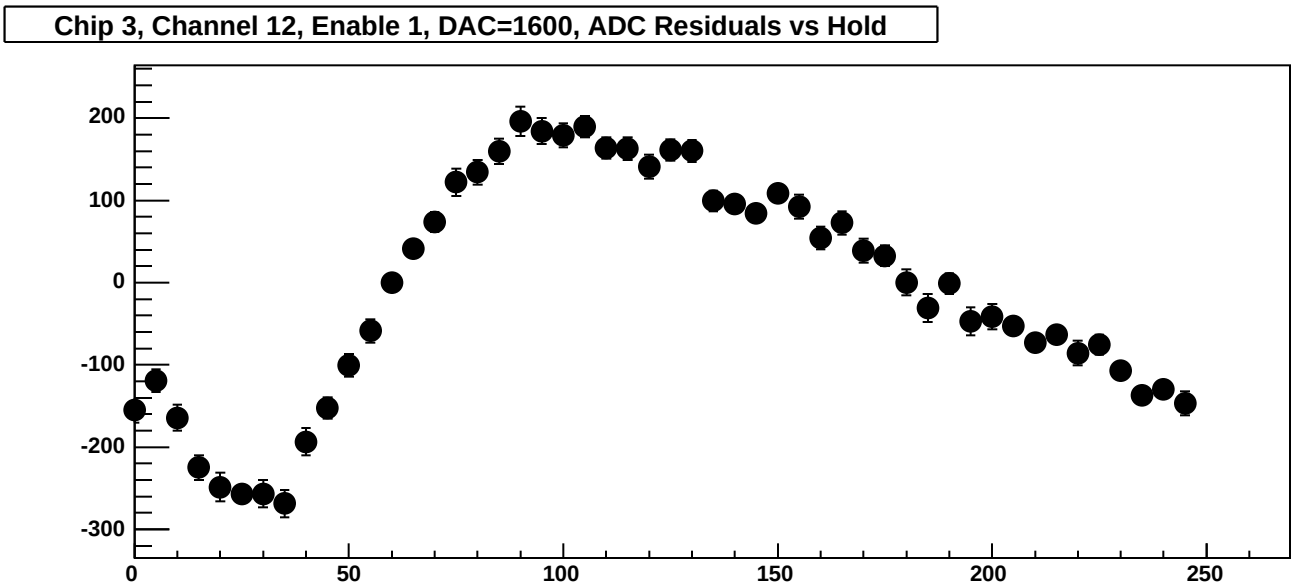


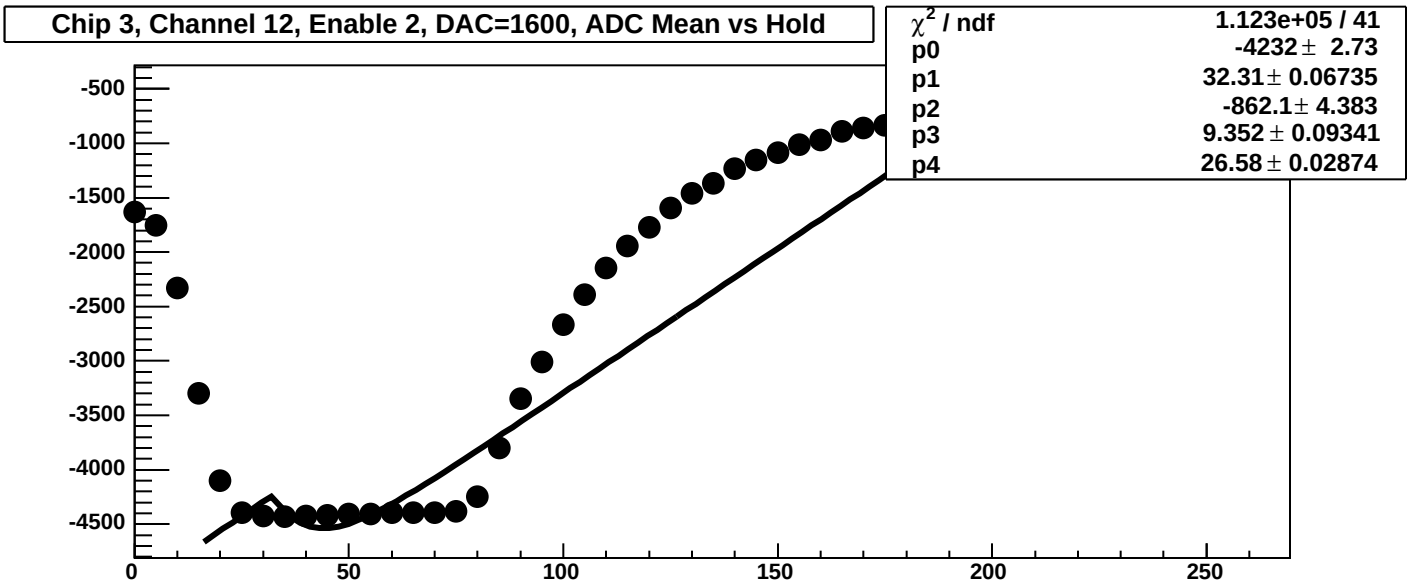
Chip 3, Channel 12, Enable 0, DAC=1600, ADC Residuals vs Hold

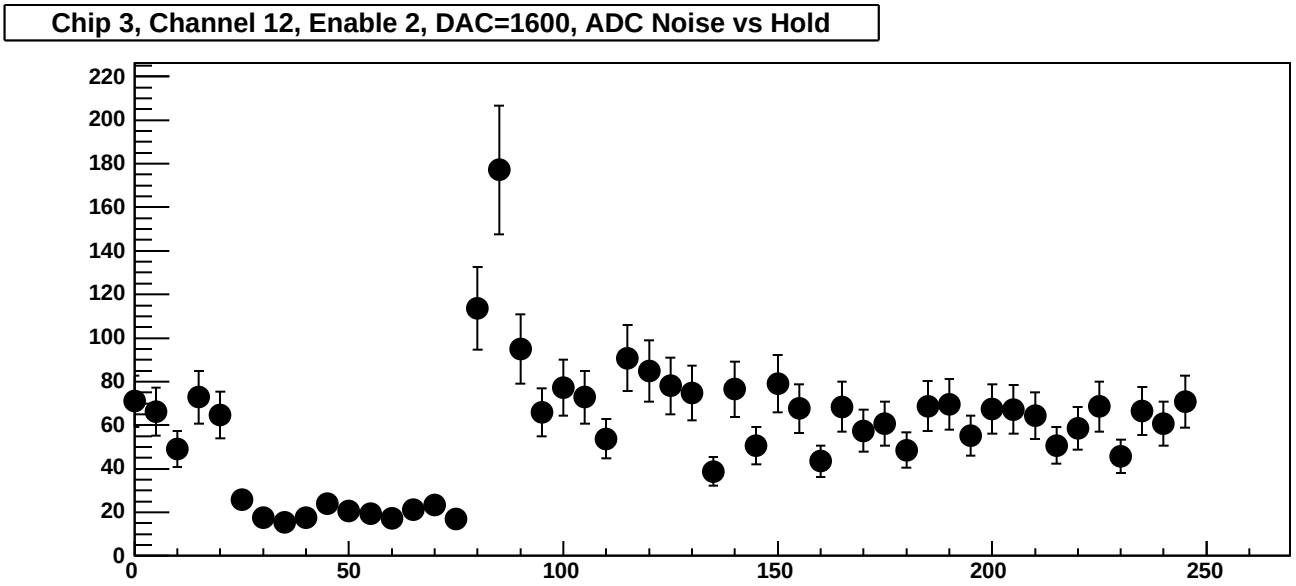


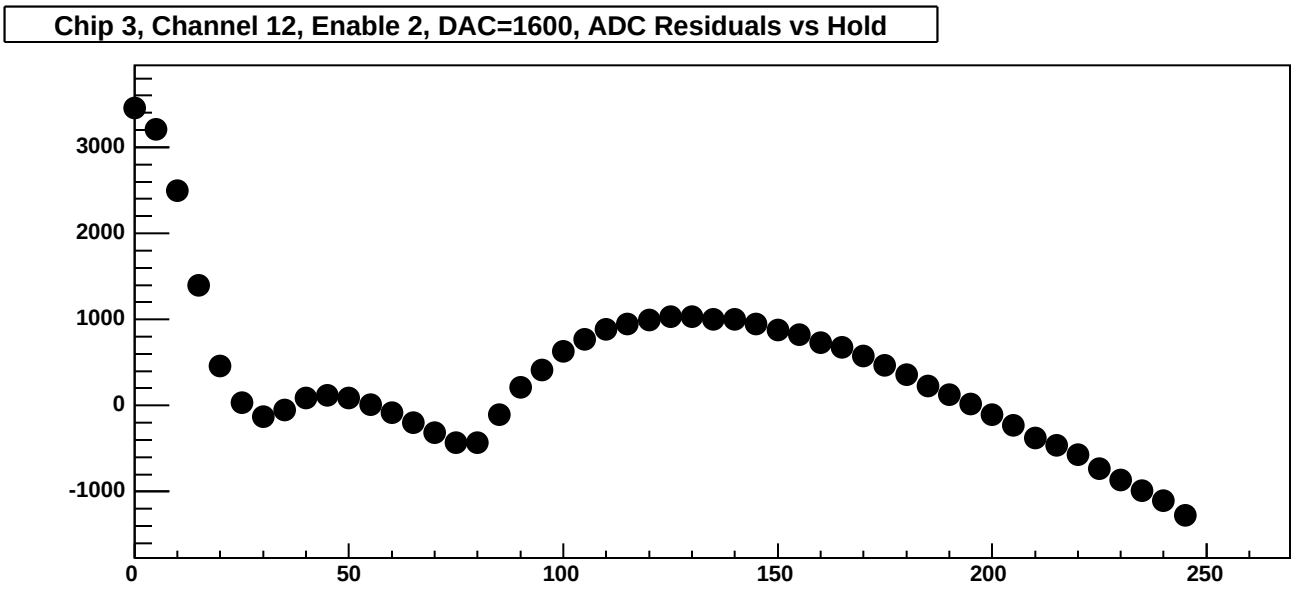

Chip 3, Channel 12, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 4371 / 41
p0 -3996 ± 10.15
p1 -8587 ± 4.308
p2 -1.249e+04 ± 12.12
p3 -1.283e+05 ± 437.8
p4 1.747 ± 0.001164
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
50
100
150
200
250

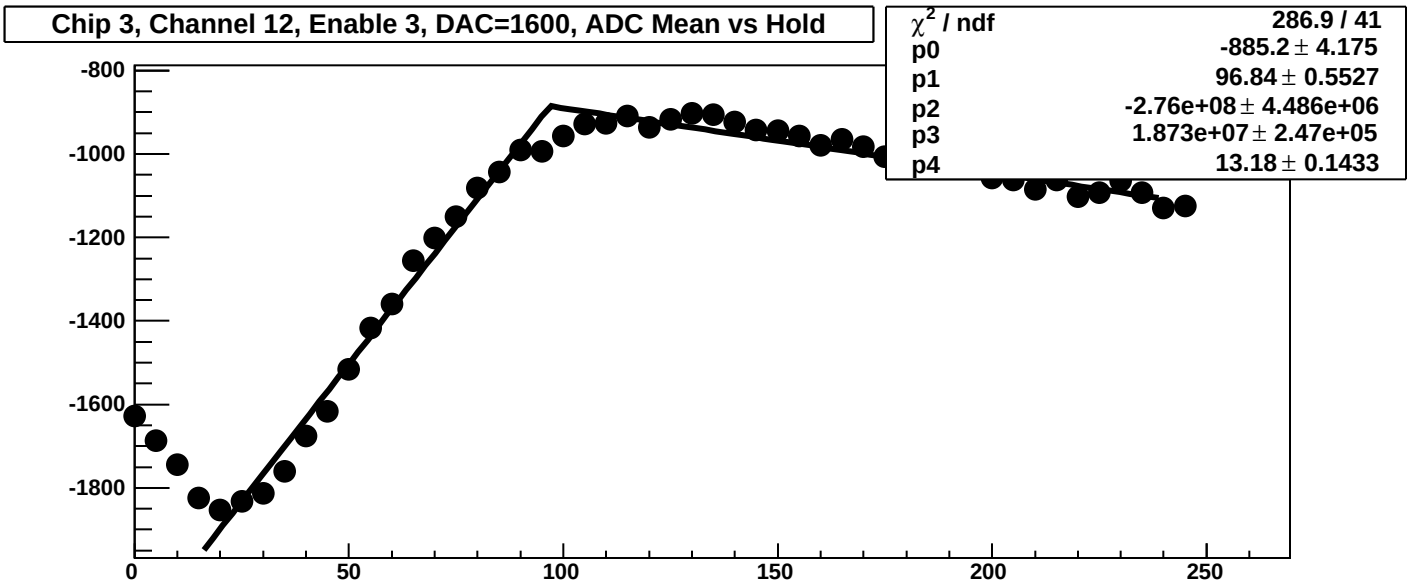

Chip 3, Channel 12, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

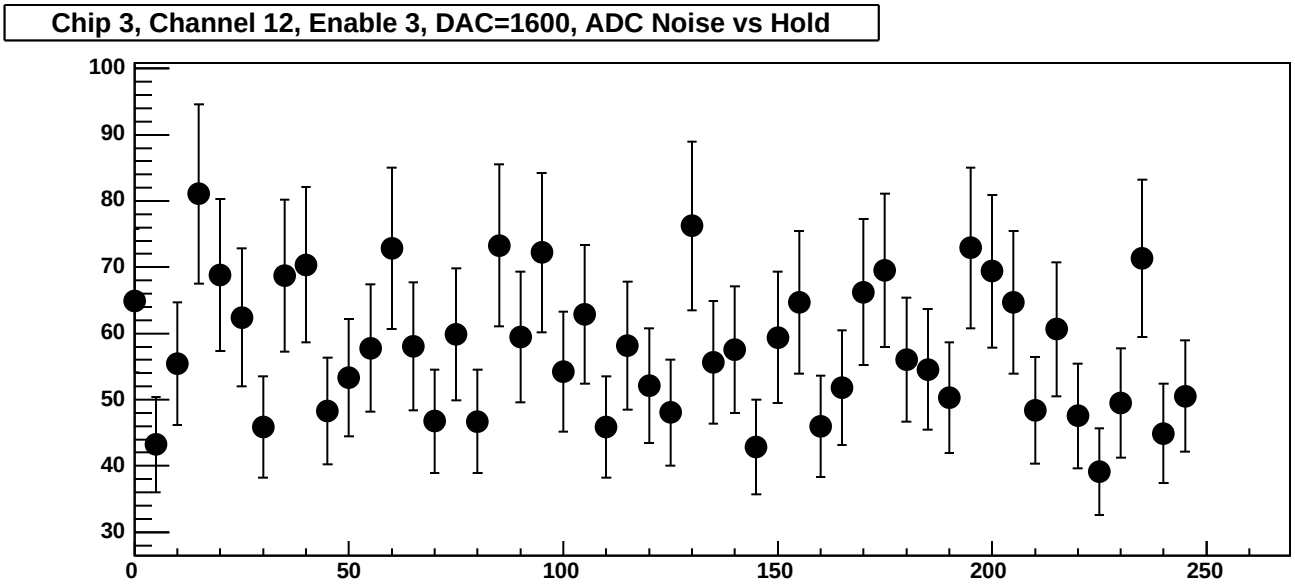

Chip 3, Channel 12, Enable 1, DAC=1600, ADC Residuals vs Hold
200
100
0
-100
-200
-300
0
50
100
150
200
250

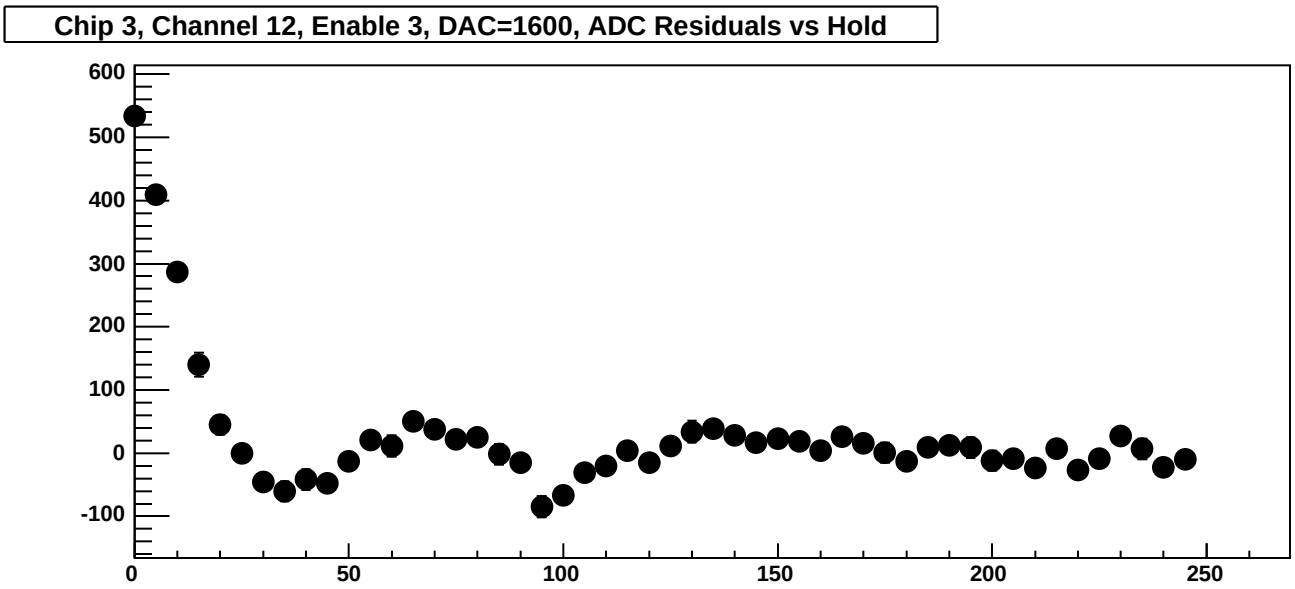

Chip 3, Channel 12, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.123e+05 / 41
p0 -4232 ± 2.73
p1 32.31 ± 0.06735
p2 -862.1 ± 4.383
p3 9.352 ± 0.09341
p4 26.58 ± 0.02874
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
-4500
0
50
100
150
200
250


Chip 3, Channel 12, Enable 2, DAC=1600, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

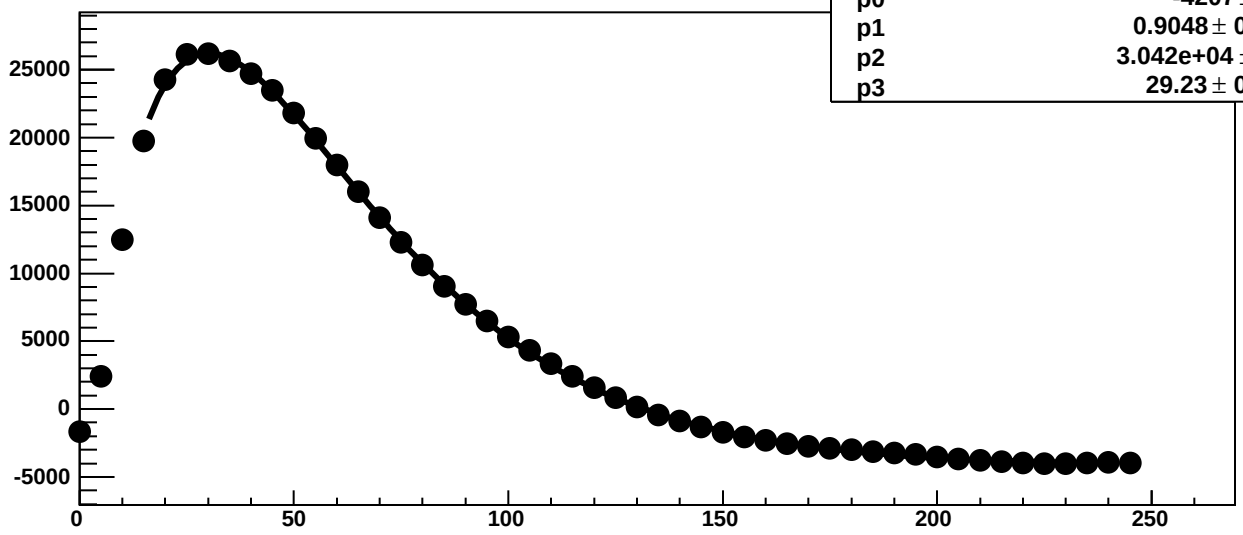

Chip 3, Channel 12, Enable 2, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 3, Channel 12, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 286.9 / 41
p0 -885.2 ± 4.175
p1 96.84 ± 0.5527
p2 -2.76e+08 ± 4.486e+06
p3 1.873e+07 ± 2.47e+05
p4 13.18 ± 0.1433
-800
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250

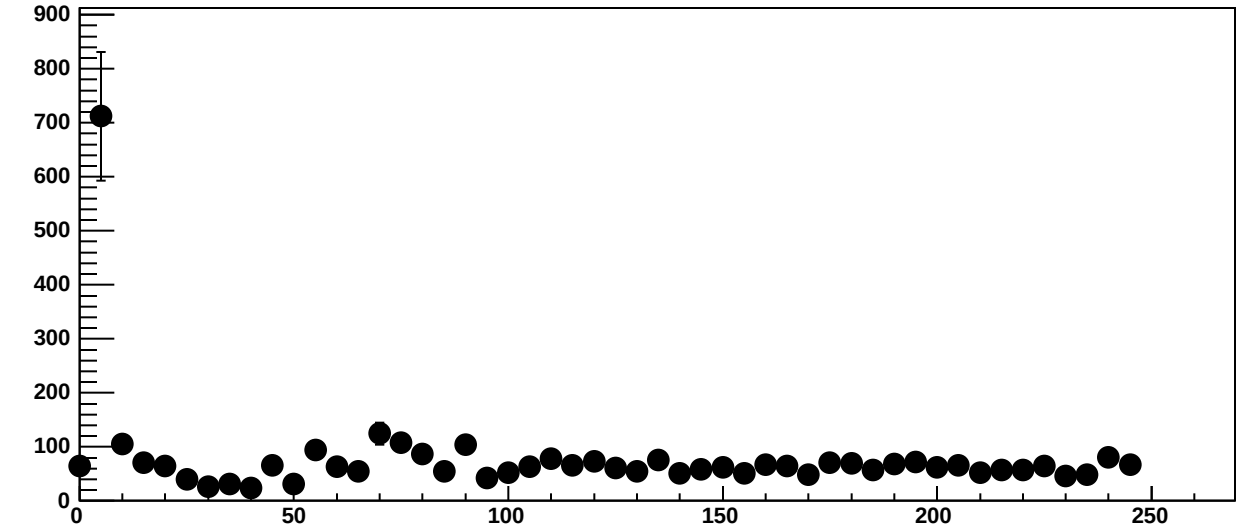

Chip 3, Channel 12, Enable 3, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 3, Channel 12, Enable 3, DAC=1600, ADC Residuals vs Hold
600
500
400
300
200
100
0
-100
0
50
100
150
200
250

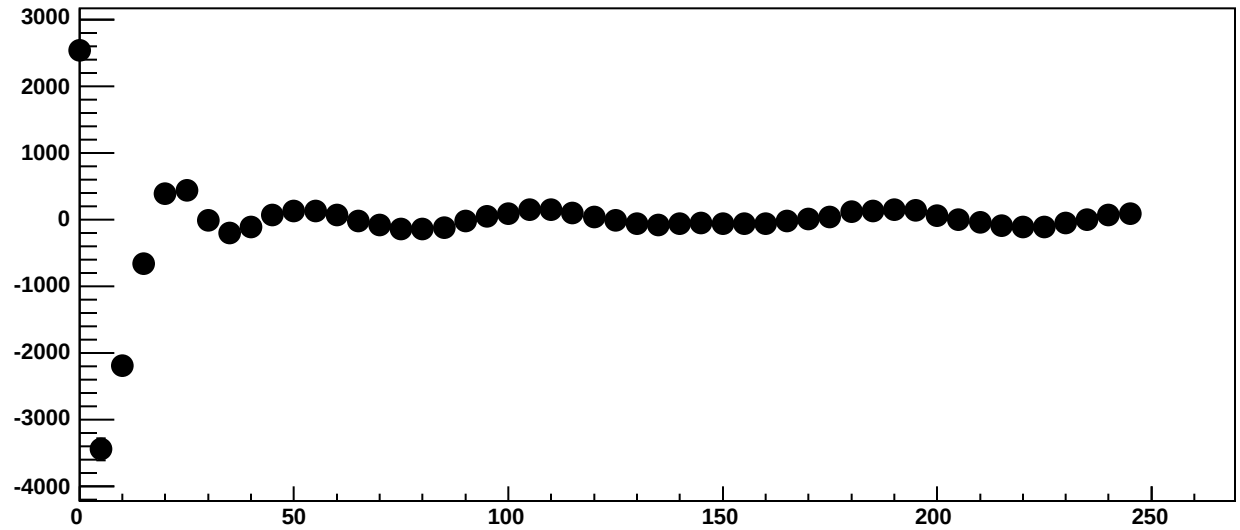
Chip 3, Channel 12, Enable 4!, DAC=1600, ADC Mean vs Hold

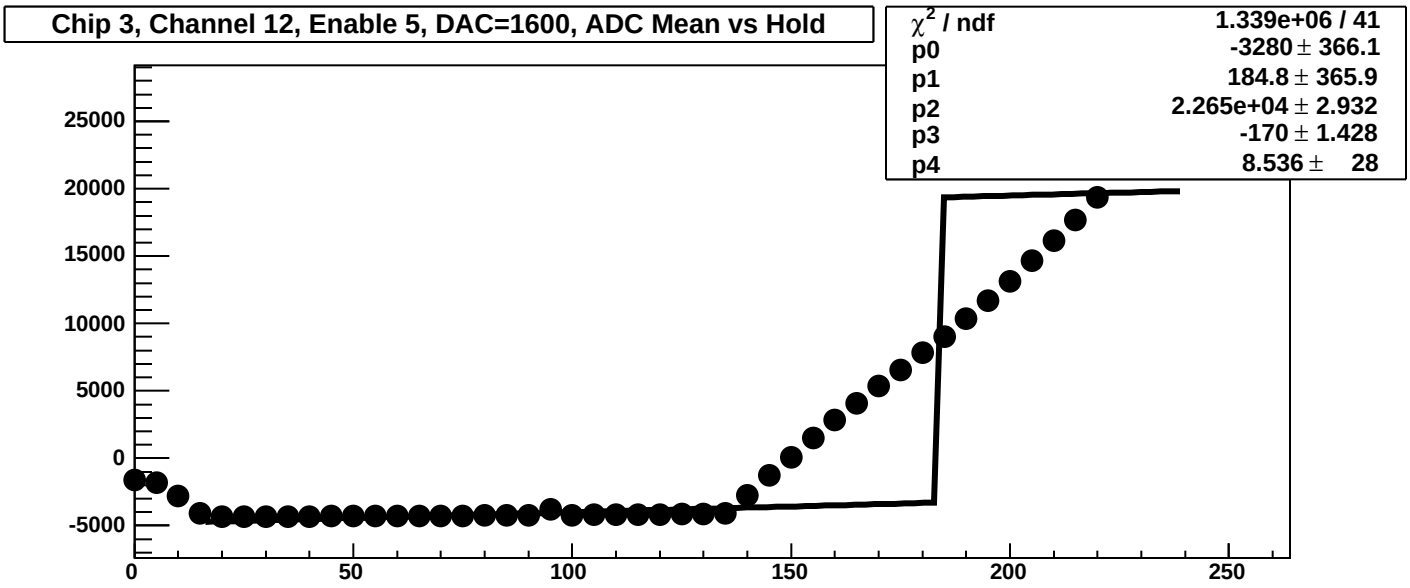


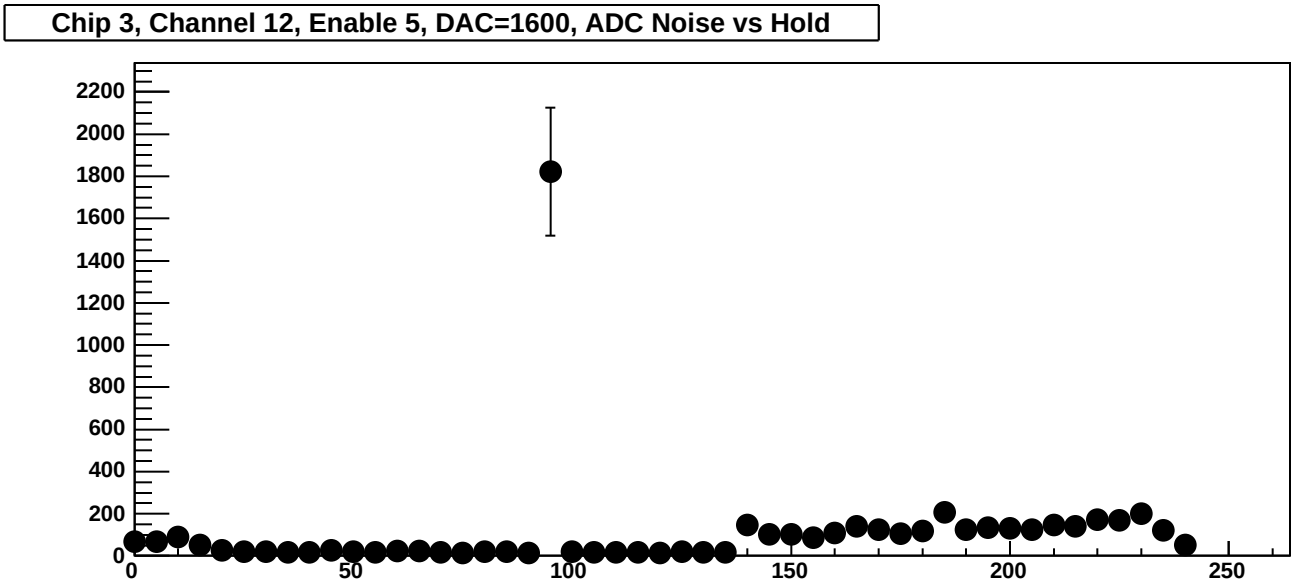
Chip 3, Channel 12, Enable 4!, DAC=1600, ADC Noise vs Hold

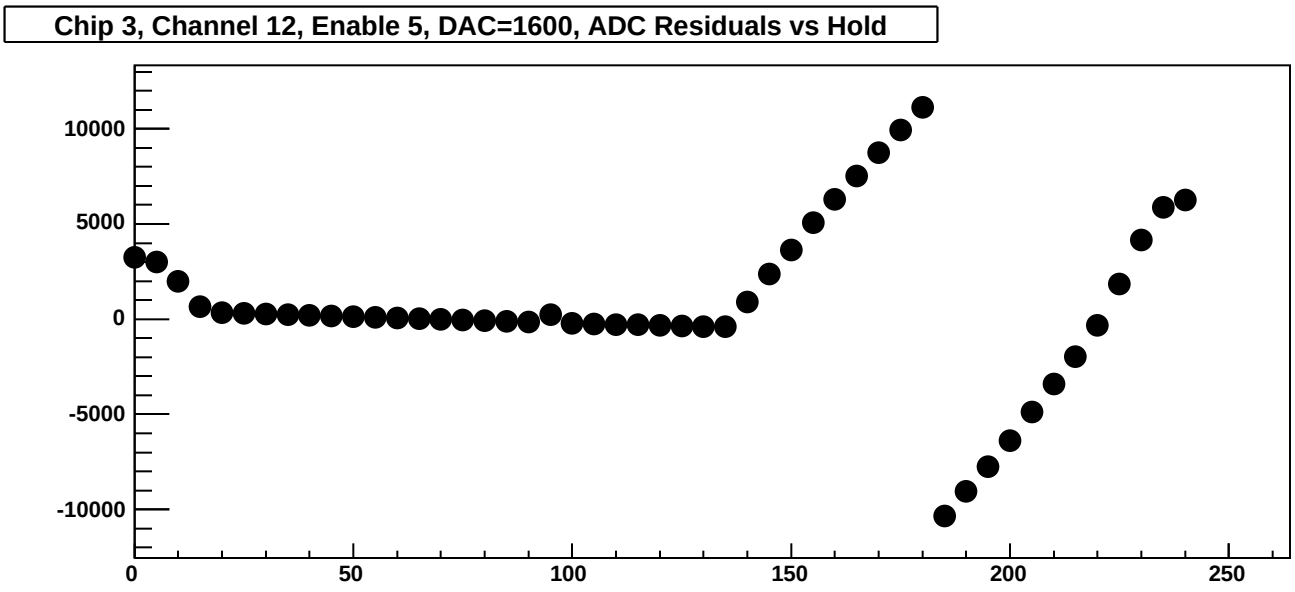


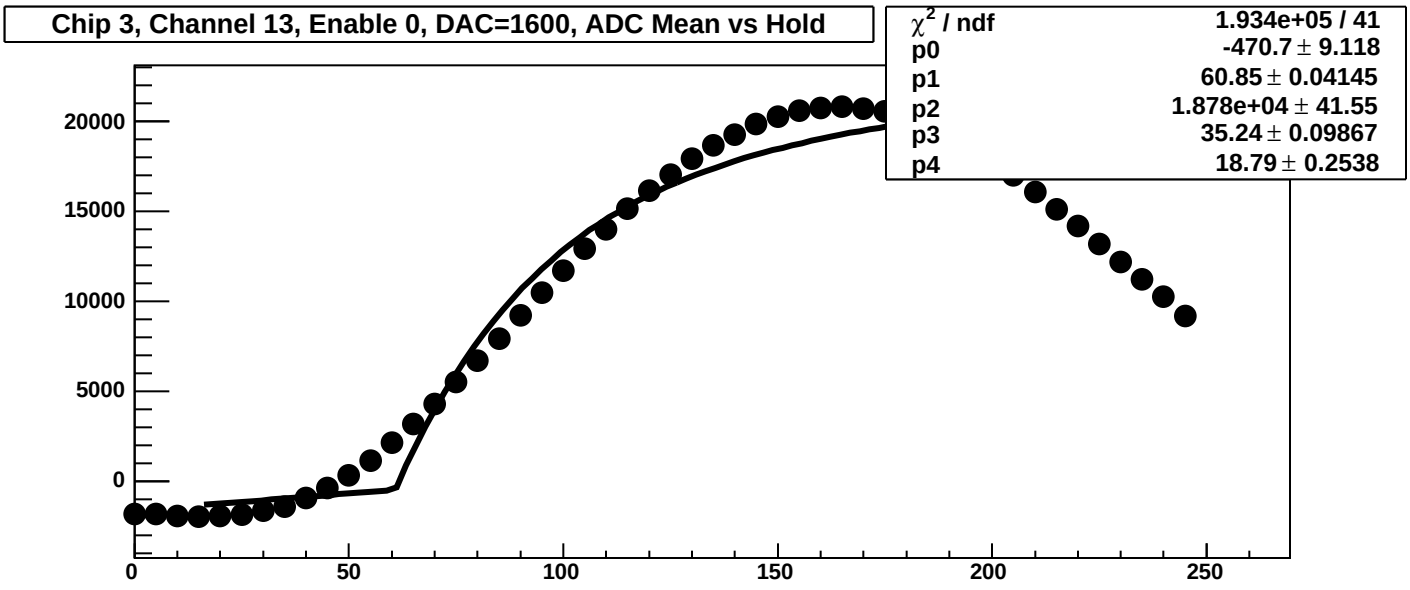
Chip 3, Channel 12, Enable 4!, DAC=1600, ADC Residuals vs Hold

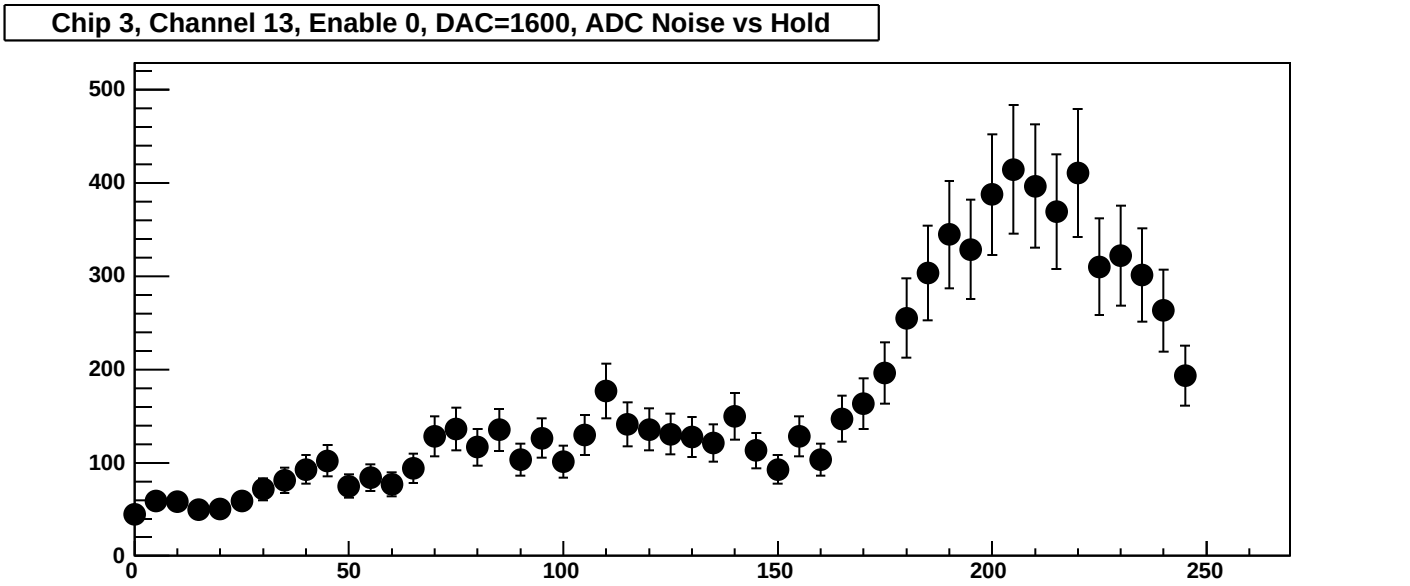


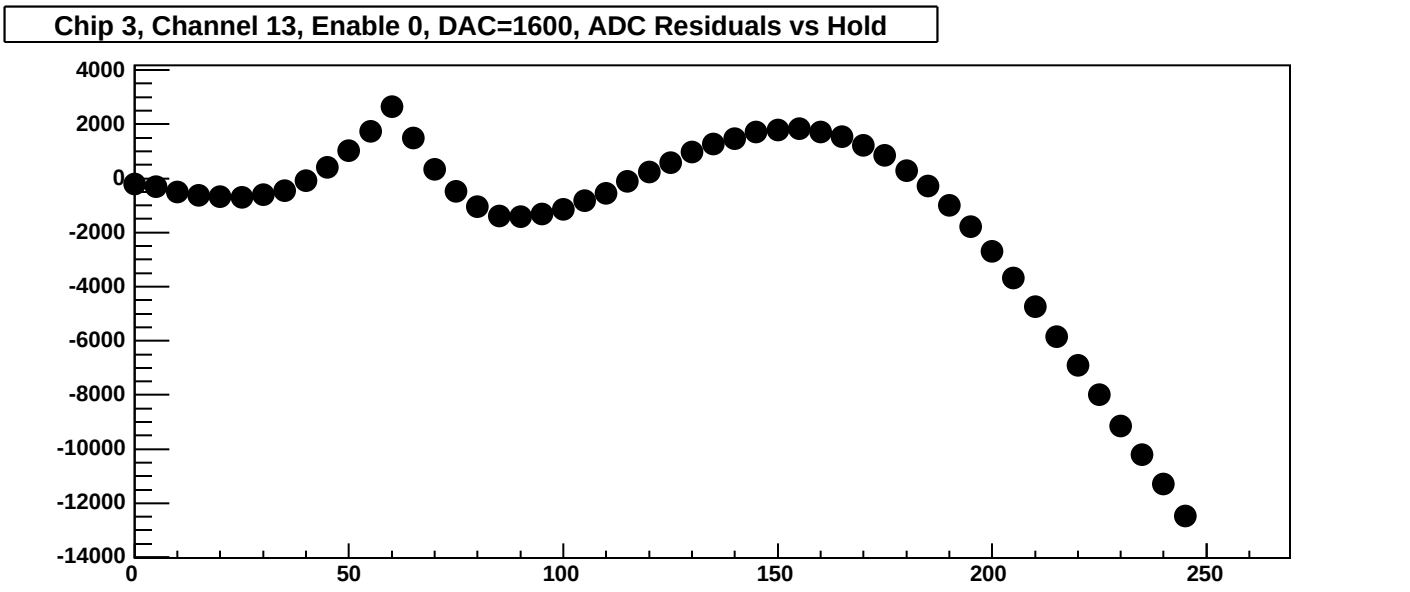

Chip 3, Channel 12, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.339e+06 / 41
p0 -3280 ± 366.1
p1 184.8 ± 365.9
p2 2.265e+04 ± 2.932
p3 -170 ± 1.428
p4 8.536 ± 28
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

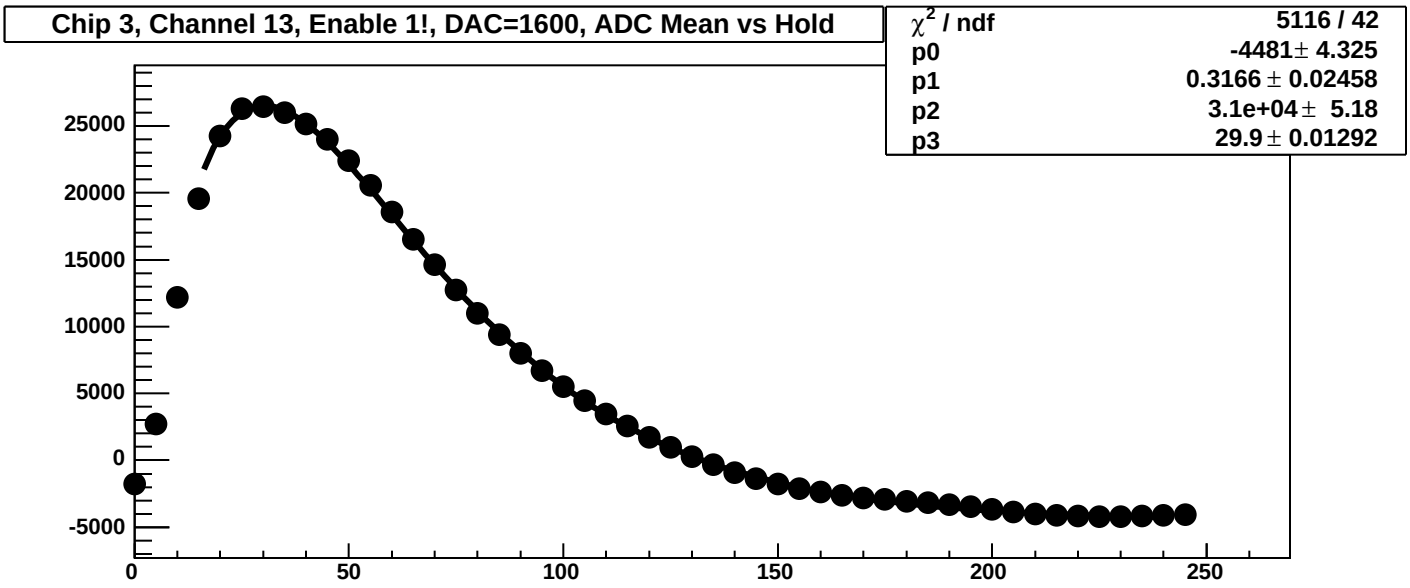

Chip 3, Channel 12, Enable 5, DAC=1600, ADC Noise vs Hold
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

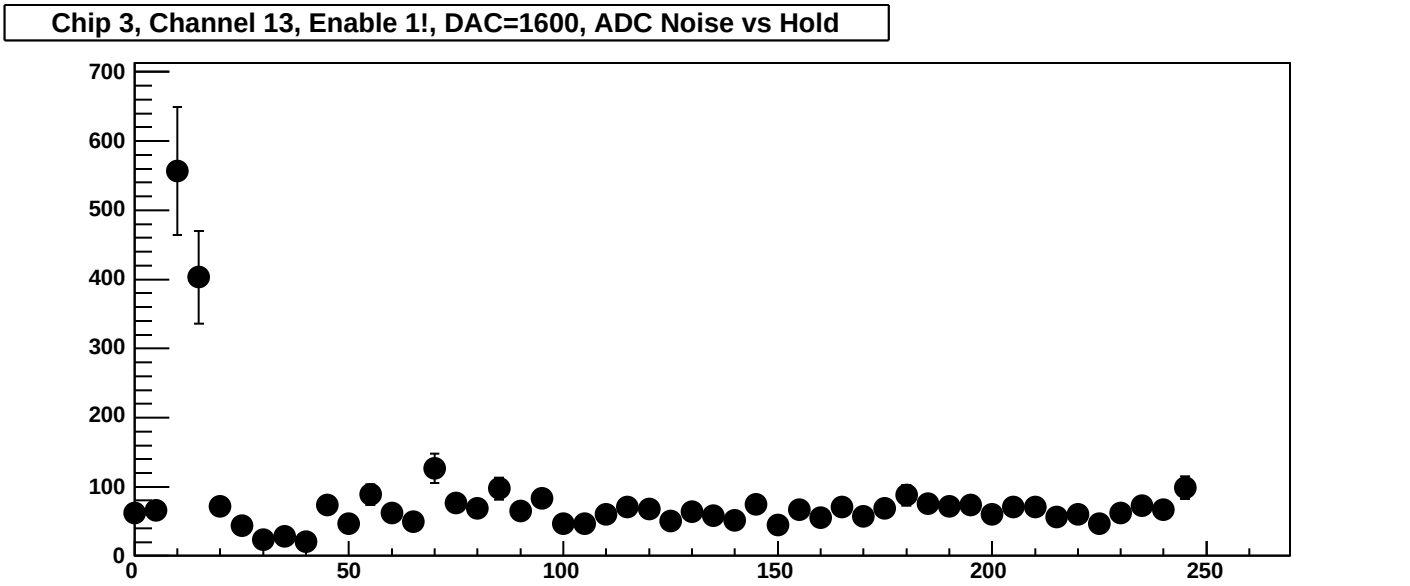

Chip 3, Channel 12, Enable 5, DAC=1600, ADC Residuals vs Hold
10000
5000
0
-5000
-10000
0
50
100
150
200
250

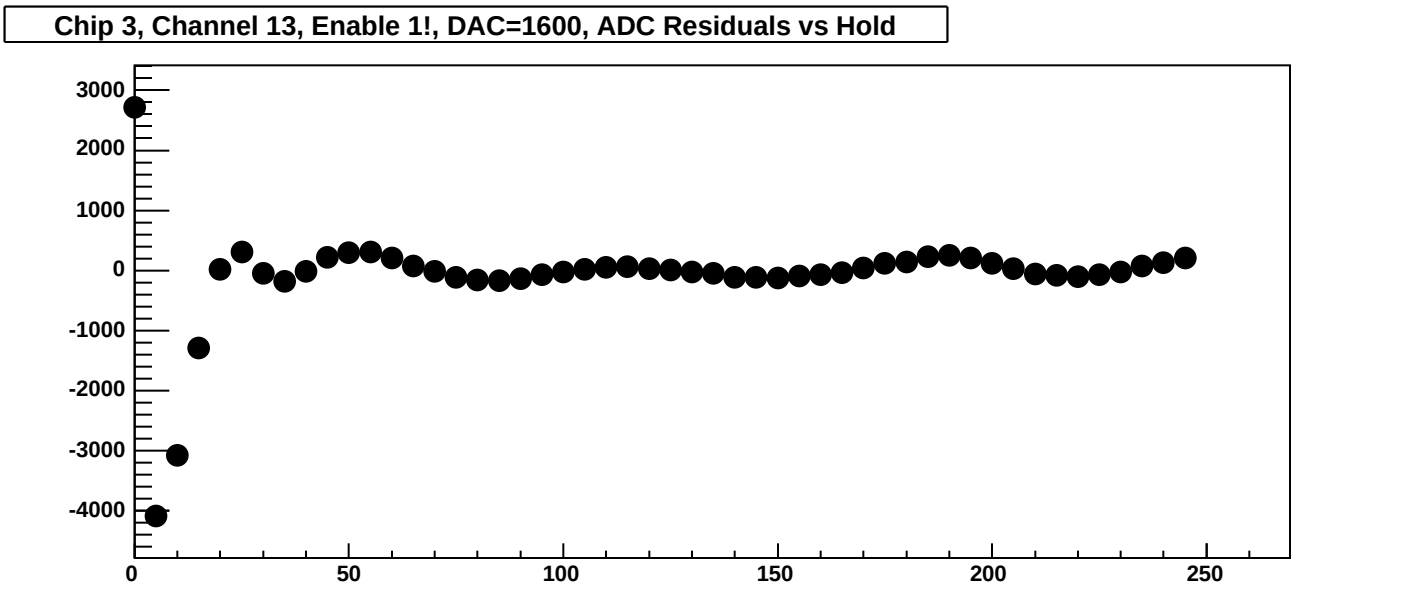

Chip 3, Channel 13, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.934e+05 / 41
p0 -470.7 ± 9.118
p1 60.85 ± 0.04145
p2 1.878e+04 ± 41.55
p3 35.24 ± 0.09867
p4 18.79 ± 0.2538
20000
15000
10000
5000
0
0
50
100
150
200
250

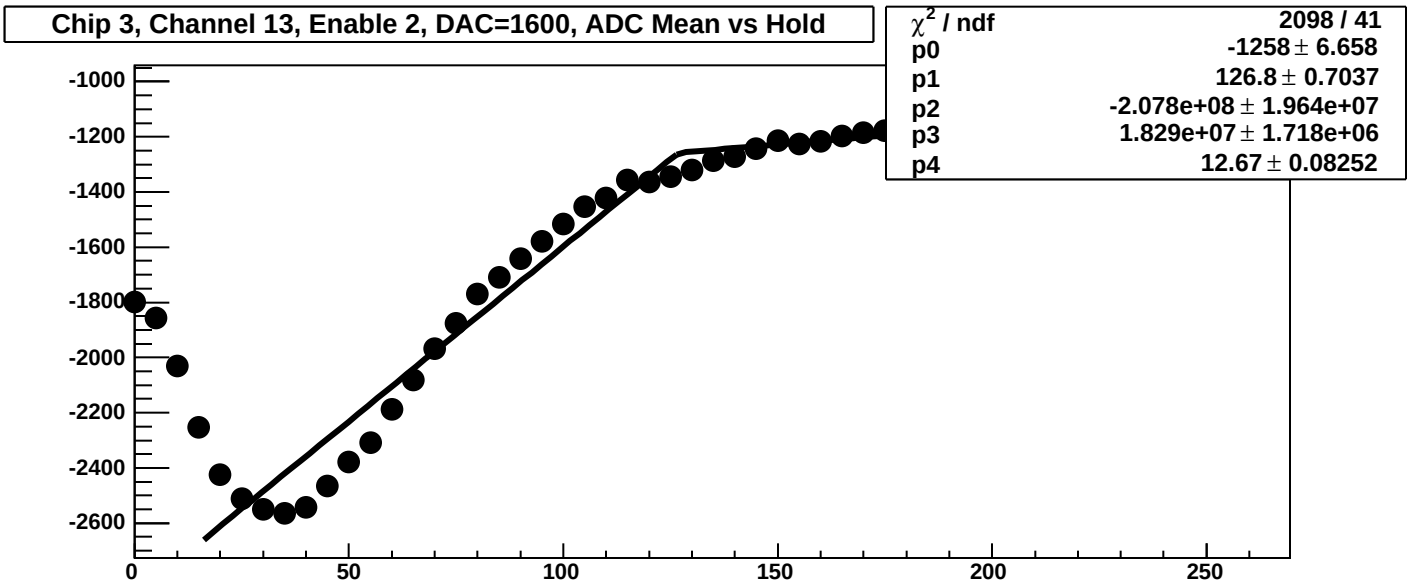

Chip 3, Channel 13, Enable 0, DAC=1600, ADC Noise vs Hold
500
400
300
200
100
0
0
50
100
150
200
250

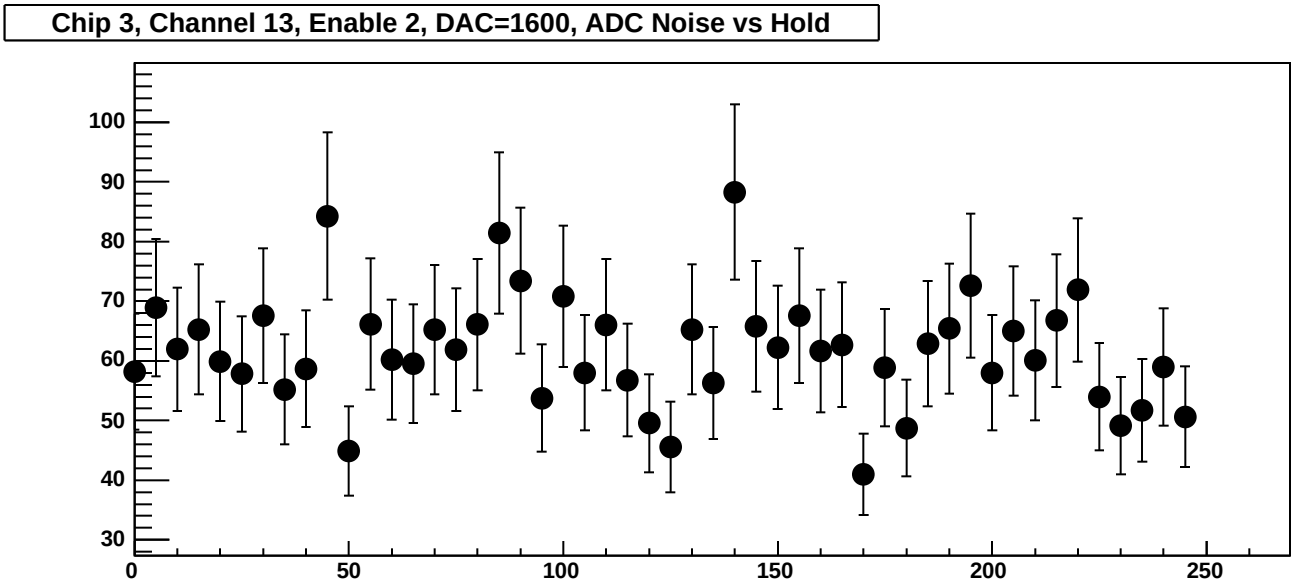

Chip 3, Channel 13, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
50
100
150
200
250

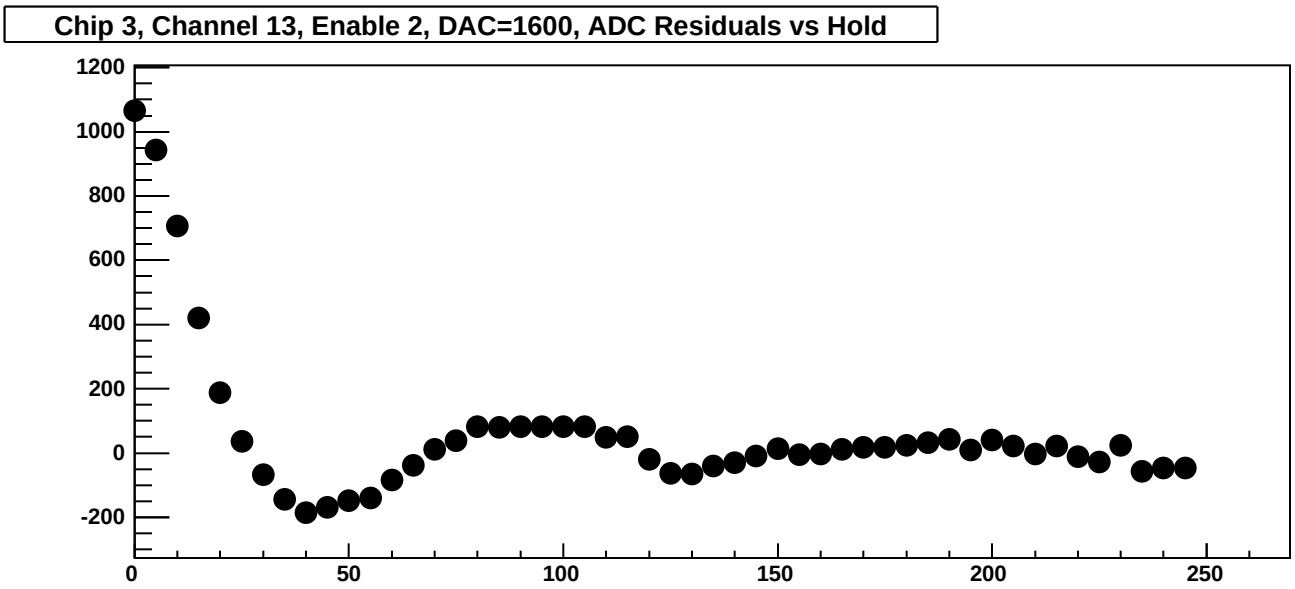

Chip 3, Channel 13, Enable 1!, DAC=1600, ADC Mean vs Hold
χ² / ndf 5116 / 42
p0 -4481 ± 4.325
p1 0.3166 ± 0.02458
p2 3.1e+04 ± 5.18
p3 29.9 ± 0.01292
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

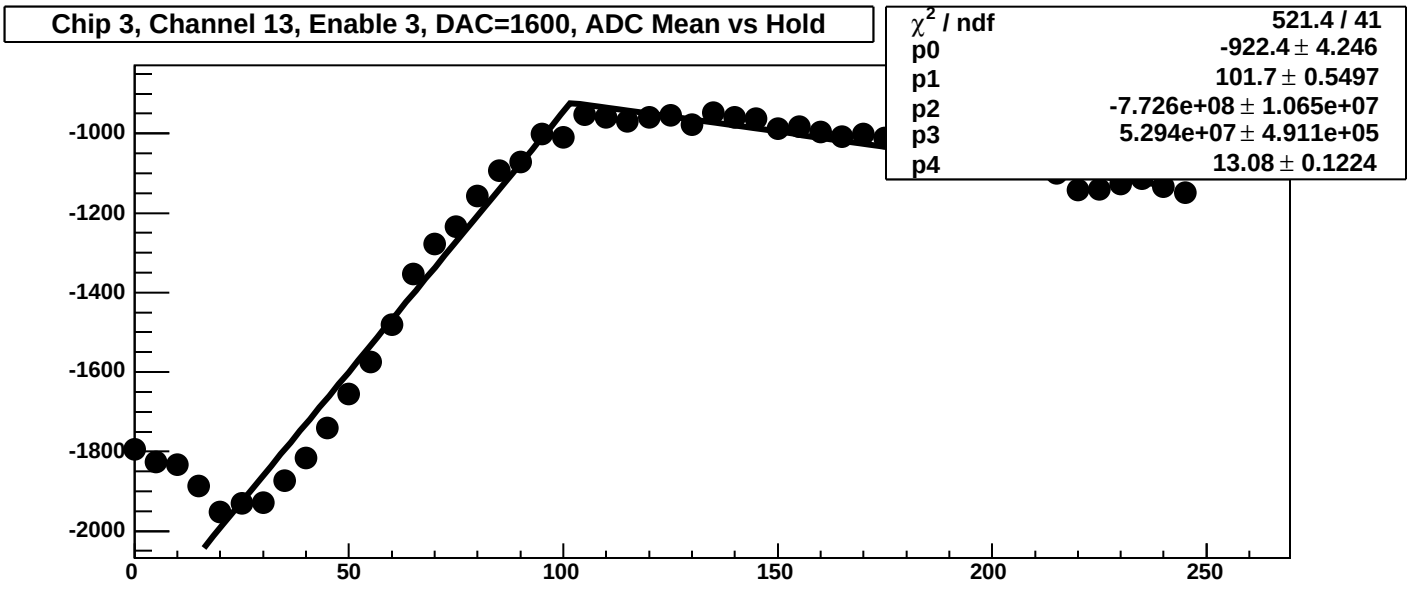

Chip 3, Channel 13, Enable 1!, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250

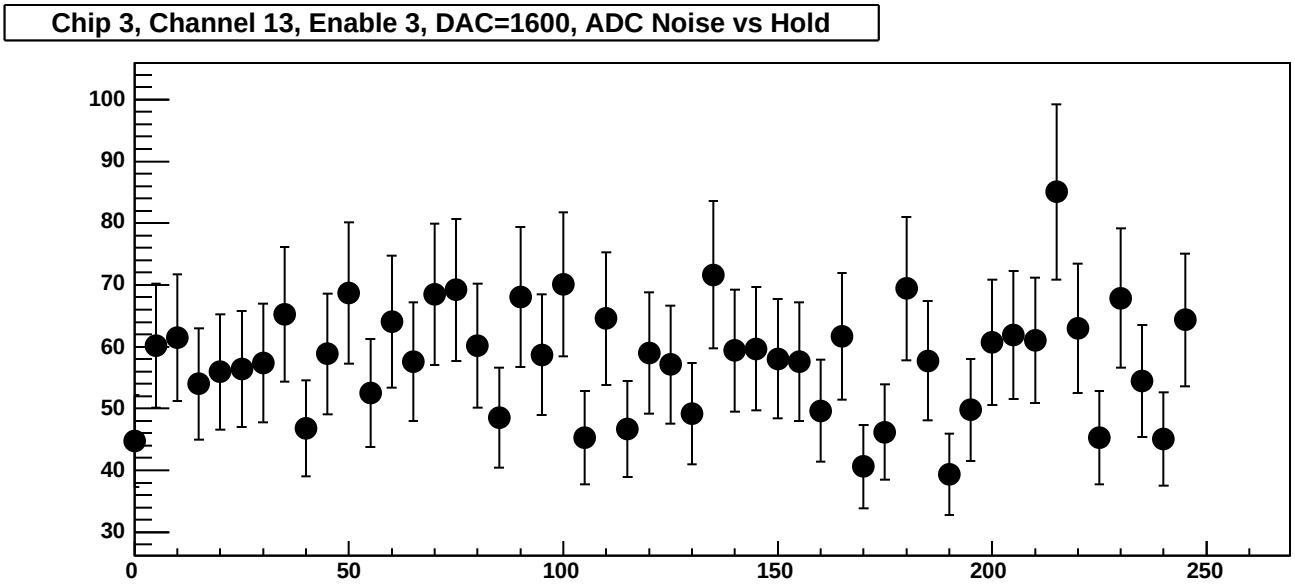

Chip 3, Channel 13, Enable 1!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

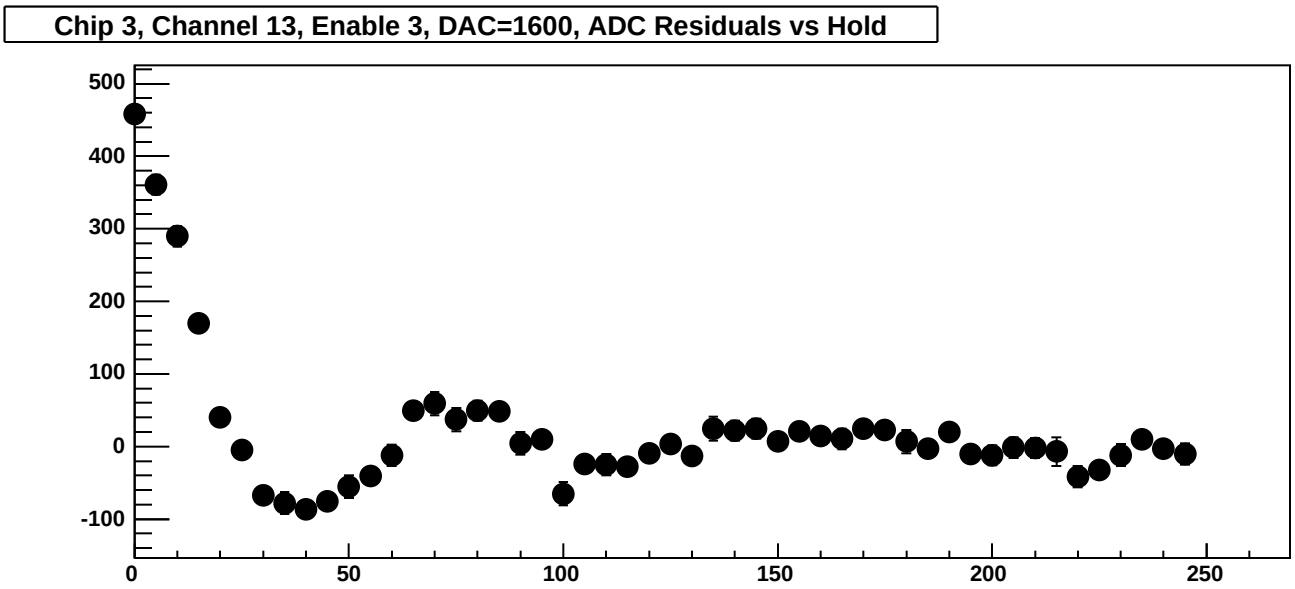

Chip 3, Channel 13, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 2098 / 41
p0 -1258 ± 6.658
p1 126.8 ± 0.7037
p2 -2.078e+08 ± 1.964e+07
p3 1.829e+07 ± 1.718e+06
p4 12.67 ± 0.08252
-1000
-1200
-1400
-1600
-1800
-2000
-2200
-2400
-2600
0
50
100
150
200
250

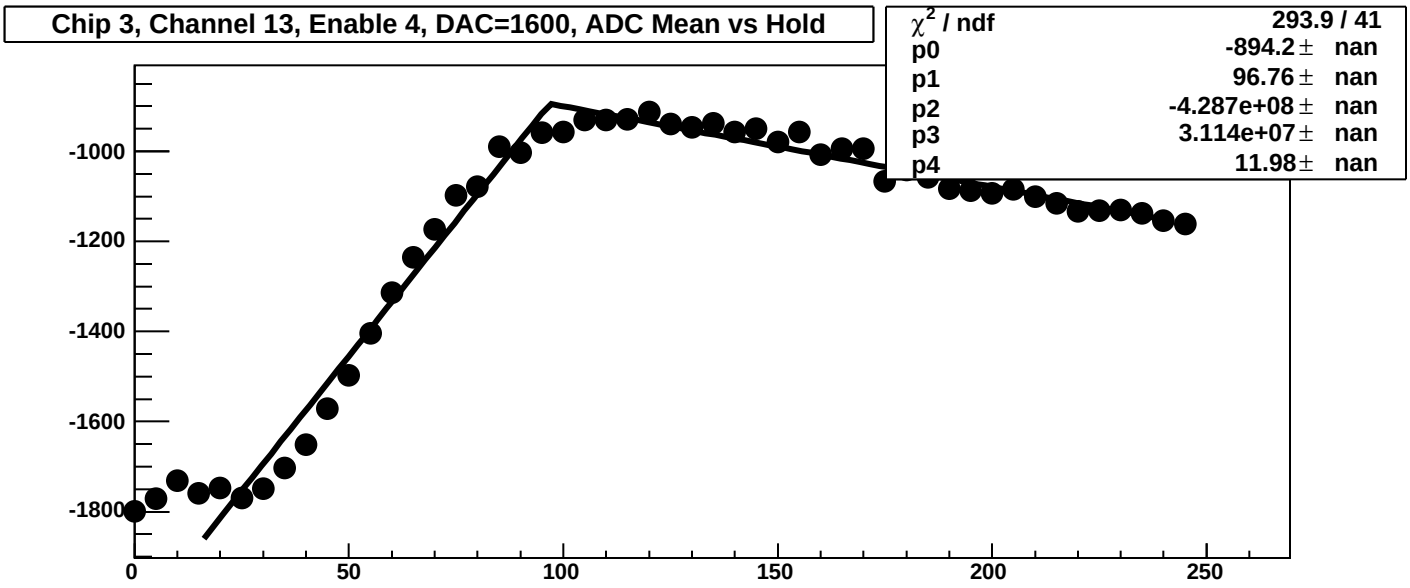

Chip 3, Channel 13, Enable 2, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250

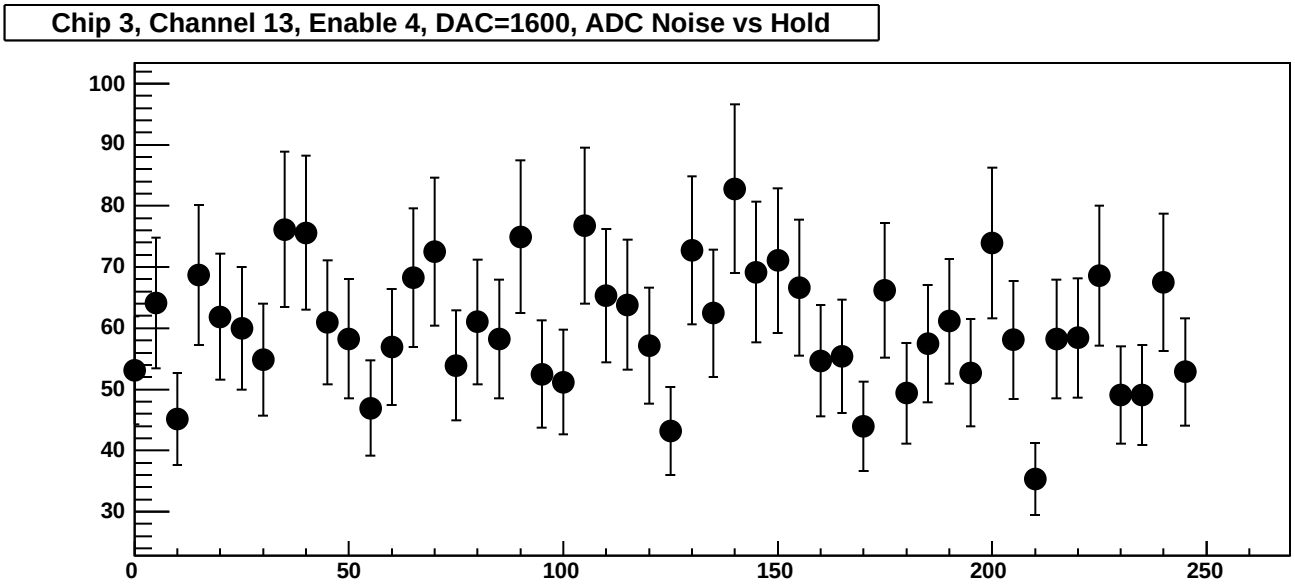

Chip 3, Channel 13, Enable 2, DAC=1600, ADC Residuals vs Hold
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

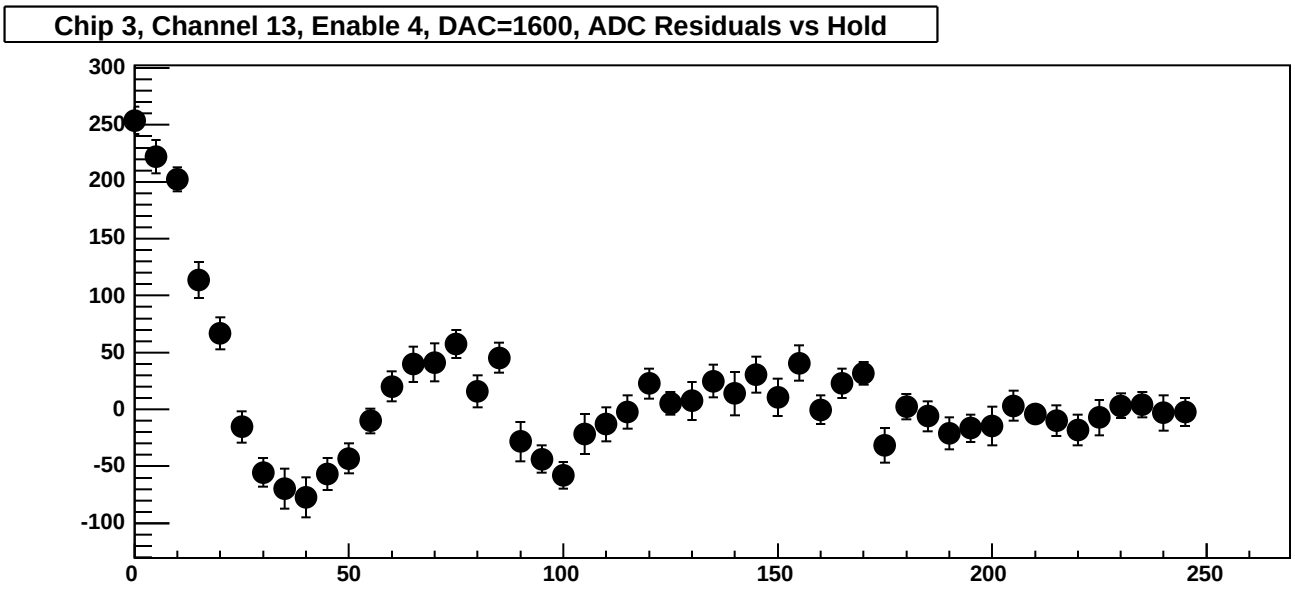

Chip 3, Channel 13, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 521.4 / 41
p0 -922.4 ± 4.246
p1 101.7 ± 0.5497
p2 -7.726e+08 ± 1.065e+07
p3 5.294e+07 ± 4.911e+05
p4 13.08 ± 0.1224
-1000
-1200
-1400
-1600
-1800
-2000
0
50
100
150
200
250

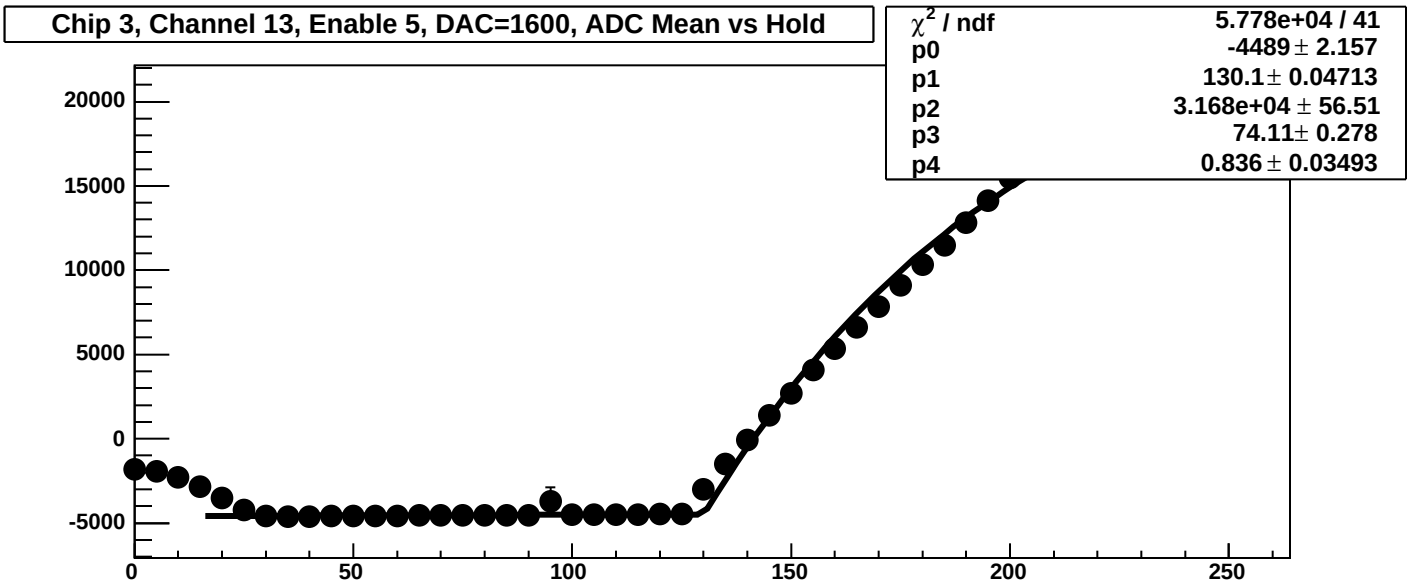

Chip 3, Channel 13, Enable 3, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250

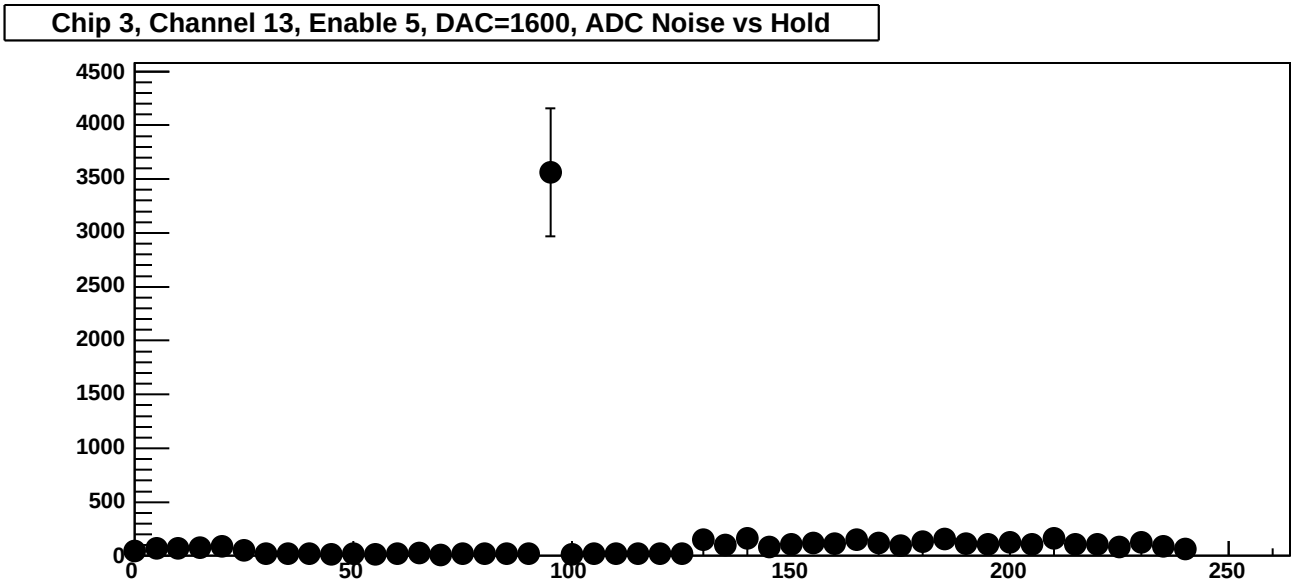

Chip 3, Channel 13, Enable 3, DAC=1600, ADC Residuals vs Hold
500
400
300
200
100
0
-100
0
50
100
150
200
250

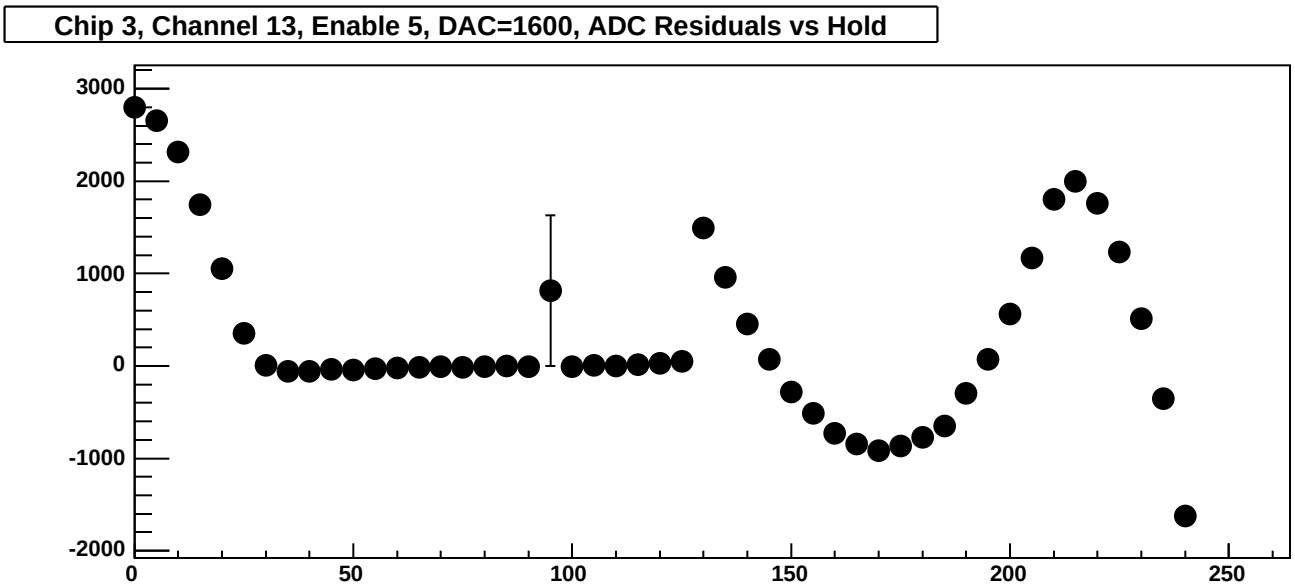

Chip 3, Channel 13, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 293.9 / 41
p0 -894.2 ± nan
p1 96.76 ± nan
p2 -4.287e+08 ± nan
p3 3.114e+07 ± nan
p4 11.98 ± nan
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250

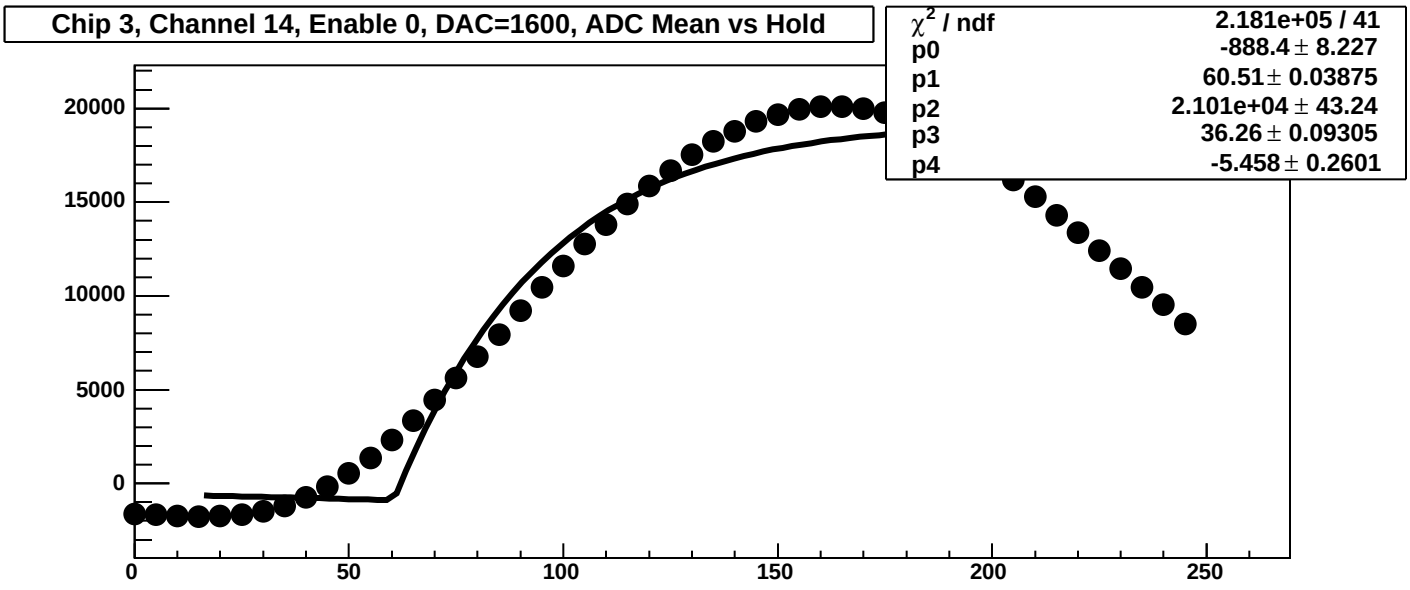

Chip 3, Channel 13, Enable 4, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250

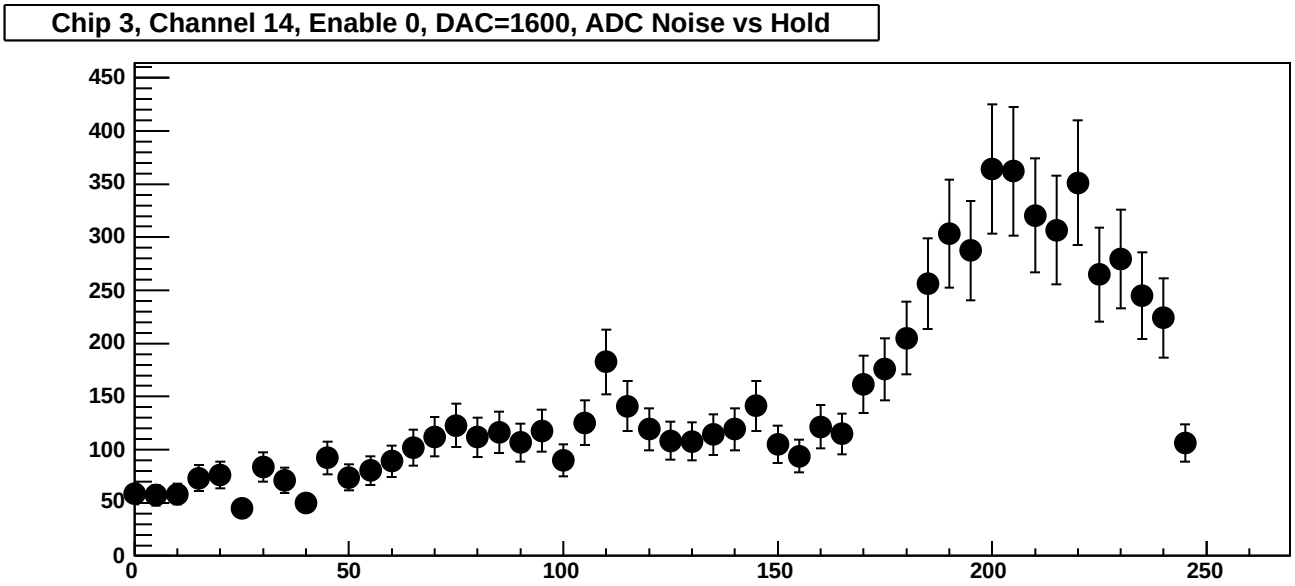

Chip 3, Channel 13, Enable 4, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

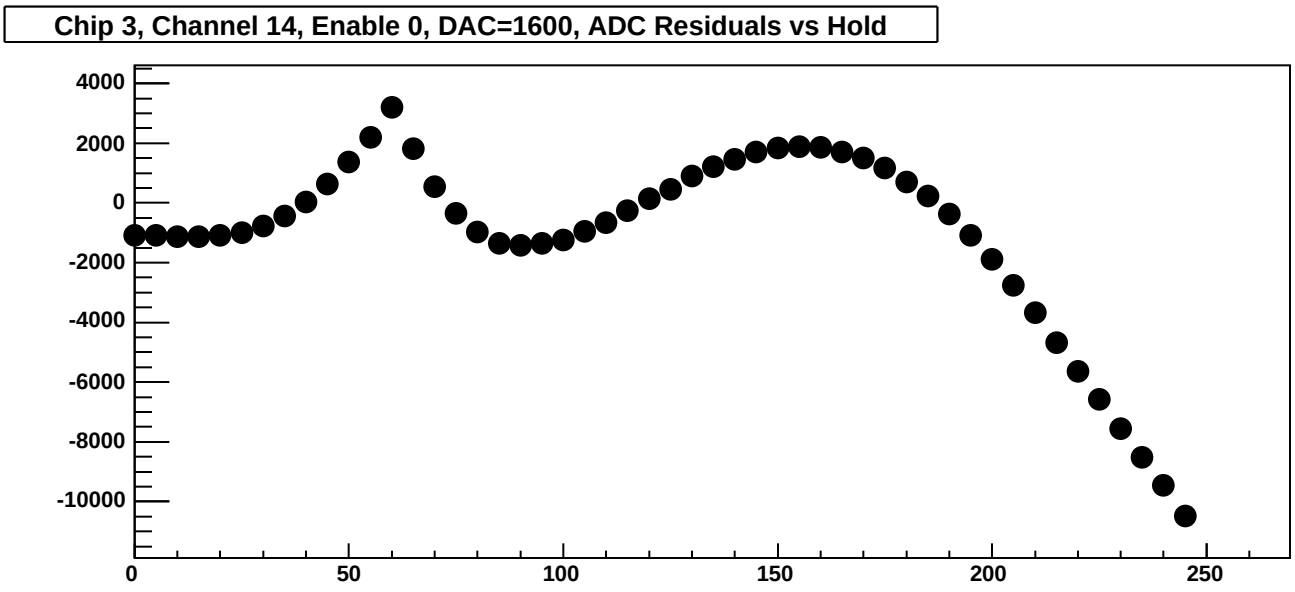

Chip 3, Channel 13, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.778e+04 / 41
p0 -4489 ± 2.157
p1 130.1 ± 0.04713
p2 3.168e+04 ± 56.51
p3 74.11 ± 0.278
p4 0.836 ± 0.03493
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 3, Channel 13, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250

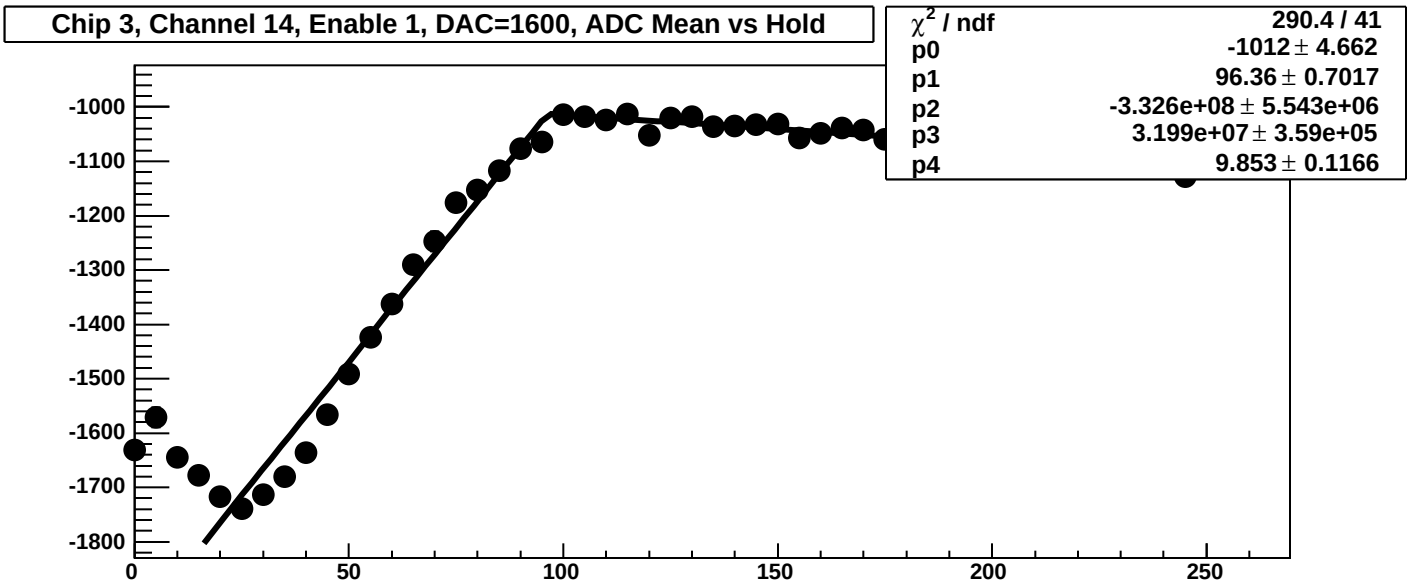

Chip 3, Channel 13, Enable 5, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
0
50
100
150
200
250


Chip 3, Channel 14, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.181e+05 / 41
p0 -888.4 ± 8.227
p1 60.51 ± 0.03875
p2 2.101e+04 ± 43.24
p3 36.26 ± 0.09305
p4 -5.458 ± 0.2601
20000
15000
10000
5000
0
0
50
100
150
200
250

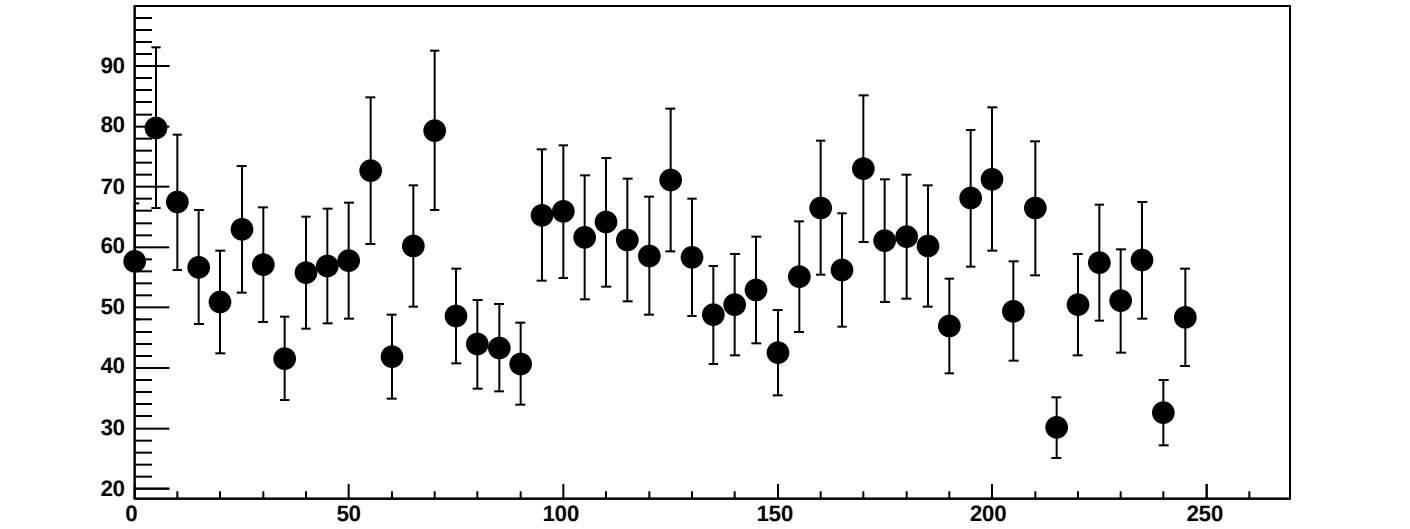

Chip 3, Channel 14, Enable 0, DAC=1600, ADC Noise vs Hold
450
400
350
300
250
200
150
100
50
0
0
50
100
150
200
250


Chip 3, Channel 14, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

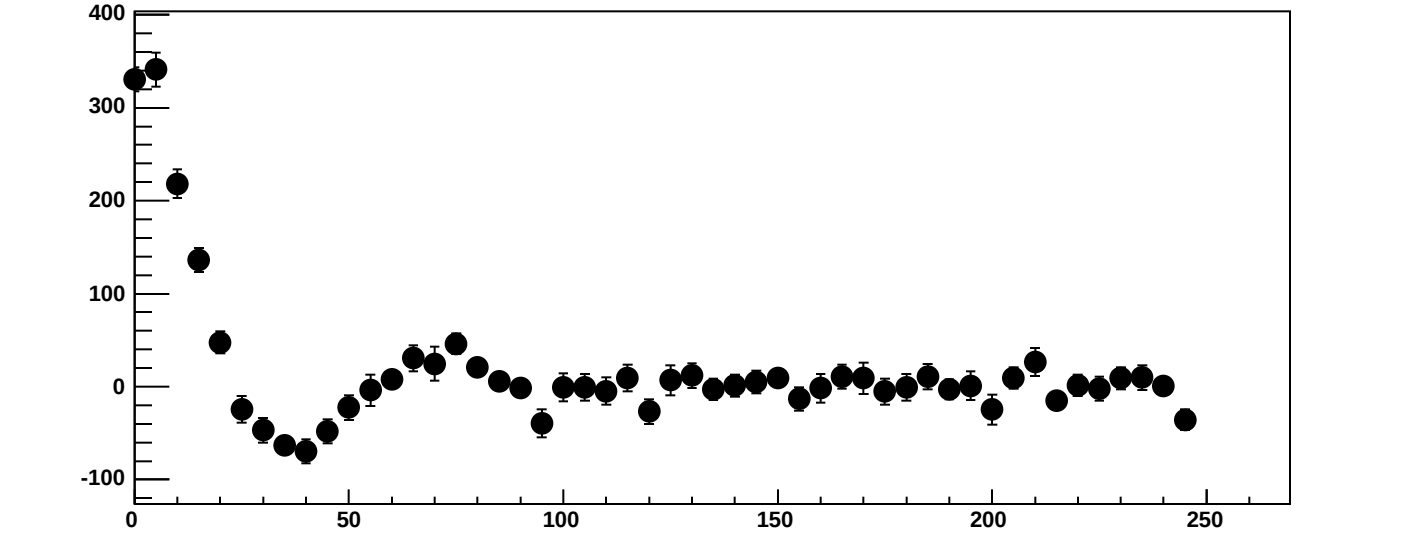
Chip 3, Channel 14, Enable 1, DAC=1600, ADC Mean vs Hold



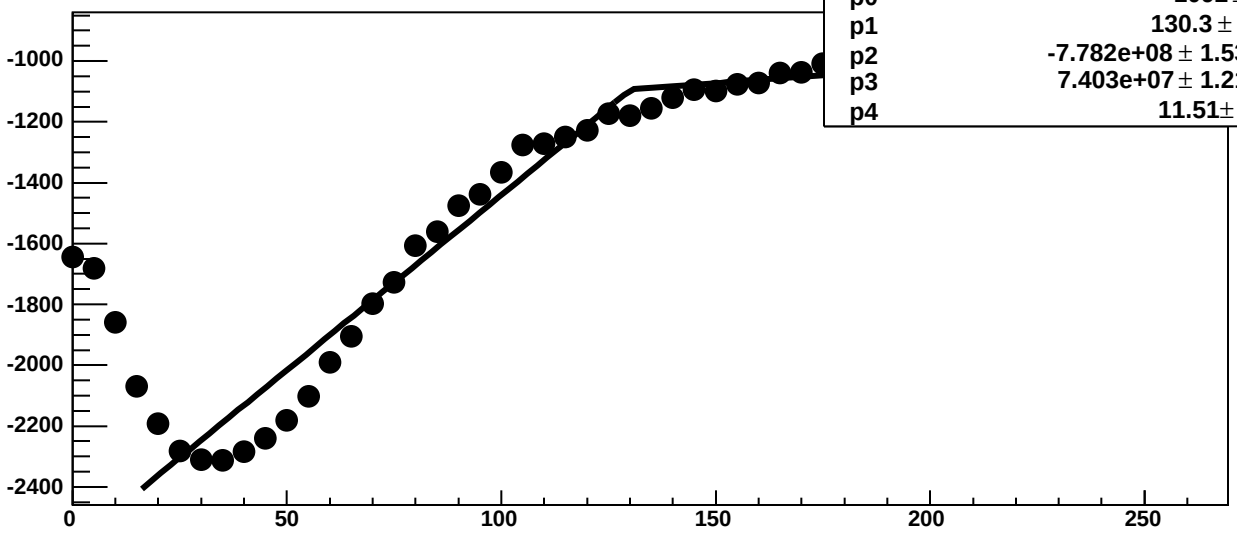
Chip 3, Channel 14, Enable 1, DAC=1600, ADC Noise vs Hold



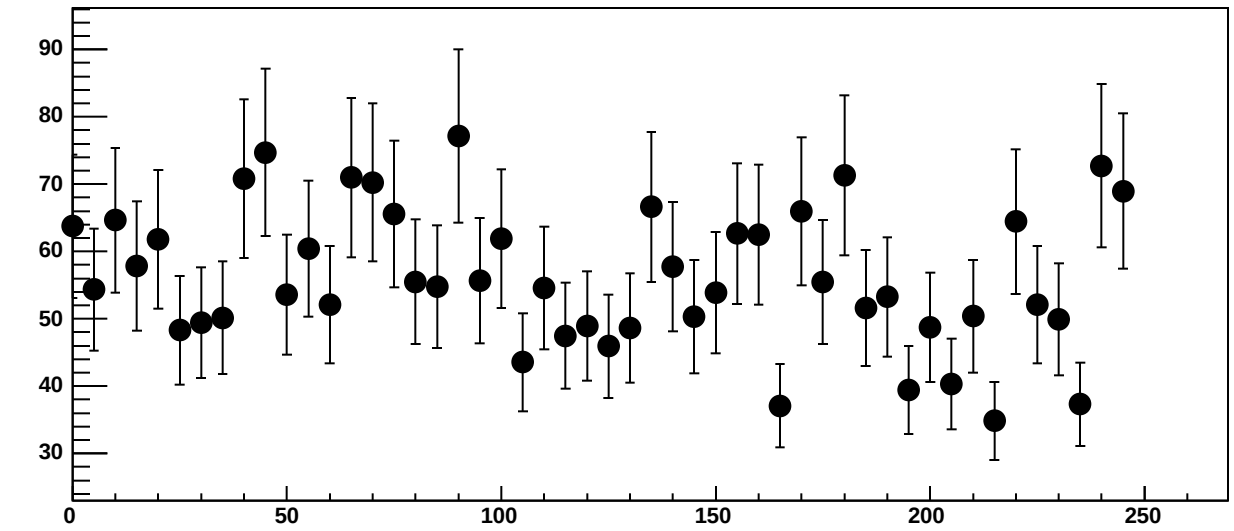
Chip 3, Channel 14, Enable 1, DAC=1600, ADC Residuals vs Hold



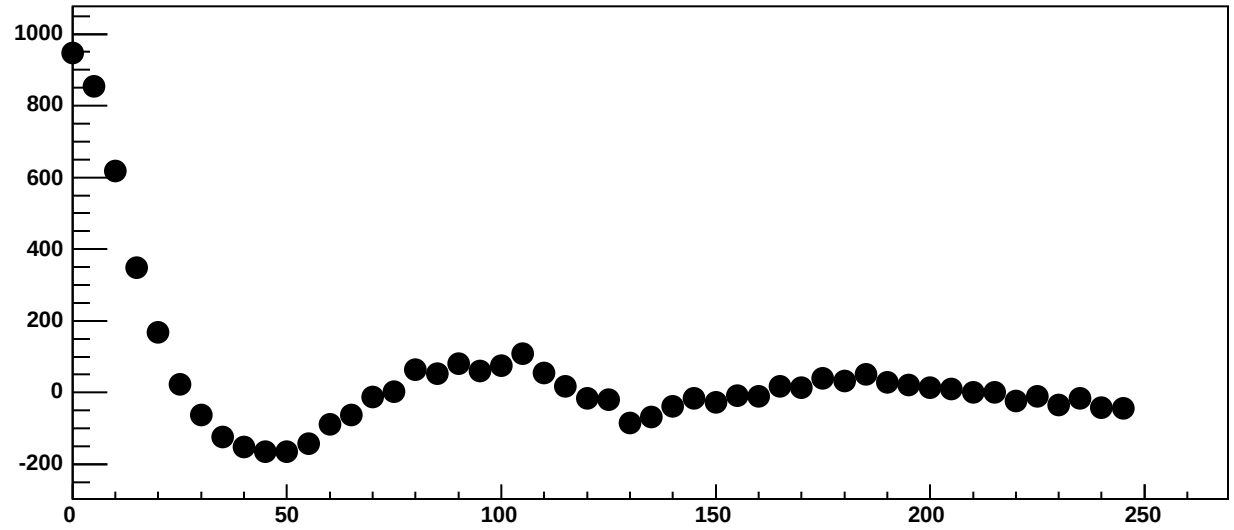
Chip 3, Channel 14, Enable 2, DAC=1600, ADC Mean vs Hold



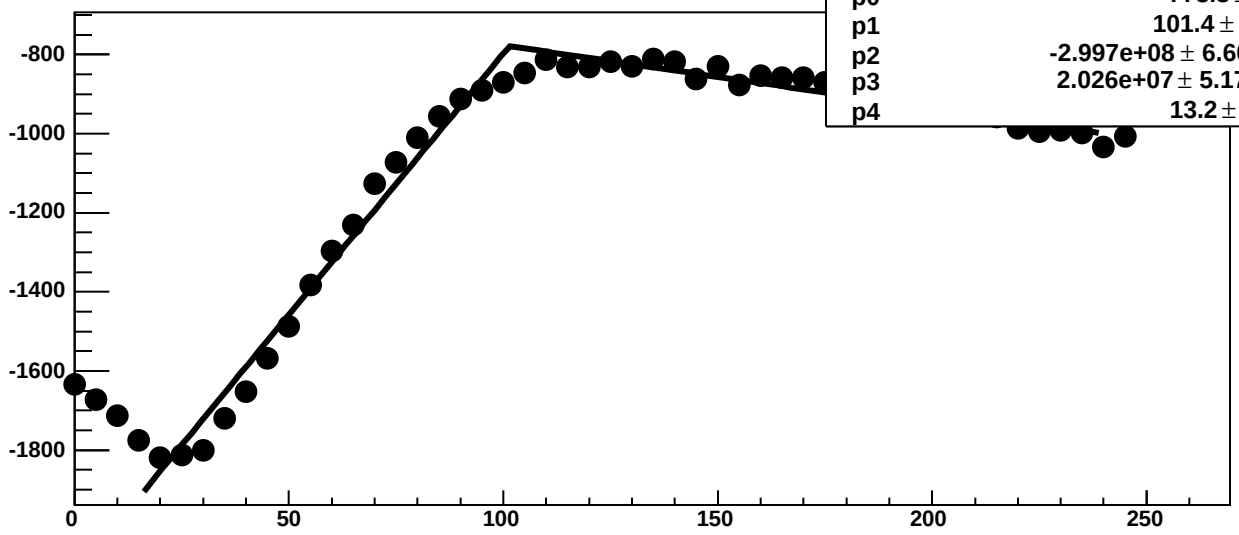
Chip 3, Channel 14, Enable 2, DAC=1600, ADC Noise vs Hold



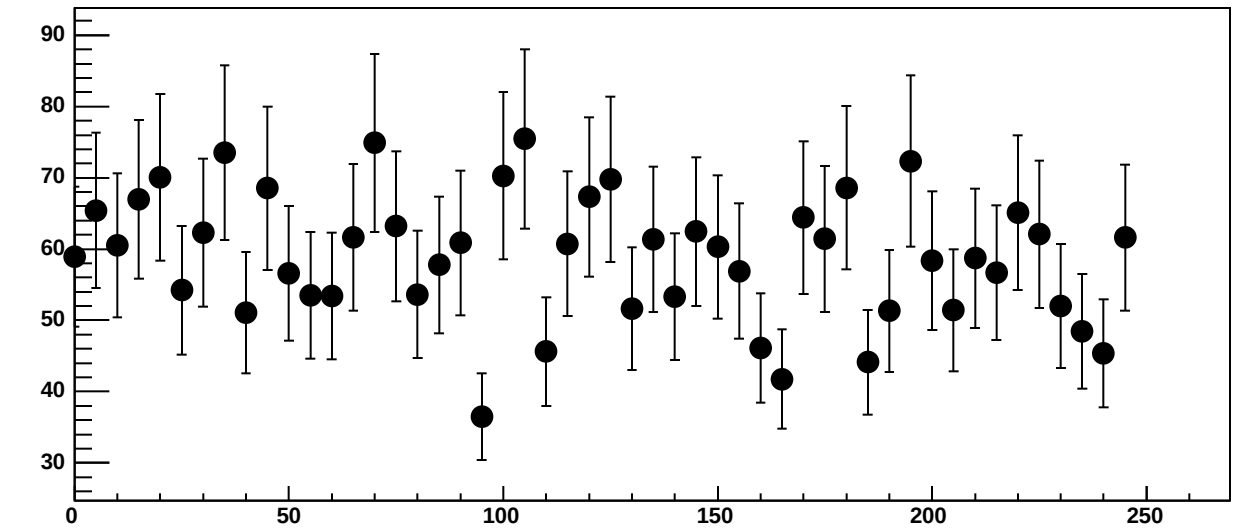
Chip 3, Channel 14, Enable 2, DAC=1600, ADC Residuals vs Hold



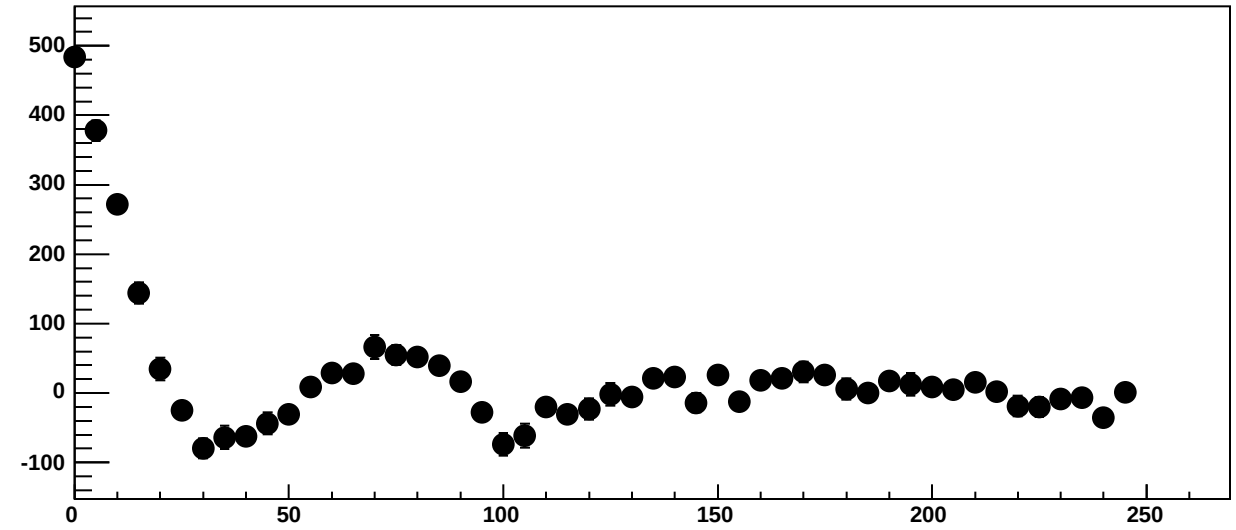
Chip 3, Channel 14, Enable 3, DAC=1600, ADC Mean vs Hold

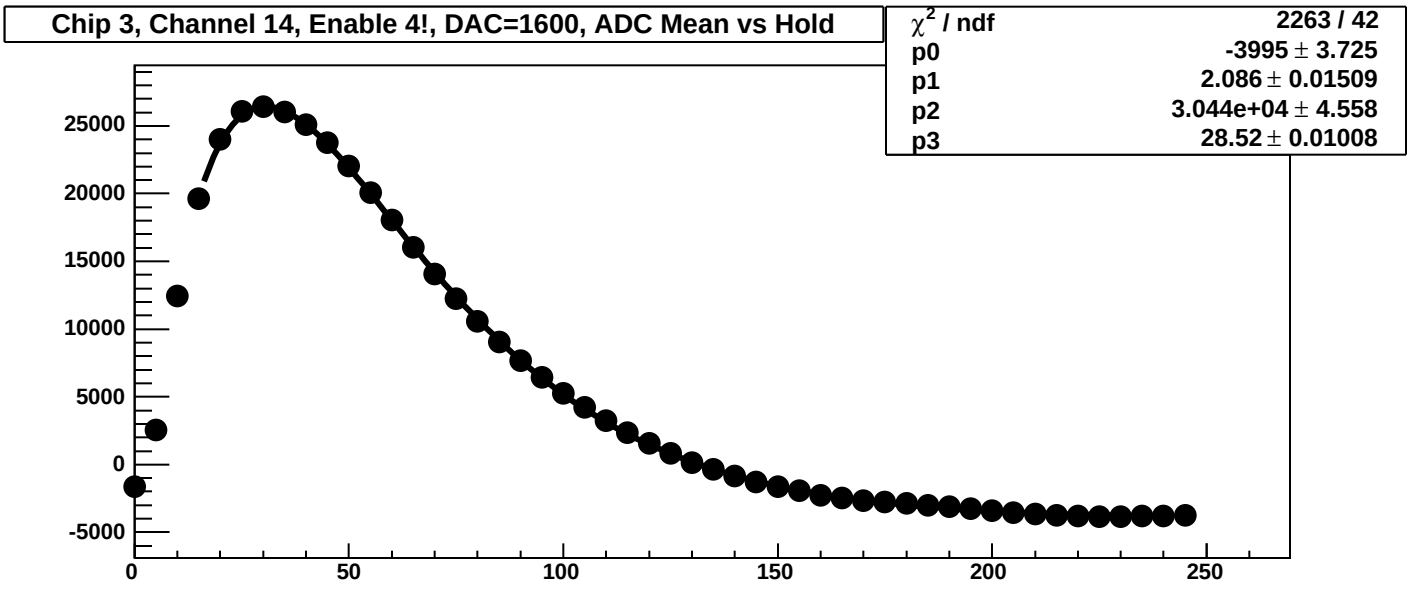


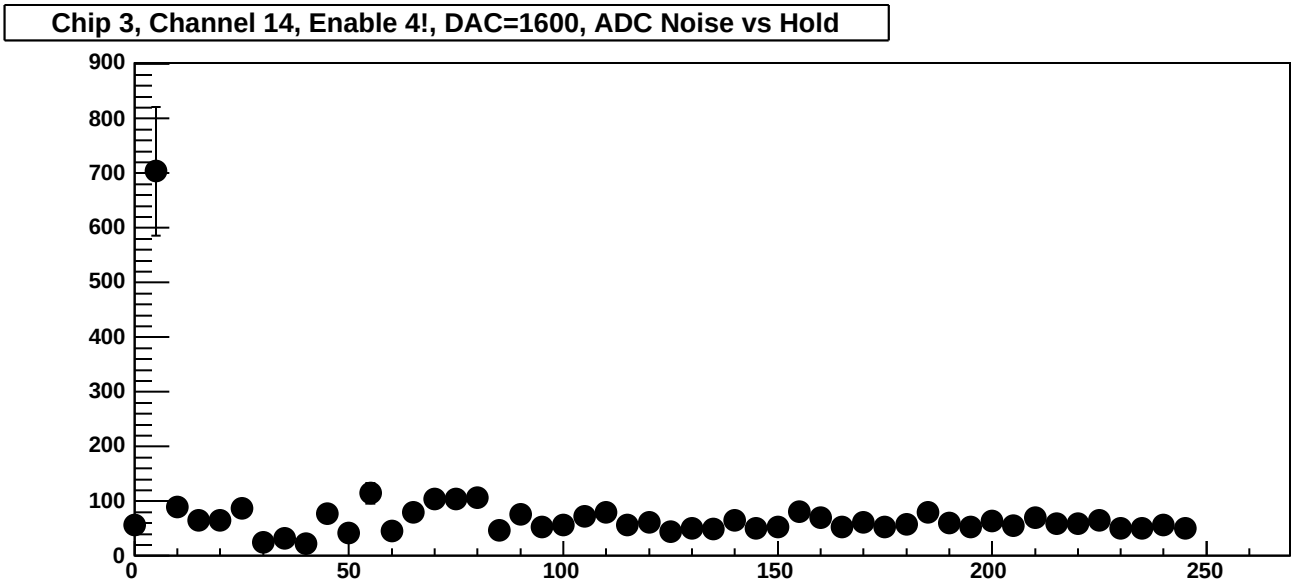
Chip 3, Channel 14, Enable 3, DAC=1600, ADC Noise vs Hold

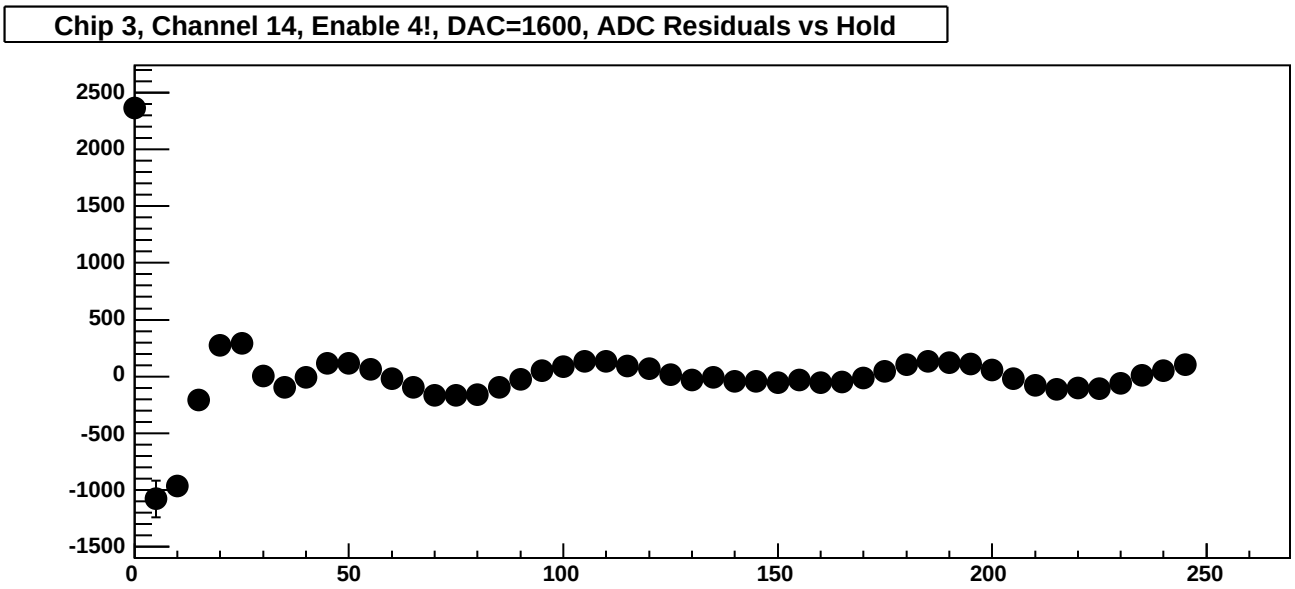


Chip 3, Channel 14, Enable 3, DAC=1600, ADC Residuals vs Hold

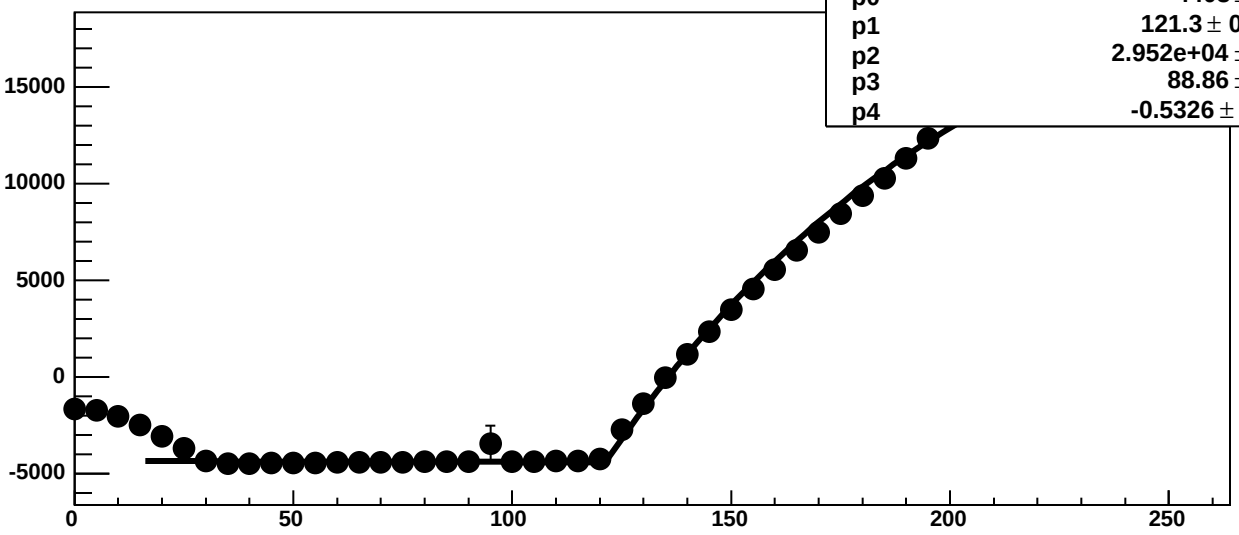



Chip 3, Channel 14, Enable 4!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2263 / 42
p0 -3995 ± 3.725
p1 2.086 ± 0.01509
p2 3.044e+04 ± 4.558
p3 28.52 ± 0.01008
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

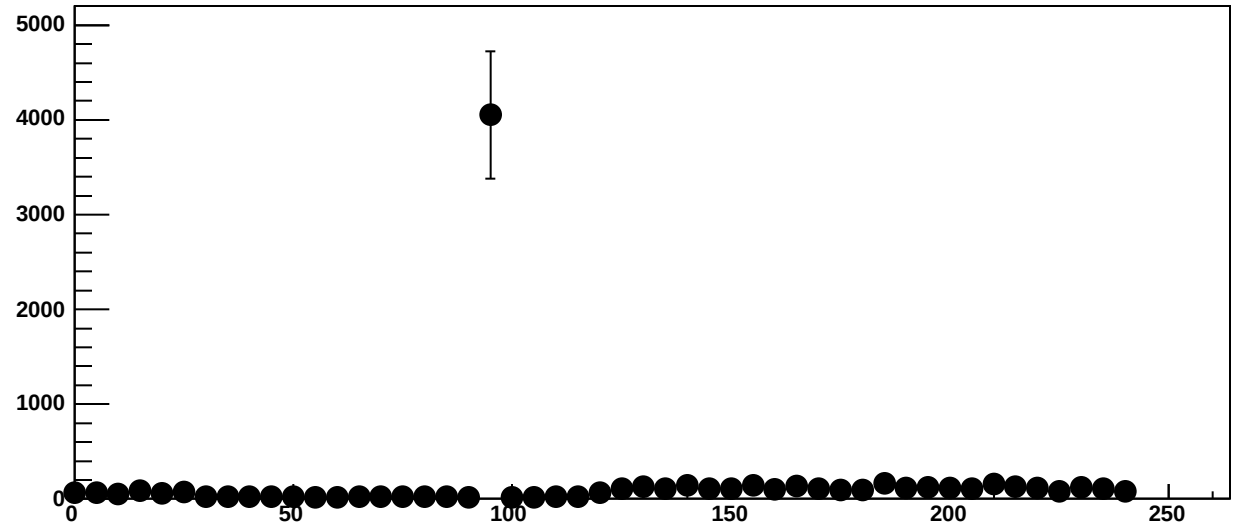

Chip 3, Channel 14, Enable 4!, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 3, Channel 14, Enable 4!, DAC=1600, ADC Residuals vs Hold
2500
2000
1500
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

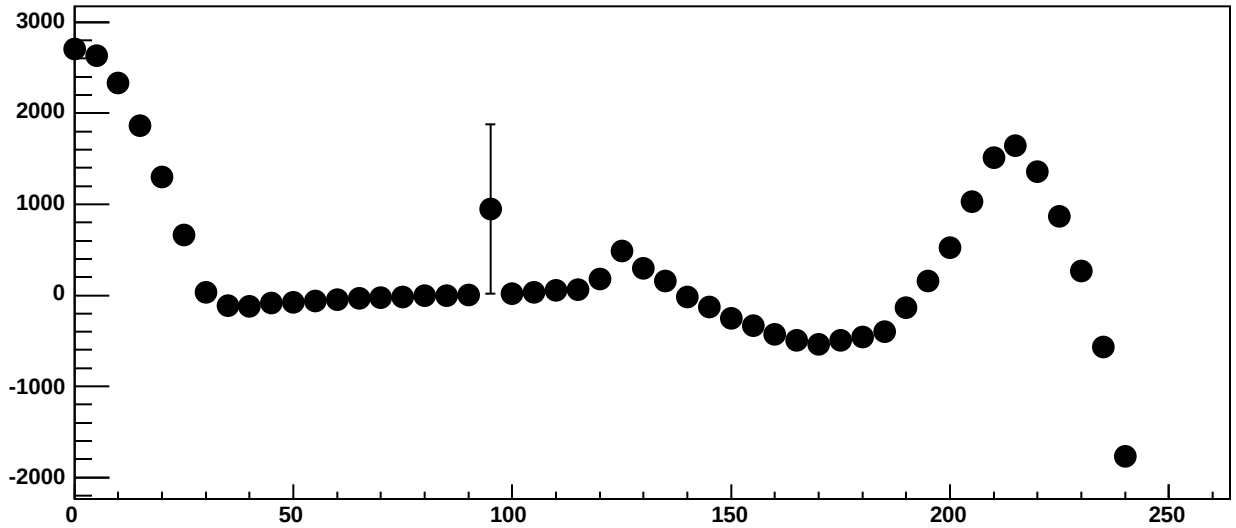
Chip 3, Channel 14, Enable 5, DAC=1600, ADC Mean vs Hold

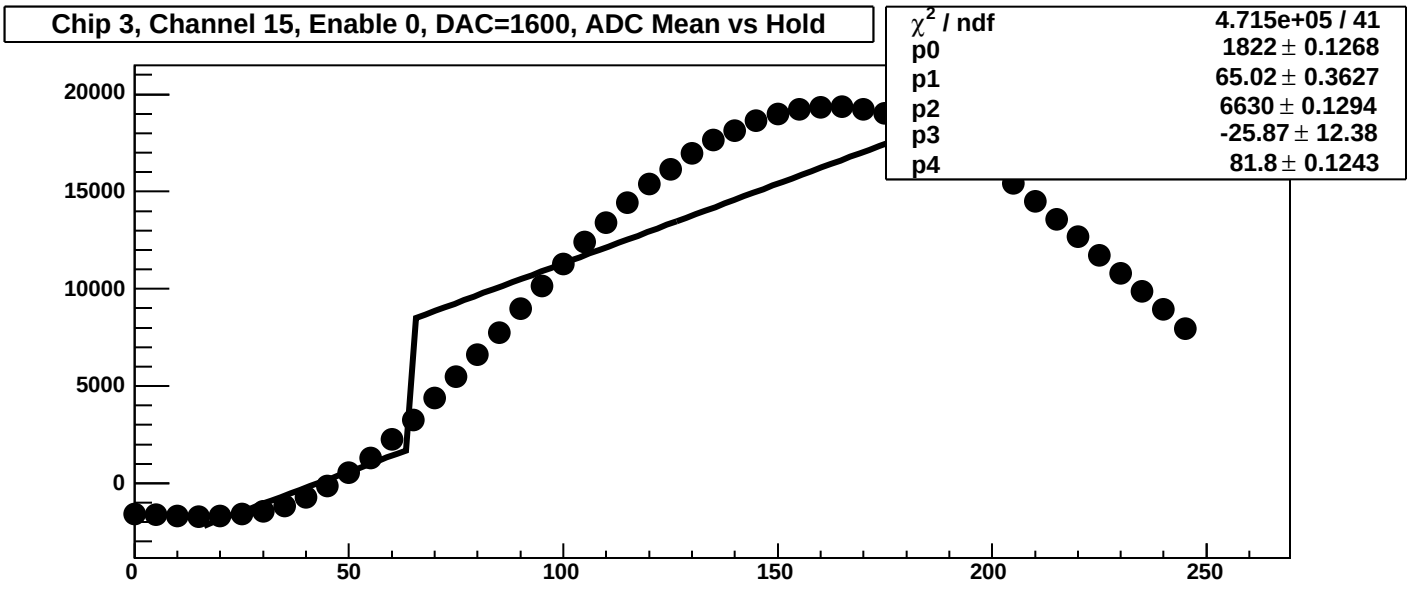


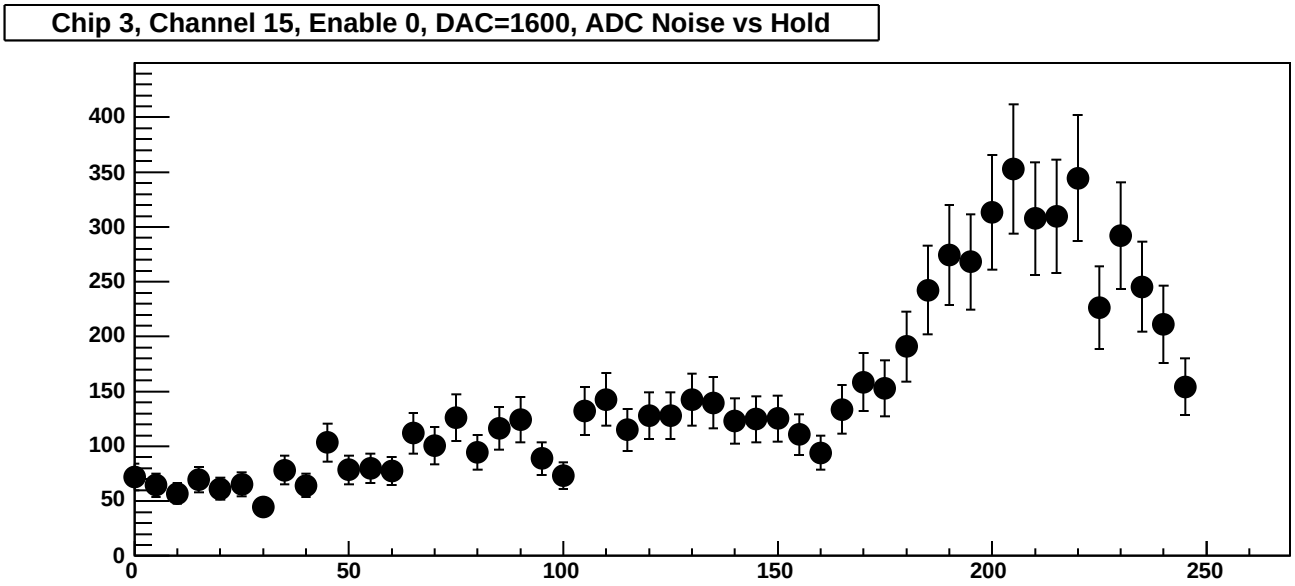
Chip 3, Channel 14, Enable 5, DAC=1600, ADC Noise vs Hold

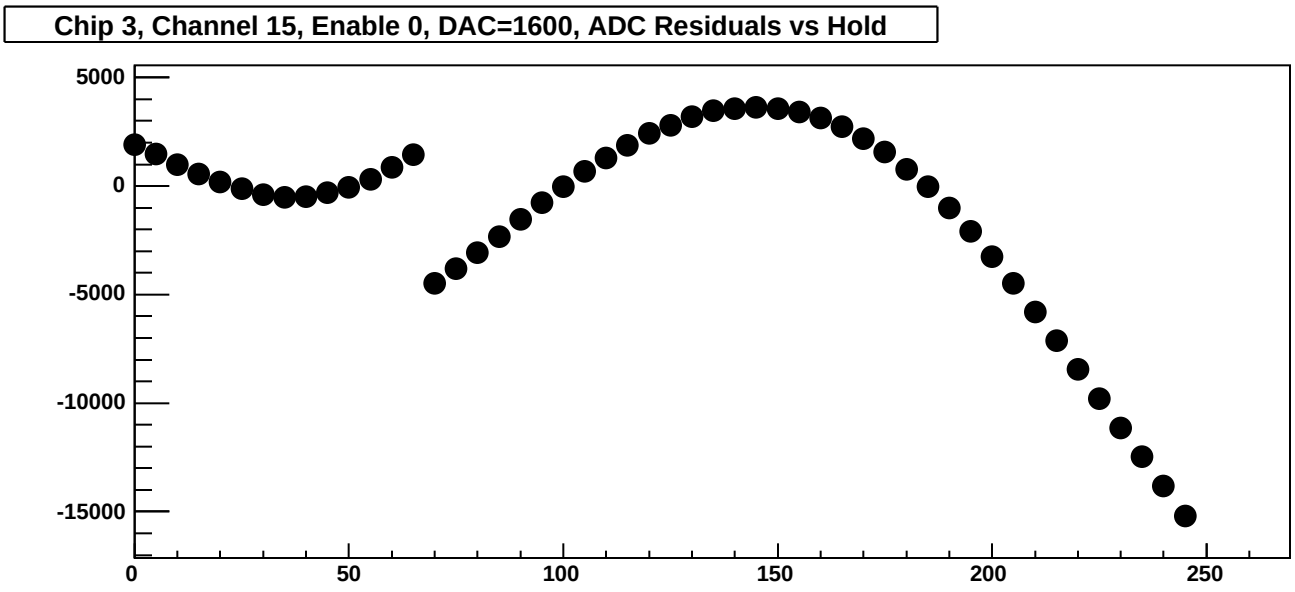


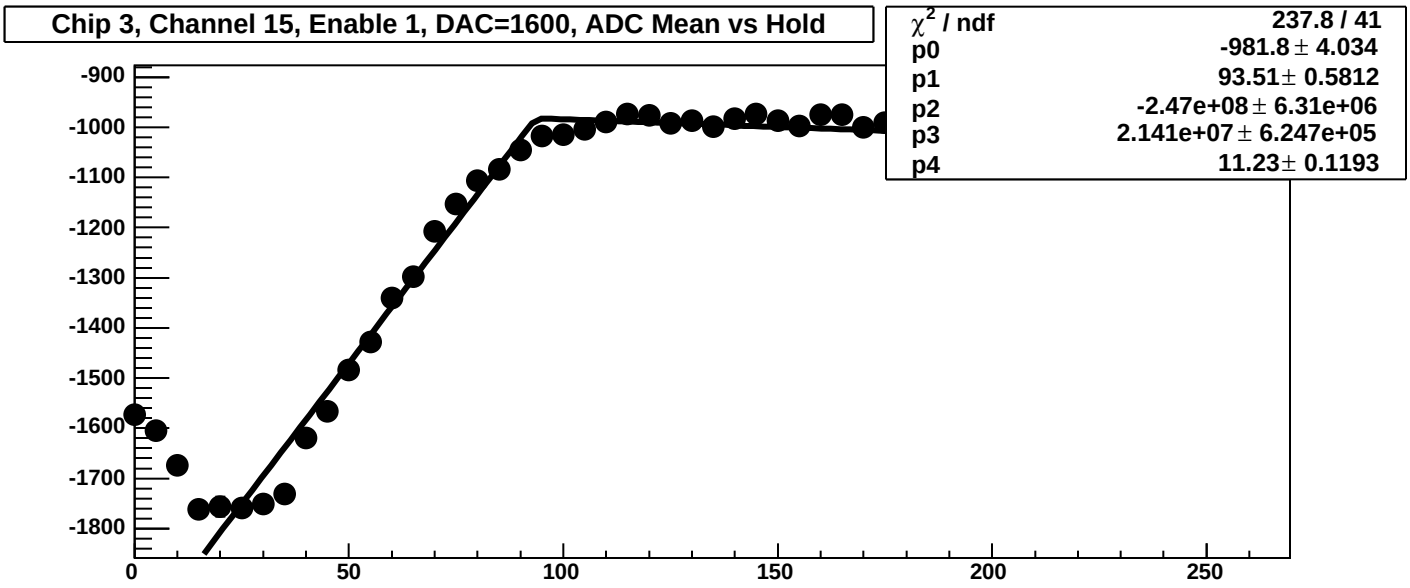
Chip 3, Channel 14, Enable 5, DAC=1600, ADC Residuals vs Hold

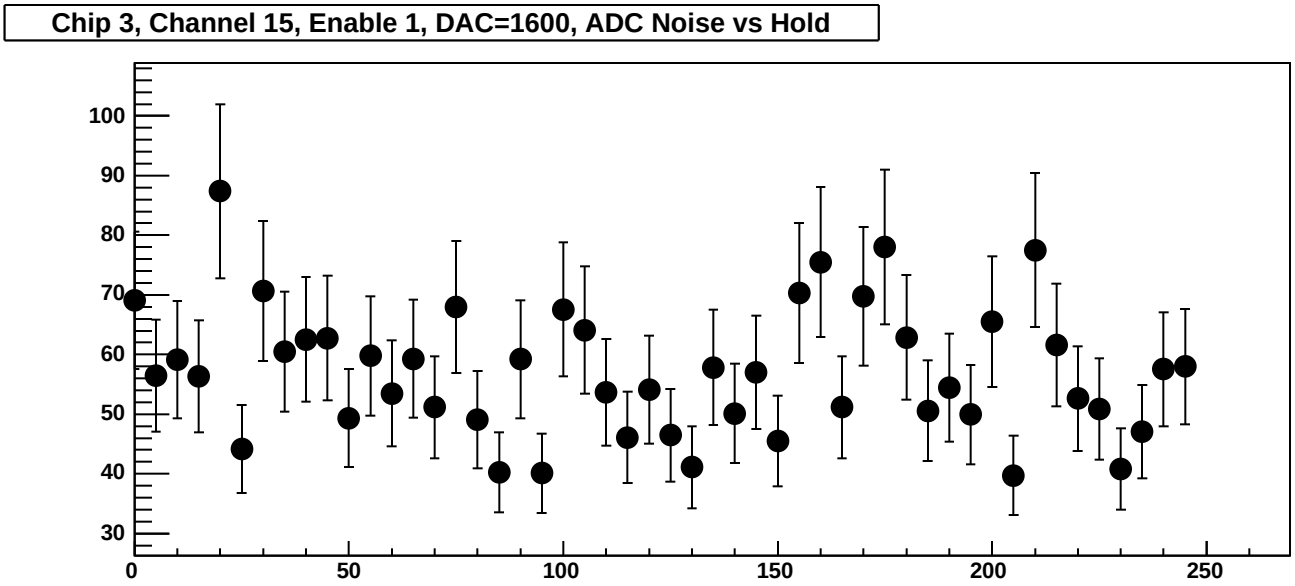


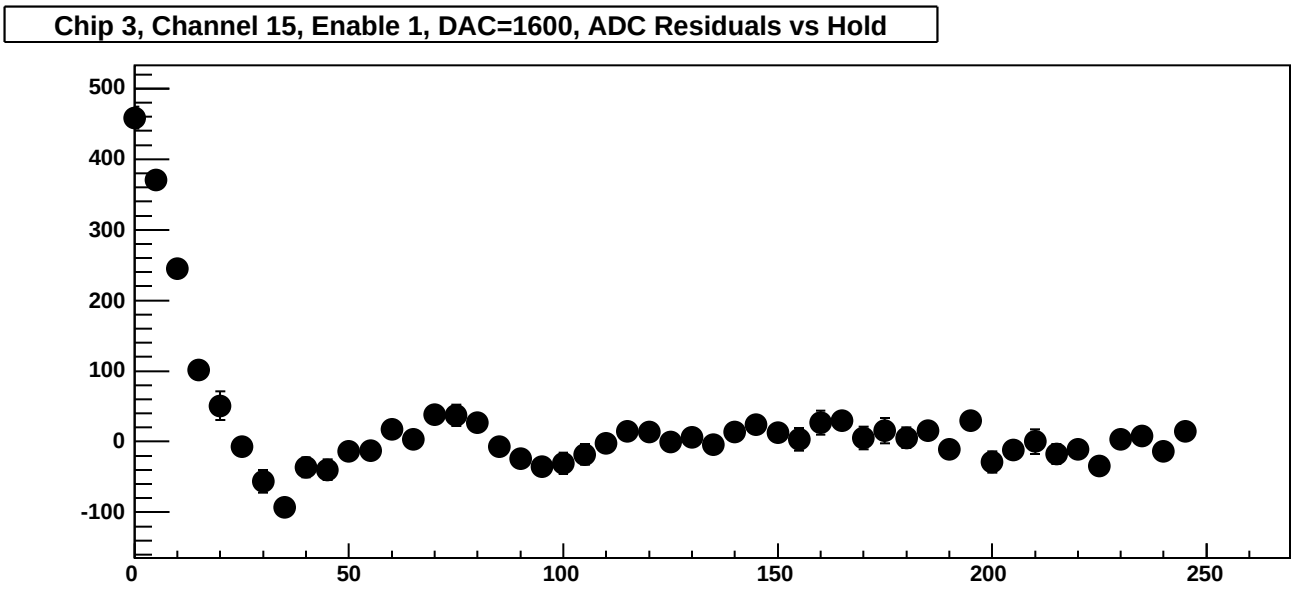

Chip 3, Channel 15, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.715e+05 / 41
p0 1822 ± 0.1268
p1 65.02 ± 0.3627
p2 6630 ± 0.1294
p3 -25.87 ± 12.38
p4 81.8 ± 0.1243
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 3, Channel 15, Enable 0, DAC=1600, ADC Noise vs Hold
400
350
300
250
200
150
100
50
0
0
50
100
150
200
250

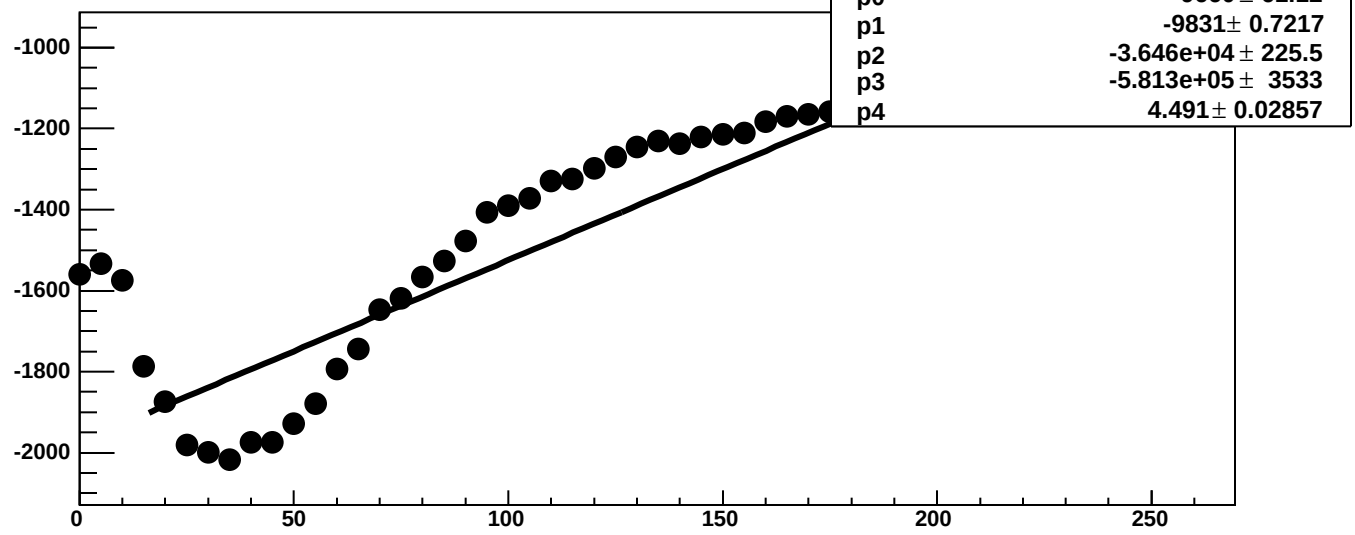

Chip 3, Channel 15, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
0
50
100
150
200
250


Chip 3, Channel 15, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 237.8 / 41
p0 -981.8 ± 4.034
p1 93.51 ± 0.5812
p2 -2.47e+08 ± 6.31e+06
p3 2.141e+07 ± 6.247e+05
p4 11.23 ± 0.1193
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
-1800
0
50
100
150
200
250

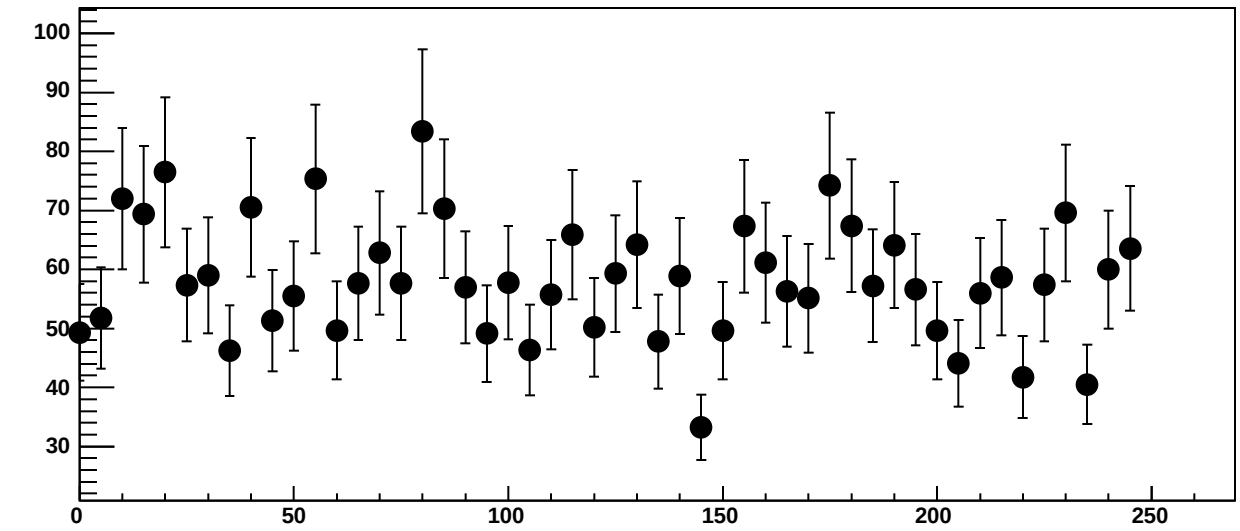

Chip 3, Channel 15, Enable 1, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 3, Channel 15, Enable 1, DAC=1600, ADC Residuals vs Hold
500
400
300
200
100
0
-100
0
50
100
150
200
250

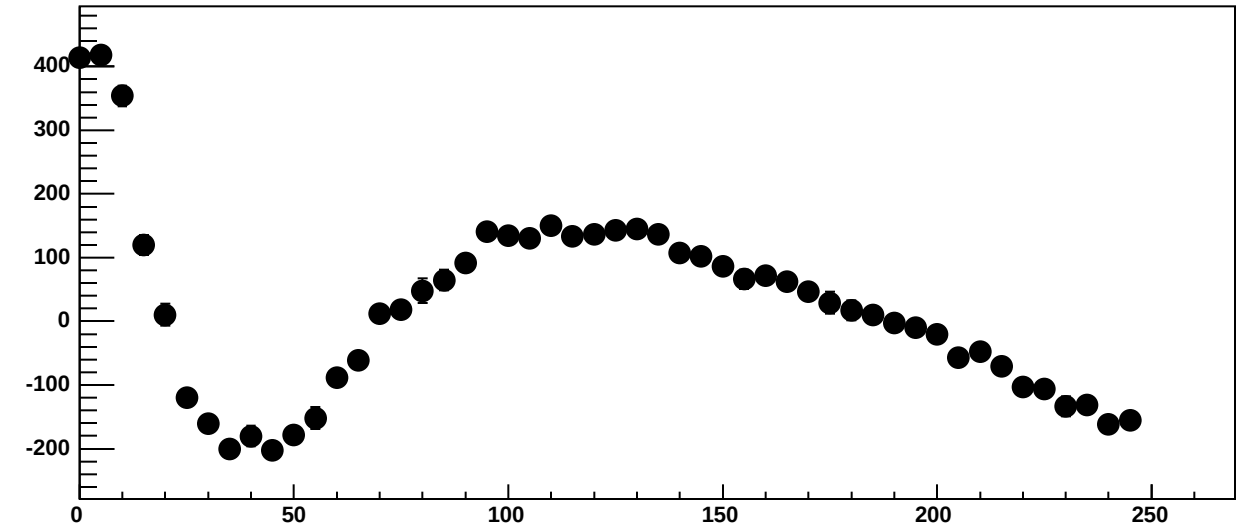
Chip 3, Channel 15, Enable 2, DAC=1600, ADC Mean vs Hold

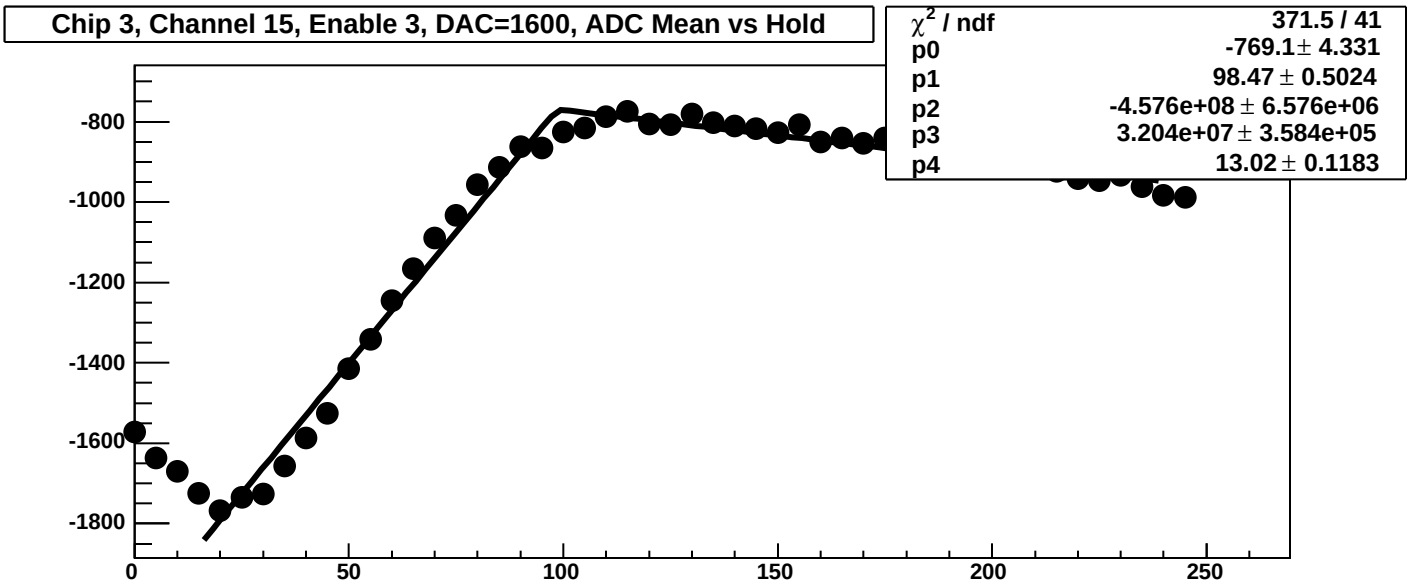


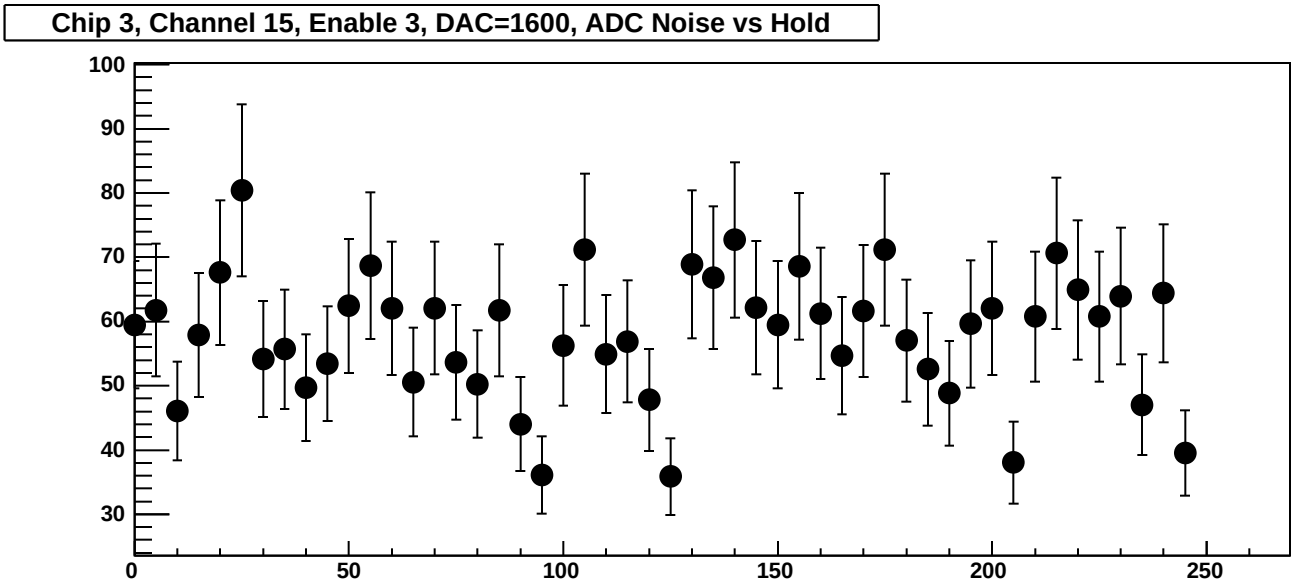
Chip 3, Channel 15, Enable 2, DAC=1600, ADC Noise vs Hold

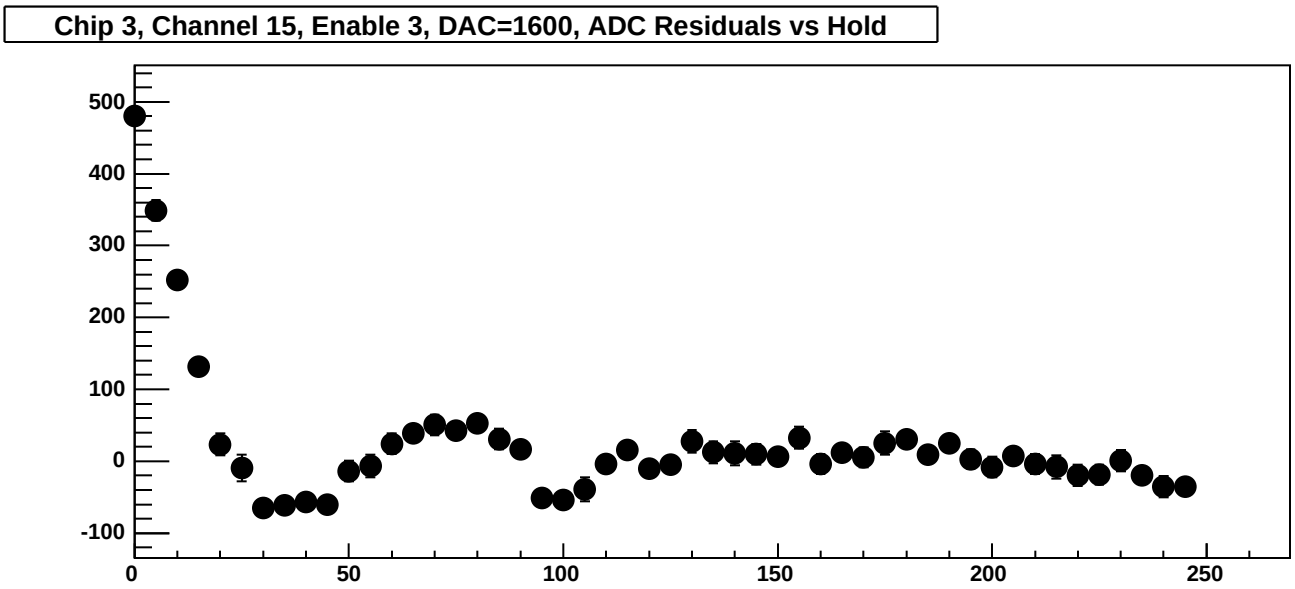


Chip 3, Channel 15, Enable 2, DAC=1600, ADC Residuals vs Hold

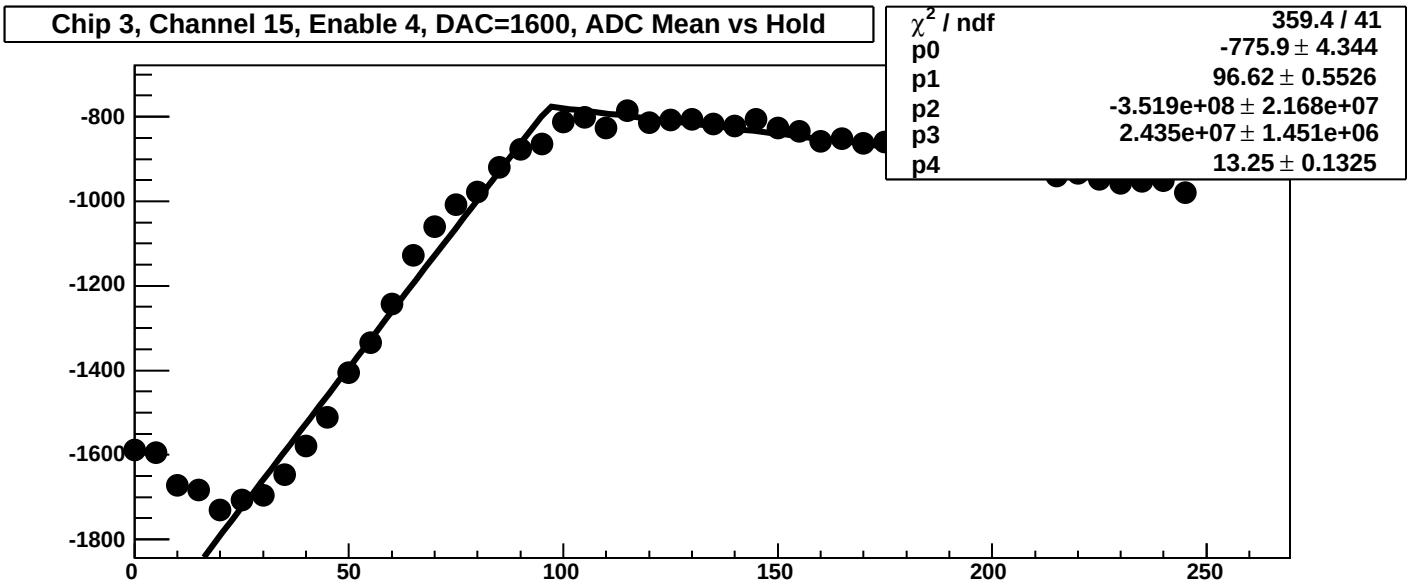



Chip 3, Channel 15, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 371.5 / 41
p0 -769.1 ± 4.331
p1 98.47 ± 0.5024
p2 -4.576e+08 ± 6.576e+06
p3 3.204e+07 ± 3.584e+05
p4 13.02 ± 0.1183
-800
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250

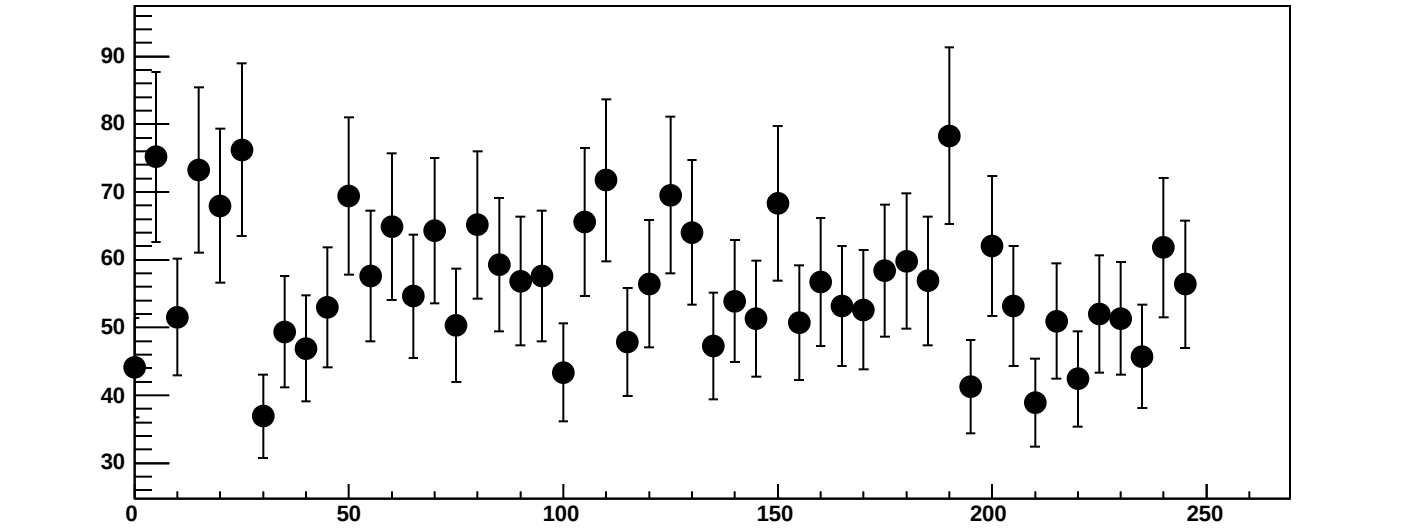

Chip 3, Channel 15, Enable 3, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 3, Channel 15, Enable 3, DAC=1600, ADC Residuals vs Hold
500
400
300
200
100
0
-100
0
50
100
150
200
250

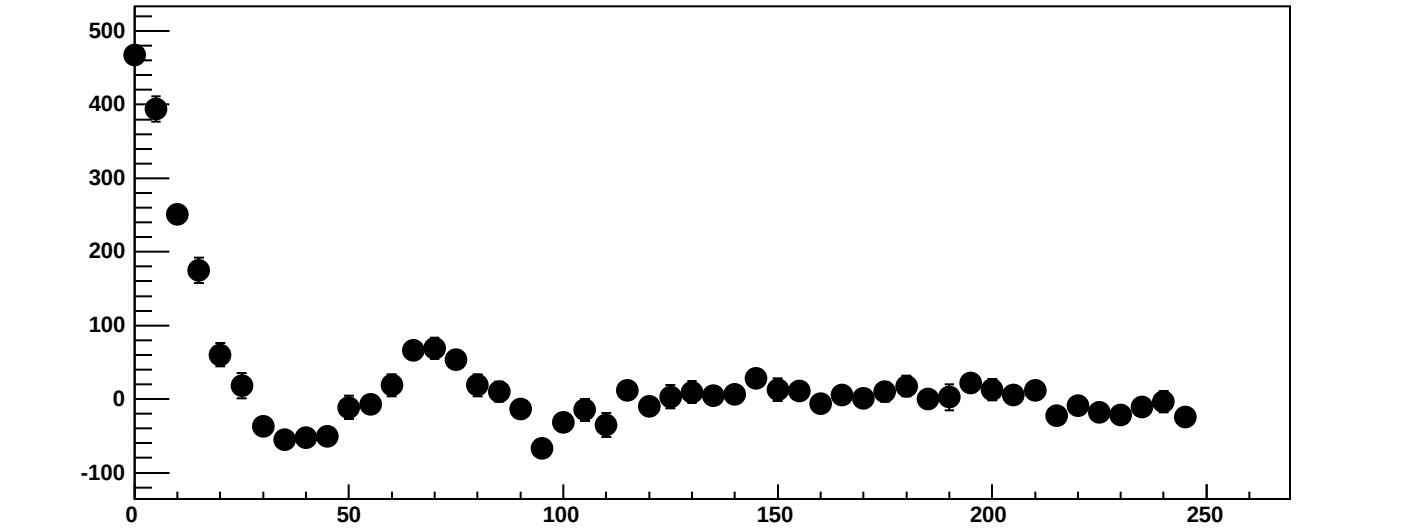
Chip 3, Channel 15, Enable 4, DAC=1600, ADC Mean vs Hold



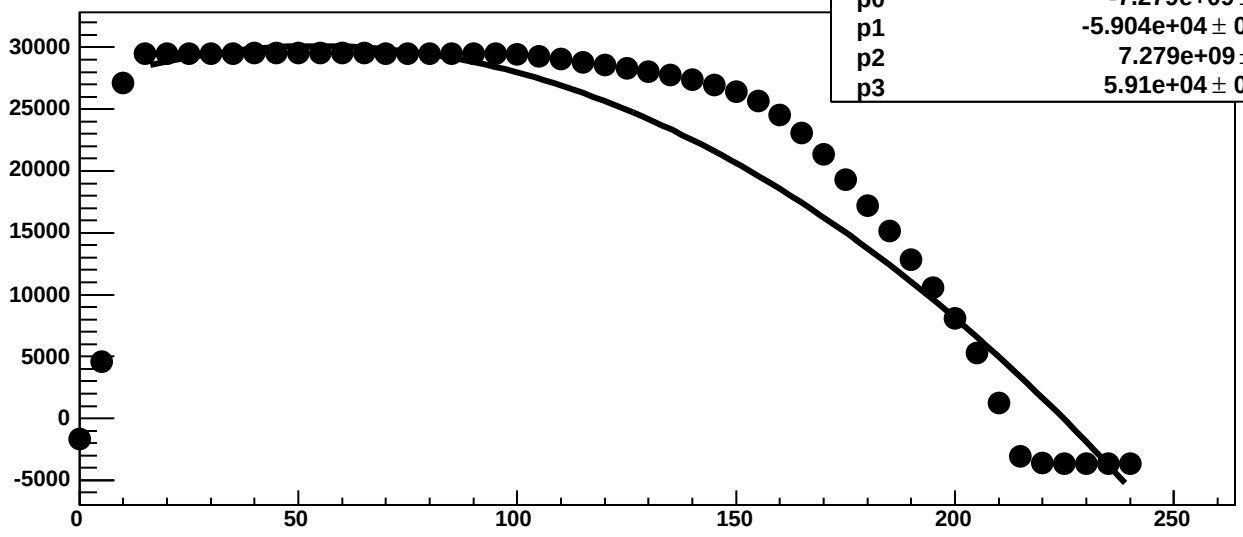
Chip 3, Channel 15, Enable 4, DAC=1600, ADC Noise vs Hold



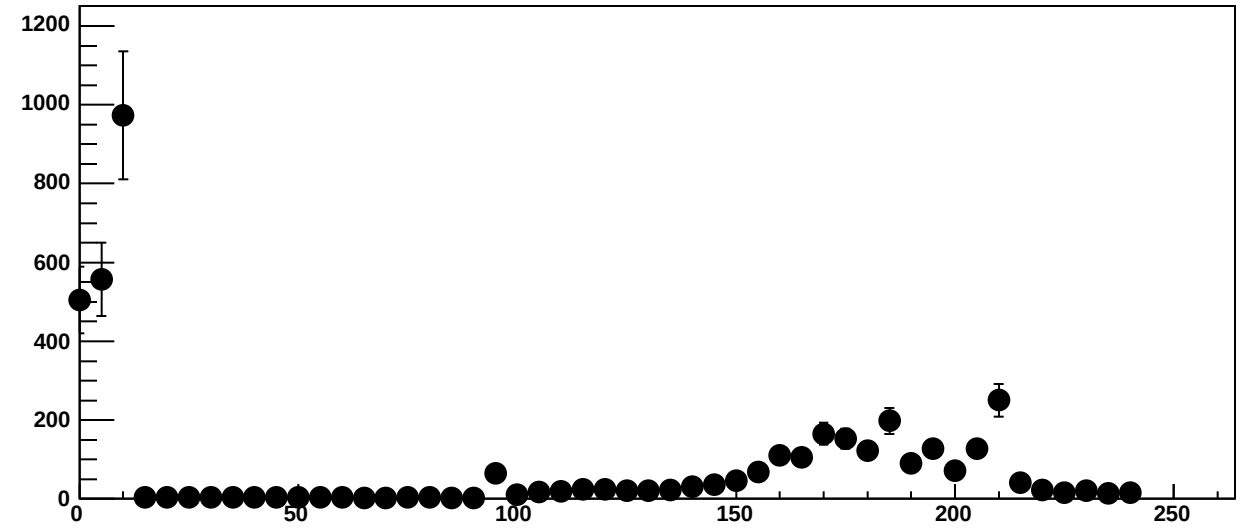
Chip 3, Channel 15, Enable 4, DAC=1600, ADC Residuals vs Hold



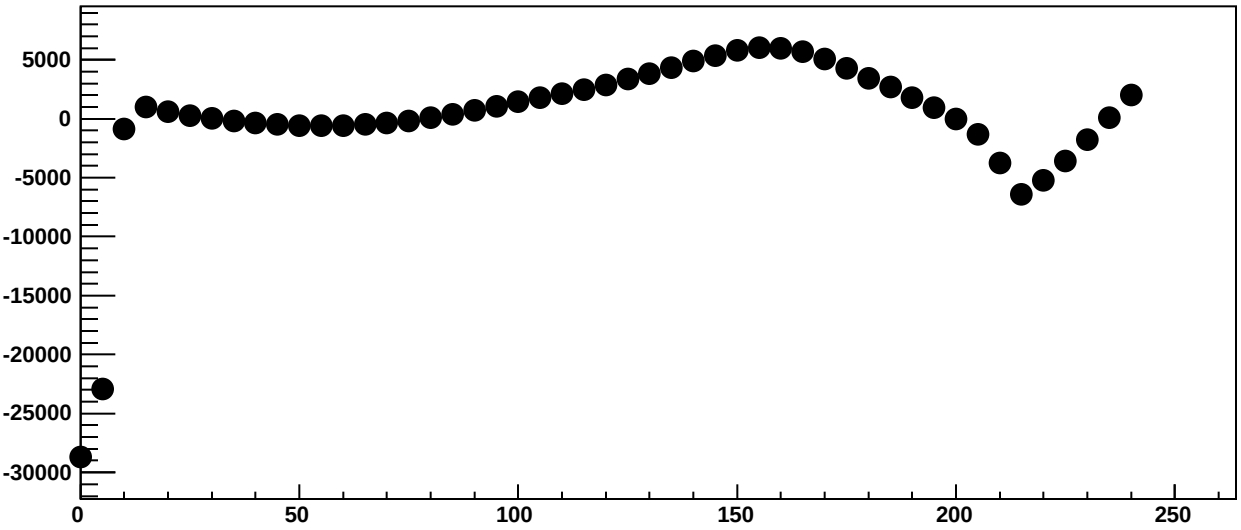
Chip 3, Channel 15, Enable 5!, DAC=1600, ADC Mean vs Hold



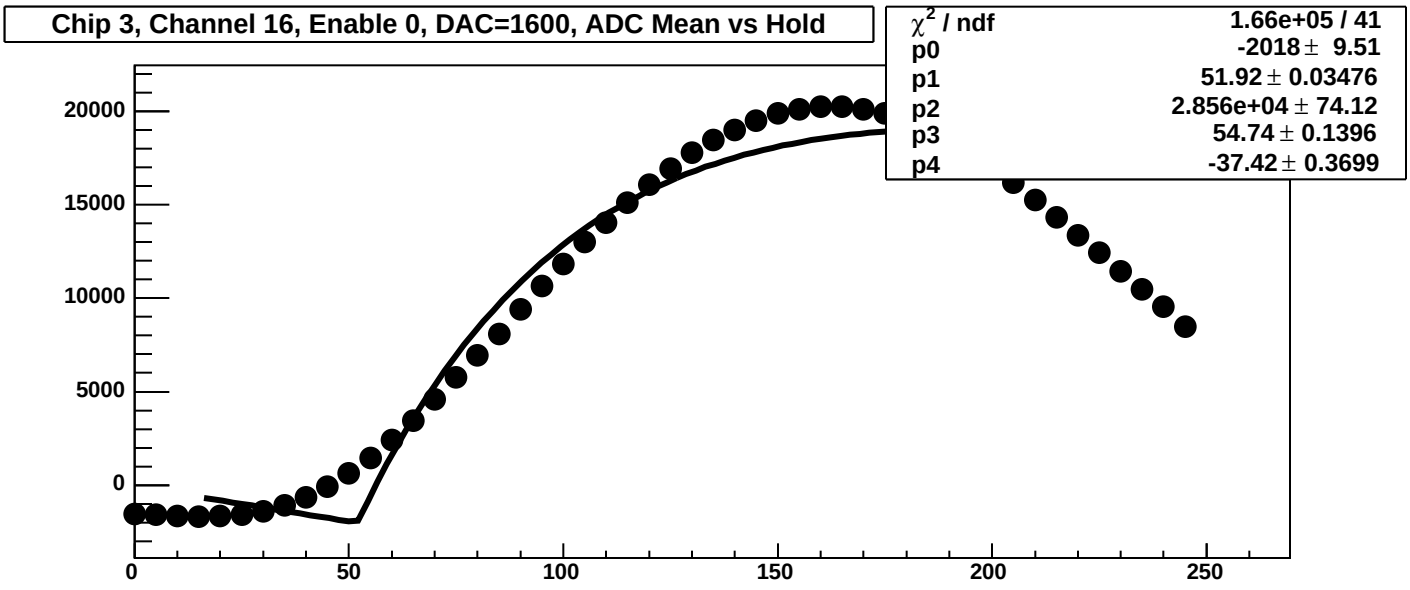
Chip 3, Channel 15, Enable 5!, DAC=1600, ADC Noise vs Hold



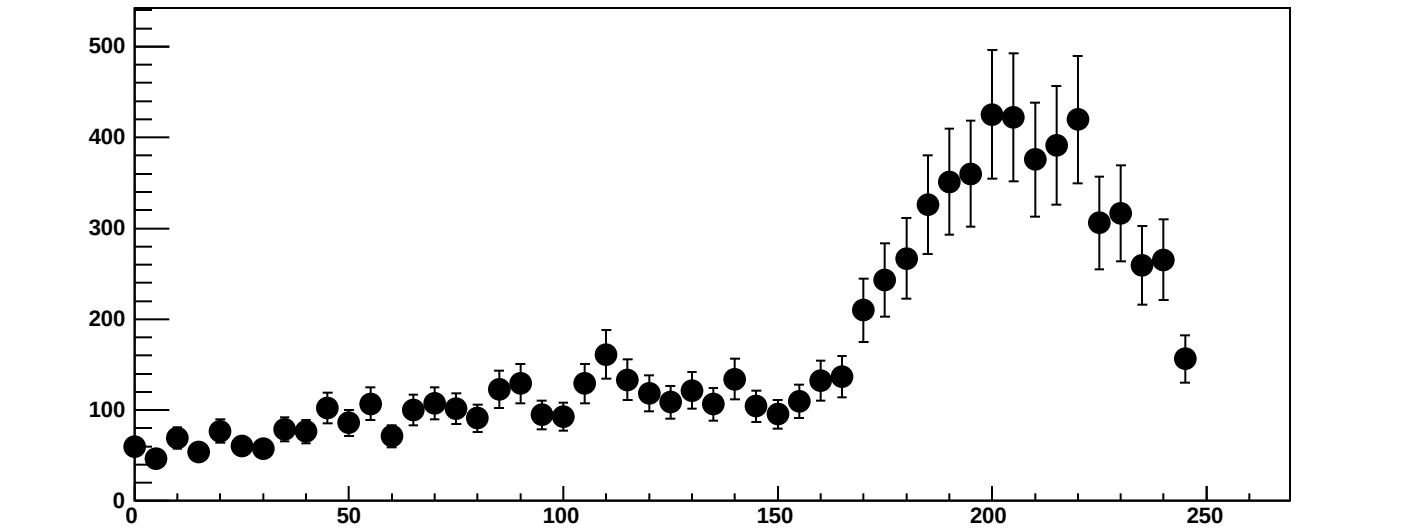
Chip 3, Channel 15, Enable 5!, DAC=1600, ADC Residuals vs Hold



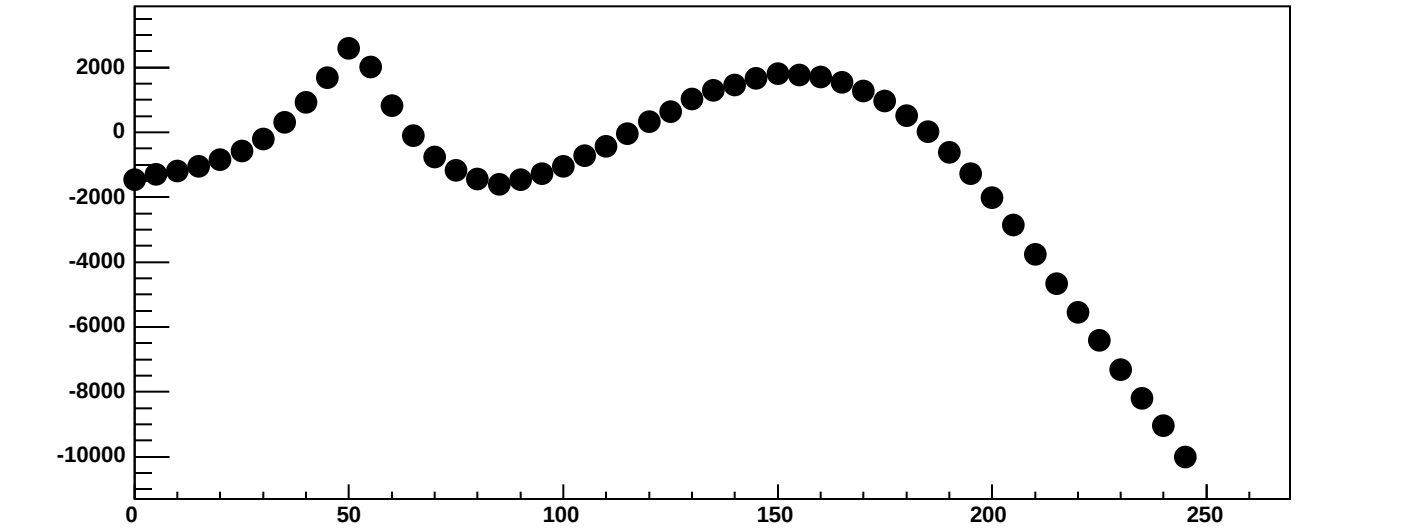
Chip 3, Channel 16, Enable 0, DAC=1600, ADC Mean vs Hold



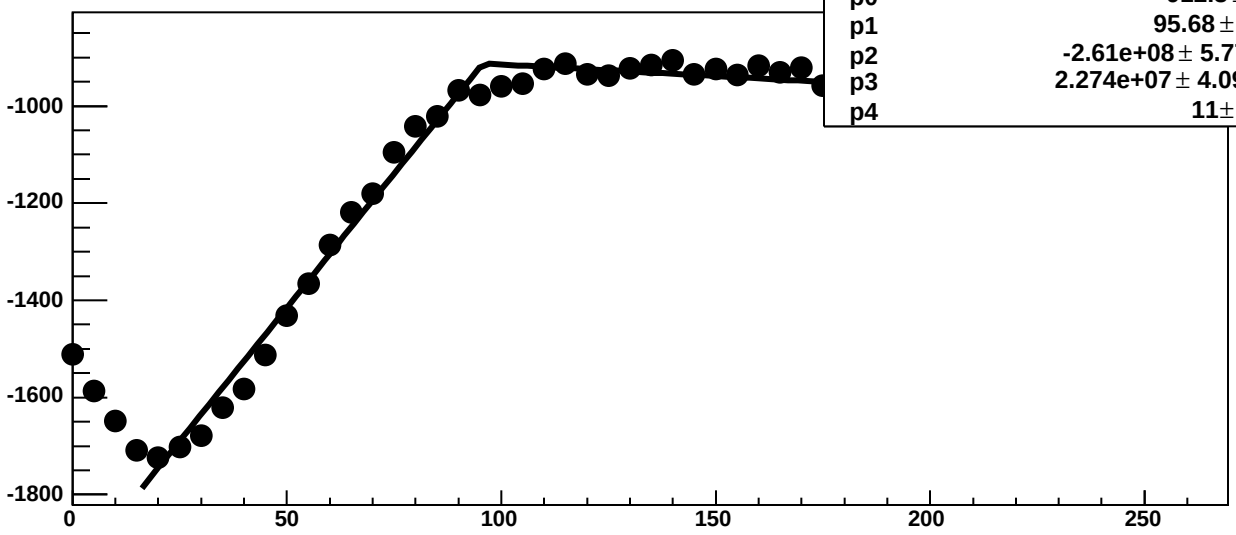
Chip 3, Channel 16, Enable 0, DAC=1600, ADC Noise vs Hold



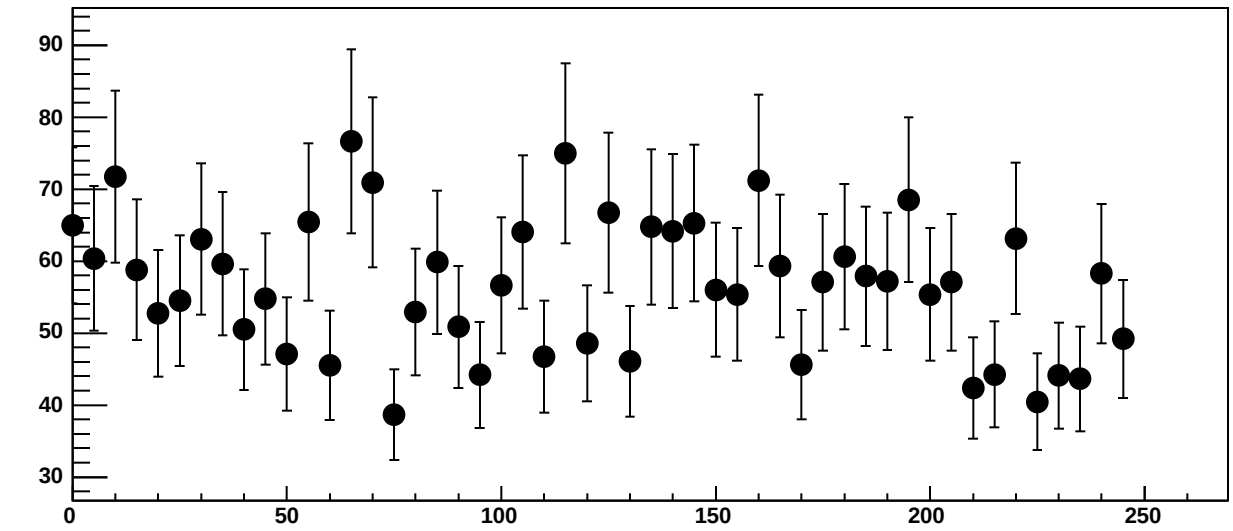
Chip 3, Channel 16, Enable 0, DAC=1600, ADC Residuals vs Hold



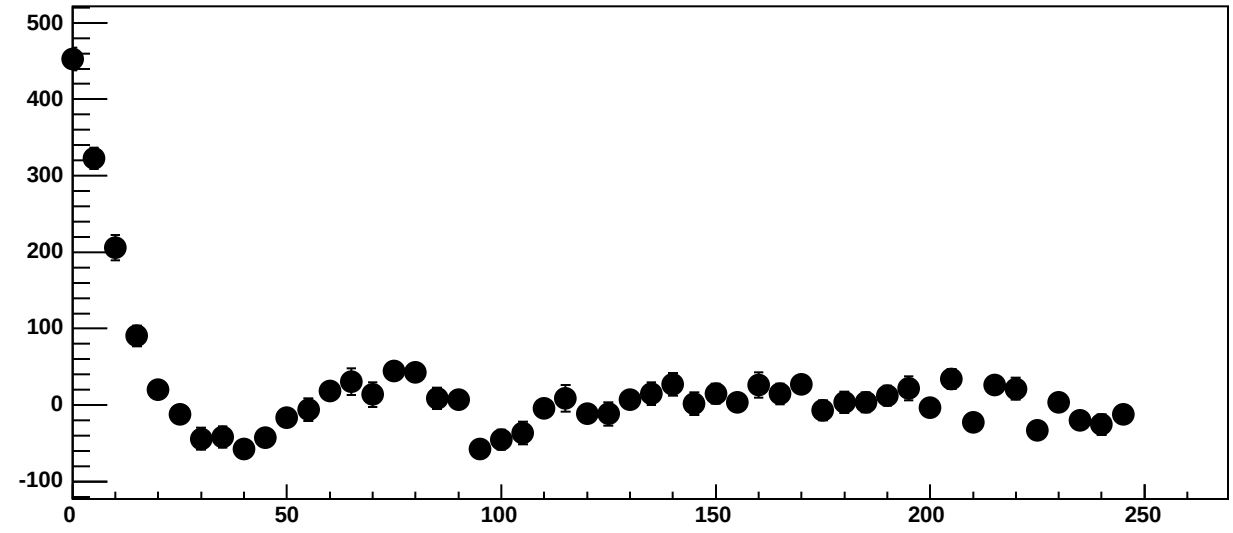
Chip 3, Channel 16, Enable 1, DAC=1600, ADC Mean vs Hold

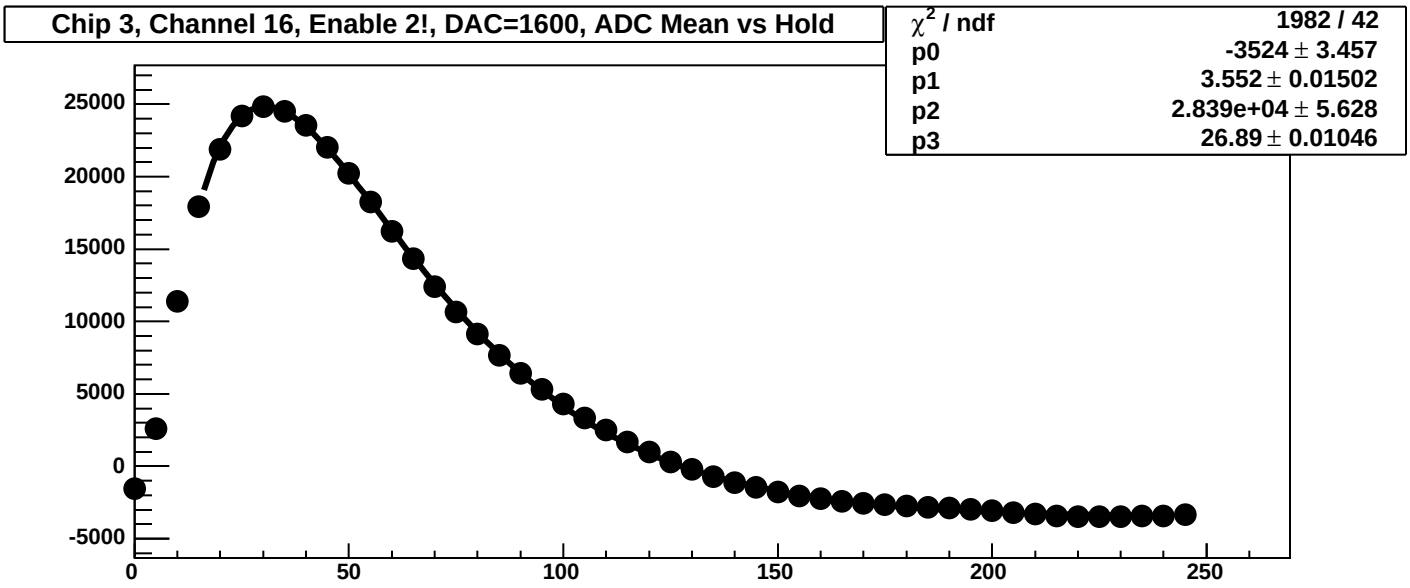


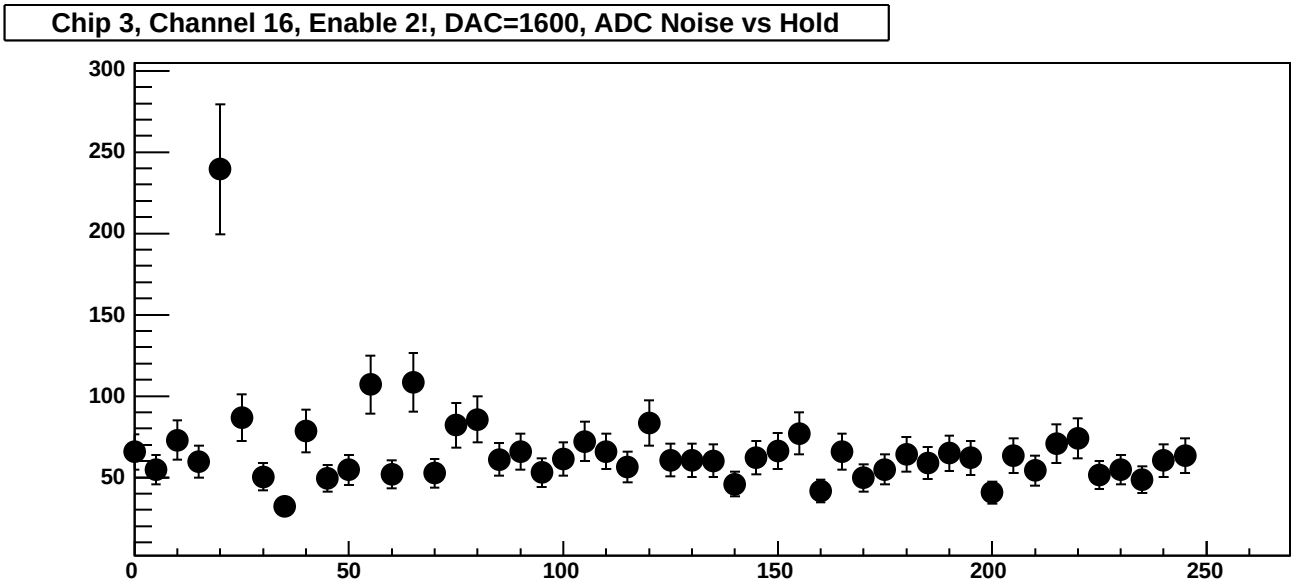
Chip 3, Channel 16, Enable 1, DAC=1600, ADC Noise vs Hold

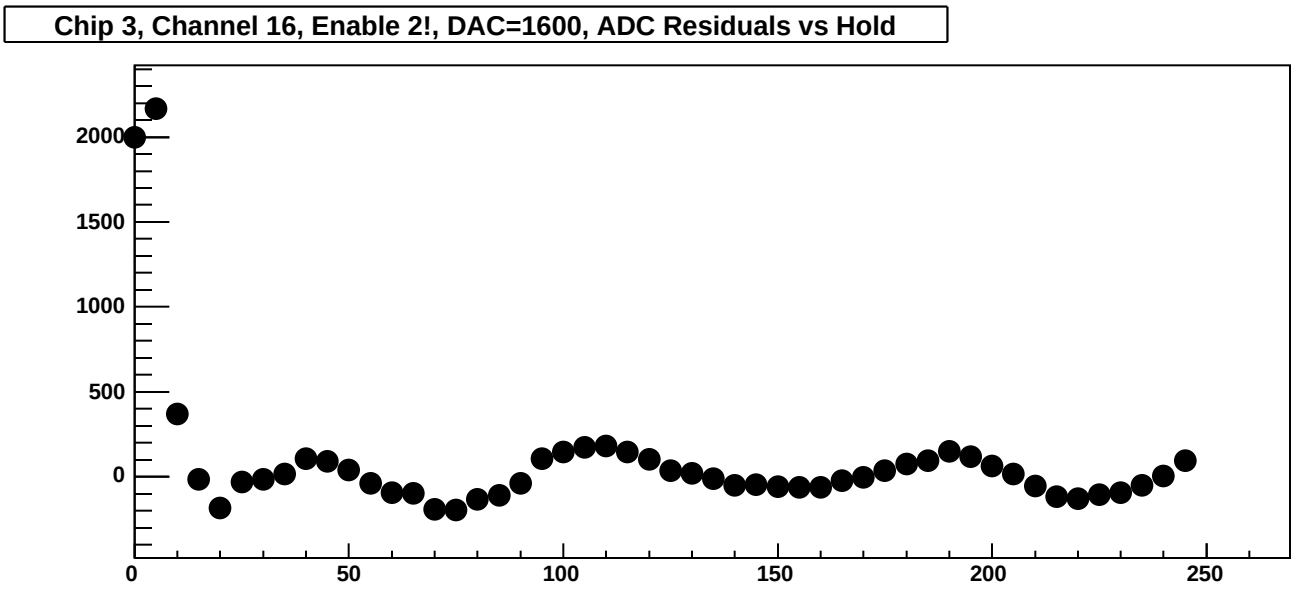


Chip 3, Channel 16, Enable 1, DAC=1600, ADC Residuals vs Hold

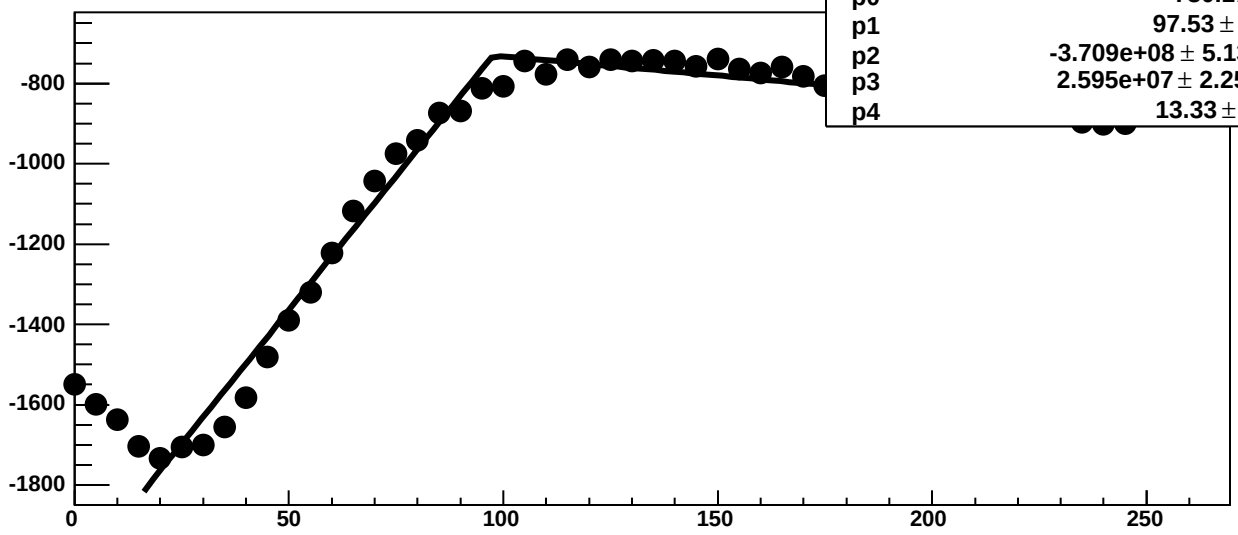



Chip 3, Channel 16, Enable 2!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1982 / 42
p0 -3524 ± 3.457
p1 3.552 ± 0.01502
p2 2.839e+04 ± 5.628
p3 26.89 ± 0.01046
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

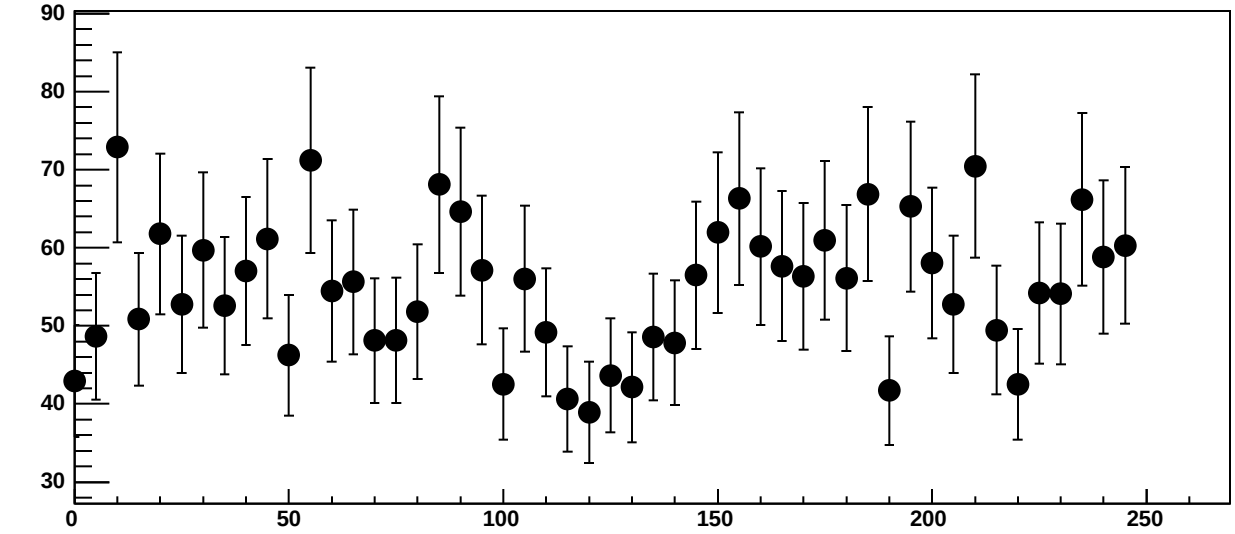

Chip 3, Channel 16, Enable 2!, DAC=1600, ADC Noise vs Hold
300
250
200
150
100
50
0
50
100
150
200
250


Chip 3, Channel 16, Enable 2!, DAC=1600, ADC Residuals vs Hold
2000
1500
1000
500
0
0
50
100
150
200
250

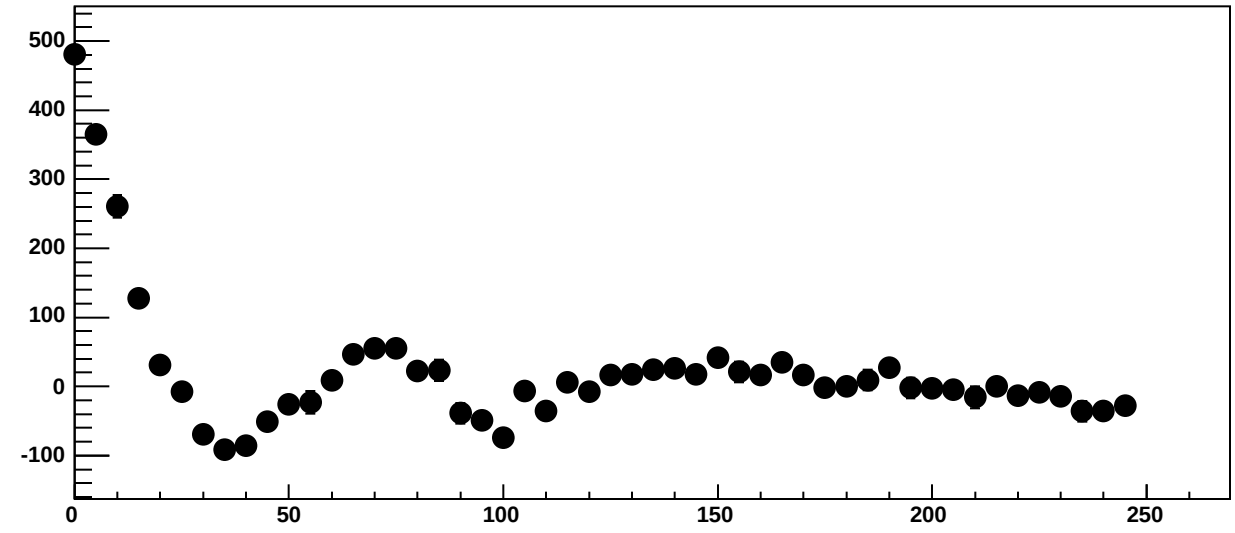
Chip 3, Channel 16, Enable 3, DAC=1600, ADC Mean vs Hold

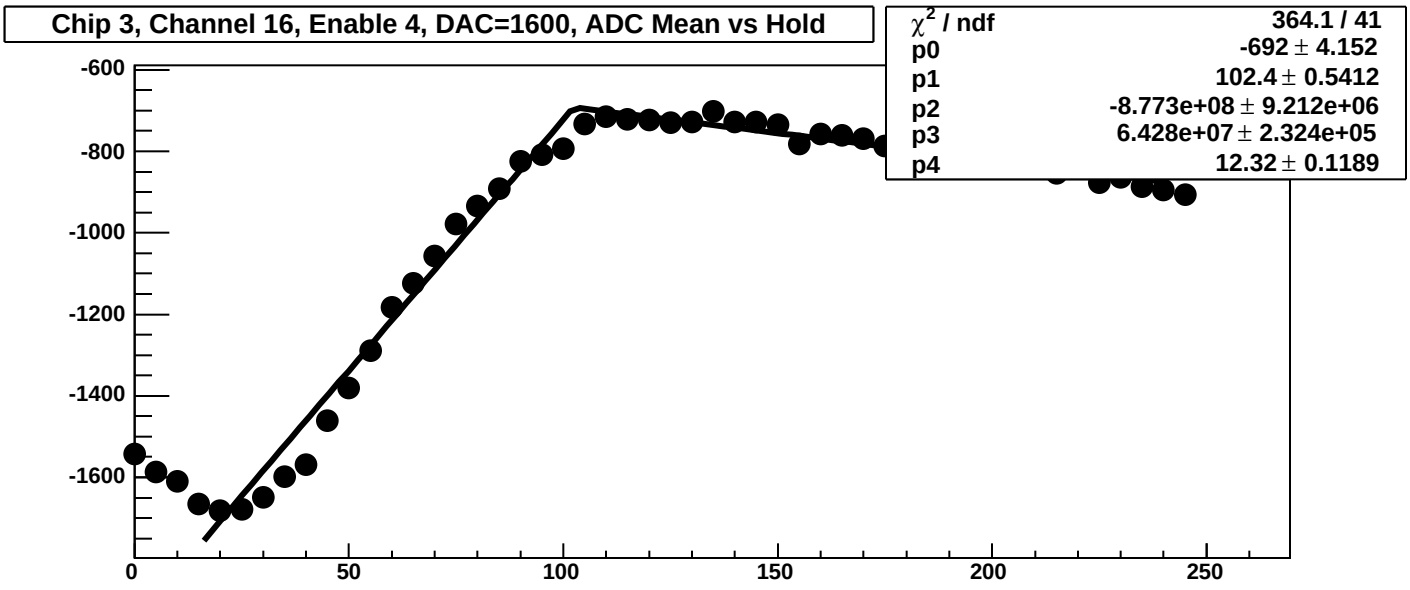


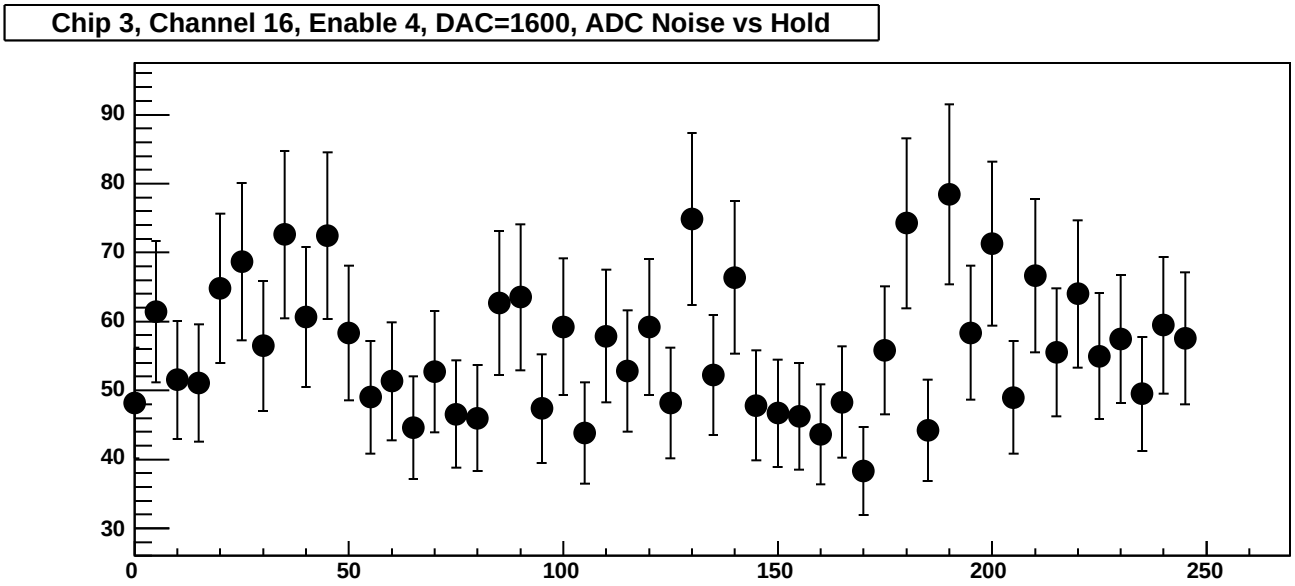
Chip 3, Channel 16, Enable 3, DAC=1600, ADC Noise vs Hold

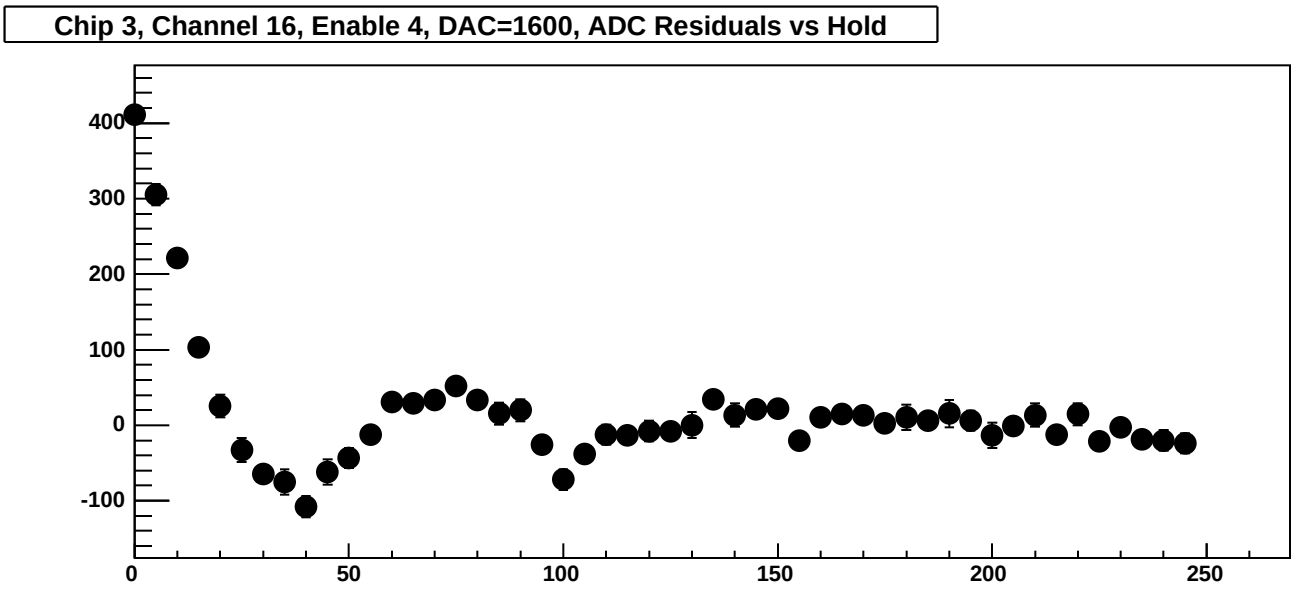


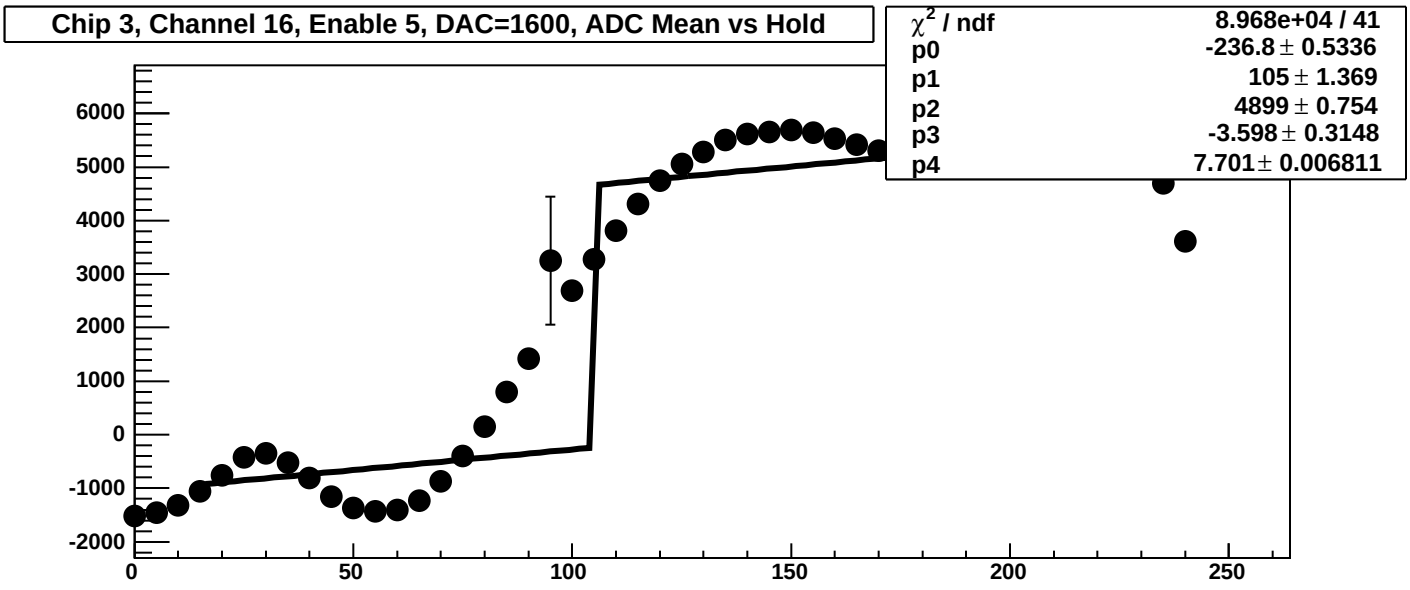
Chip 3, Channel 16, Enable 3, DAC=1600, ADC Residuals vs Hold

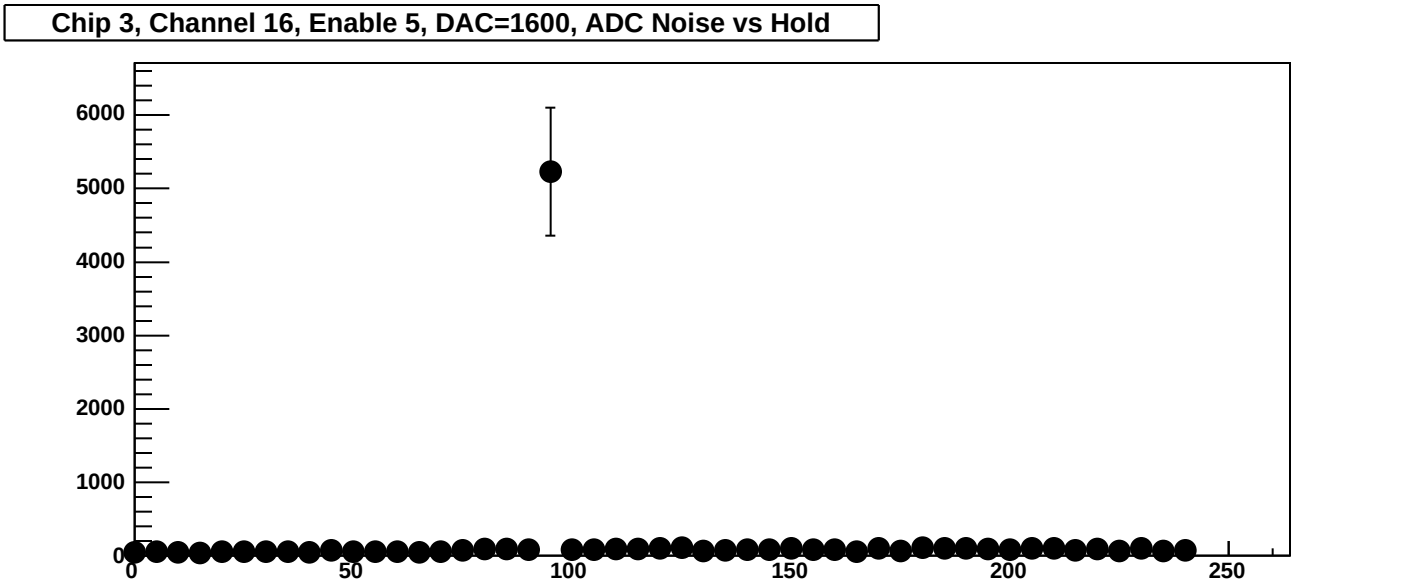


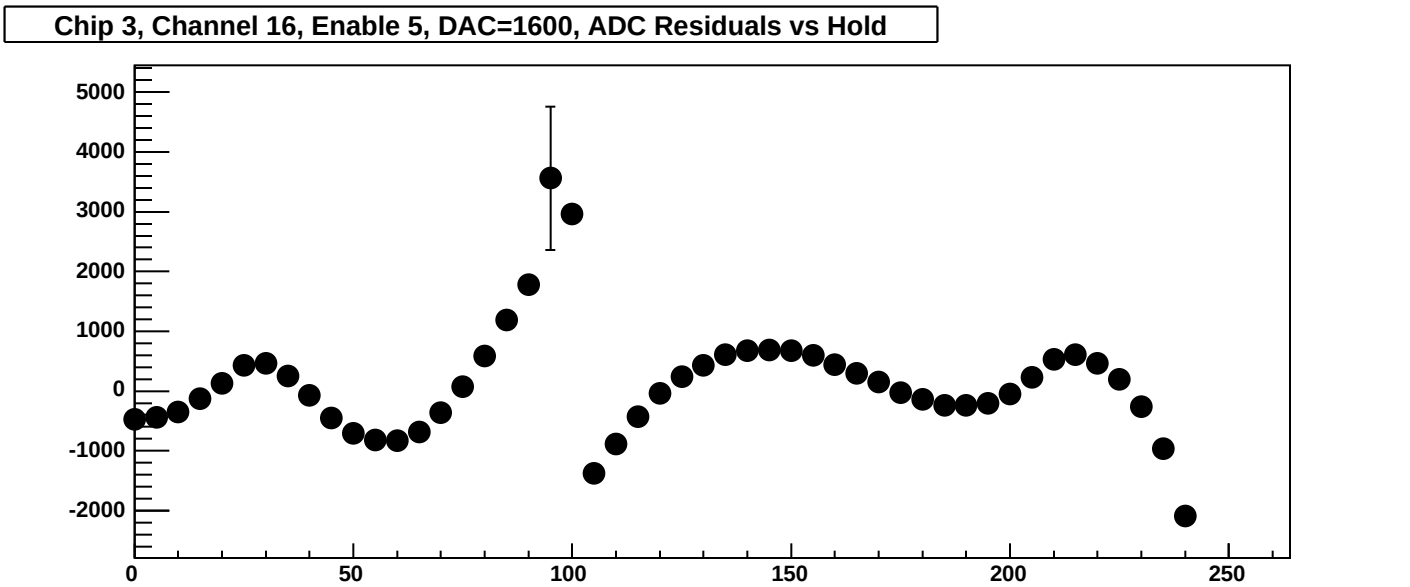

Chip 3, Channel 16, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 364.1 / 41
p0 -692 ± 4.152
p1 102.4 ± 0.5412
p2 -8.773e+08 ± 9.212e+06
p3 6.428e+07 ± 2.324e+05
p4 12.32 ± 0.1189
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250

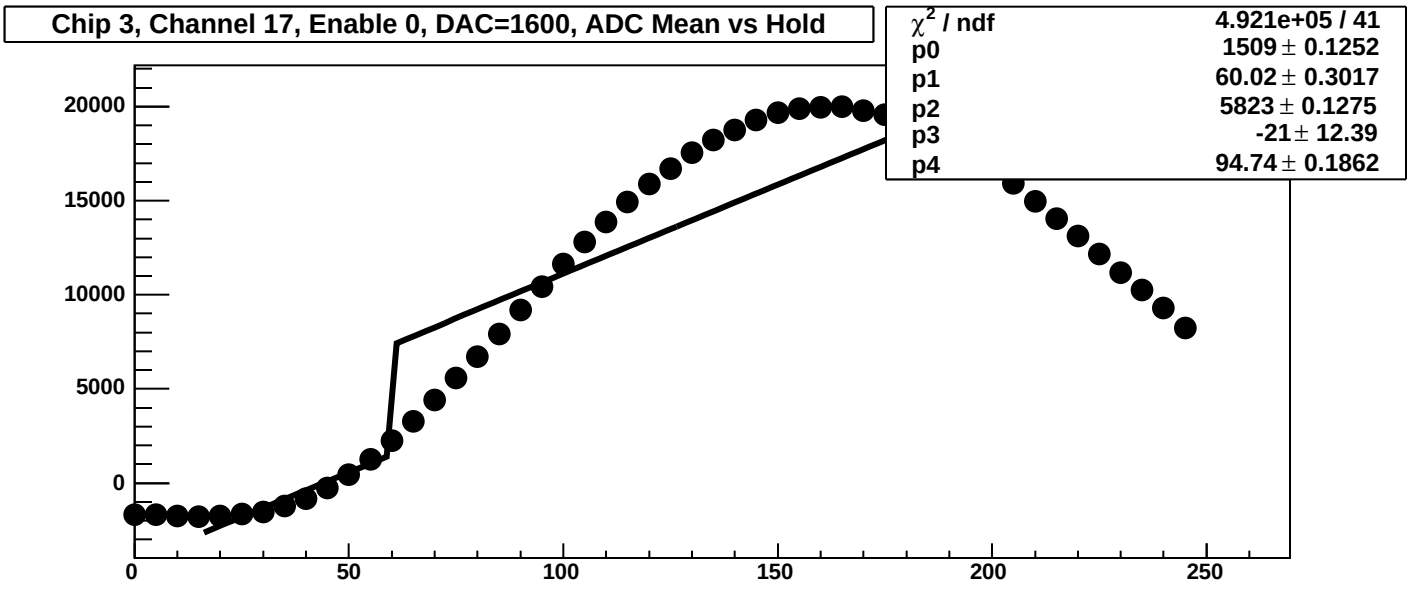

Chip 3, Channel 16, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

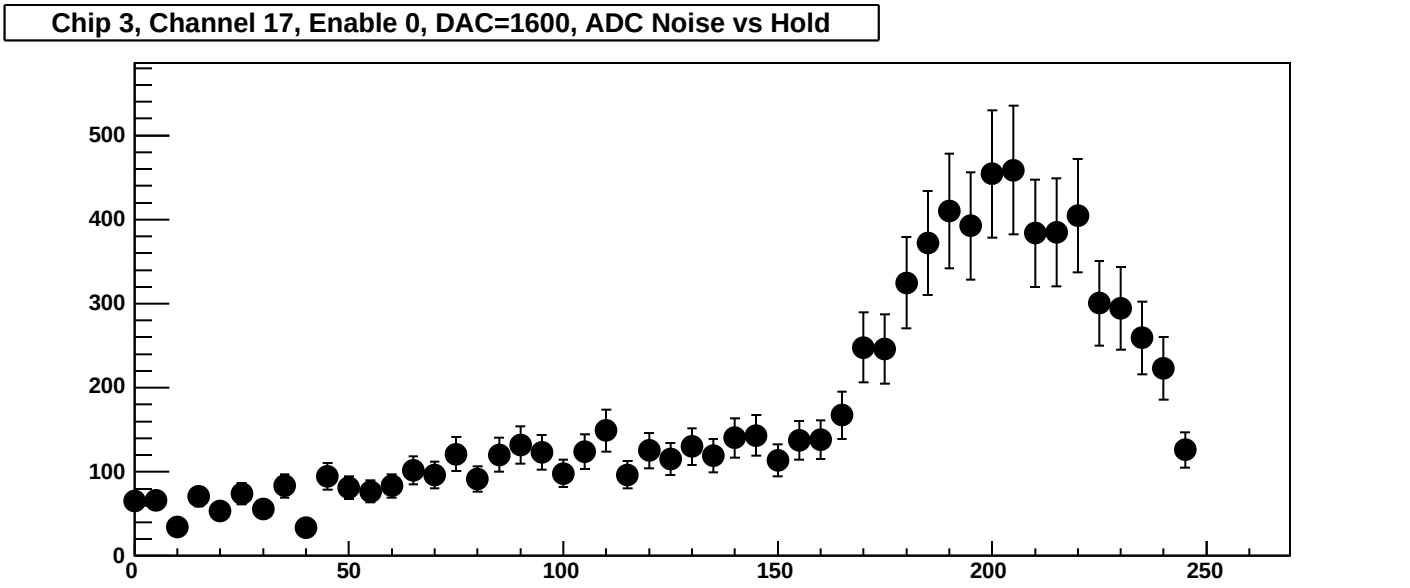

Chip 3, Channel 16, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

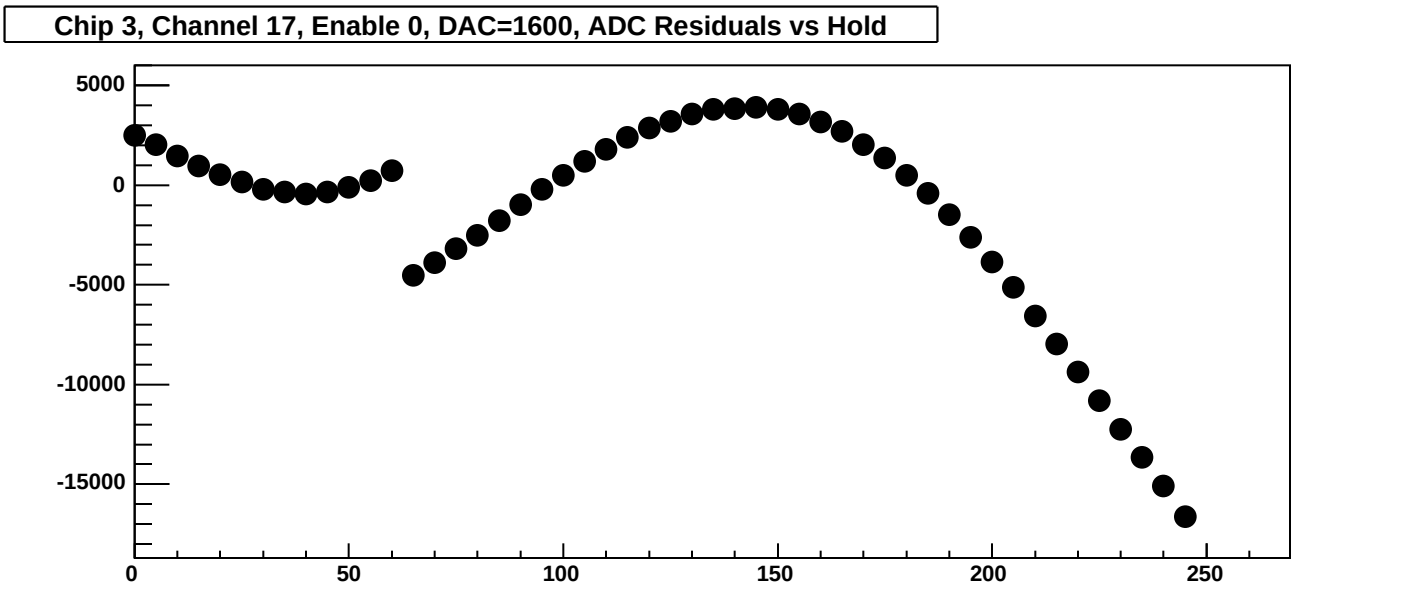

Chip 3, Channel 16, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 8.968e+04 / 41
p0 -236.8 ± 0.5336
p1 105 ± 1.369
p2 4899 ± 0.754
p3 -3.598 ± 0.3148
p4 7.701 ± 0.006811
6000
5000
4000
3000
2000
1000
0
-1000
-2000
0
50
100
150
200
250


Chip 3, Channel 16, Enable 5, DAC=1600, ADC Noise vs Hold
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

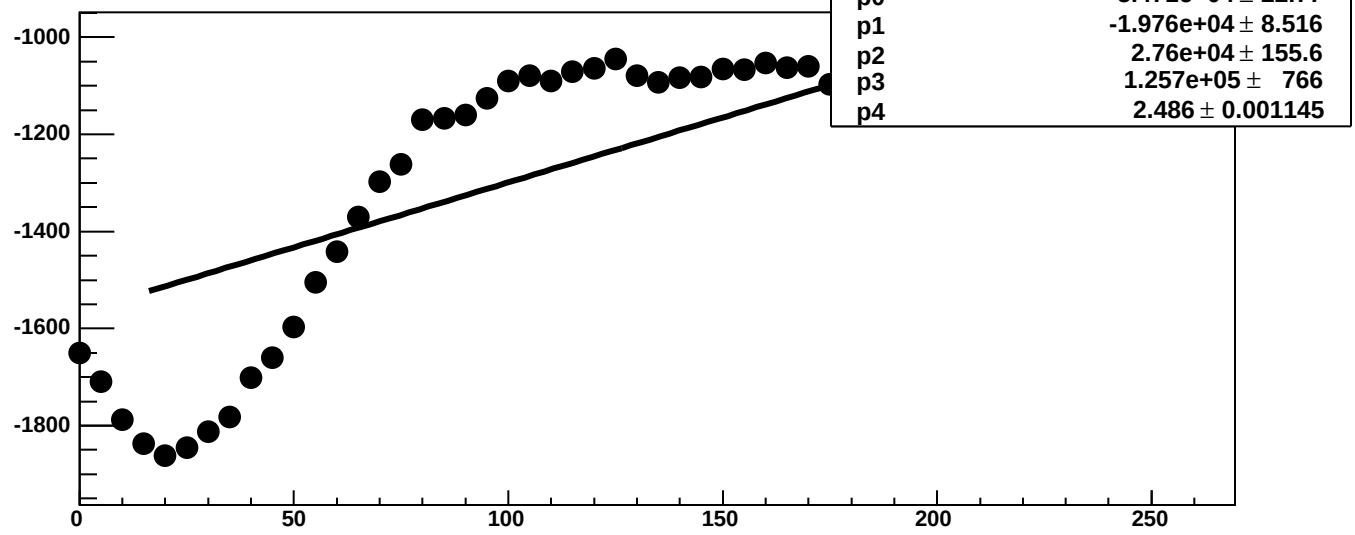

Chip 3, Channel 16, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
4000
3000
2000
1000
0
-1000
-2000
0
50
100
150
200
250


Chip 3, Channel 17, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.921e+05 / 41
p0 1509 ± 0.1252
p1 60.02 ± 0.3017
p2 5823 ± 0.1275
p3 -21 ± 12.39
p4 94.74 ± 0.1862
20000
15000
10000
5000
0
0
50
100
150
200
250

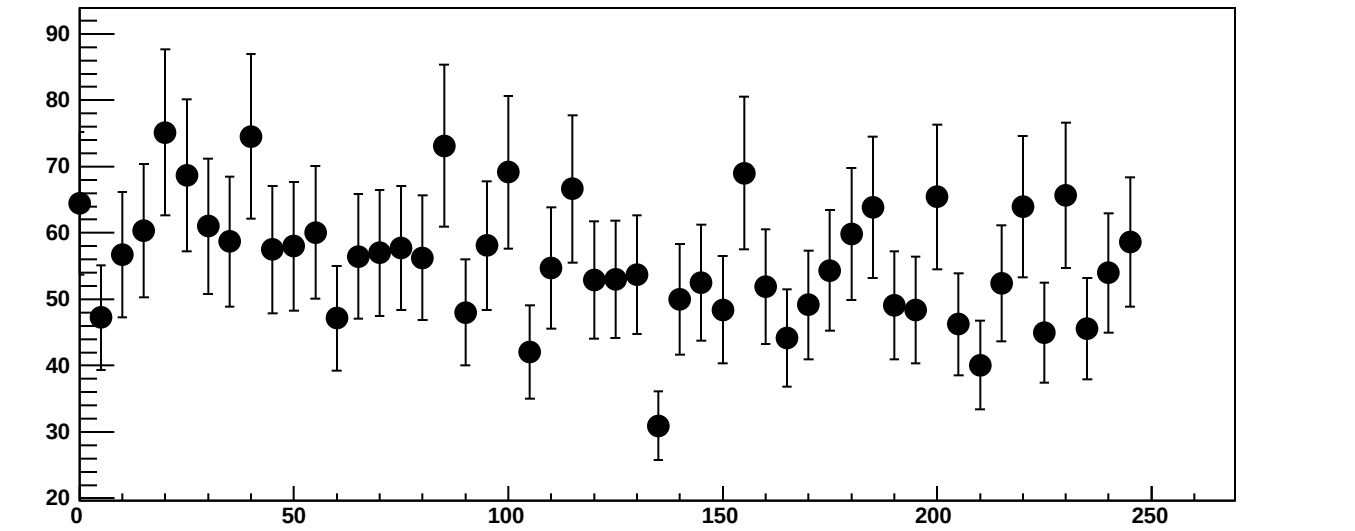

Chip 3, Channel 17, Enable 0, DAC=1600, ADC Noise vs Hold
500
400
300
200
100
0
0
50
100
150
200
250


Chip 3, Channel 17, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
0
50
100
150
200
250

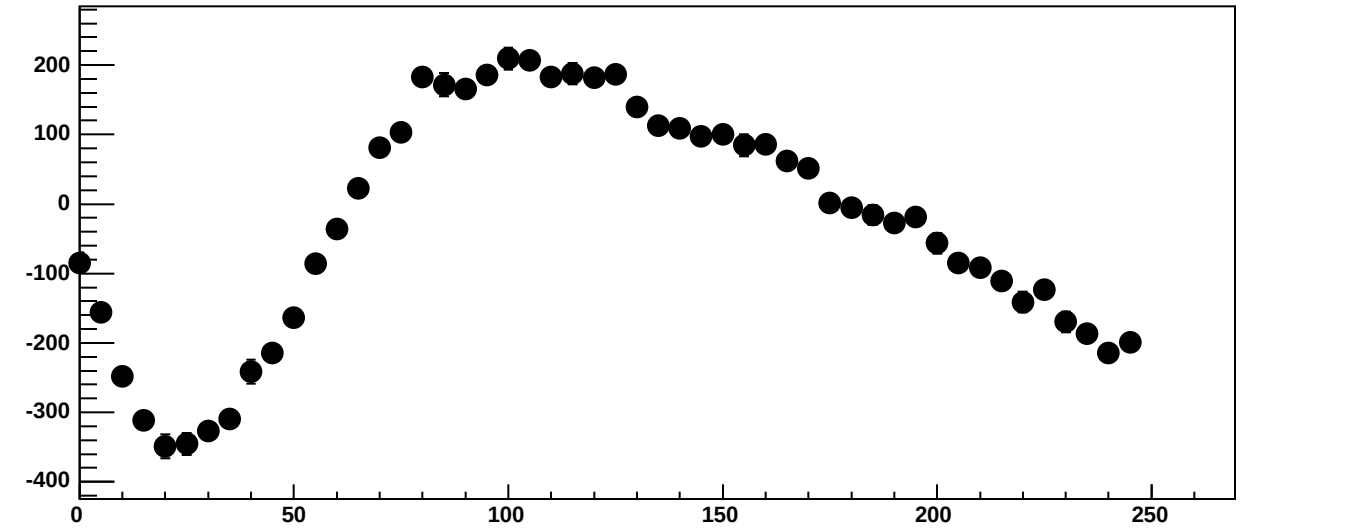
Chip 3, Channel 17, Enable 1, DAC=1600, ADC Mean vs Hold

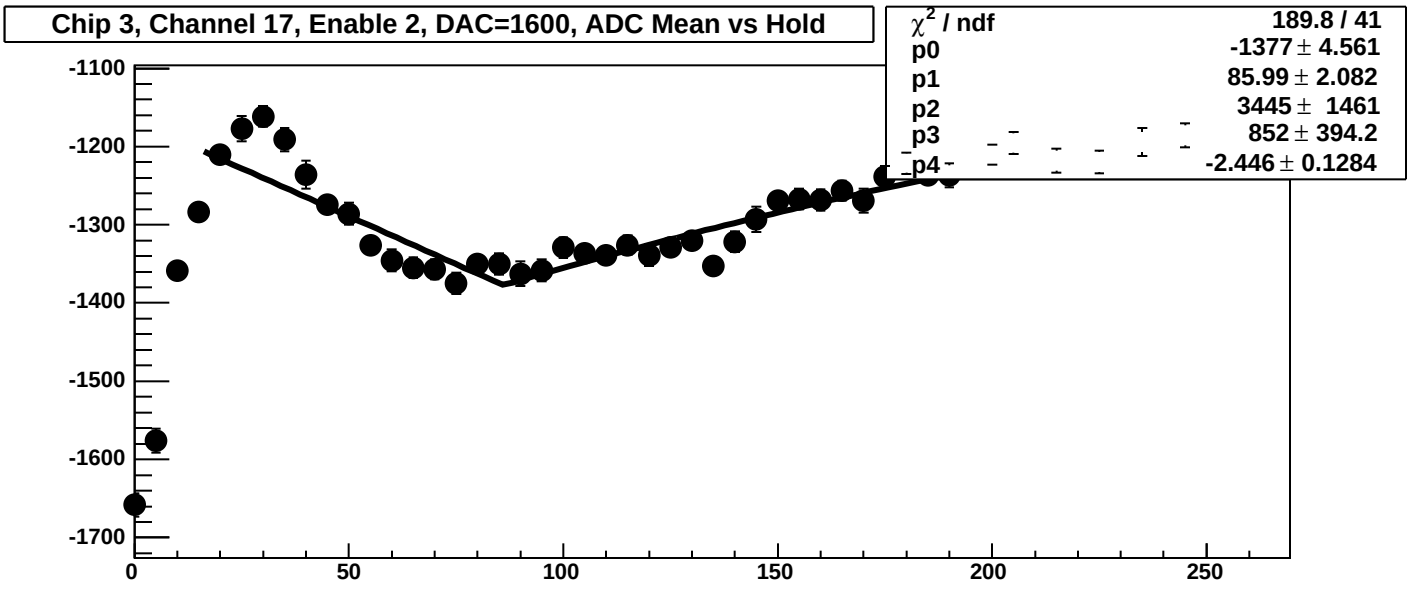


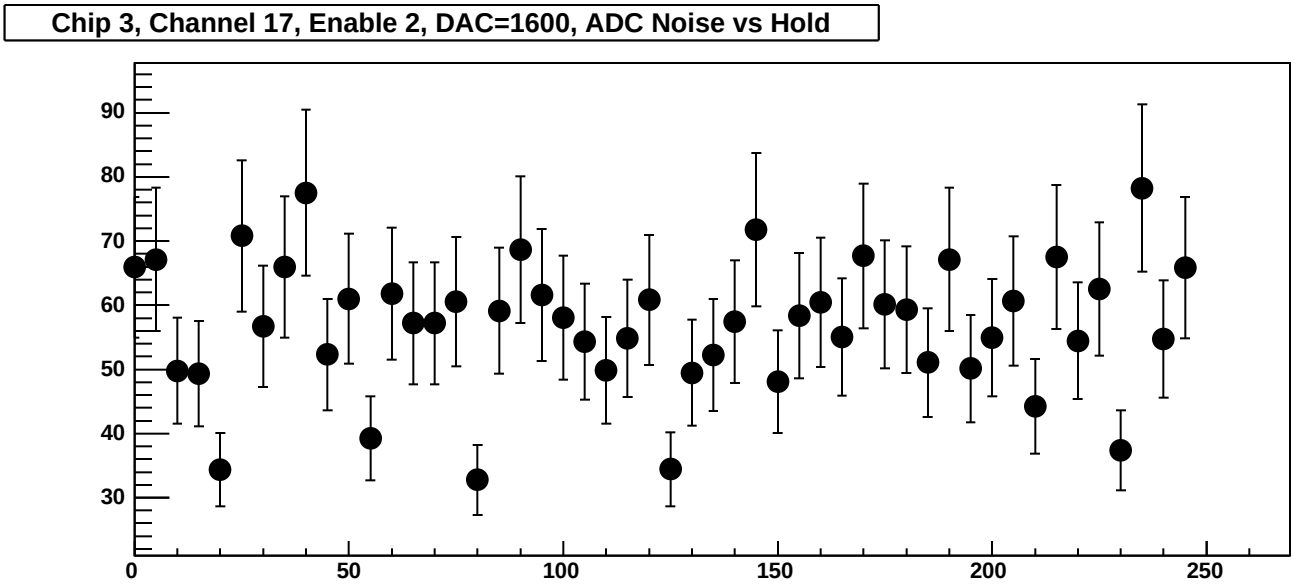
Chip 3, Channel 17, Enable 1, DAC=1600, ADC Noise vs Hold

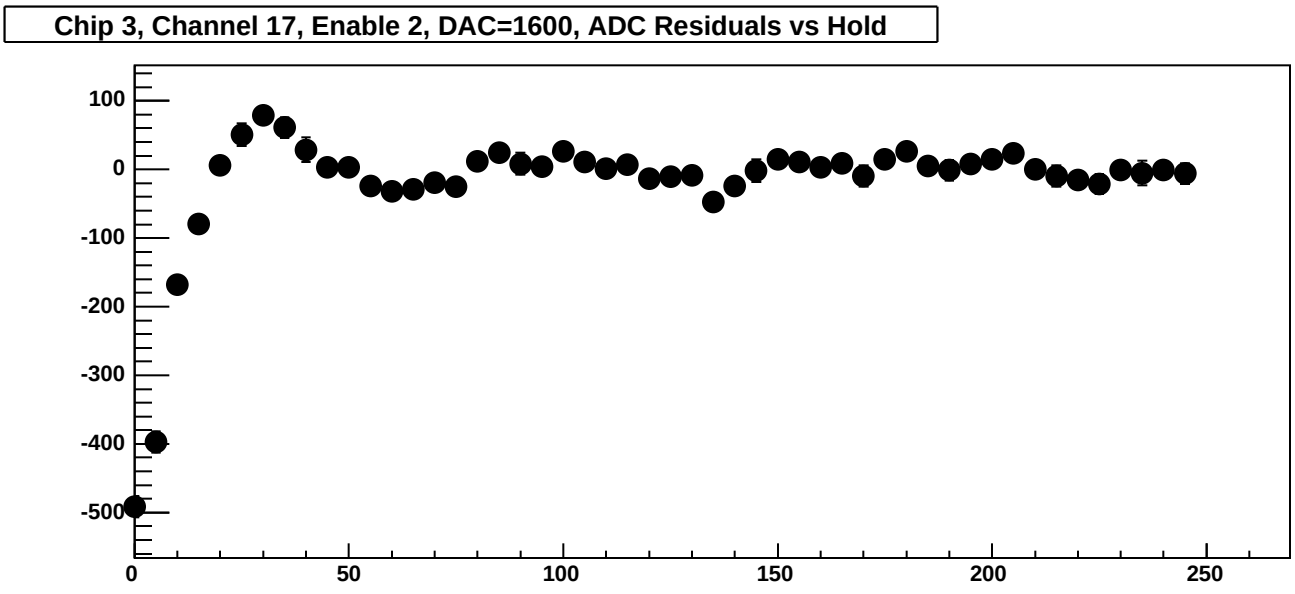


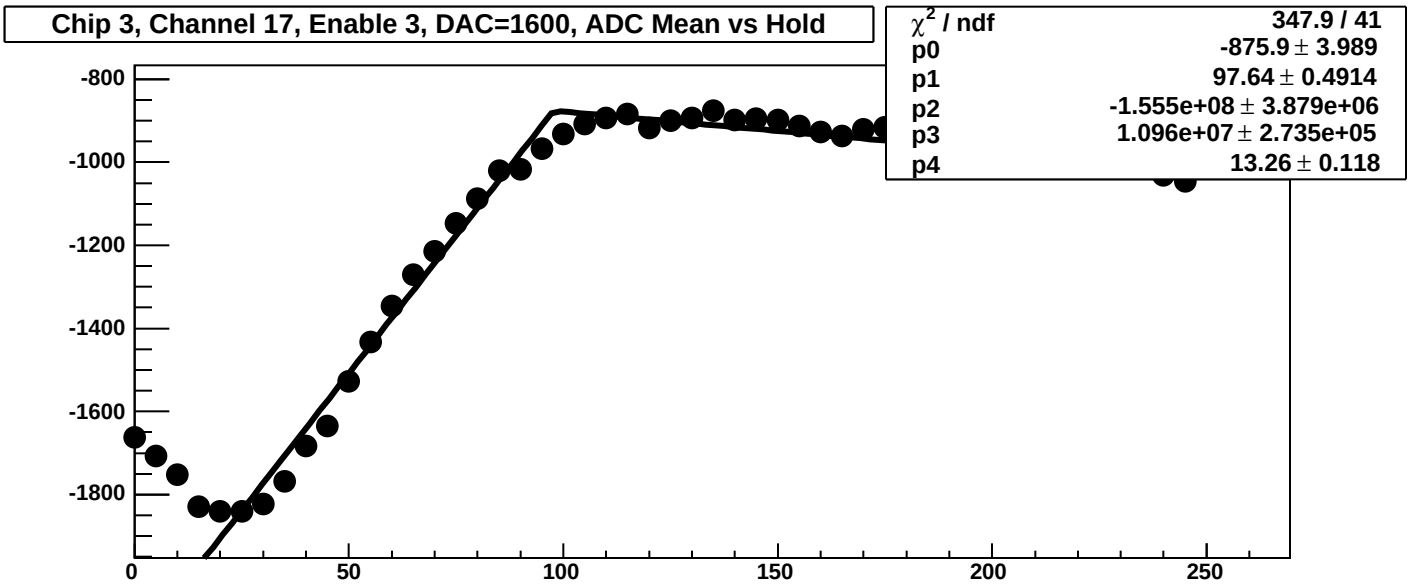
Chip 3, Channel 17, Enable 1, DAC=1600, ADC Residuals vs Hold

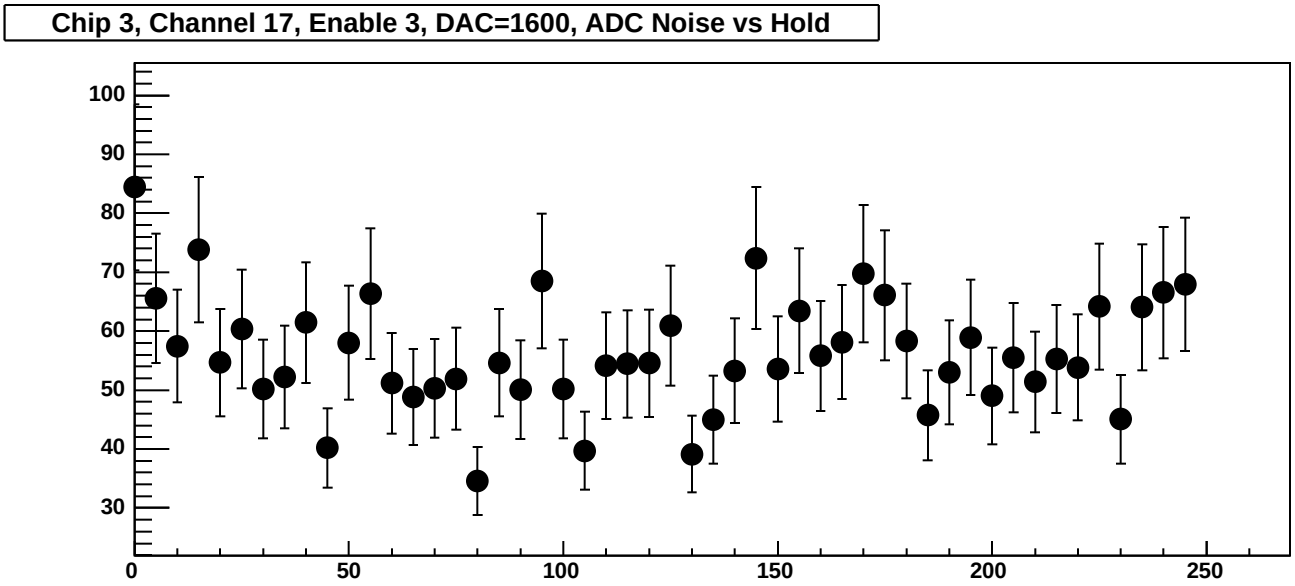


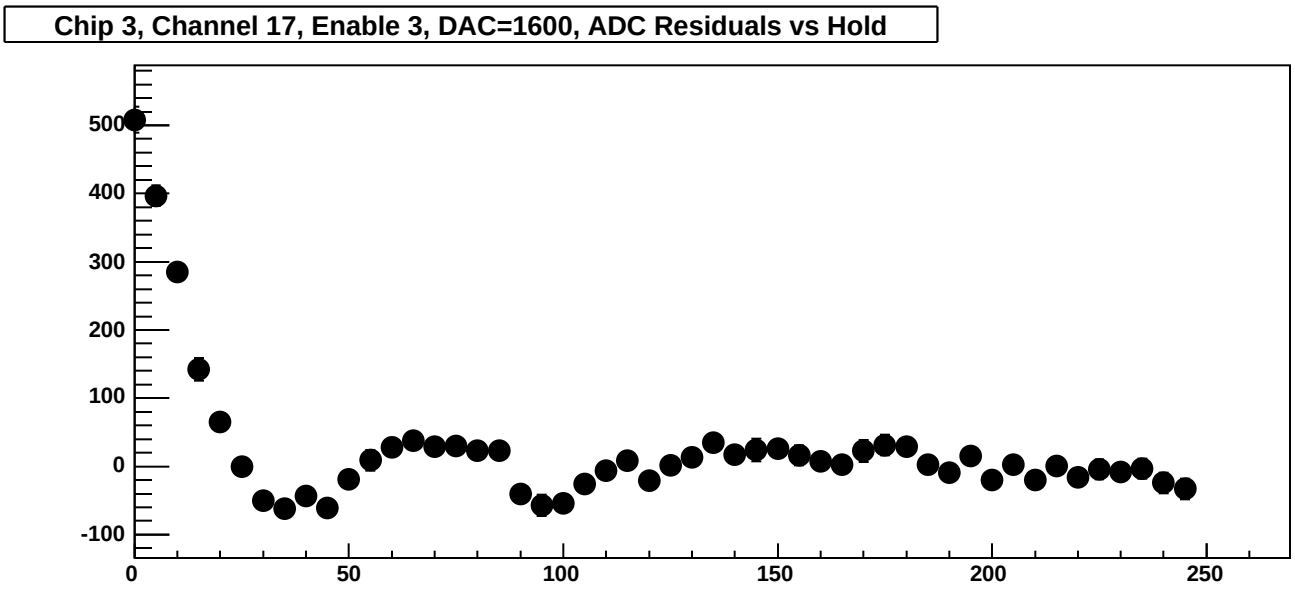

Chip 3, Channel 17, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 189.8 / 41
p0 -1377 ± 4.561
p1 85.99 ± 2.082
p2 3445 ± 1461
p3 852 ± 394.2
p4 -2.446 ± 0.1284
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
50
100
150
200
250


Chip 3, Channel 17, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

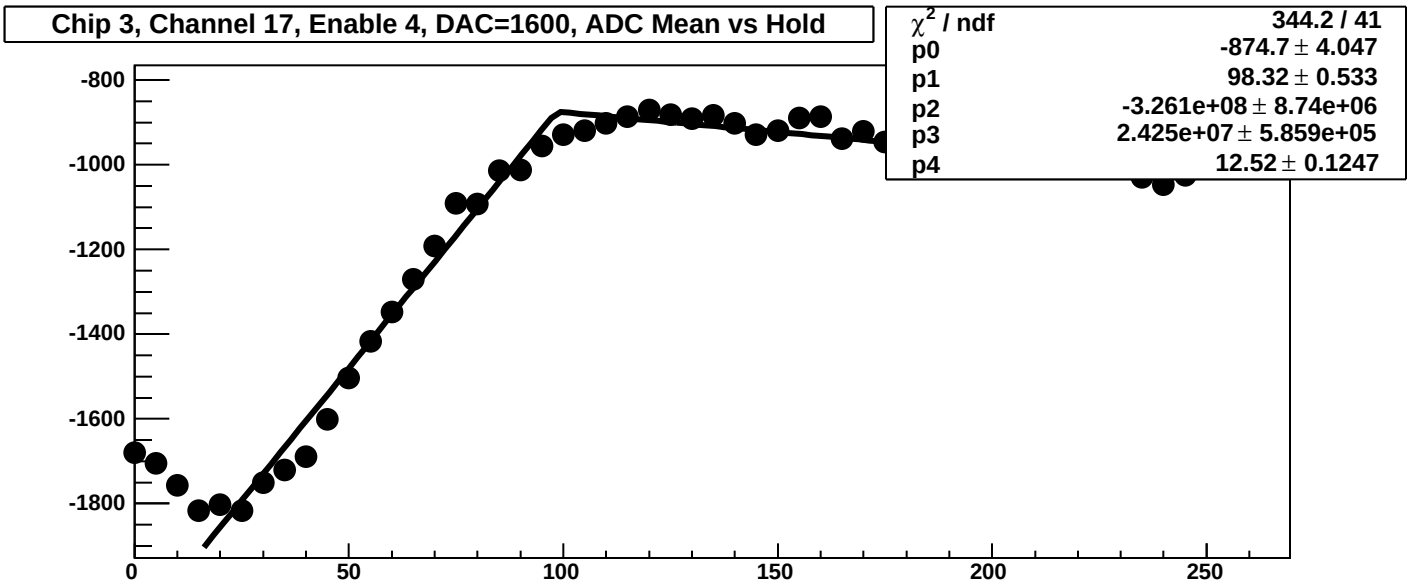

Chip 3, Channel 17, Enable 2, DAC=1600, ADC Residuals vs Hold
100
0
-100
-200
-300
-400
-500
0
50
100
150
200
250


Chip 3, Channel 17, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 347.9 / 41
p0 -875.9 ± 3.989
p1 97.64 ± 0.4914
p2 -1.555e+08 ± 3.879e+06
p3 1.096e+07 ± 2.735e+05
p4 13.26 ± 0.118
-800
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250

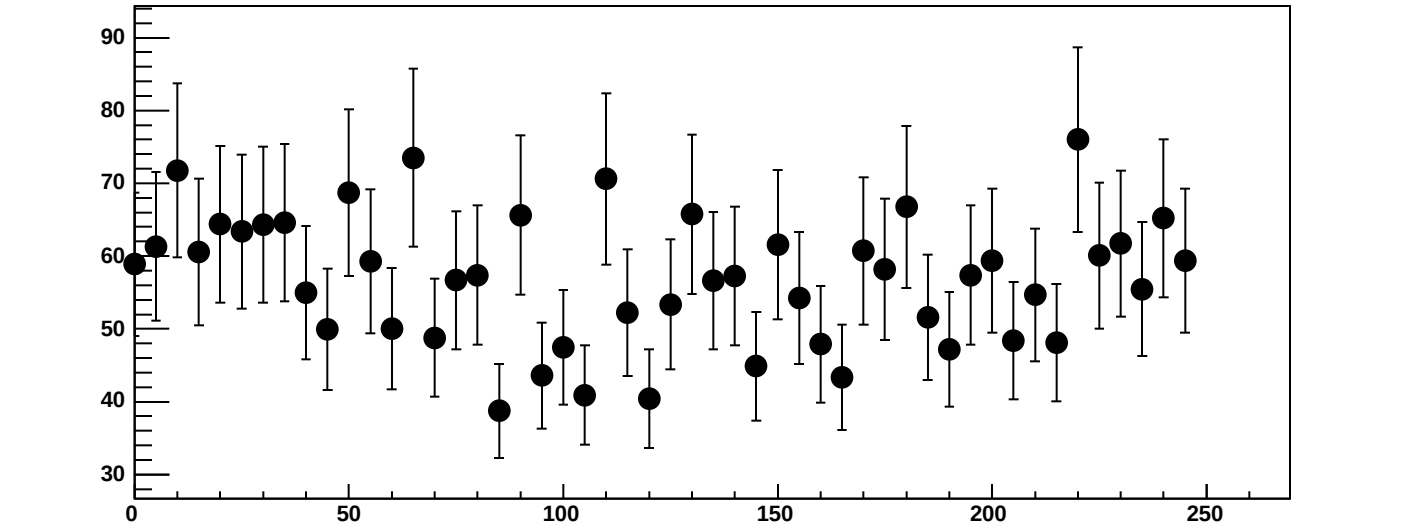

Chip 3, Channel 17, Enable 3, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 3, Channel 17, Enable 3, DAC=1600, ADC Residuals vs Hold
500
400
300
200
100
0
-100
0
50
100
150
200
250

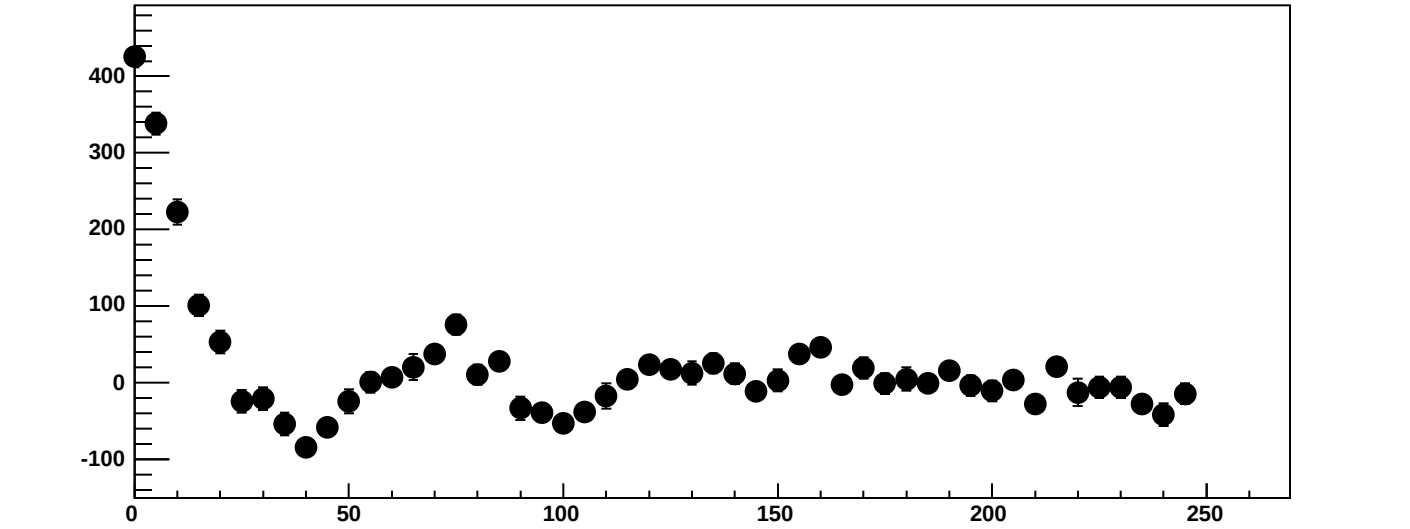
Chip 3, Channel 17, Enable 4, DAC=1600, ADC Mean vs Hold



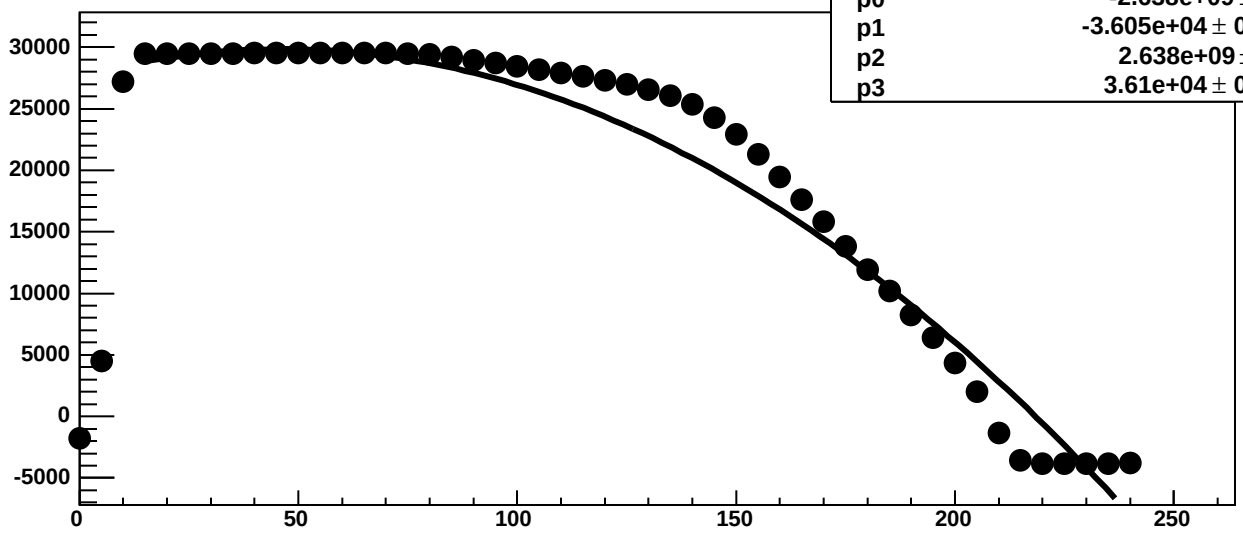
Chip 3, Channel 17, Enable 4, DAC=1600, ADC Noise vs Hold



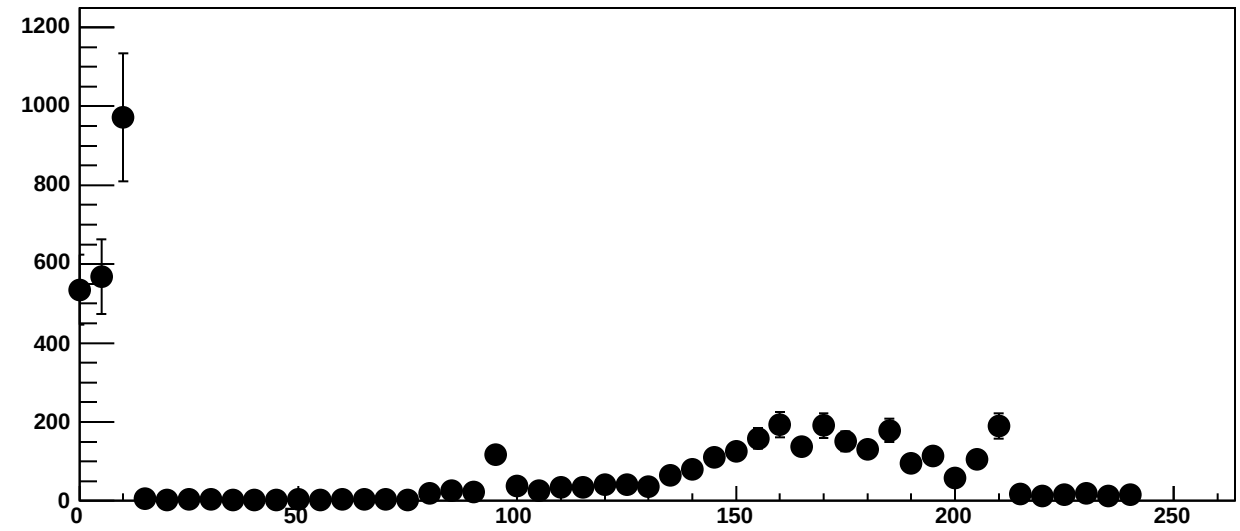
Chip 3, Channel 17, Enable 4, DAC=1600, ADC Residuals vs Hold



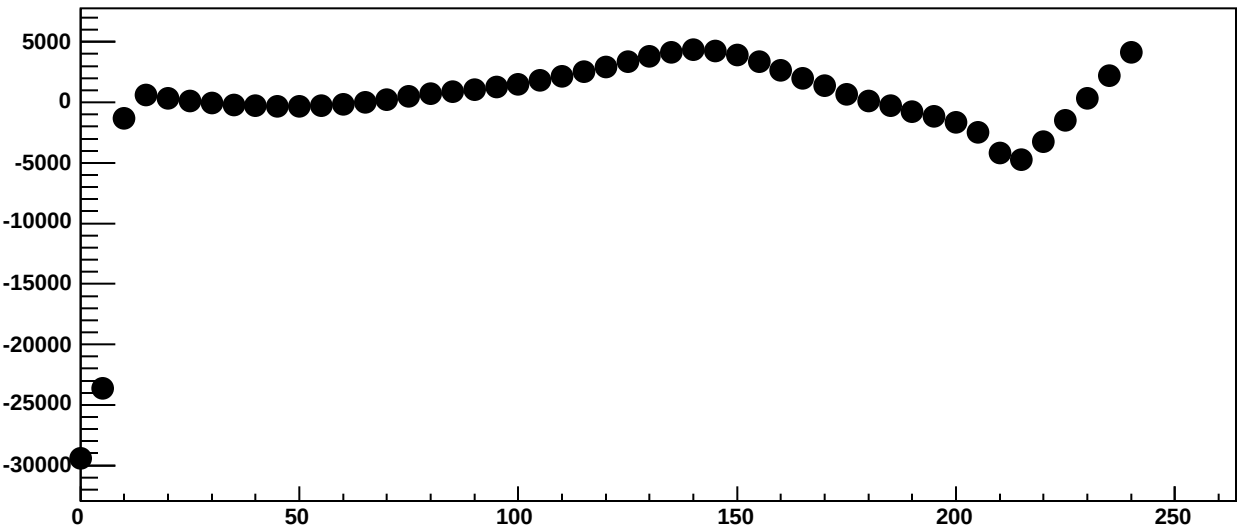
Chip 3, Channel 17, Enable 5!, DAC=1600, ADC Mean vs Hold

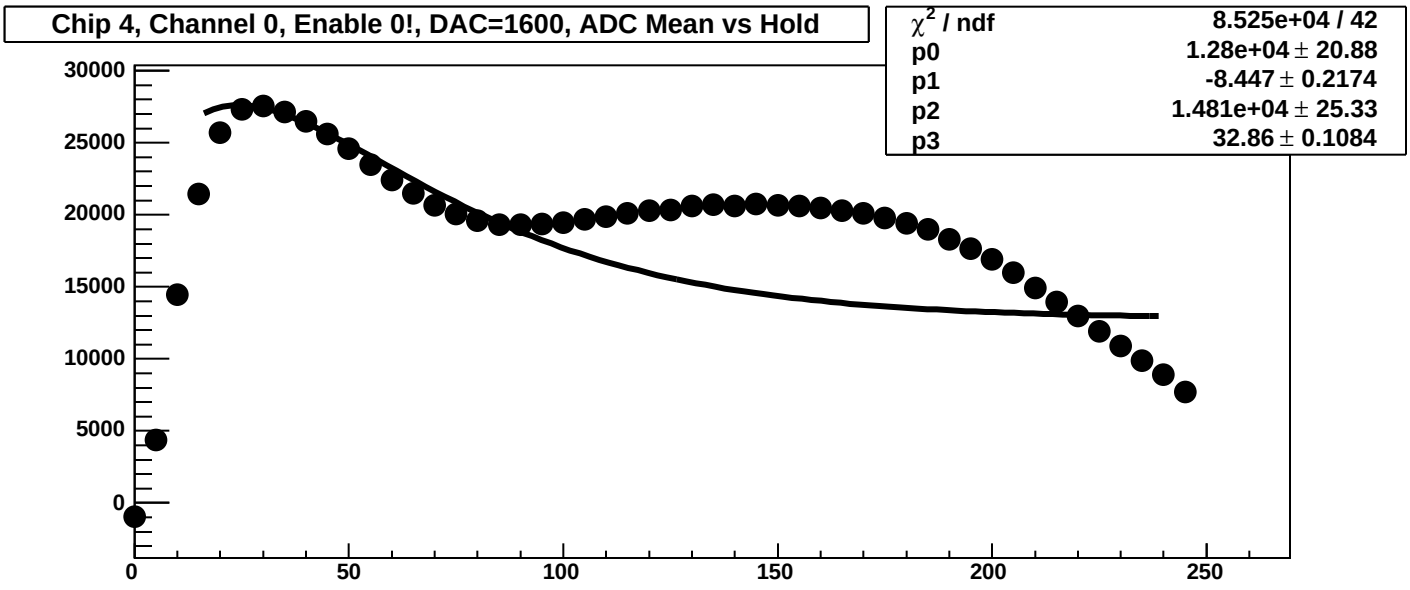


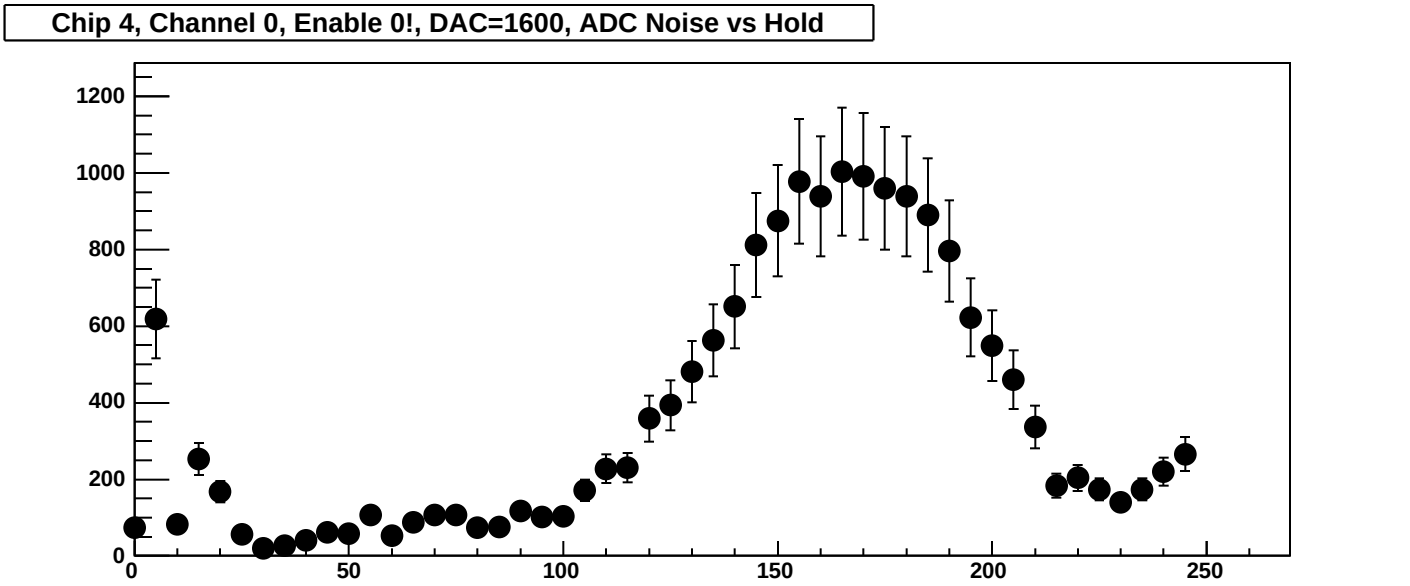
Chip 3, Channel 17, Enable 5!, DAC=1600, ADC Noise vs Hold

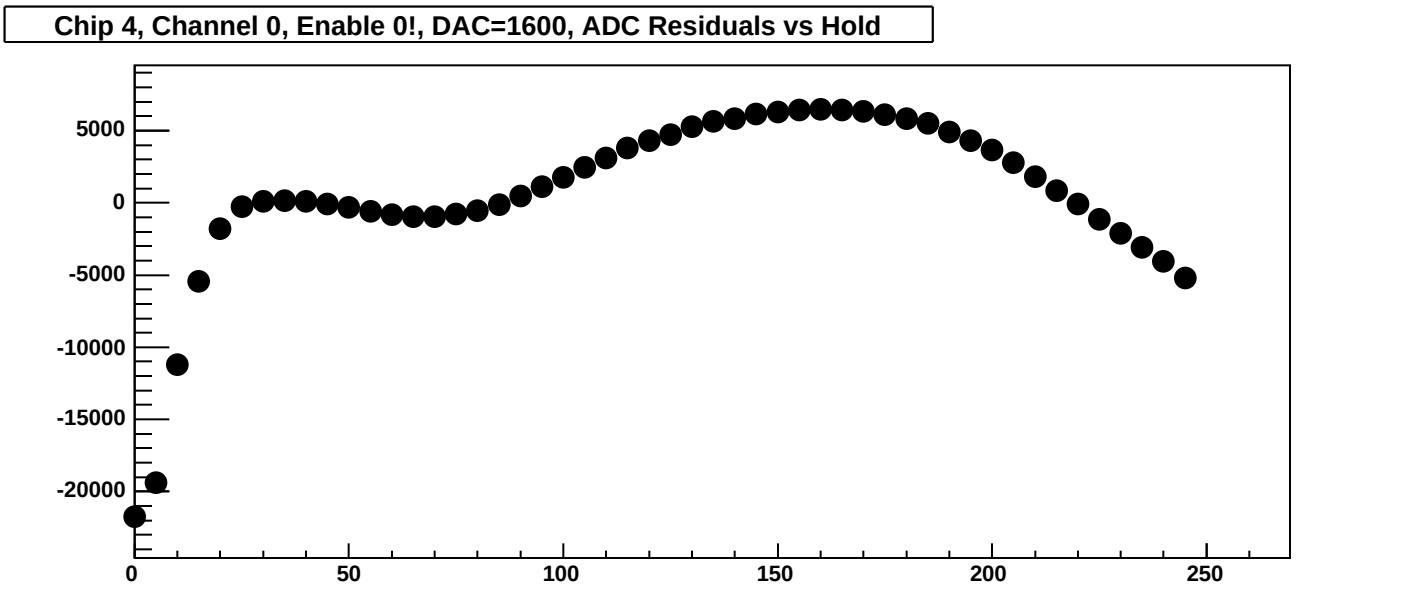


Chip 3, Channel 17, Enable 5!, DAC=1600, ADC Residuals vs Hold

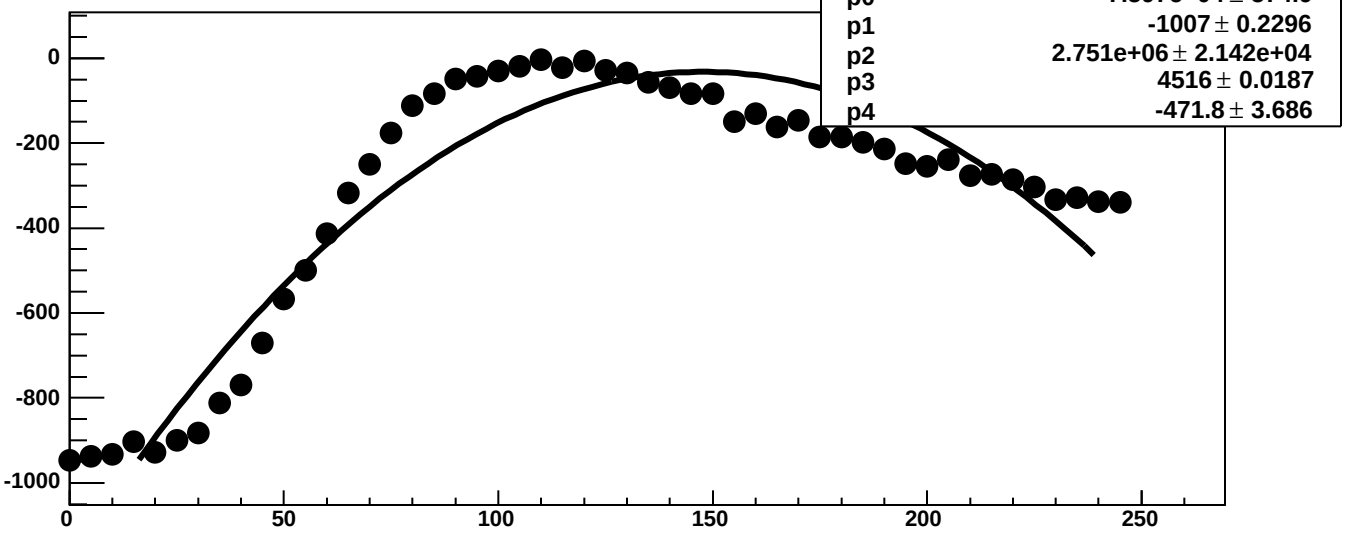



Chip 4, Channel 0, Enable 0!, DAC=1600, ADC Mean vs Hold
χ² / ndf 8.525e+04 / 42
p0 1.28e+04 ± 20.88
p1 -8.447 ± 0.2174
p2 1.481e+04 ± 25.33
p3 32.86 ± 0.1084
30000
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

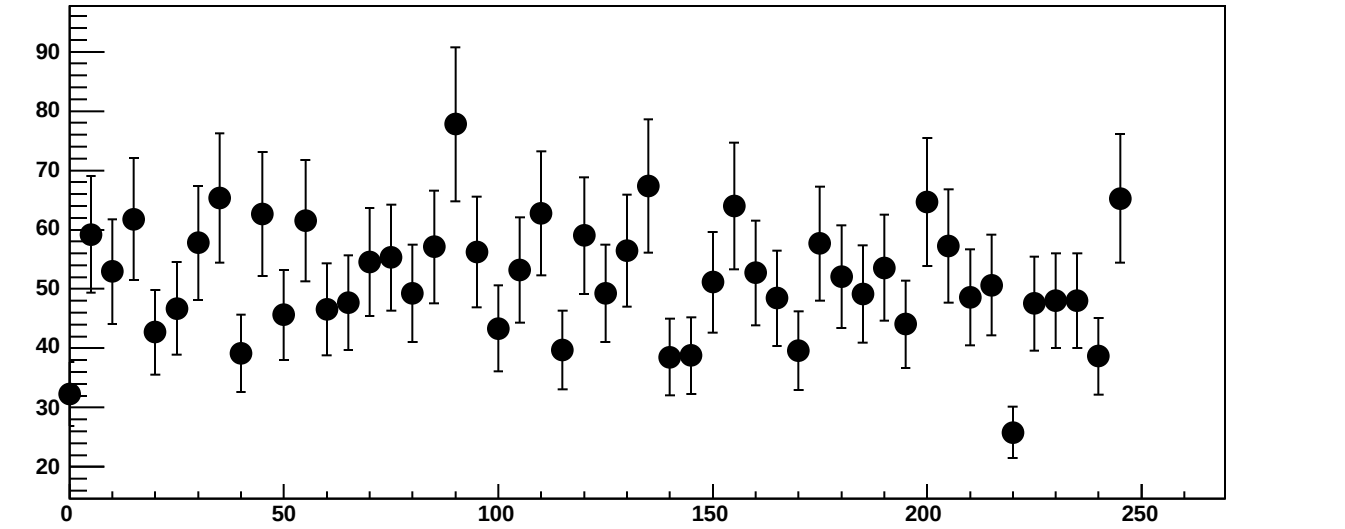

Chip 4, Channel 0, Enable 0!, DAC=1600, ADC Noise vs Hold
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 4, Channel 0, Enable 0!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

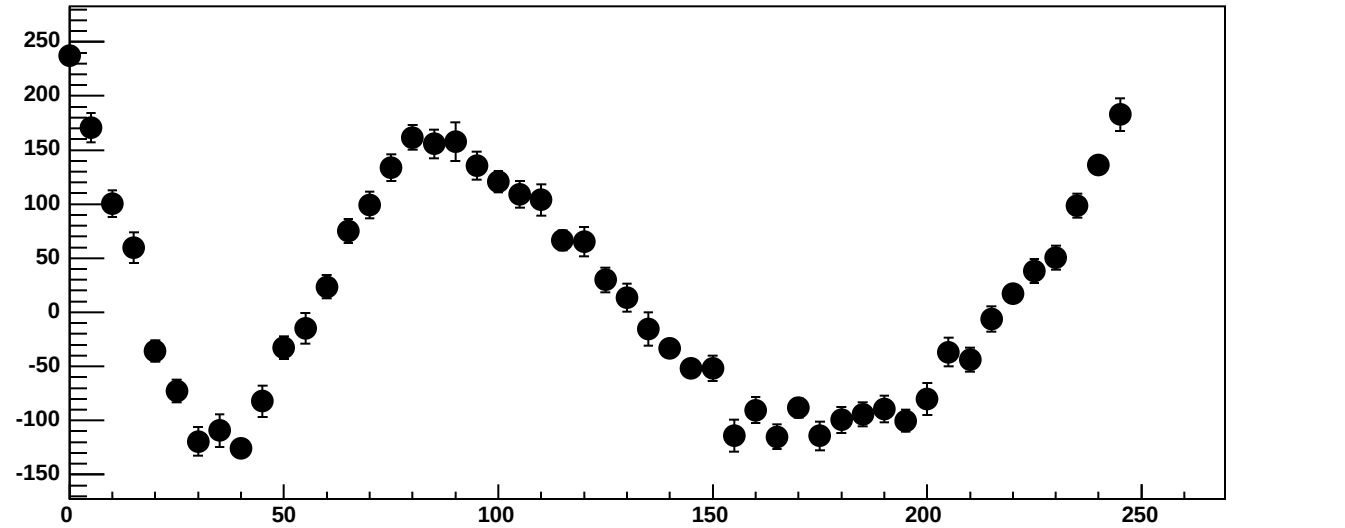
Chip 4, Channel 0, Enable 1, DAC=1600, ADC Mean vs Hold



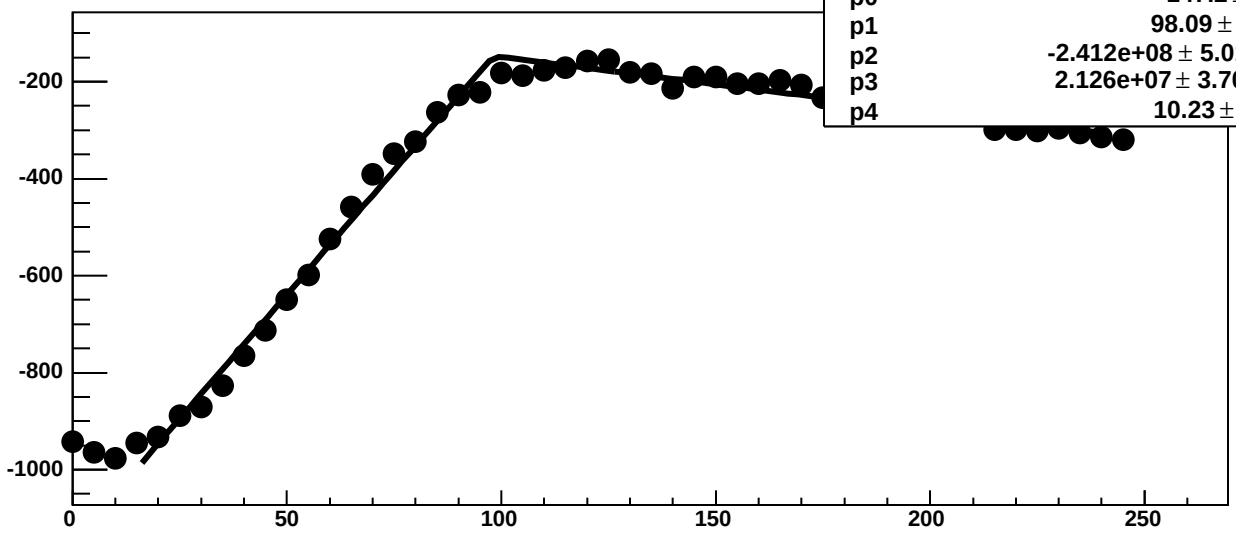
Chip 4, Channel 0, Enable 1, DAC=1600, ADC Noise vs Hold



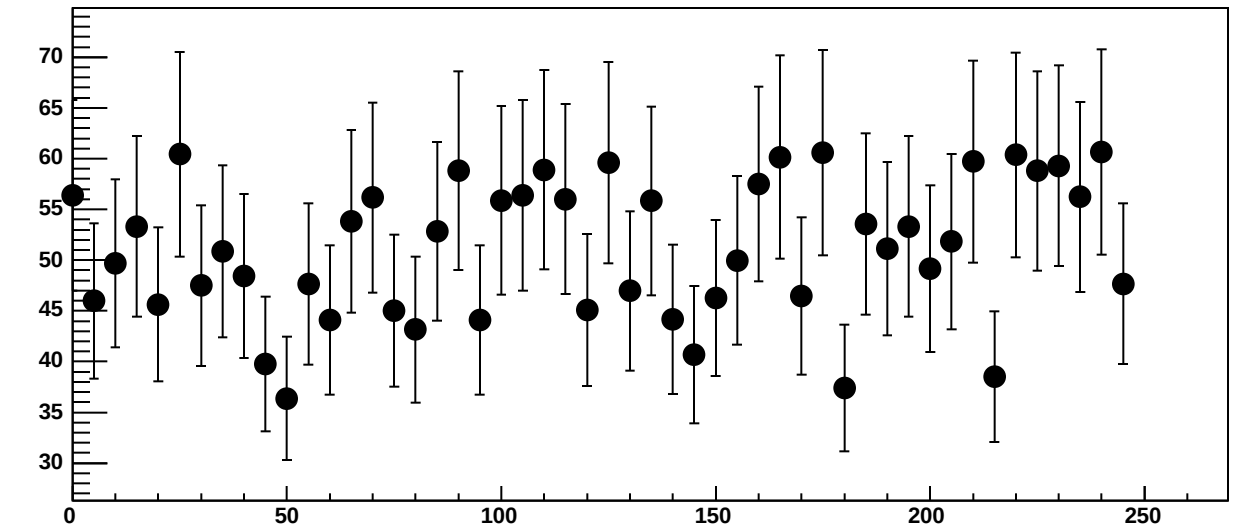
Chip 4, Channel 0, Enable 1, DAC=1600, ADC Residuals vs Hold



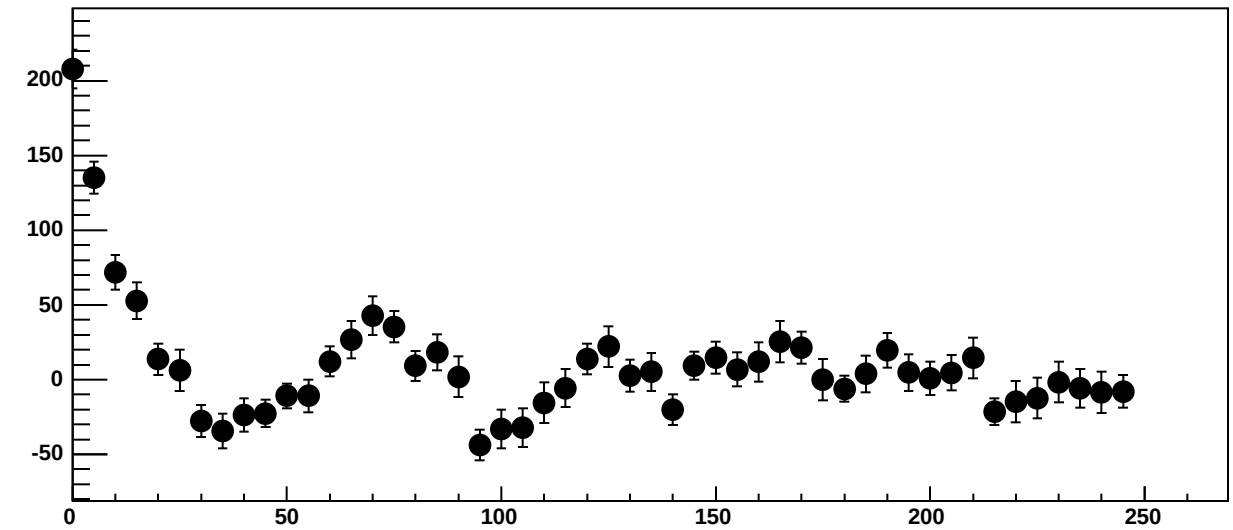
Chip 4, Channel 0, Enable 2, DAC=1600, ADC Mean vs Hold



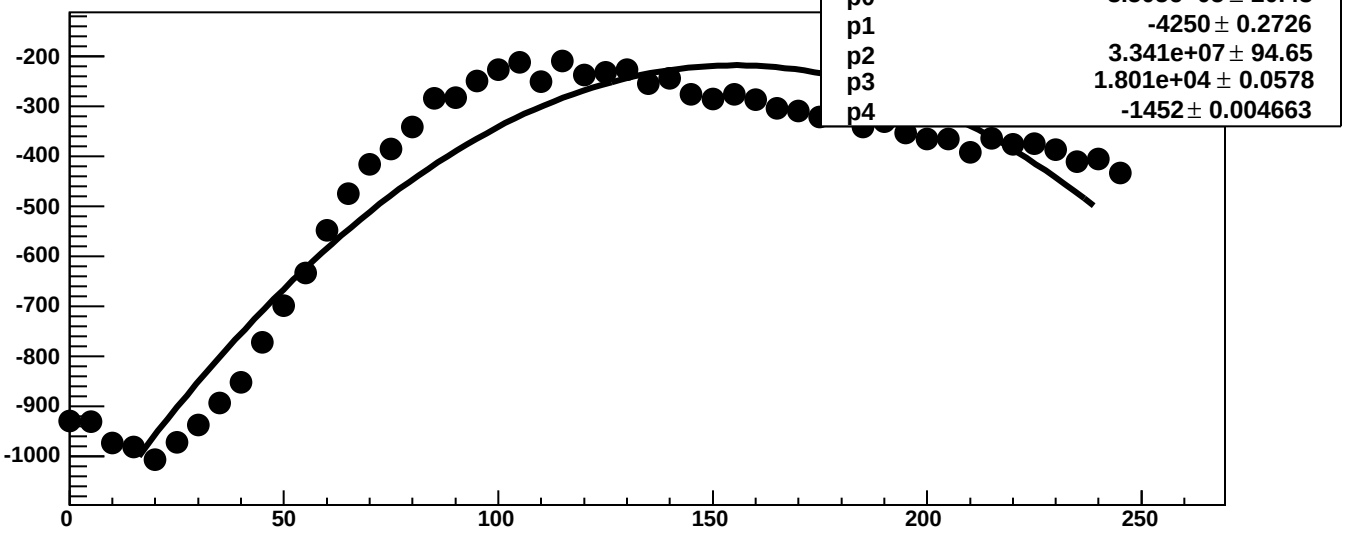
Chip 4, Channel 0, Enable 2, DAC=1600, ADC Noise vs Hold



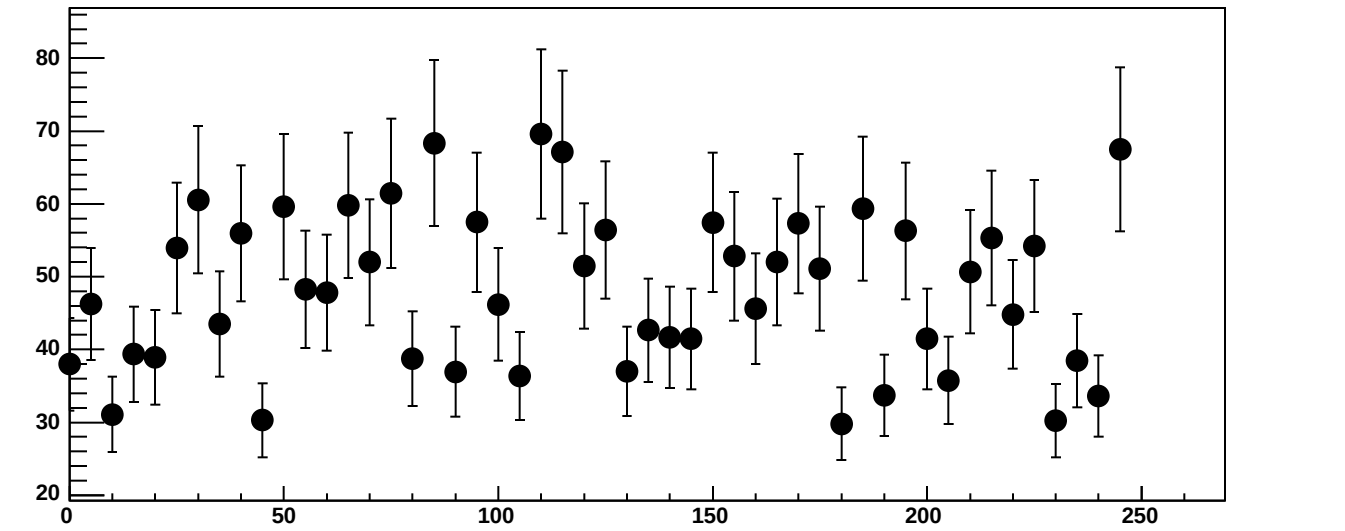
Chip 4, Channel 0, Enable 2, DAC=1600, ADC Residuals vs Hold



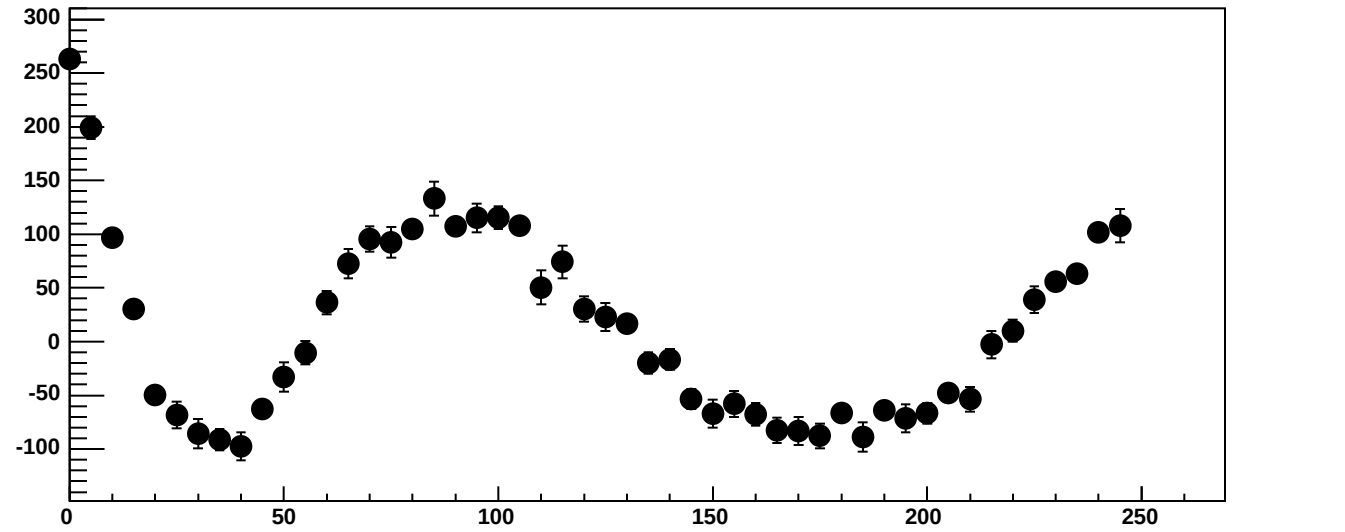
Chip 4, Channel 0, Enable 3, DAC=1600, ADC Mean vs Hold



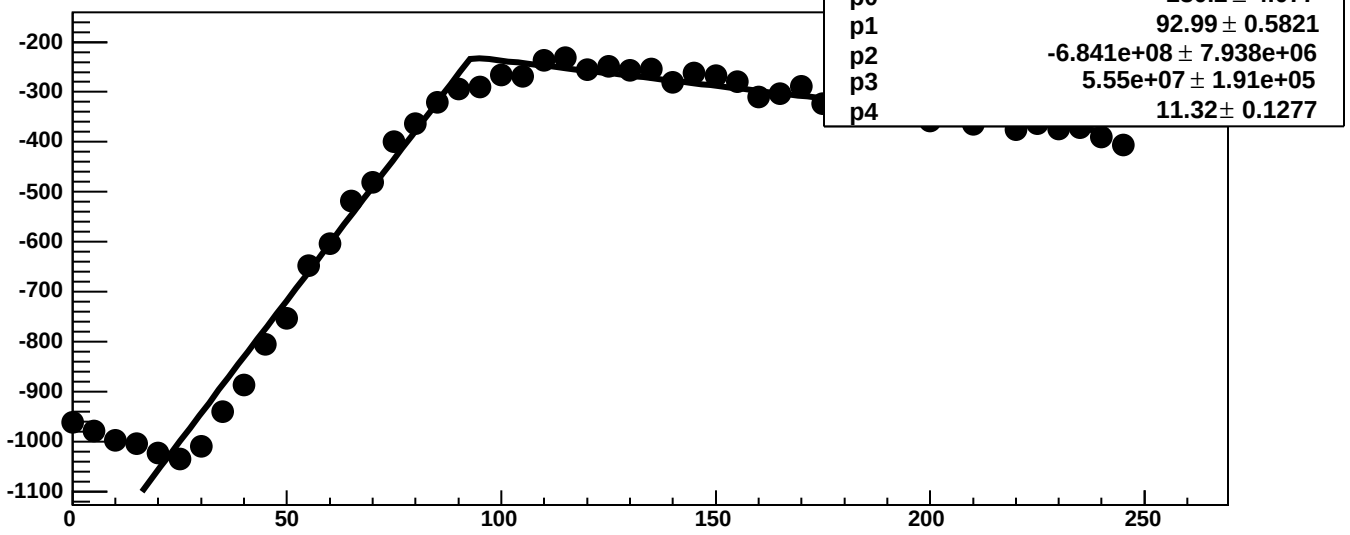
Chip 4, Channel 0, Enable 3, DAC=1600, ADC Noise vs Hold



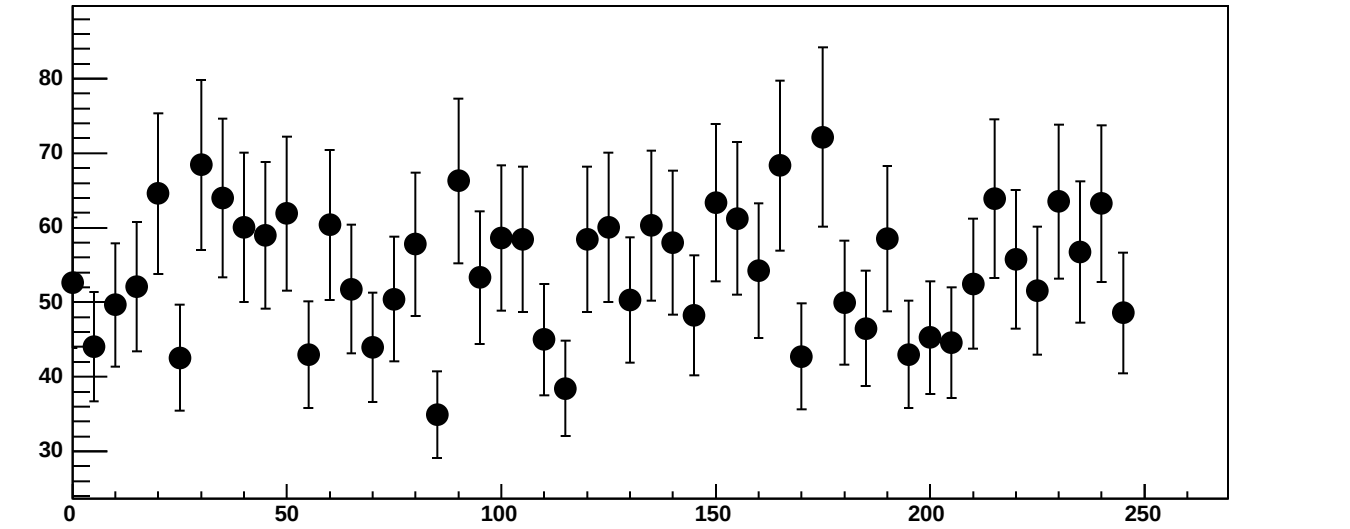
Chip 4, Channel 0, Enable 3, DAC=1600, ADC Residuals vs Hold



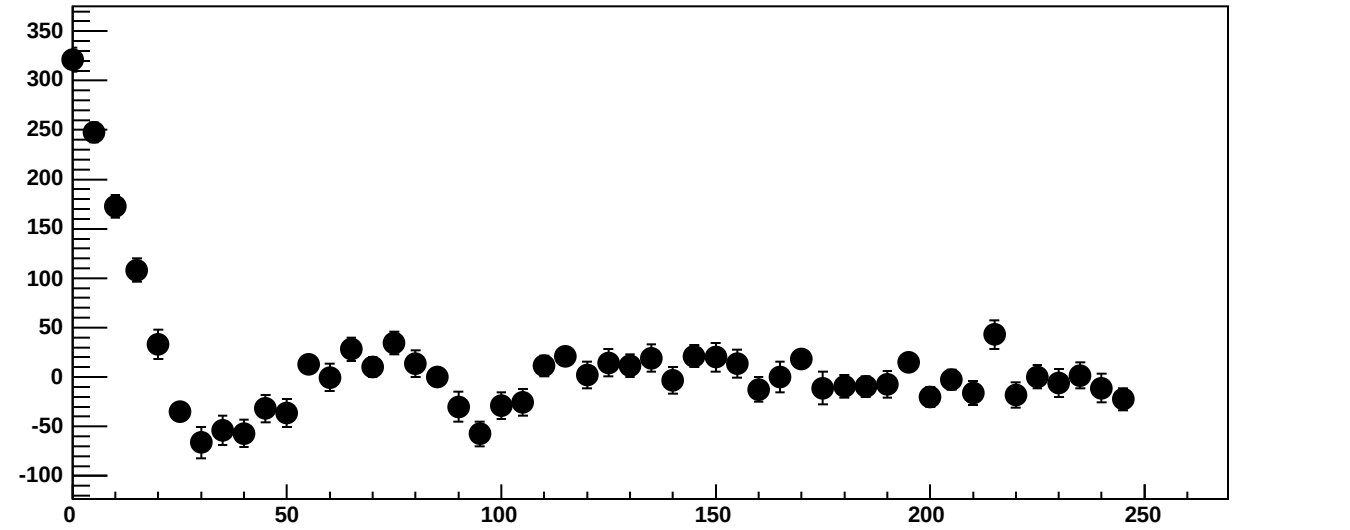
Chip 4, Channel 0, Enable 4, DAC=1600, ADC Mean vs Hold

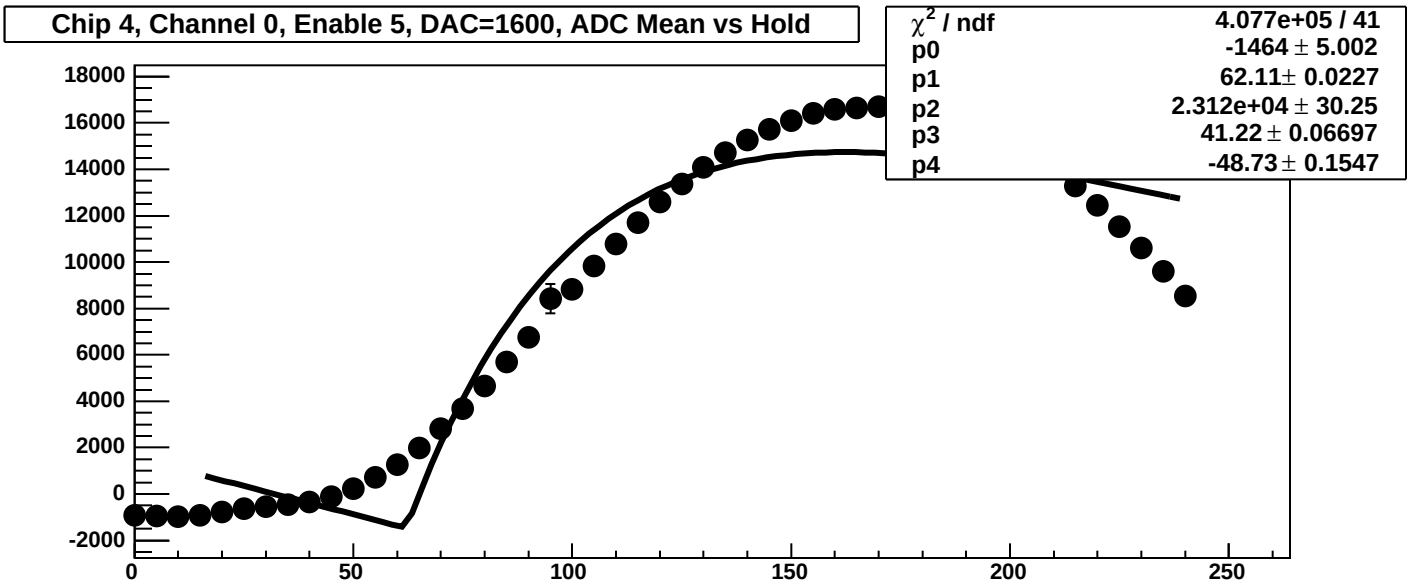


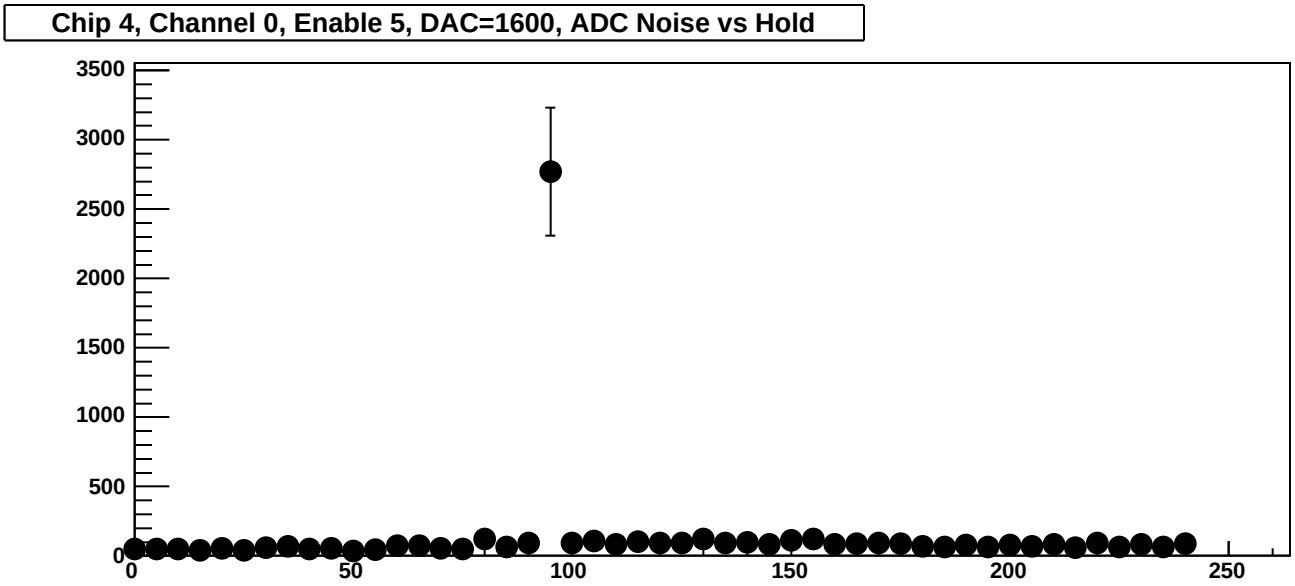
Chip 4, Channel 0, Enable 4, DAC=1600, ADC Noise vs Hold

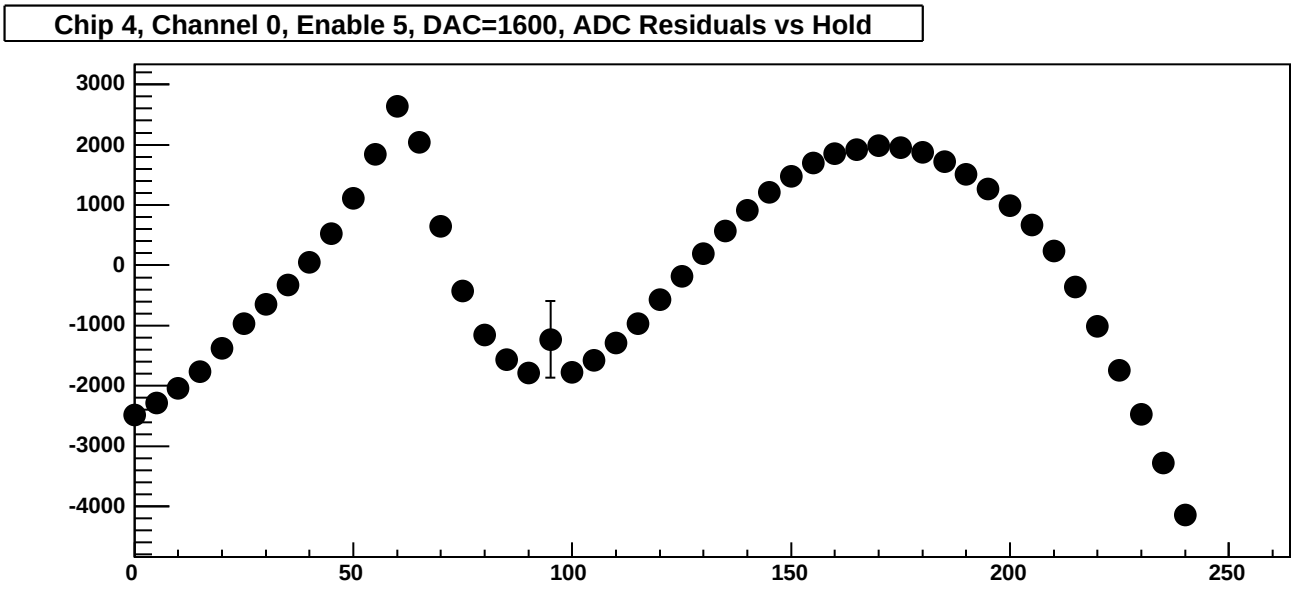


Chip 4, Channel 0, Enable 4, DAC=1600, ADC Residuals vs Hold

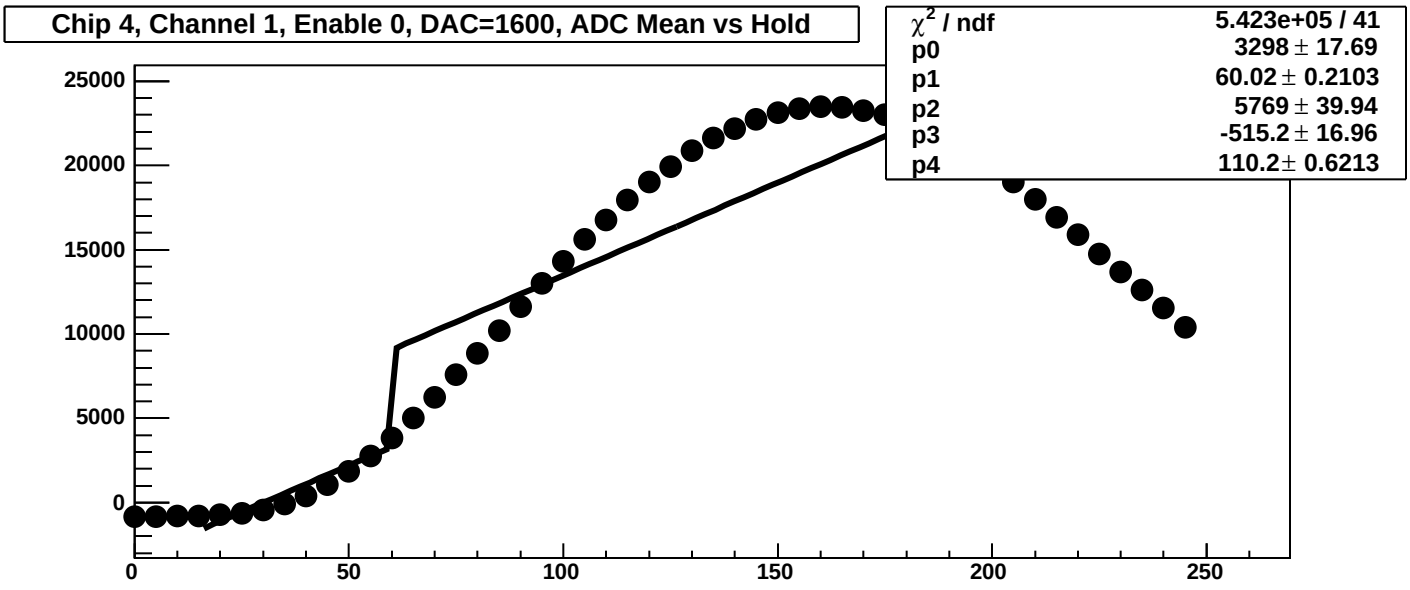



Chip 4, Channel 0, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.077e+05 / 41
p0 -1464 ± 5.002
p1 62.11 ± 0.0227
p2 2.312e+04 ± 30.25
p3 41.22 ± 0.06697
p4 -48.73 ± 0.1547
18000
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

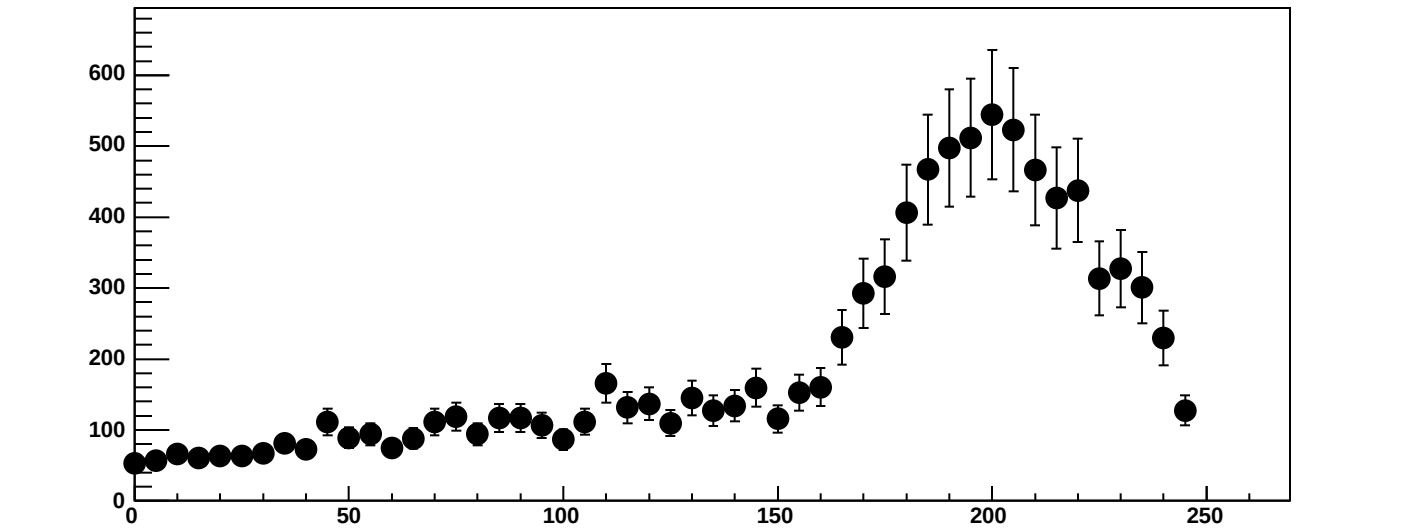

Chip 4, Channel 0, Enable 5, DAC=1600, ADC Noise vs Hold
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 4, Channel 0, Enable 5, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

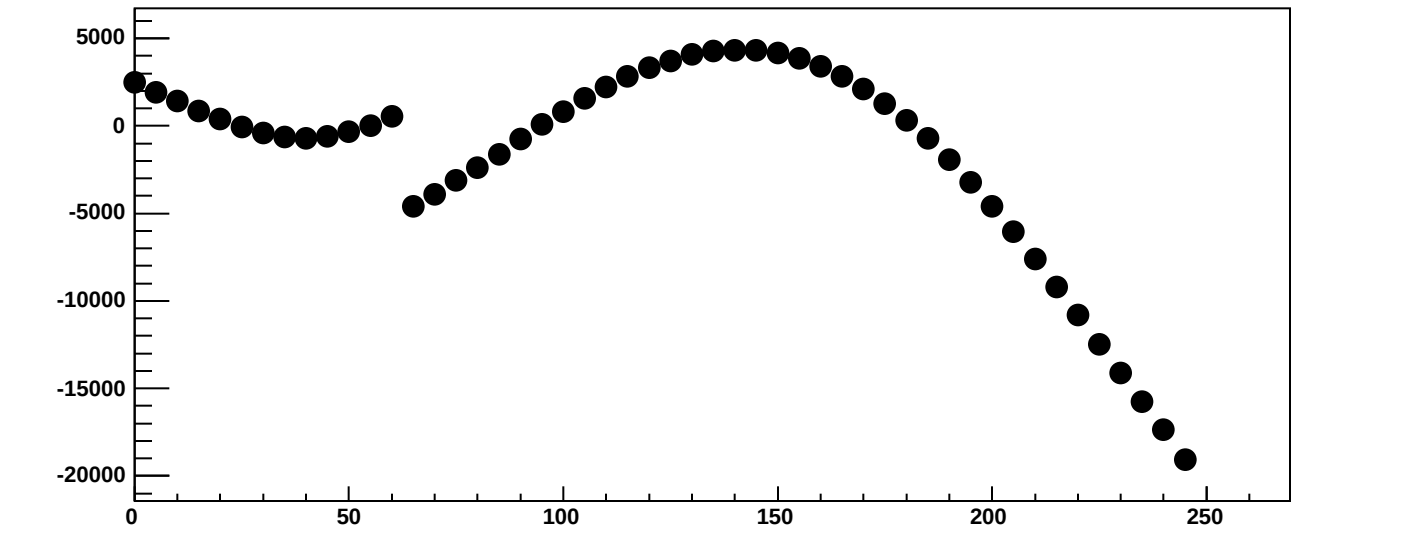
Chip 4, Channel 1, Enable 0, DAC=1600, ADC Mean vs Hold



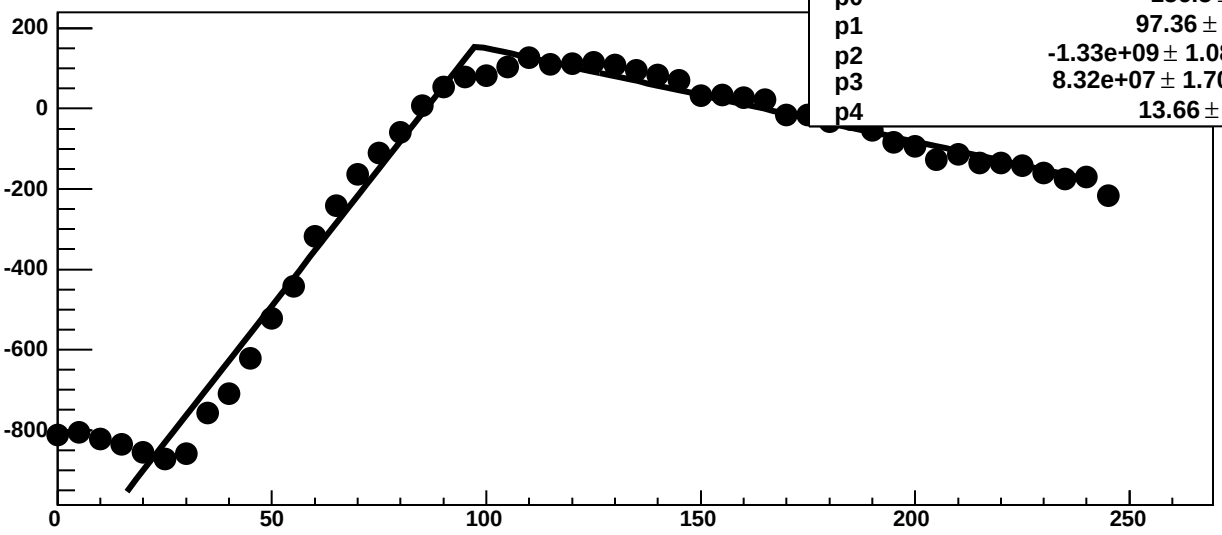
Chip 4, Channel 1, Enable 0, DAC=1600, ADC Noise vs Hold



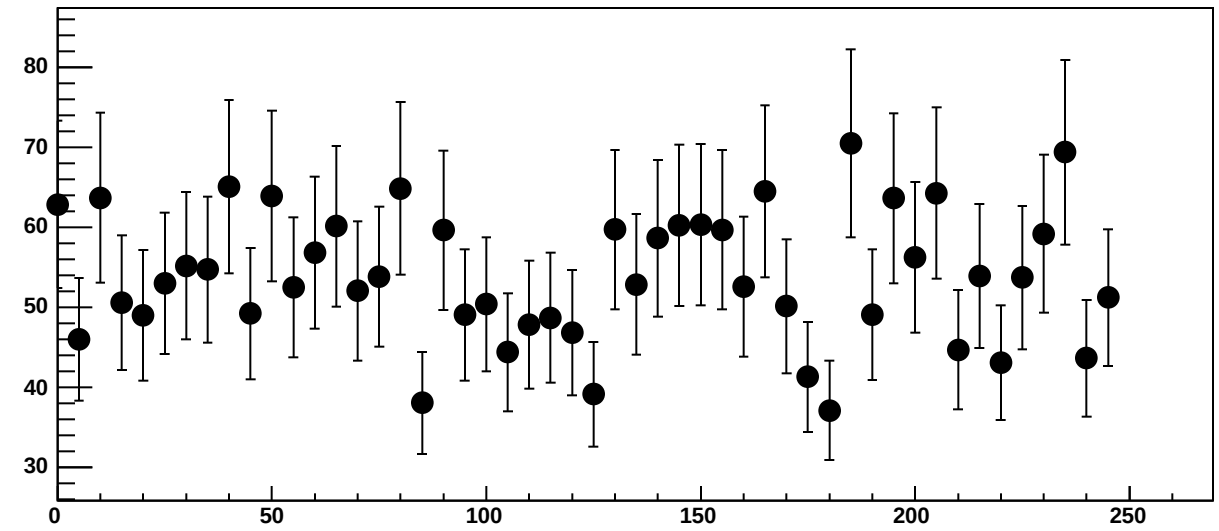
Chip 4, Channel 1, Enable 0, DAC=1600, ADC Residuals vs Hold



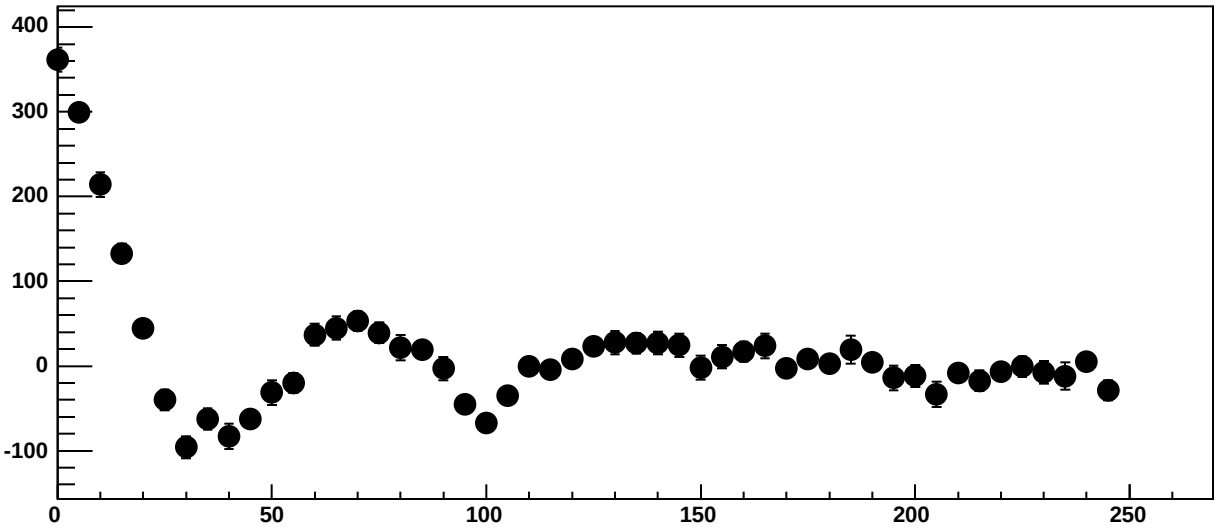
Chip 4, Channel 1, Enable 1, DAC=1600, ADC Mean vs Hold



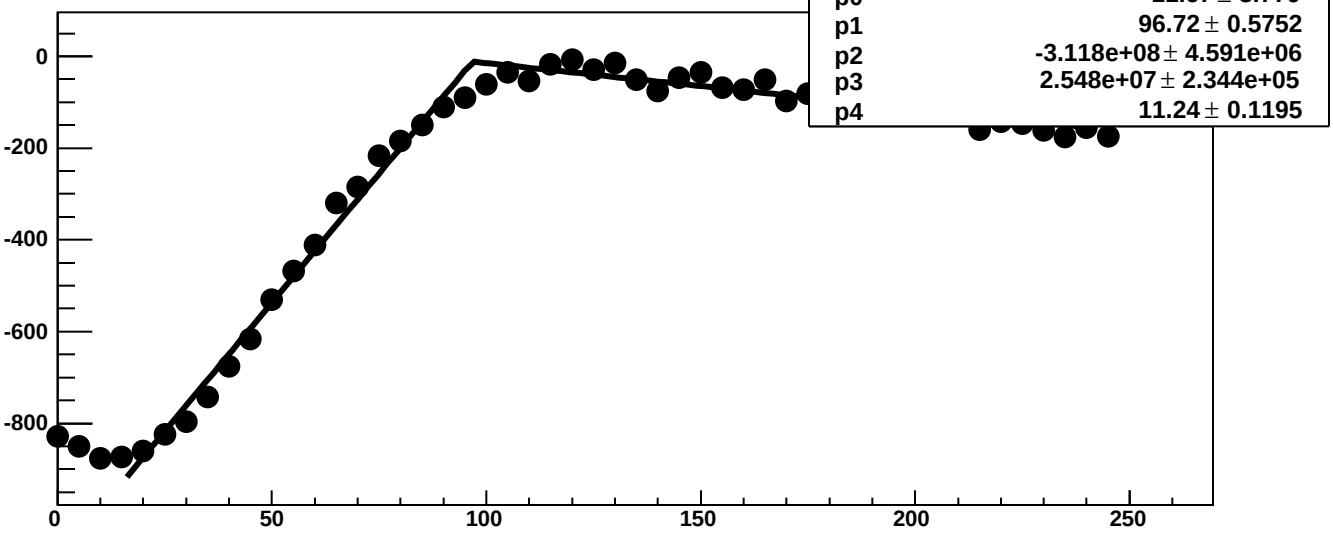
Chip 4, Channel 1, Enable 1, DAC=1600, ADC Noise vs Hold



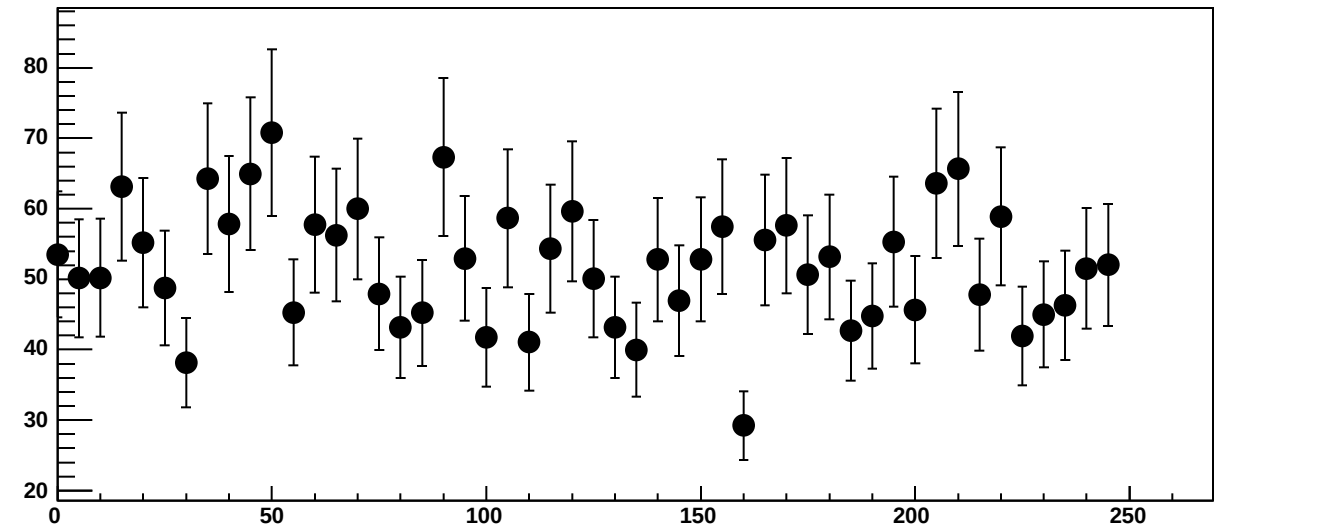
Chip 4, Channel 1, Enable 1, DAC=1600, ADC Residuals vs Hold



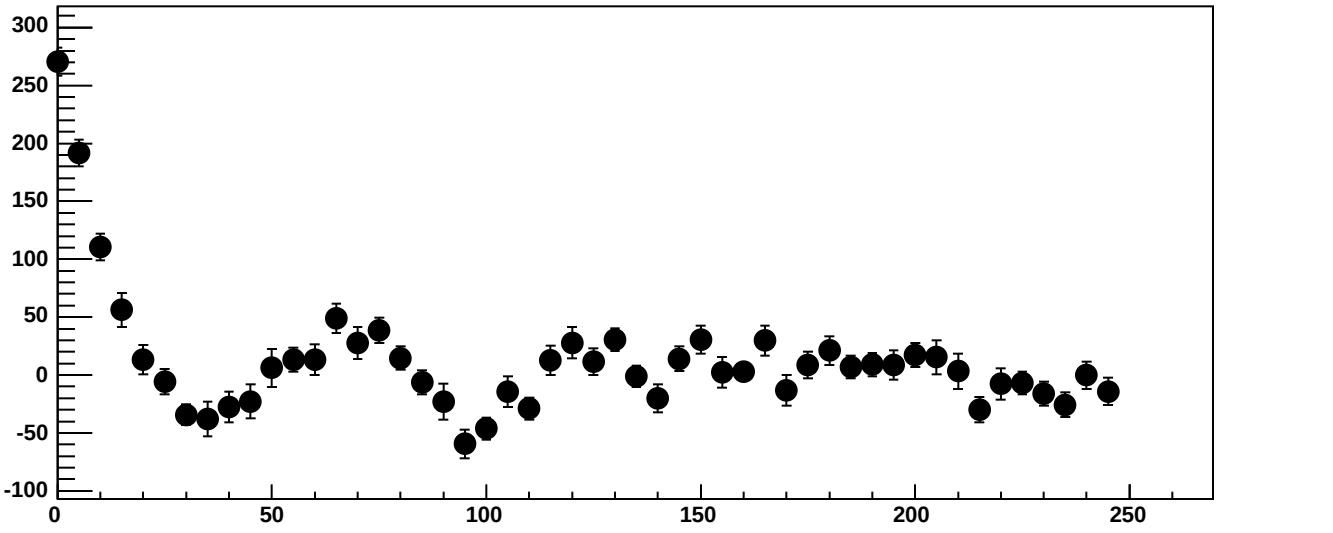
Chip 4, Channel 1, Enable 2, DAC=1600, ADC Mean vs Hold



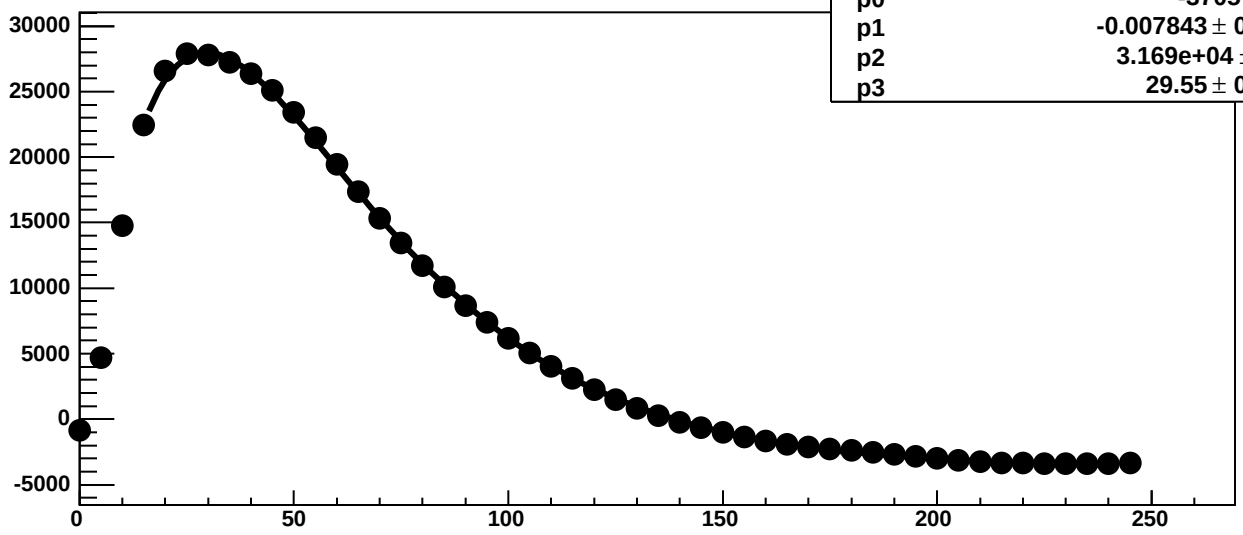
Chip 4, Channel 1, Enable 2, DAC=1600, ADC Noise vs Hold



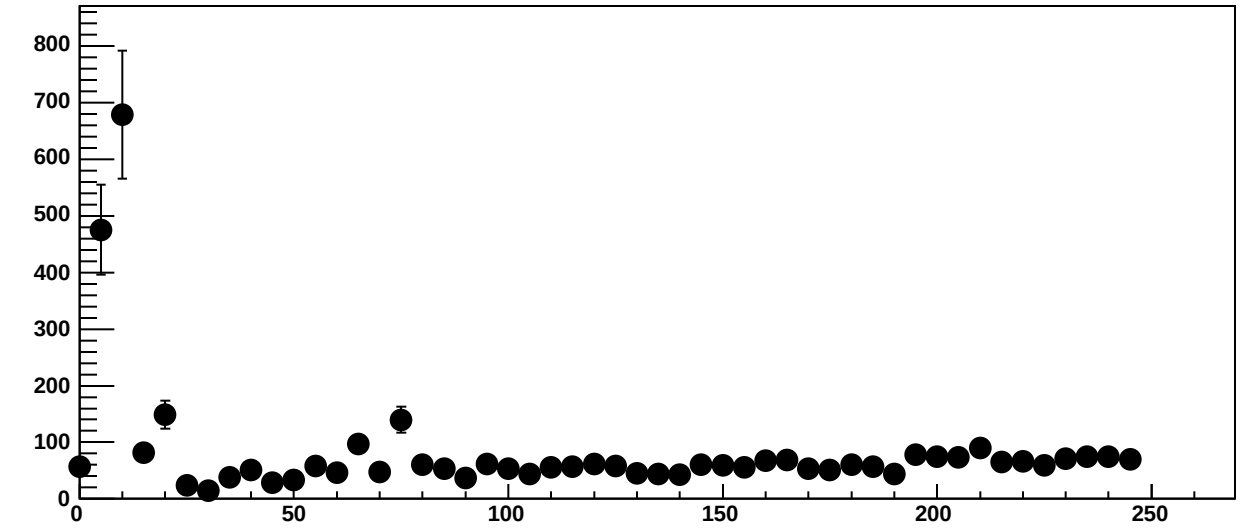
Chip 4, Channel 1, Enable 2, DAC=1600, ADC Residuals vs Hold



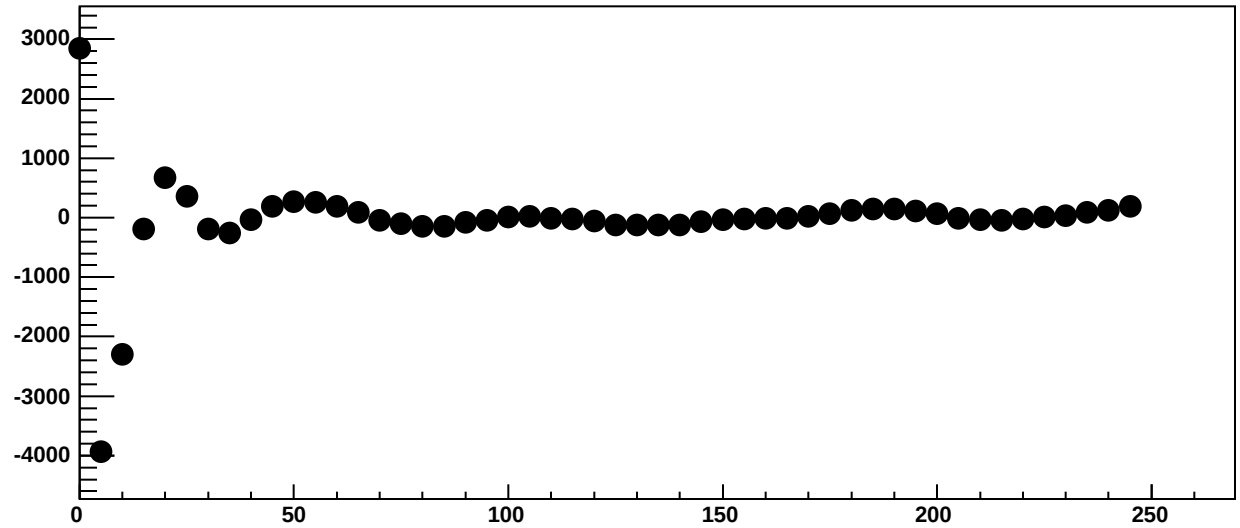
Chip 4, Channel 1, Enable 3!, DAC=1600, ADC Mean vs Hold



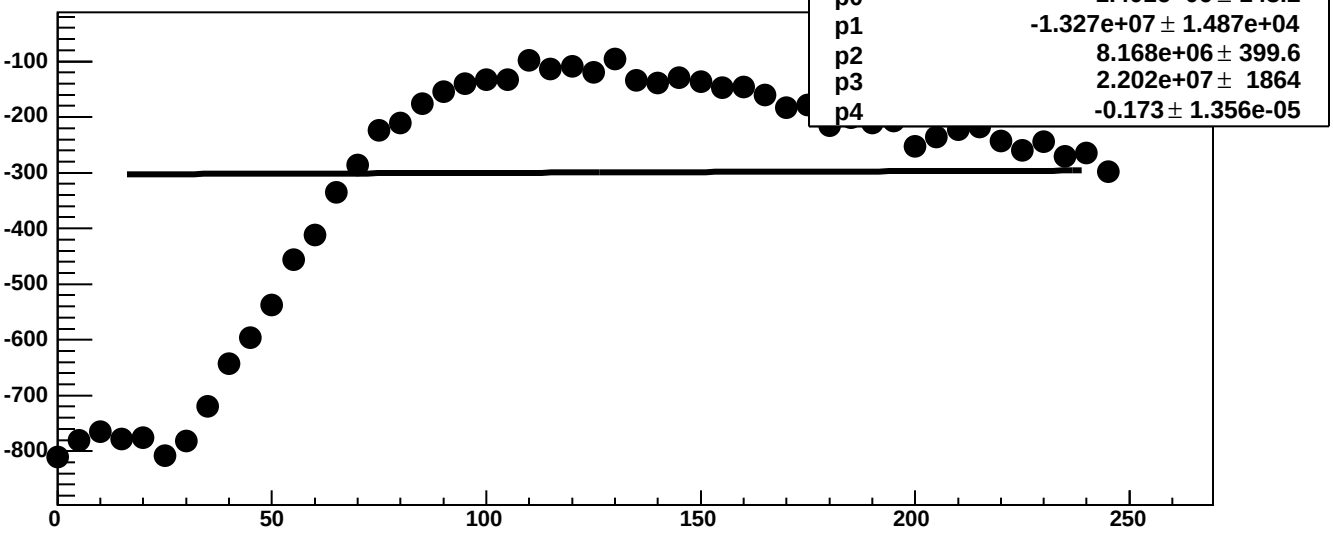
Chip 4, Channel 1, Enable 3!, DAC=1600, ADC Noise vs Hold



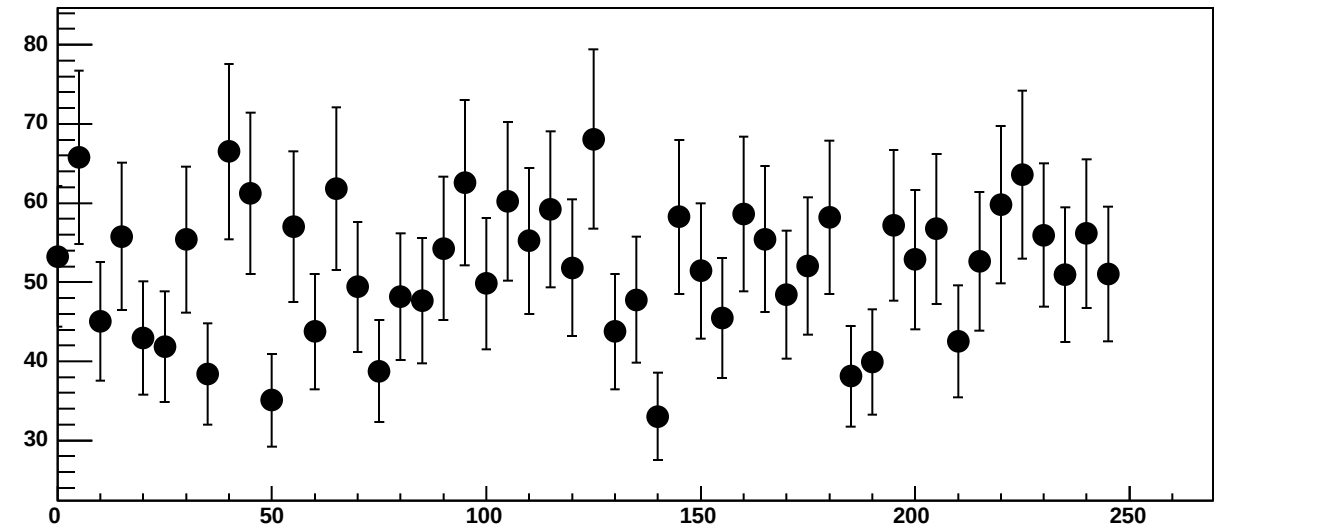
Chip 4, Channel 1, Enable 3!, DAC=1600, ADC Residuals vs Hold



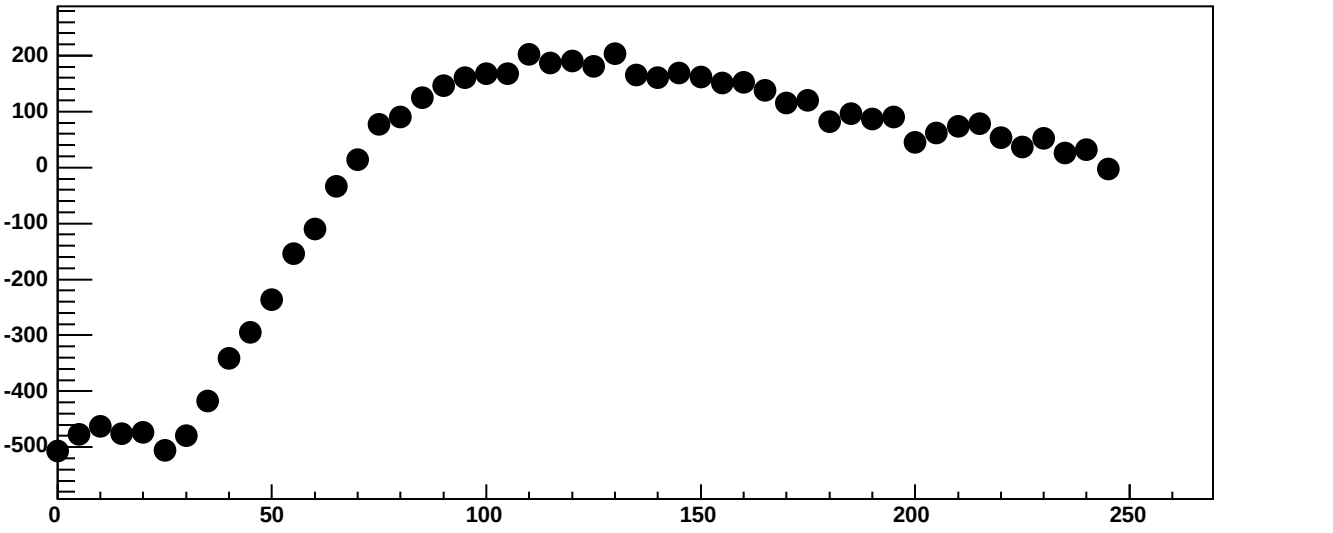
Chip 4, Channel 1, Enable 4, DAC=1600, ADC Mean vs Hold

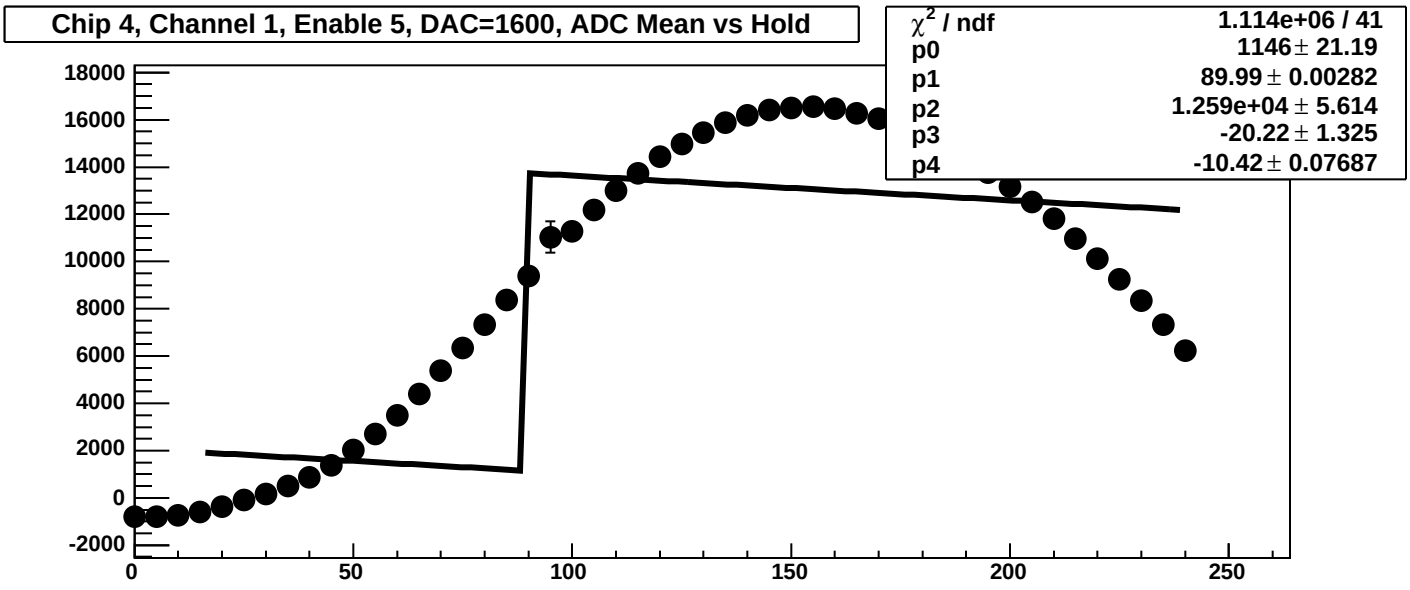


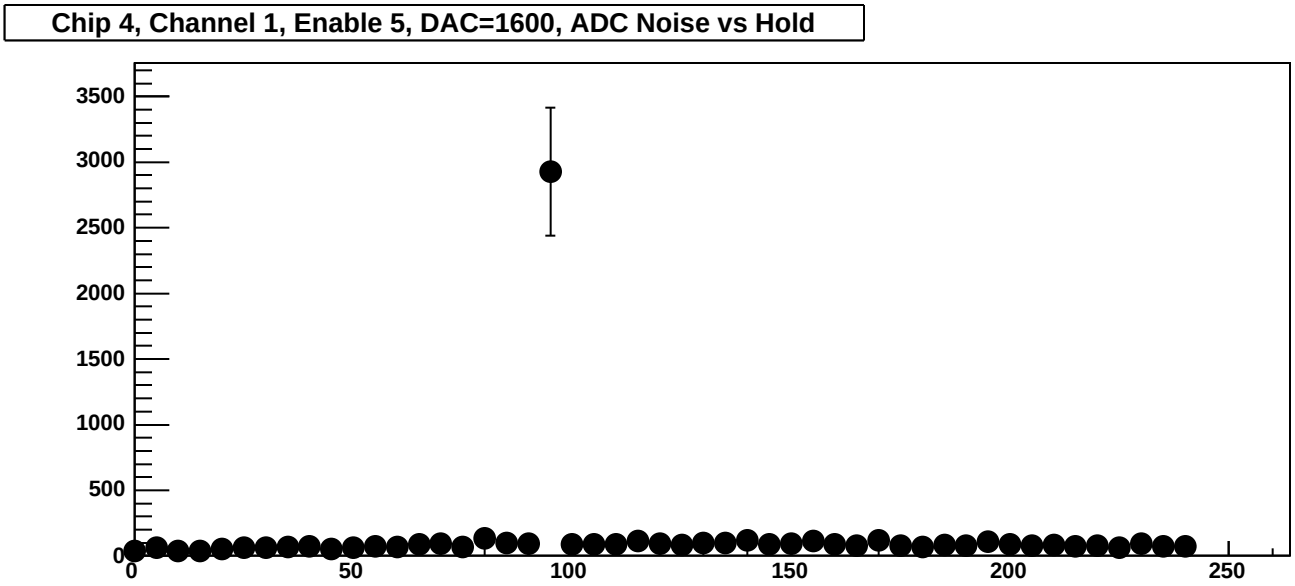
Chip 4, Channel 1, Enable 4, DAC=1600, ADC Noise vs Hold

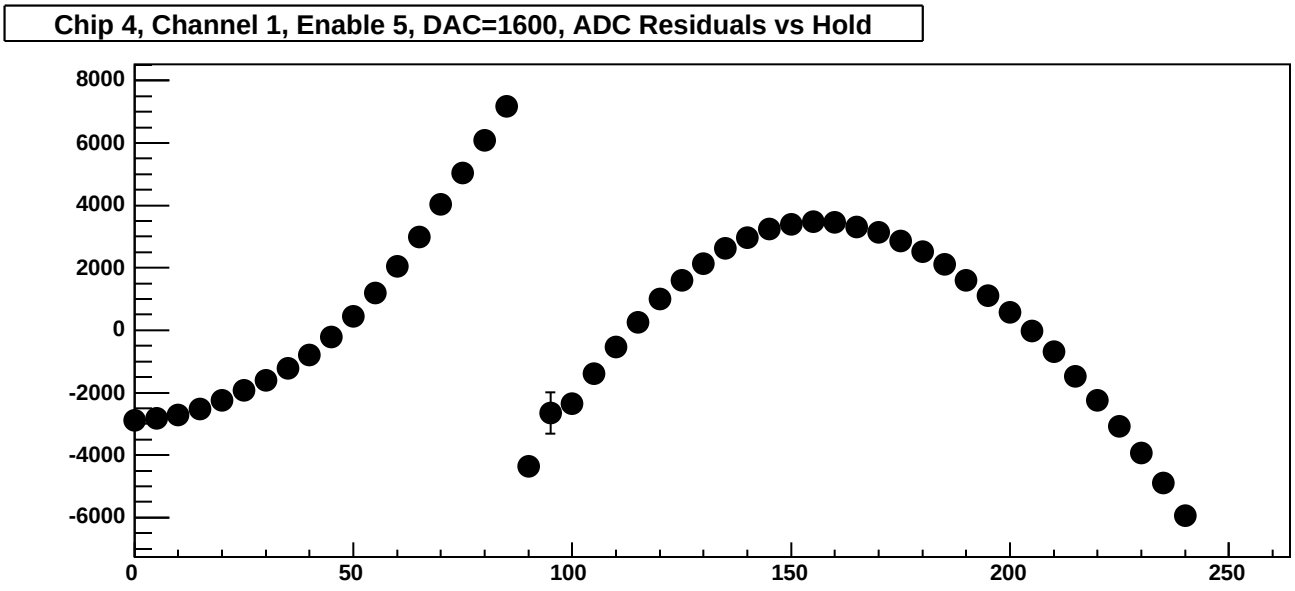


Chip 4, Channel 1, Enable 4, DAC=1600, ADC Residuals vs Hold

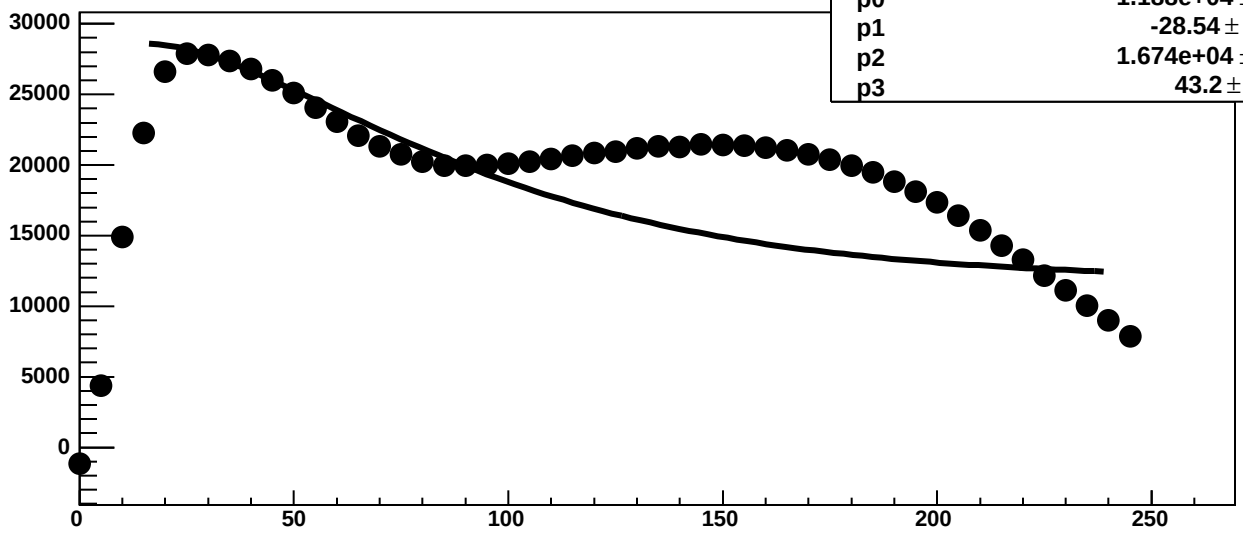



Chip 4, Channel 1, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.114e+06 / 41
p0 1146 ± 21.19
p1 89.99 ± 0.00282
p2 1.259e+04 ± 5.614
p3 -20.22 ± 1.325
p4 -10.42 ± 0.07687
18000
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

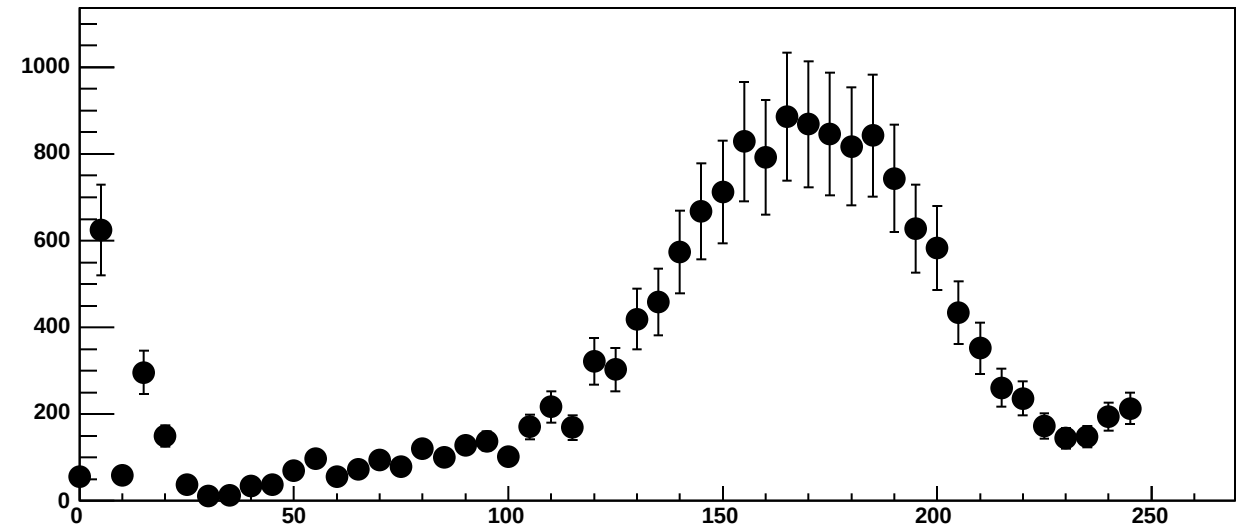

Chip 4, Channel 1, Enable 5, DAC=1600, ADC Noise vs Hold
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 4, Channel 1, Enable 5, DAC=1600, ADC Residuals vs Hold
8000
6000
4000
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

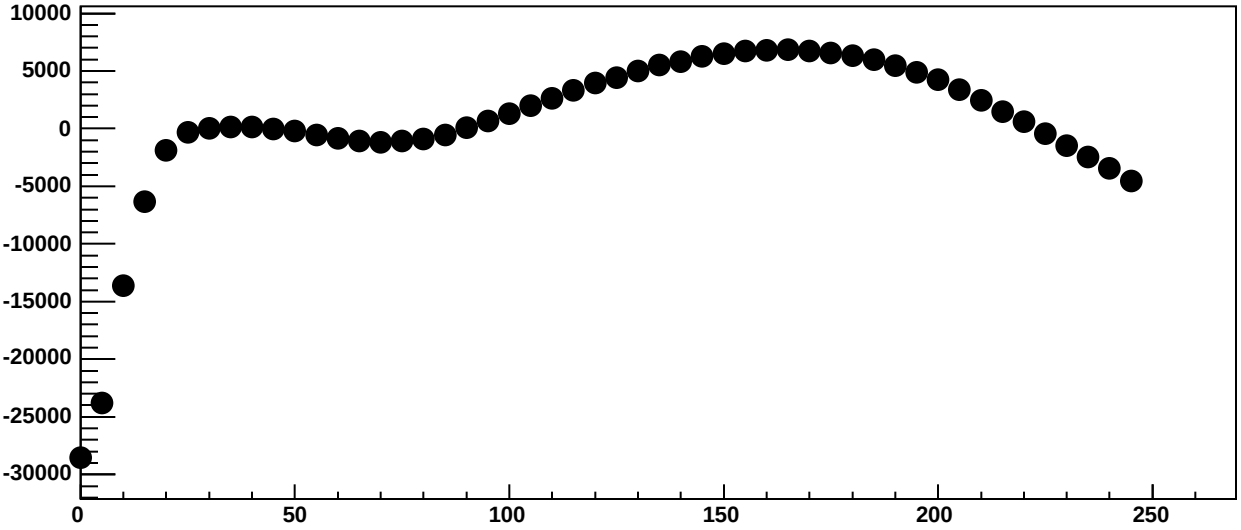
Chip 4, Channel 2, Enable 0!, DAC=1600, ADC Mean vs Hold



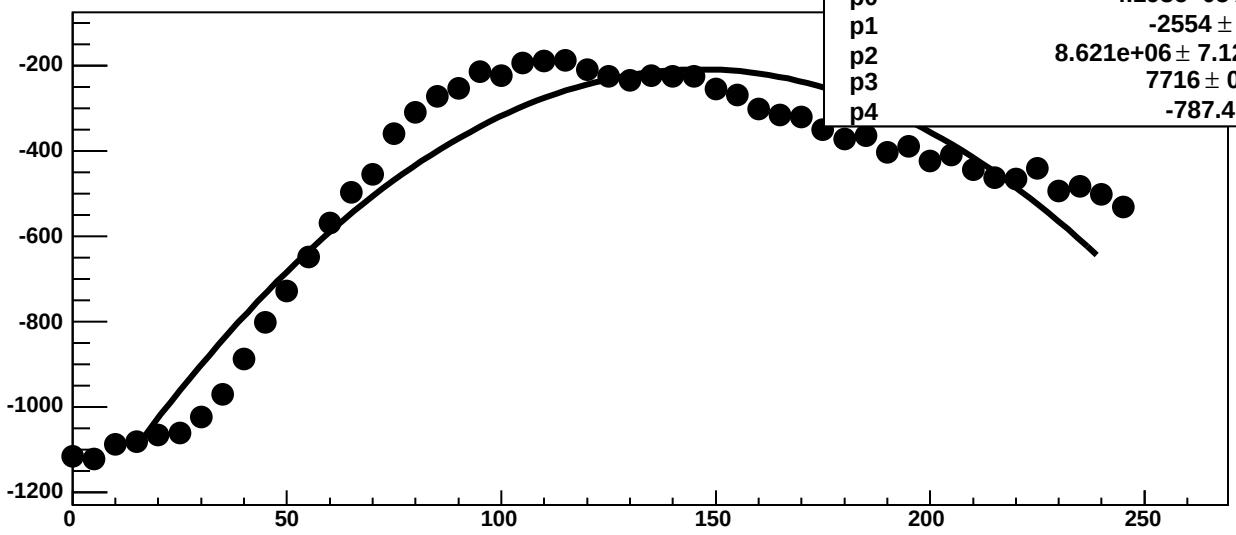
Chip 4, Channel 2, Enable 0!, DAC=1600, ADC Noise vs Hold



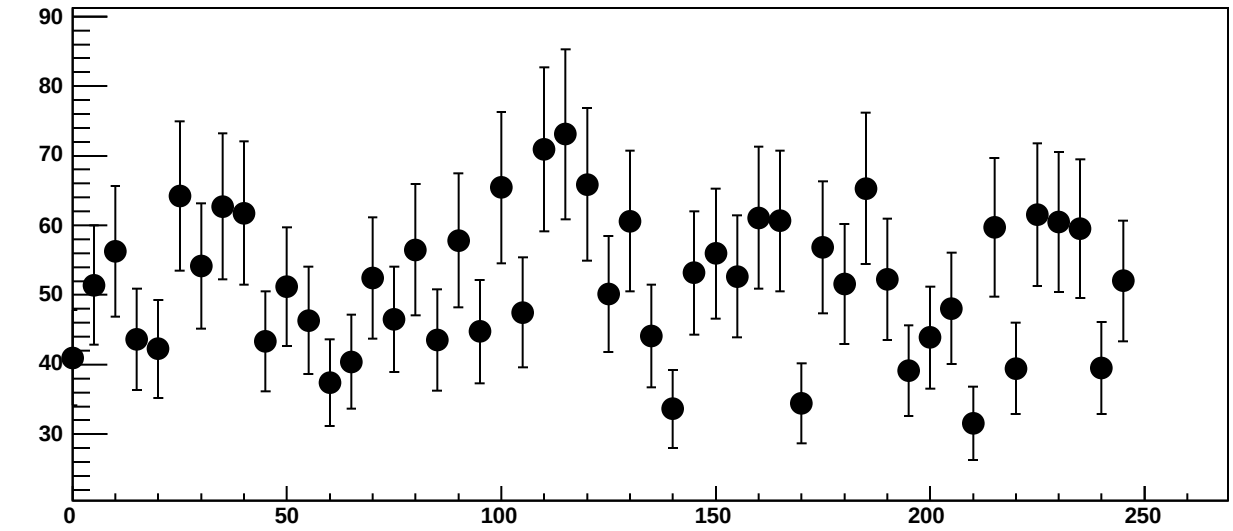
Chip 4, Channel 2, Enable 0!, DAC=1600, ADC Residuals vs Hold



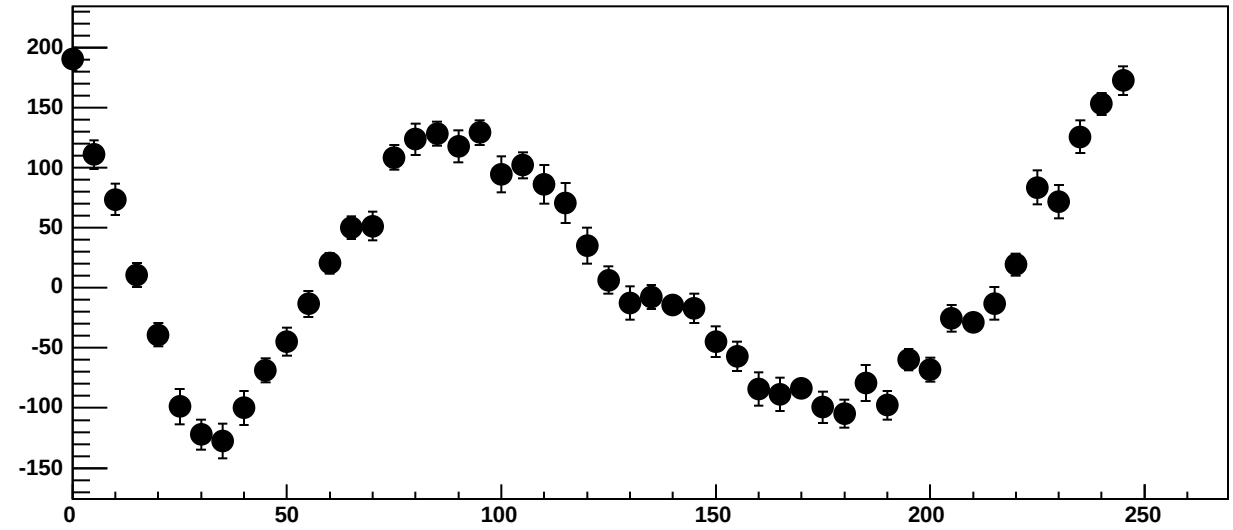
Chip 4, Channel 2, Enable 1, DAC=1600, ADC Mean vs Hold



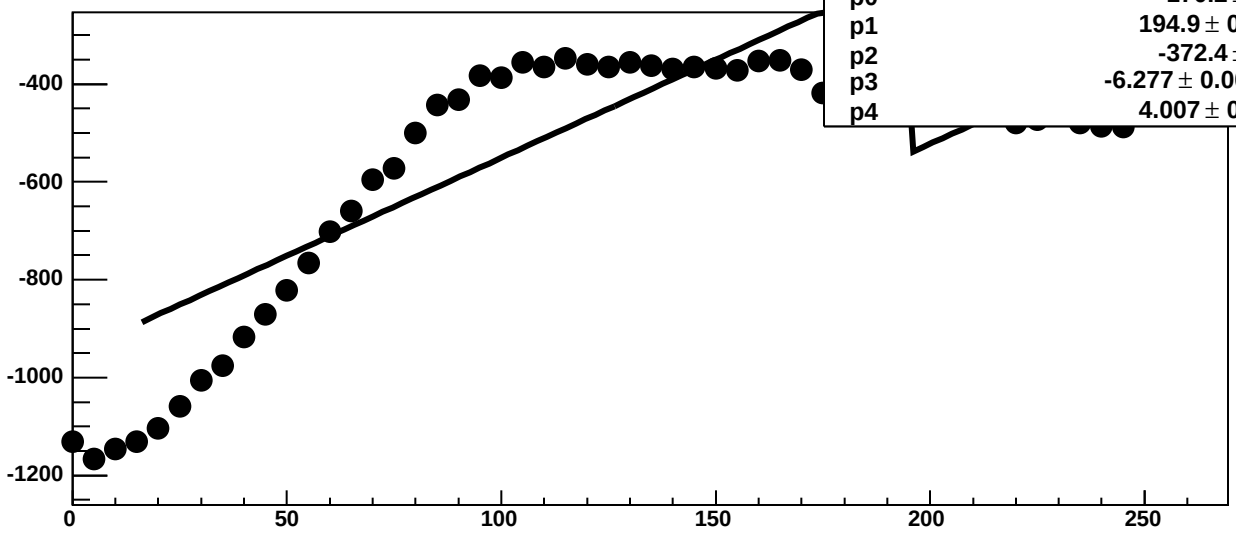
Chip 4, Channel 2, Enable 1, DAC=1600, ADC Noise vs Hold



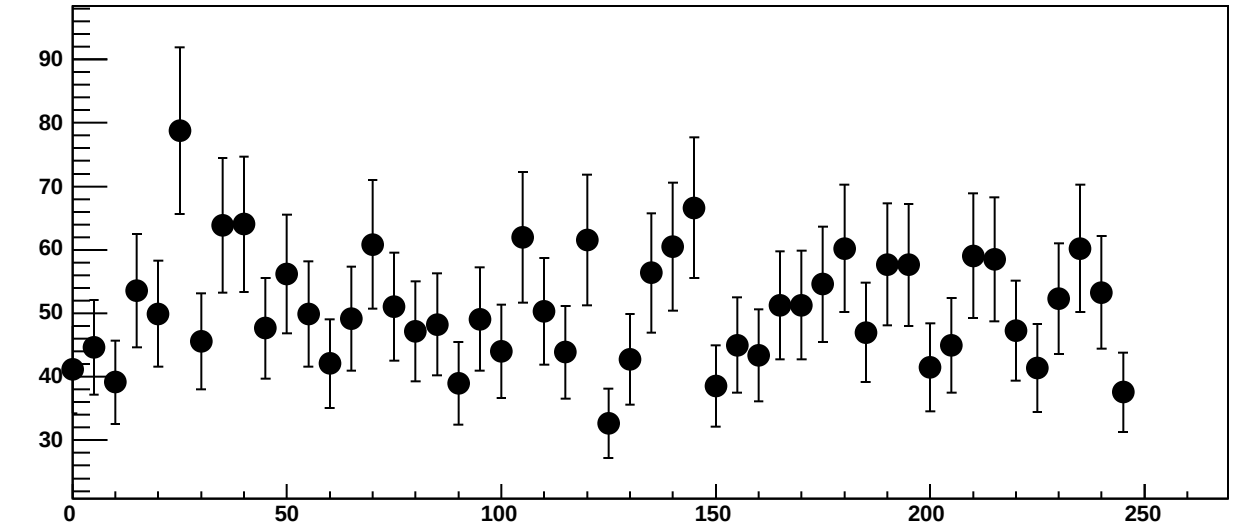
Chip 4, Channel 2, Enable 1, DAC=1600, ADC Residuals vs Hold



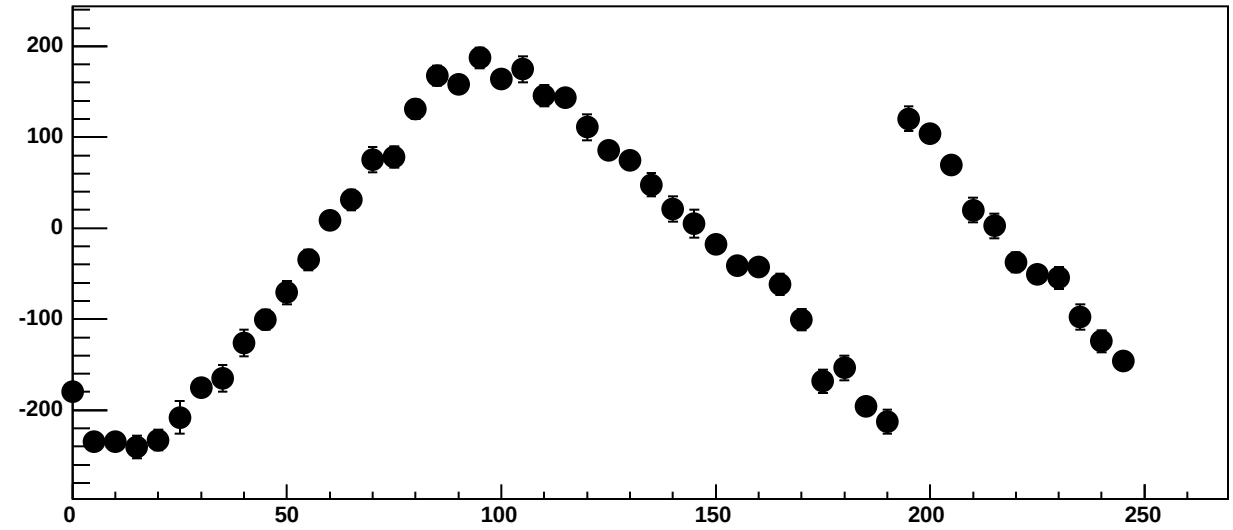
Chip 4, Channel 2, Enable 2, DAC=1600, ADC Mean vs Hold



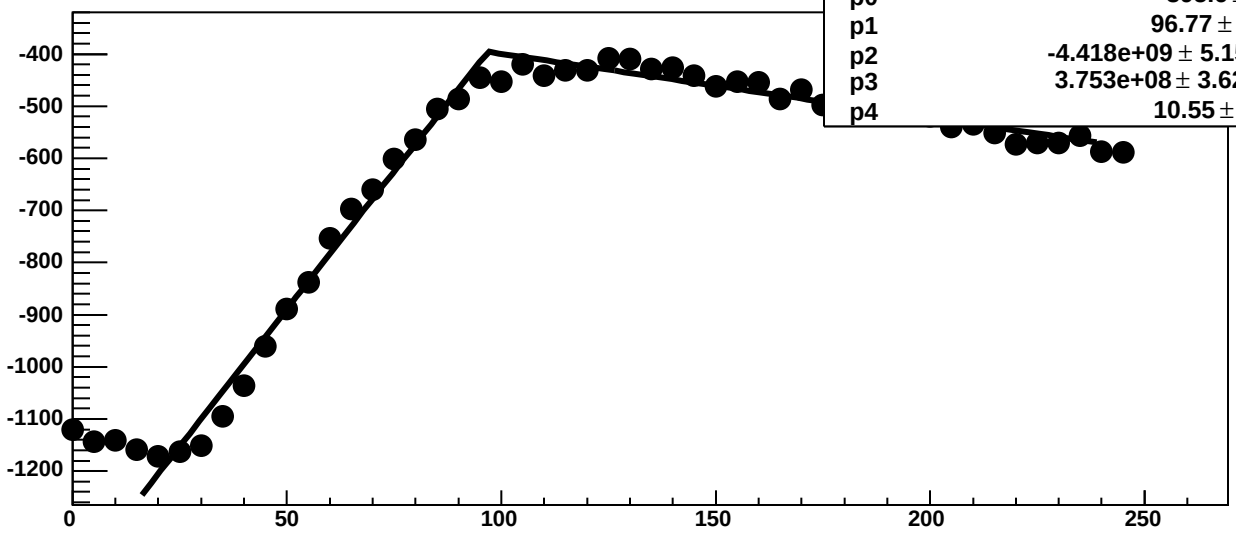
Chip 4, Channel 2, Enable 2, DAC=1600, ADC Noise vs Hold



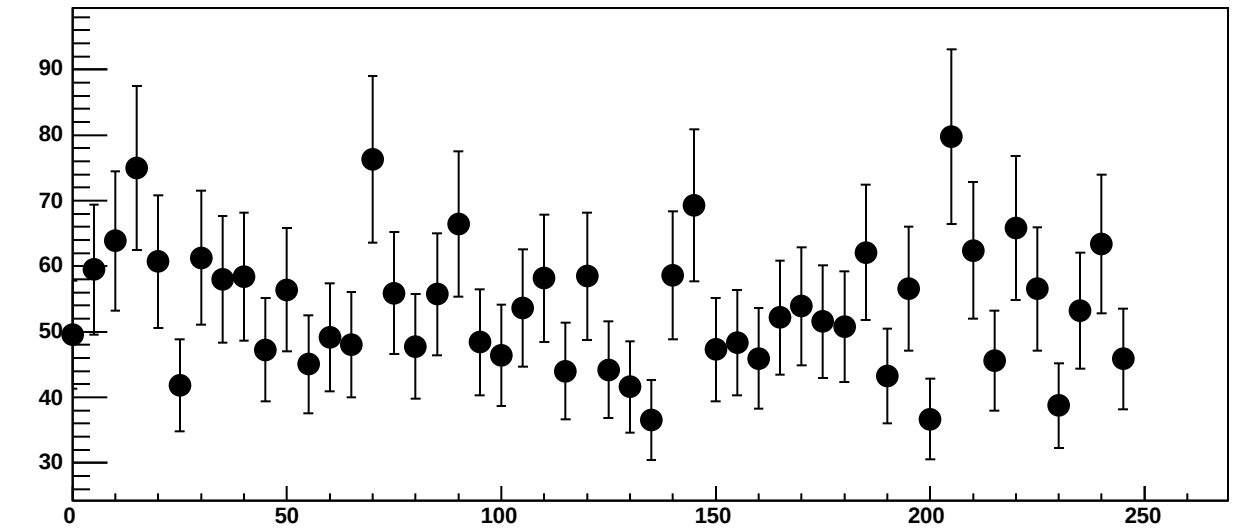
Chip 4, Channel 2, Enable 2, DAC=1600, ADC Residuals vs Hold



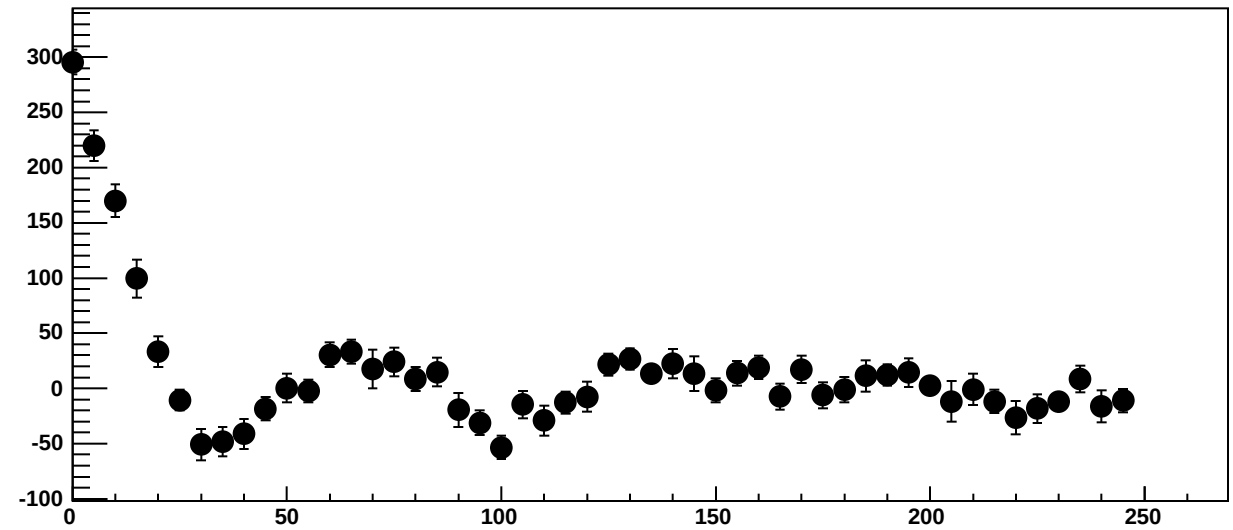
Chip 4, Channel 2, Enable 3, DAC=1600, ADC Mean vs Hold



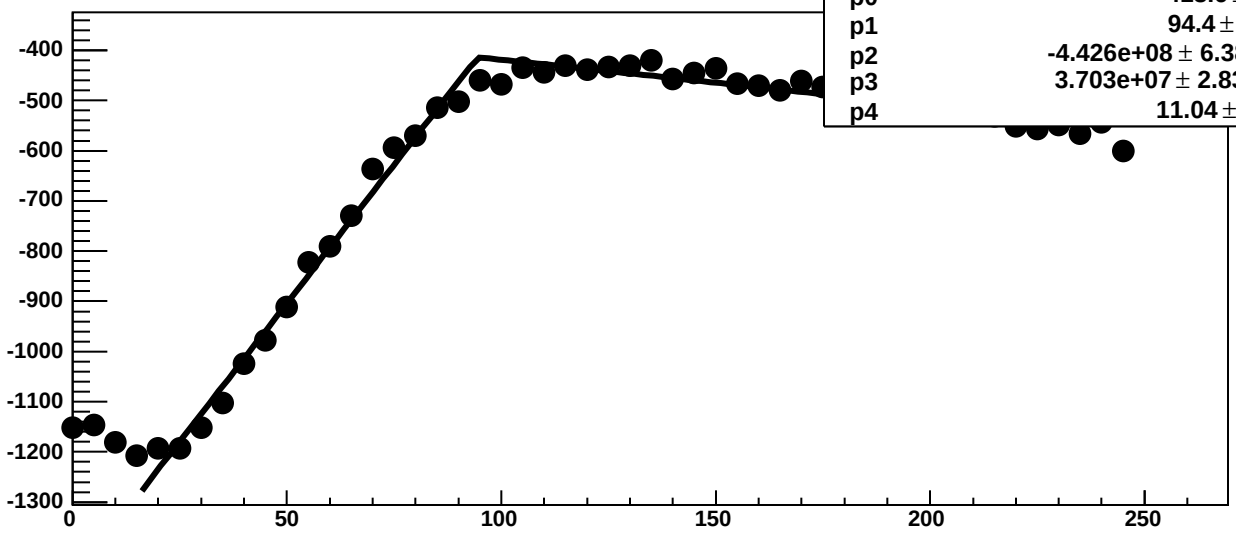
Chip 4, Channel 2, Enable 3, DAC=1600, ADC Noise vs Hold



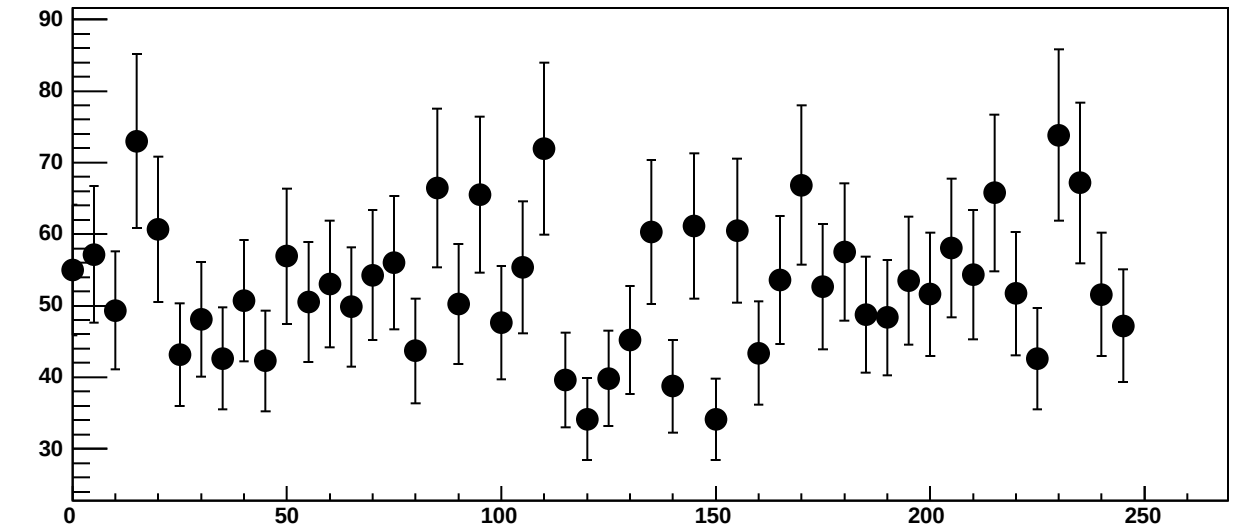
Chip 4, Channel 2, Enable 3, DAC=1600, ADC Residuals vs Hold



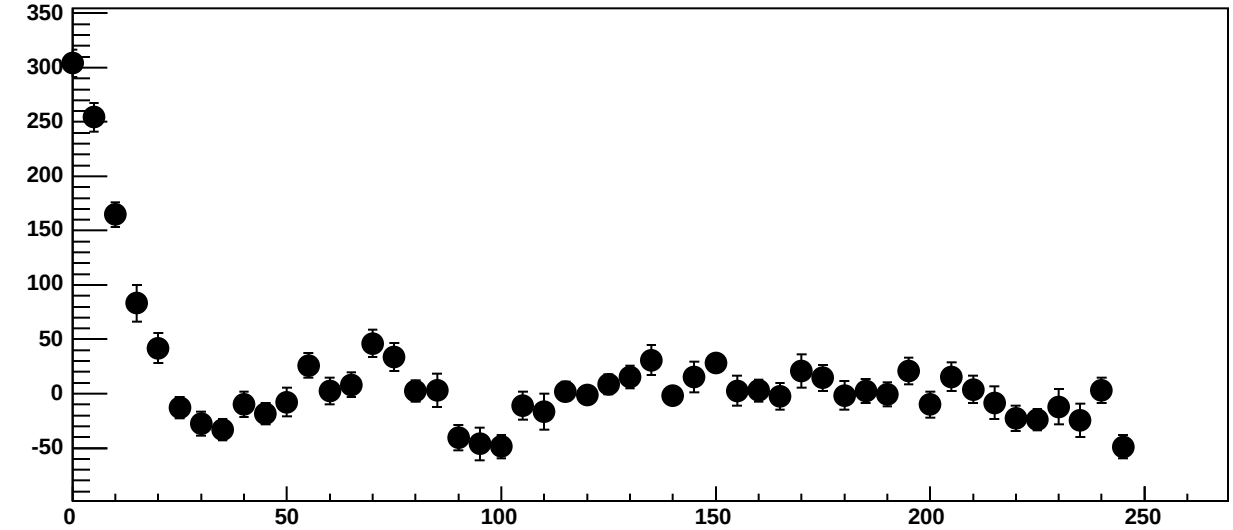
Chip 4, Channel 2, Enable 4, DAC=1600, ADC Mean vs Hold

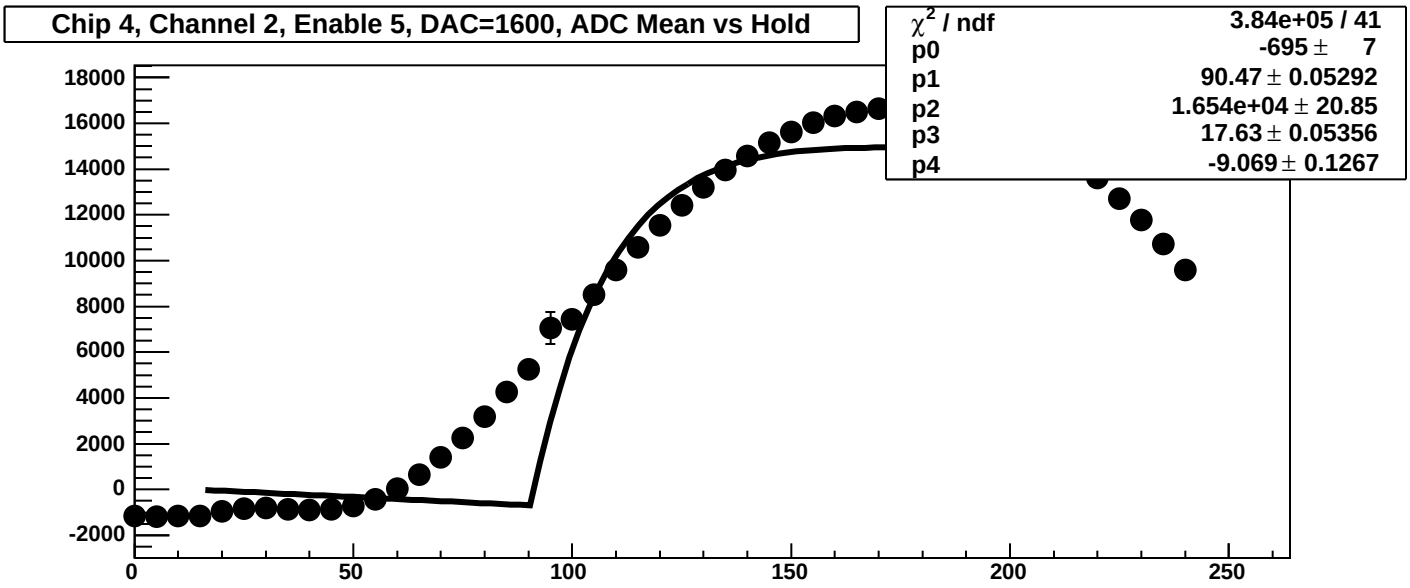


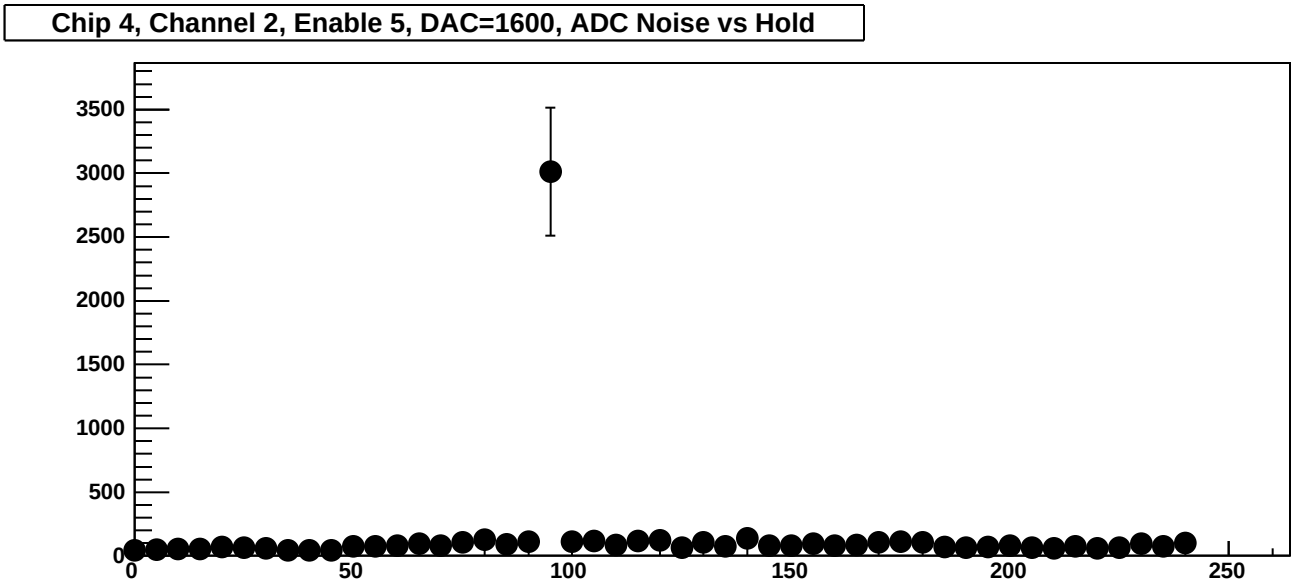
Chip 4, Channel 2, Enable 4, DAC=1600, ADC Noise vs Hold

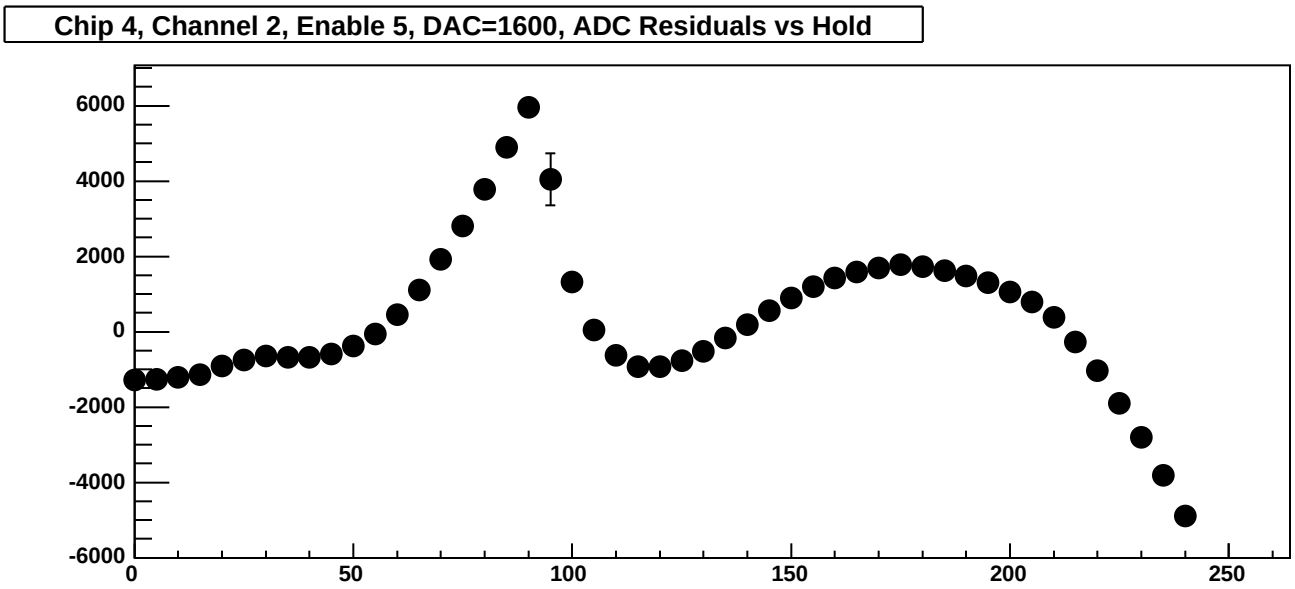


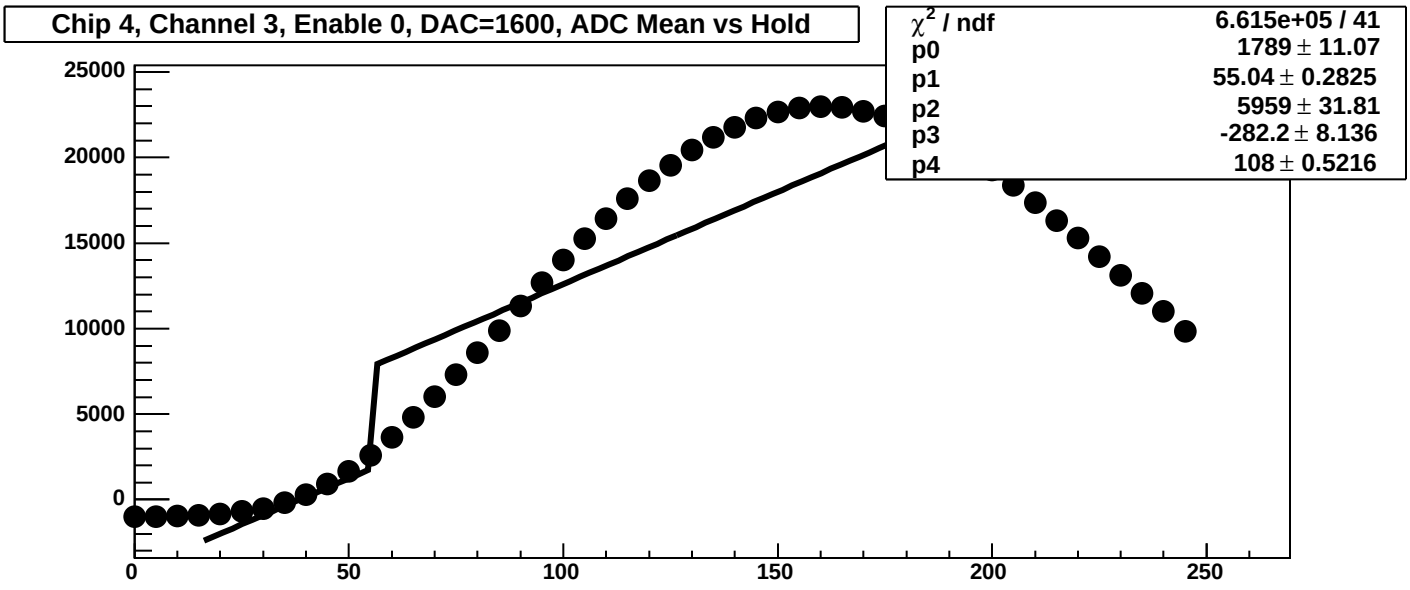
Chip 4, Channel 2, Enable 4, DAC=1600, ADC Residuals vs Hold

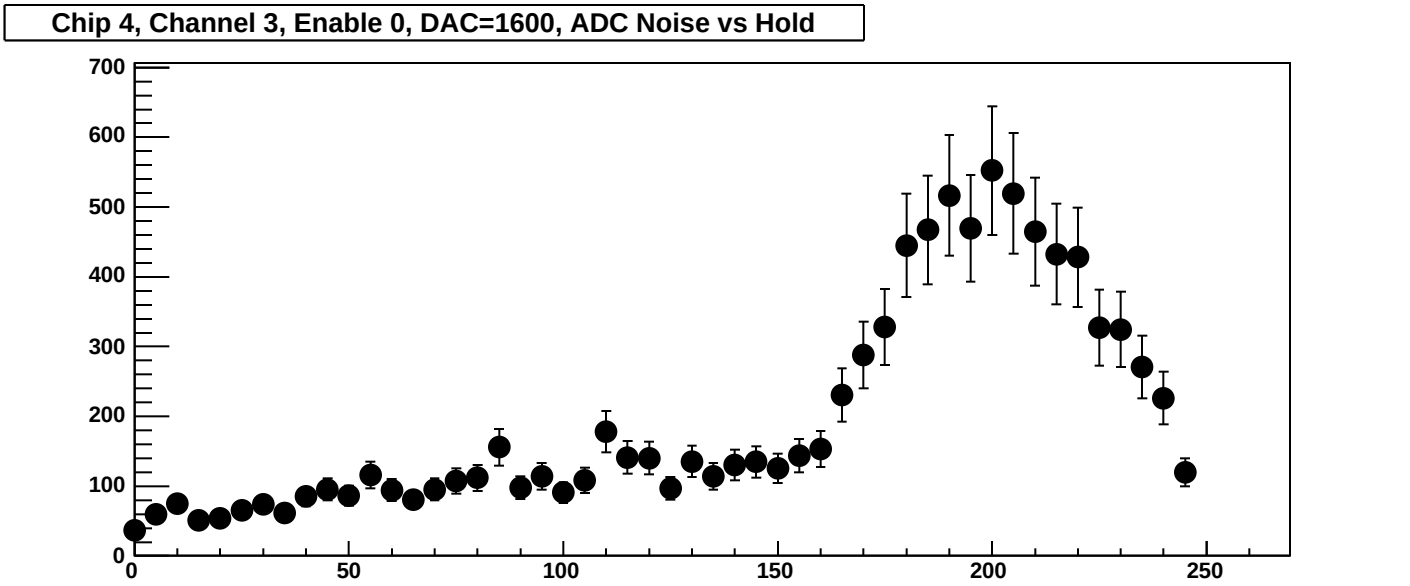


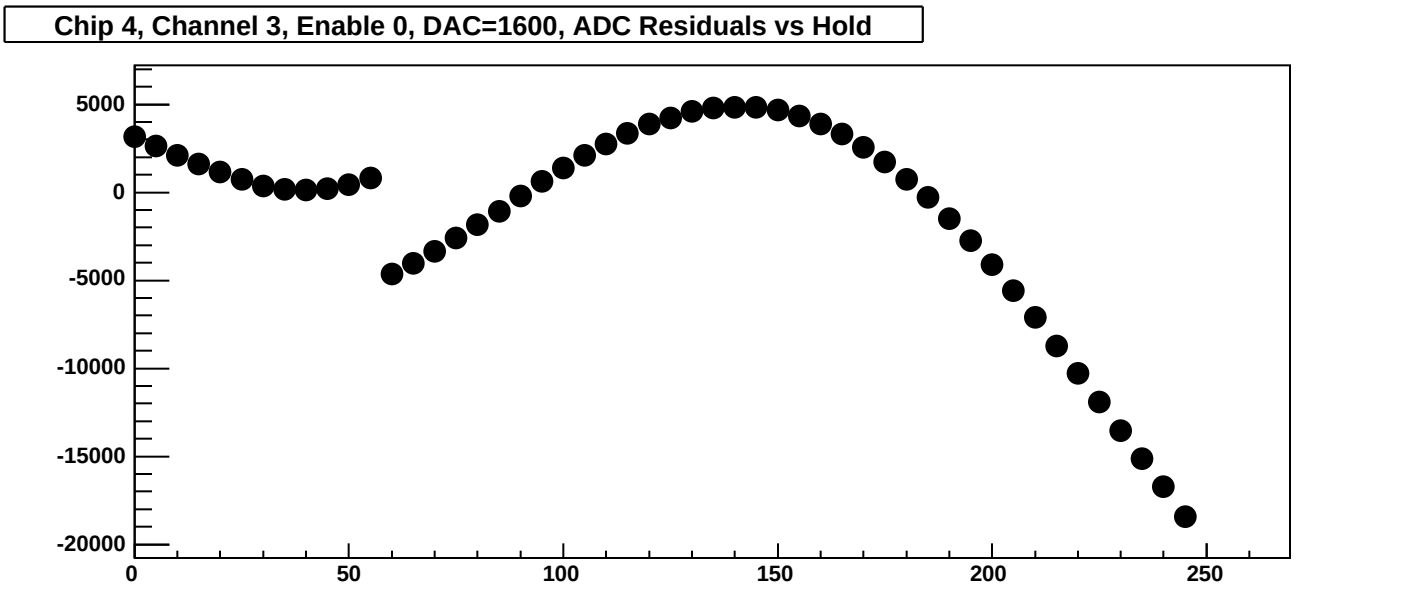

Chip 4, Channel 2, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.84e+05 / 41
p0 -695 ± 7
p1 90.47 ± 0.05292
p2 1.654e+04 ± 20.85
p3 17.63 ± 0.05356
p4 -9.069 ± 0.1267
18000
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250


Chip 4, Channel 2, Enable 5, DAC=1600, ADC Noise vs Hold
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250

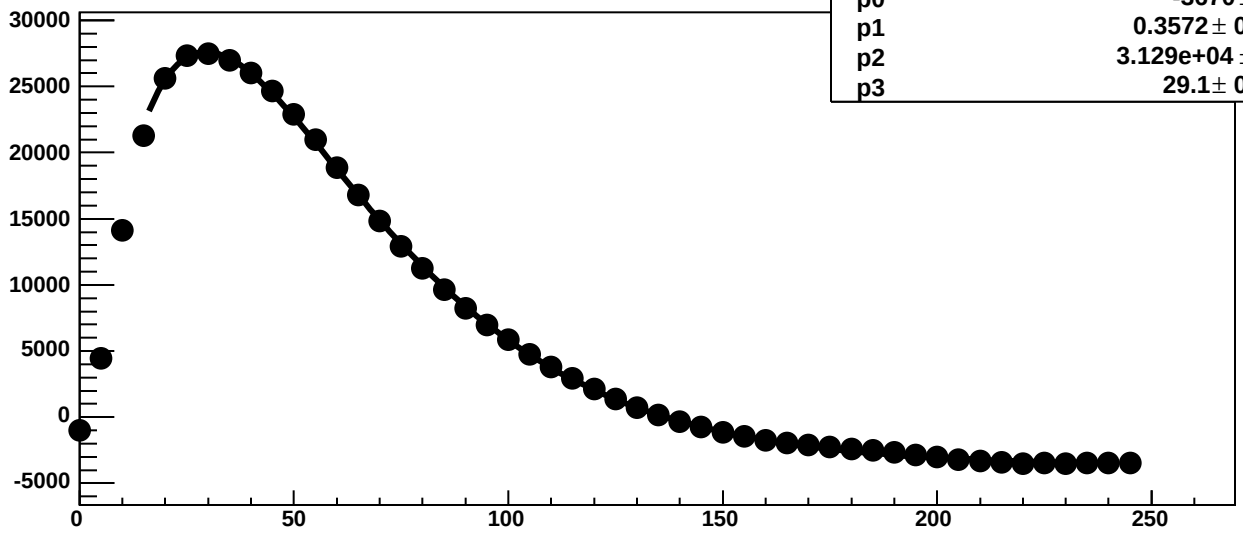

Chip 4, Channel 2, Enable 5, DAC=1600, ADC Residuals vs Hold
6000
4000
2000
0
-2000
-4000
-6000
0
50
100
150
200
250


Chip 4, Channel 3, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 6.615e+05 / 41
p0 1789 ± 11.07
p1 55.04 ± 0.2825
p2 5959 ± 31.81
p3 -282.2 ± 8.136
p4 108 ± 0.5216
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

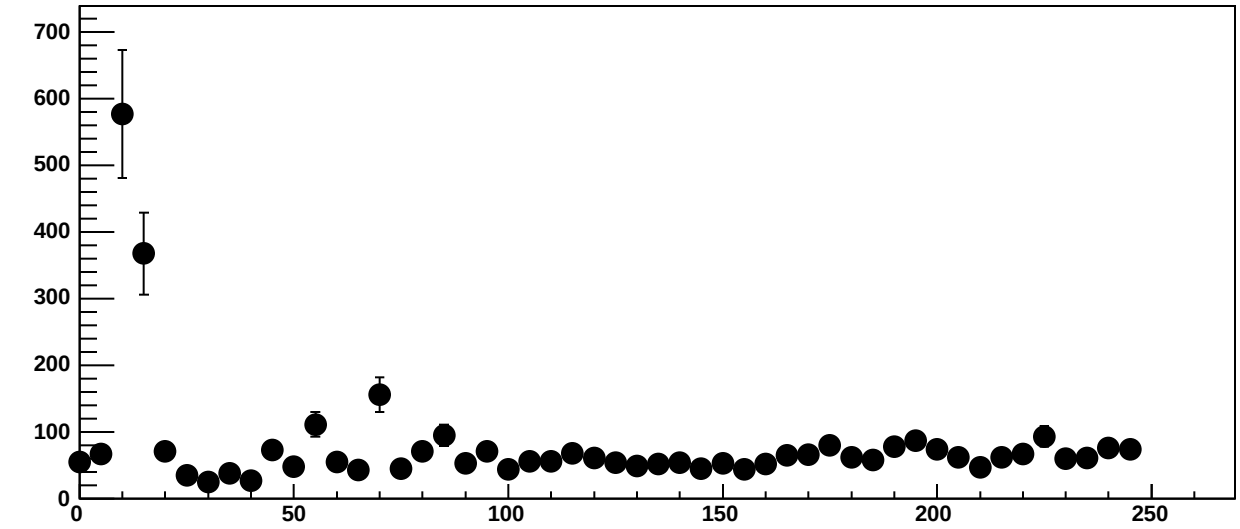

Chip 4, Channel 3, Enable 0, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 4, Channel 3, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

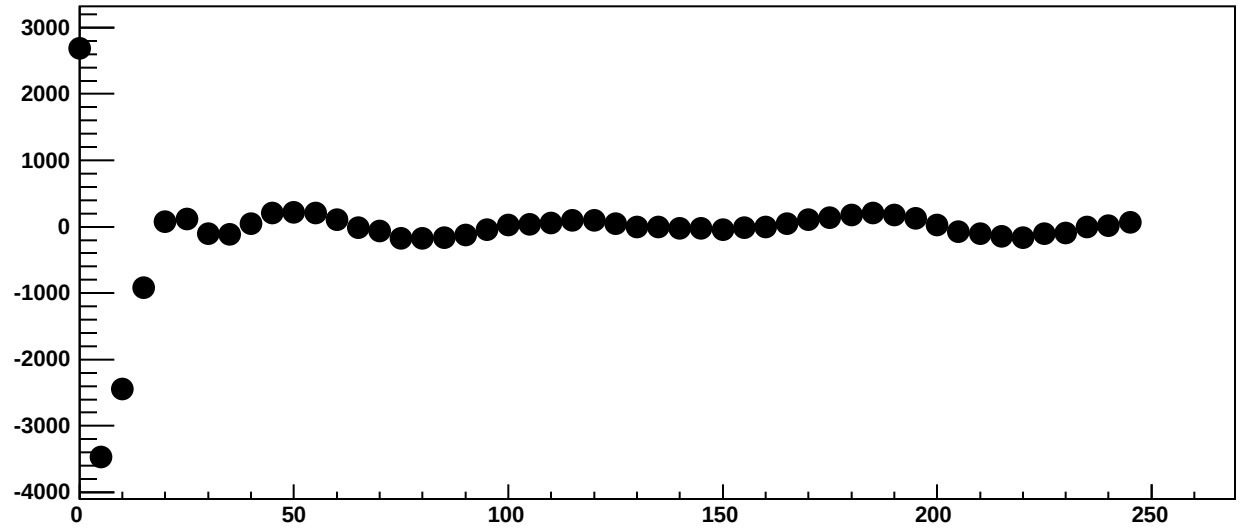
Chip 4, Channel 3, Enable 1!, DAC=1600, ADC Mean vs Hold



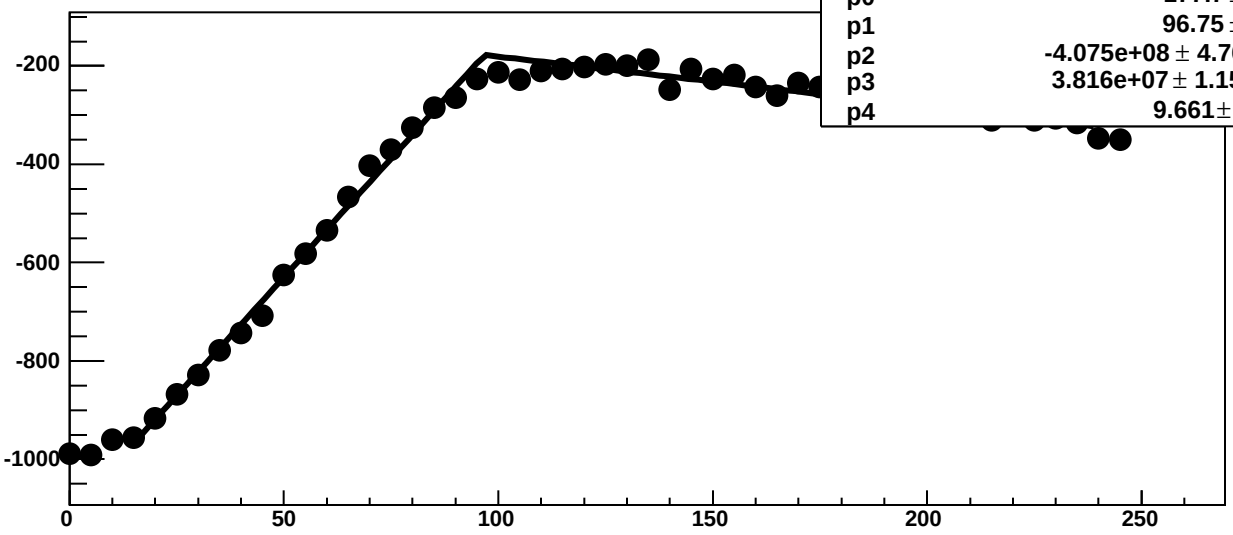
Chip 4, Channel 3, Enable 1!, DAC=1600, ADC Noise vs Hold



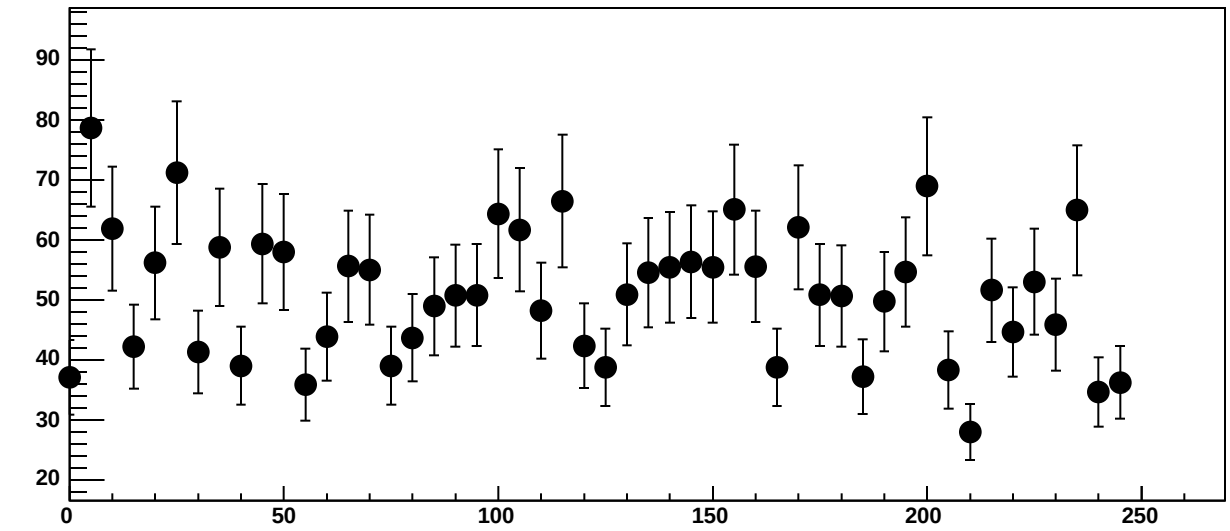
Chip 4, Channel 3, Enable 1!, DAC=1600, ADC Residuals vs Hold



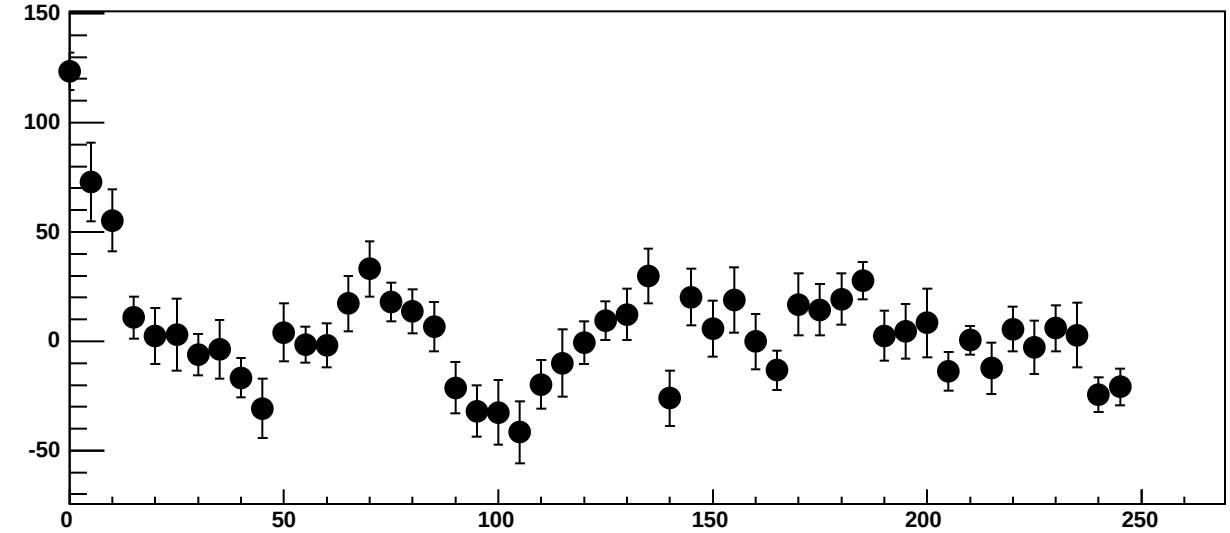
Chip 4, Channel 3, Enable 2, DAC=1600, ADC Mean vs Hold



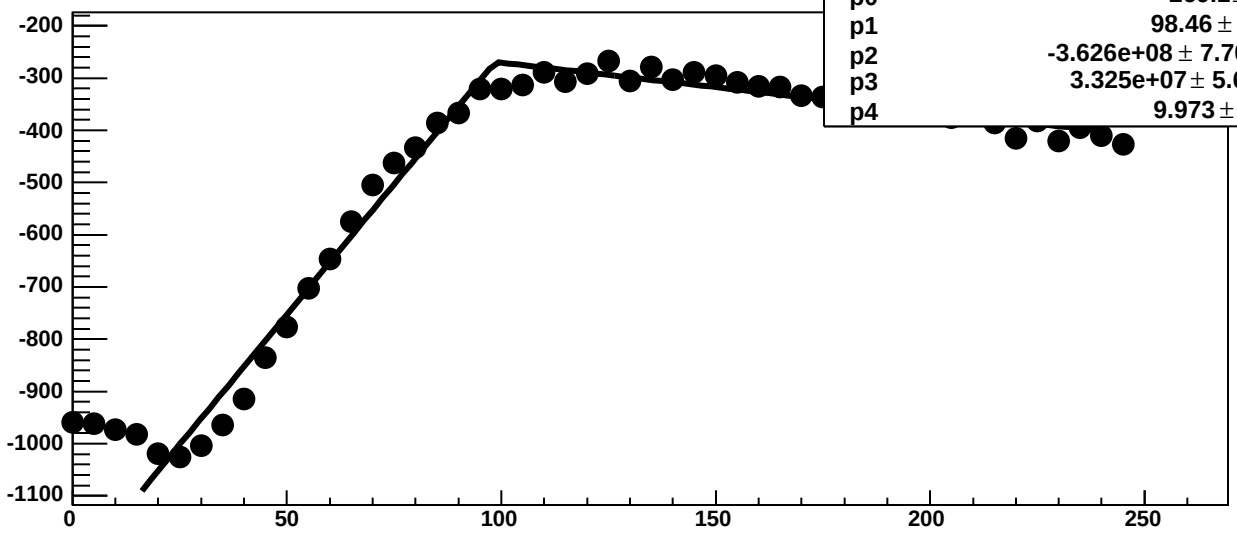
Chip 4, Channel 3, Enable 2, DAC=1600, ADC Noise vs Hold



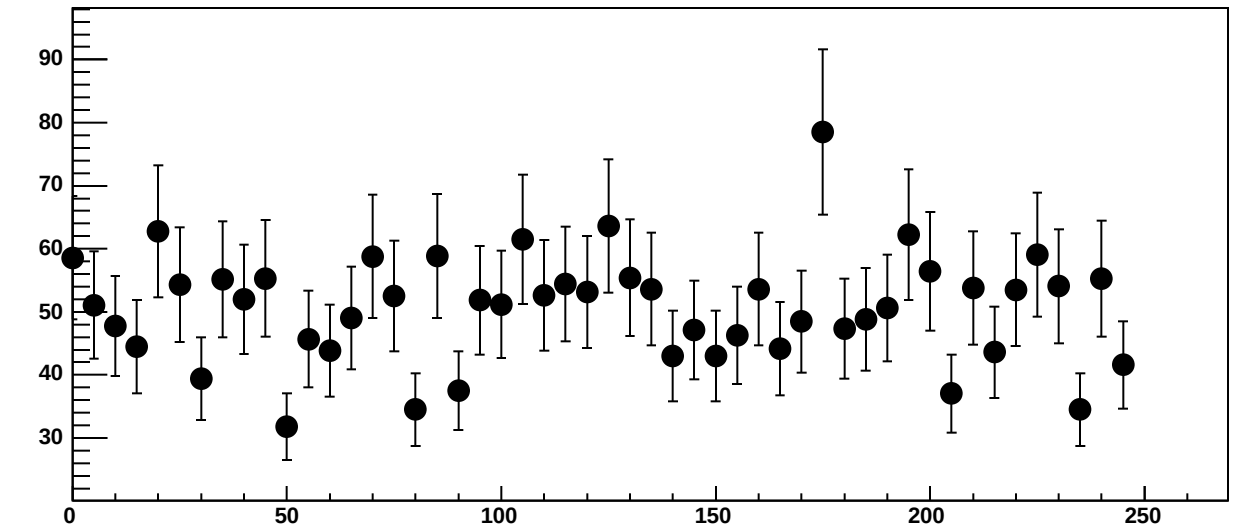
Chip 4, Channel 3, Enable 2, DAC=1600, ADC Residuals vs Hold



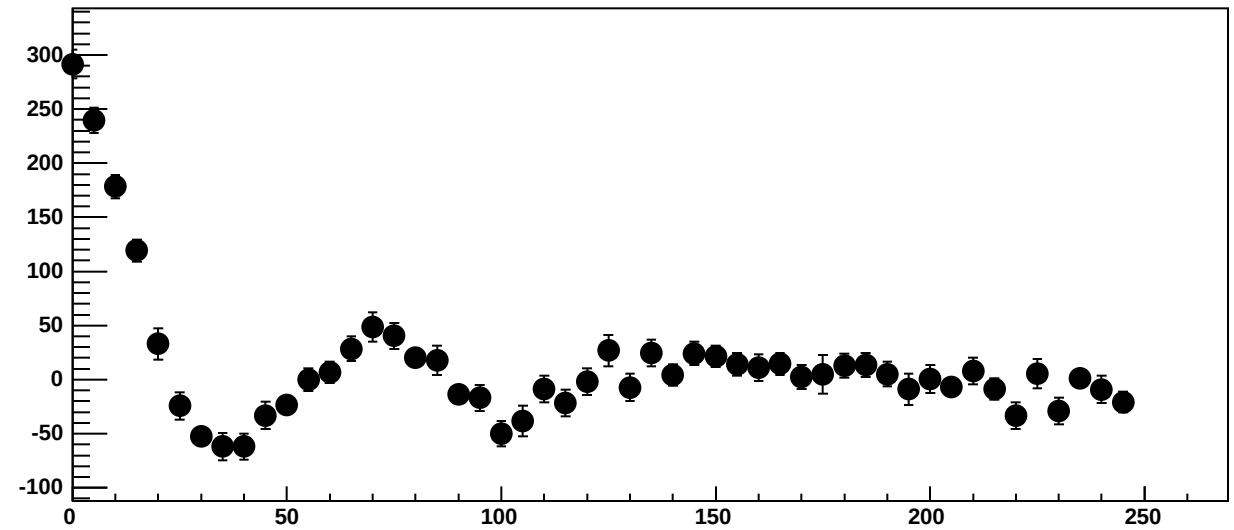
Chip 4, Channel 3, Enable 3, DAC=1600, ADC Mean vs Hold

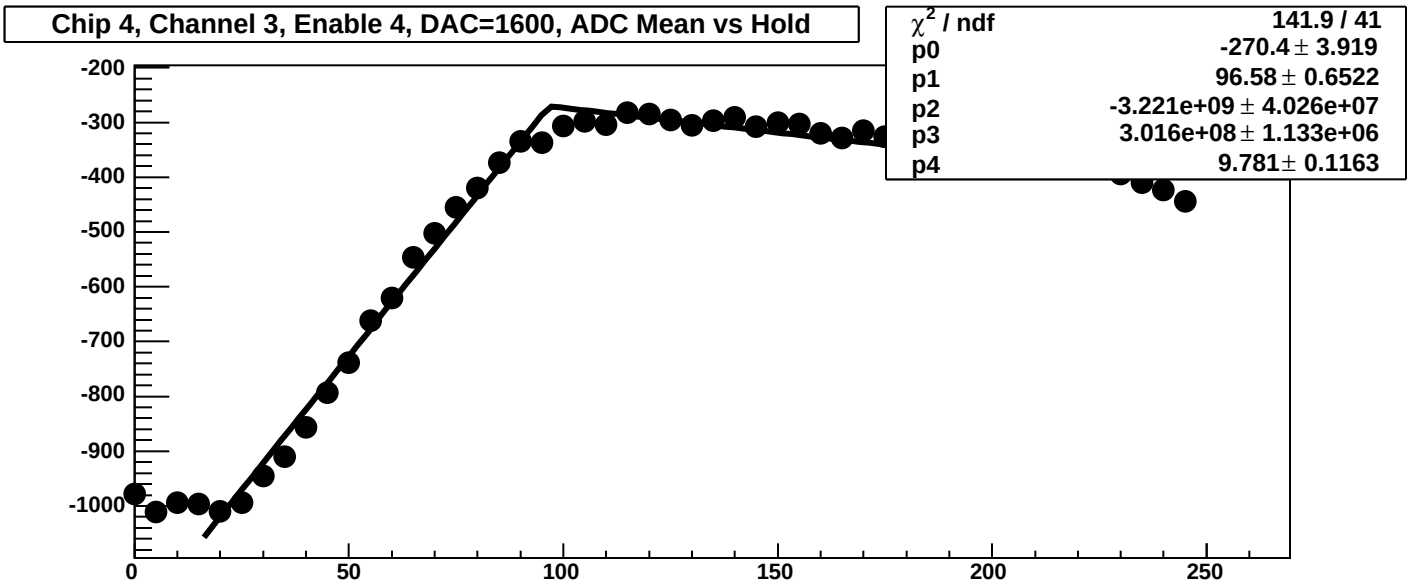


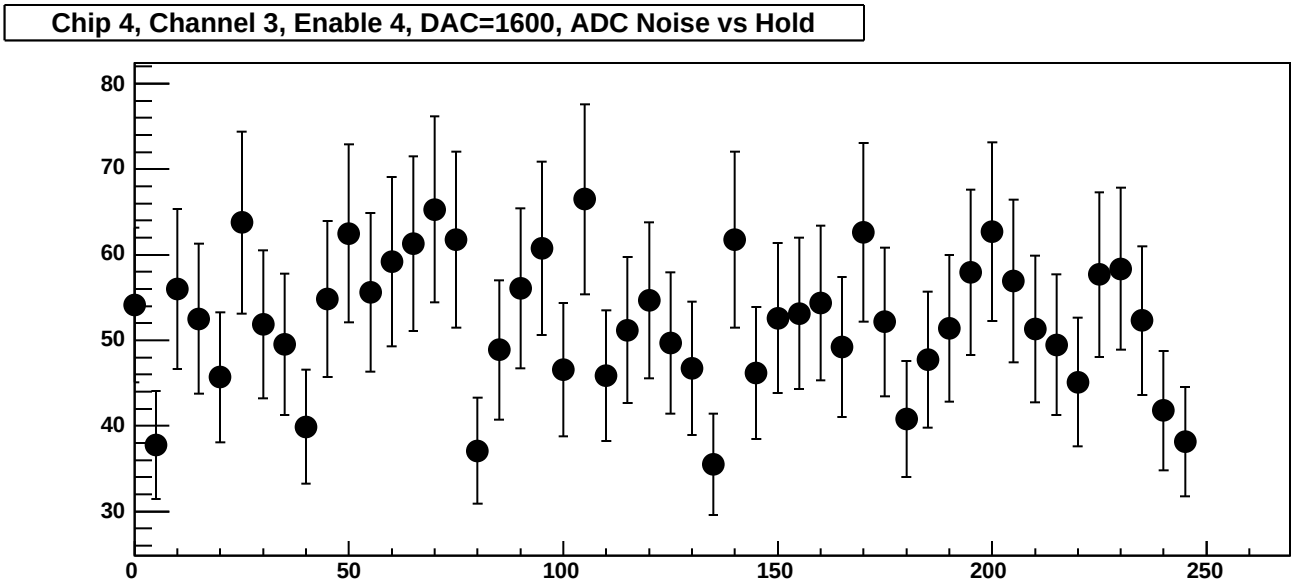
Chip 4, Channel 3, Enable 3, DAC=1600, ADC Noise vs Hold

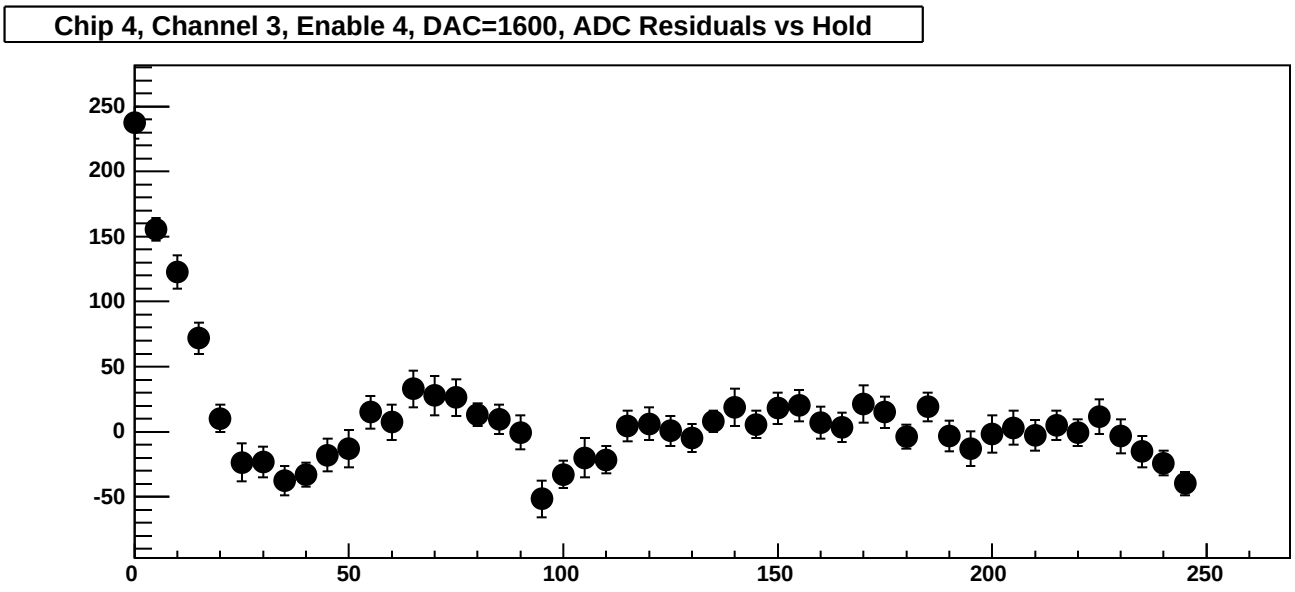


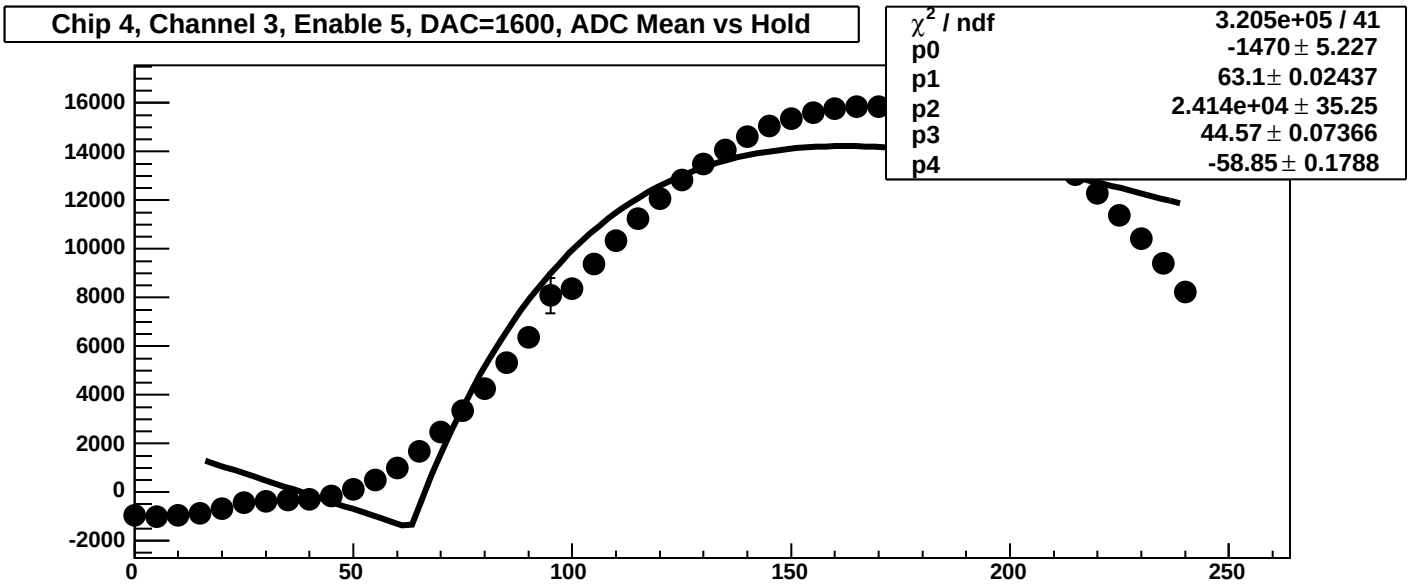
Chip 4, Channel 3, Enable 3, DAC=1600, ADC Residuals vs Hold

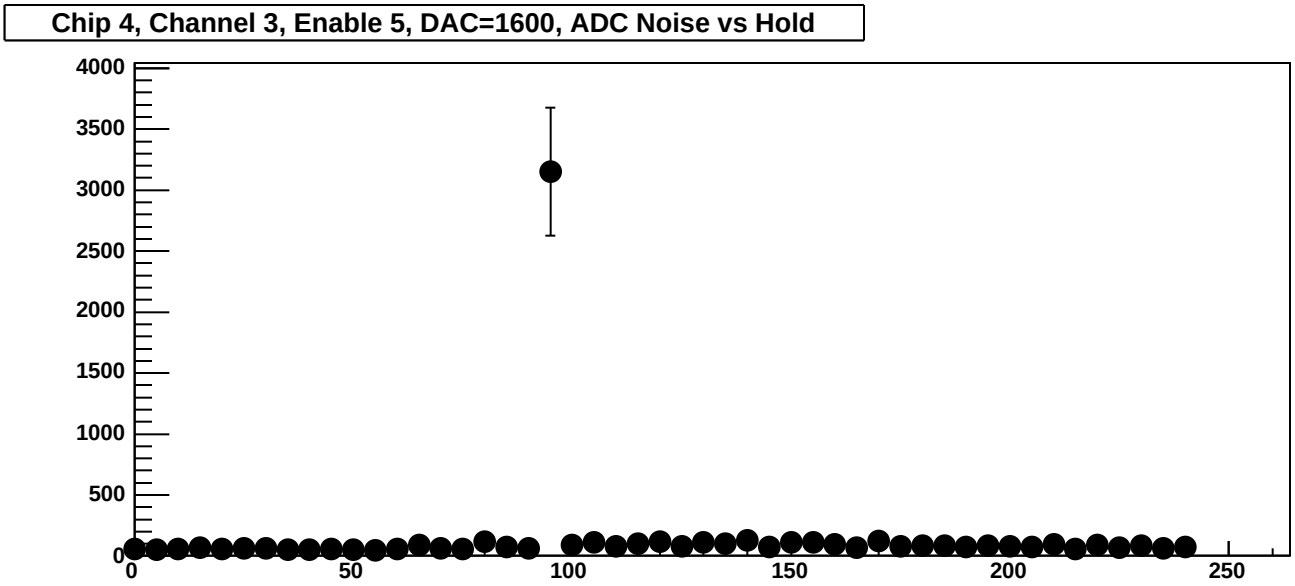


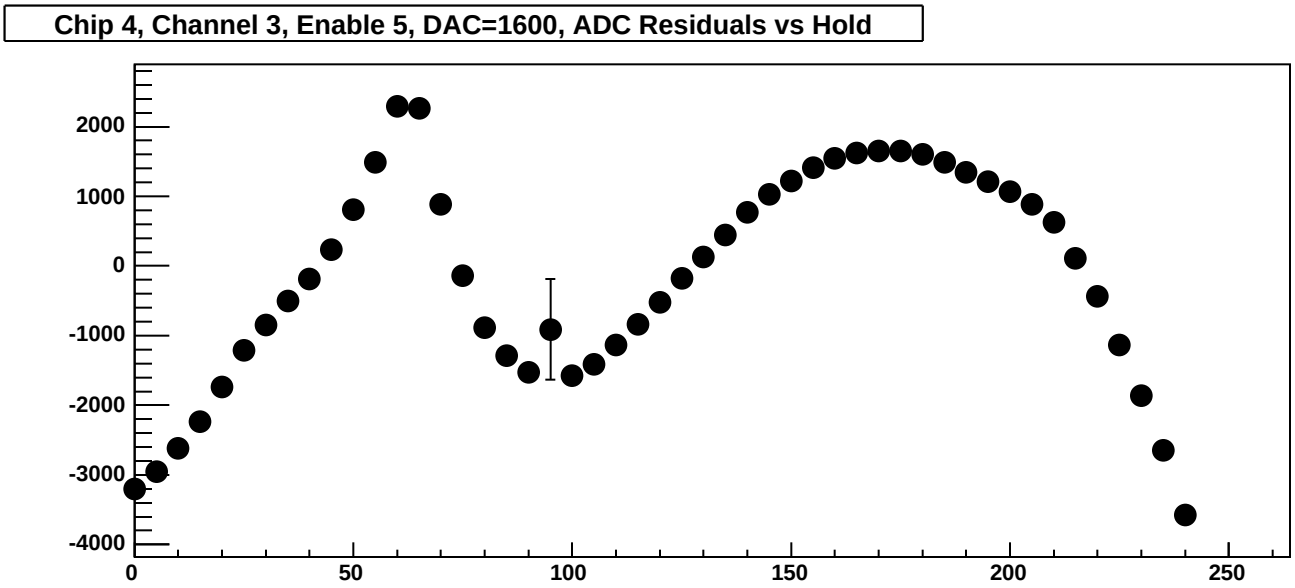

Chip 4, Channel 3, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 141.9 / 41
p0 -270.4 ± 3.919
p1 96.58 ± 0.6522
p2 -3.221e+09 ± 4.026e+07
p3 3.016e+08 ± 1.133e+06
p4 9.781 ± 0.1163
-200
-300
-400
-500
-600
-700
-800
-900
-1000
0
50
100
150
200
250


Chip 4, Channel 3, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

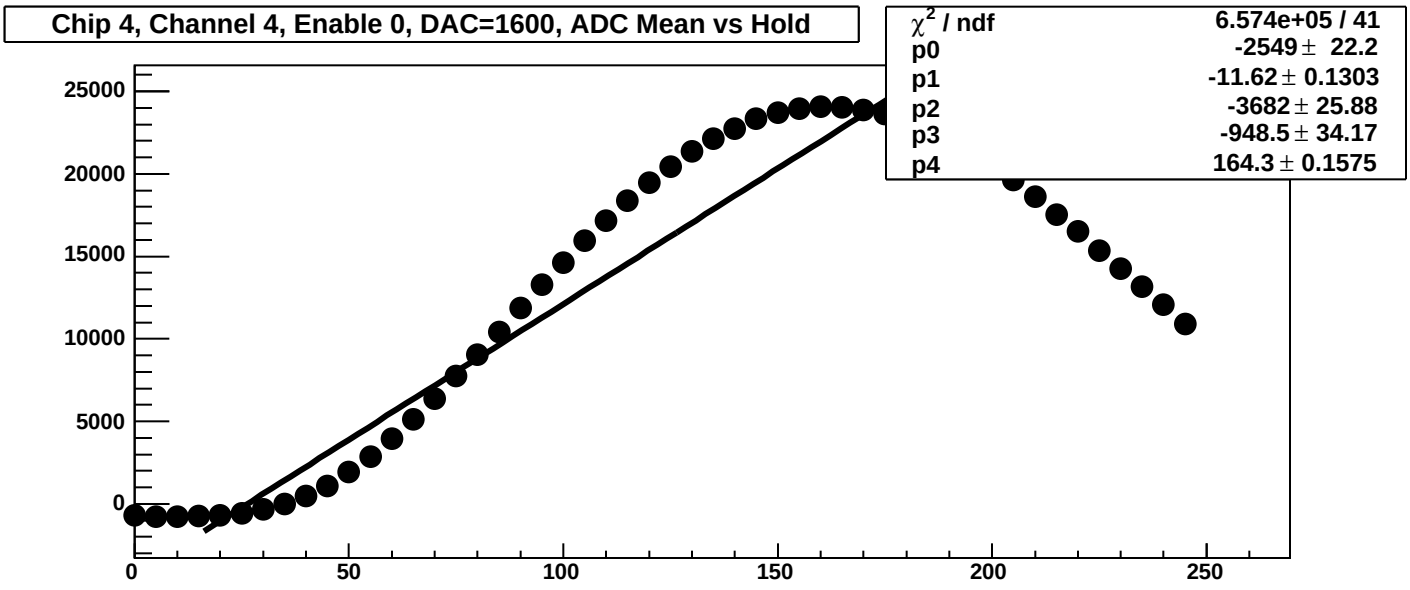

Chip 4, Channel 3, Enable 4, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
0
50
100
150
200
250


Chip 4, Channel 3, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.205e+05 / 41
p0 -1470 ± 5.227
p1 63.1 ± 0.02437
p2 2.414e+04 ± 35.25
p3 44.57 ± 0.07366
p4 -58.85 ± 0.1788
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

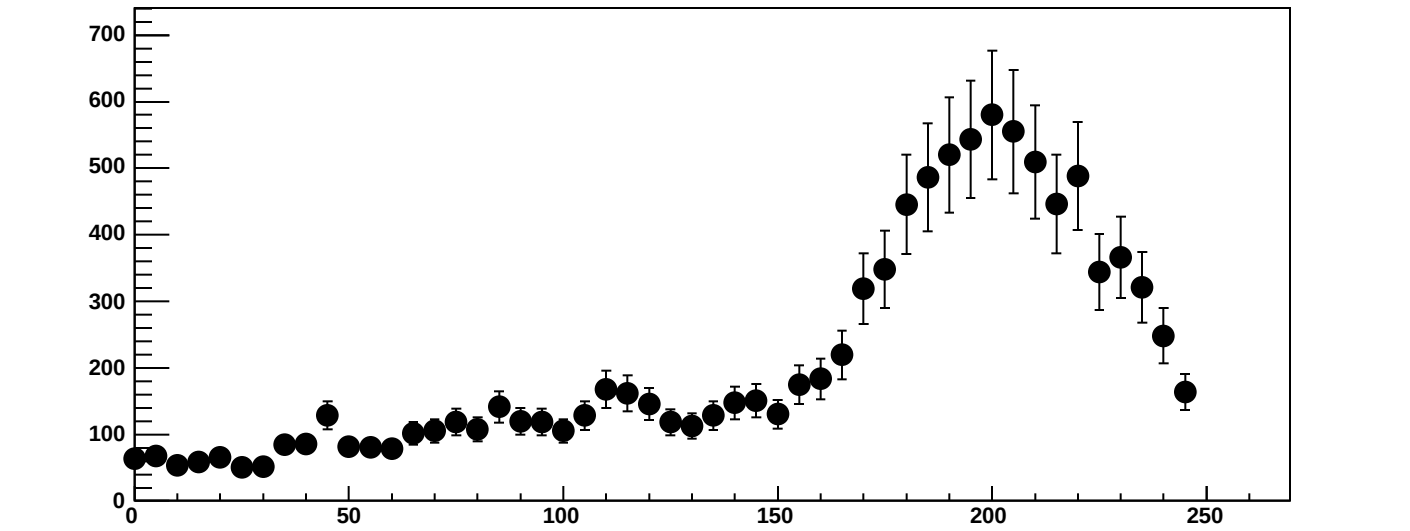

Chip 4, Channel 3, Enable 5, DAC=1600, ADC Noise vs Hold
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 4, Channel 3, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

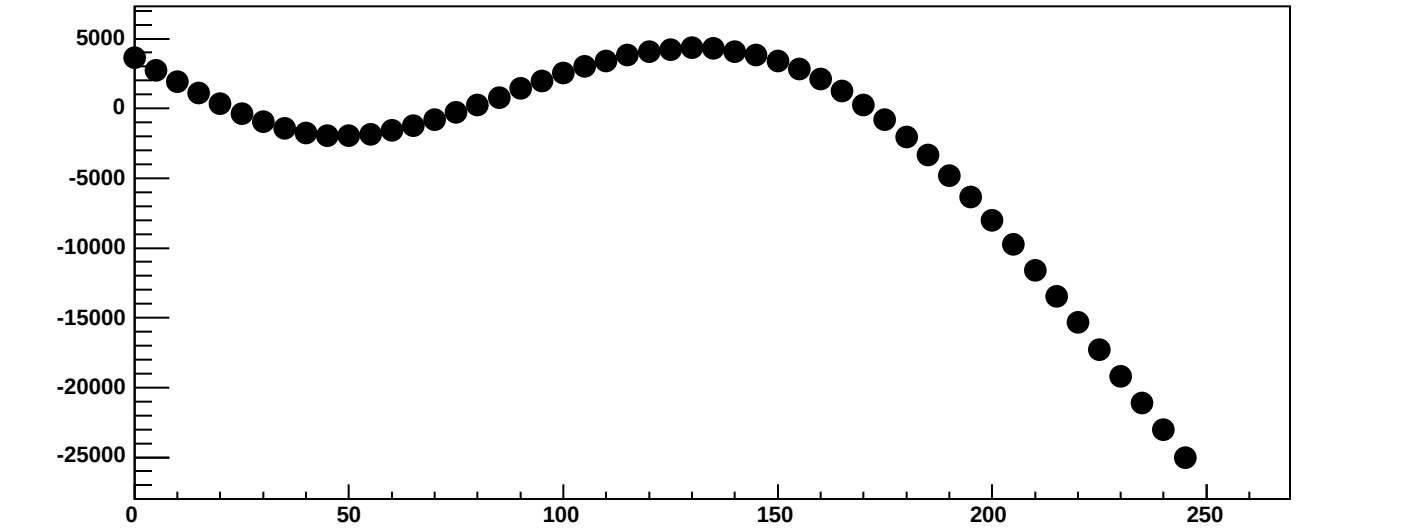
Chip 4, Channel 4, Enable 0, DAC=1600, ADC Mean vs Hold

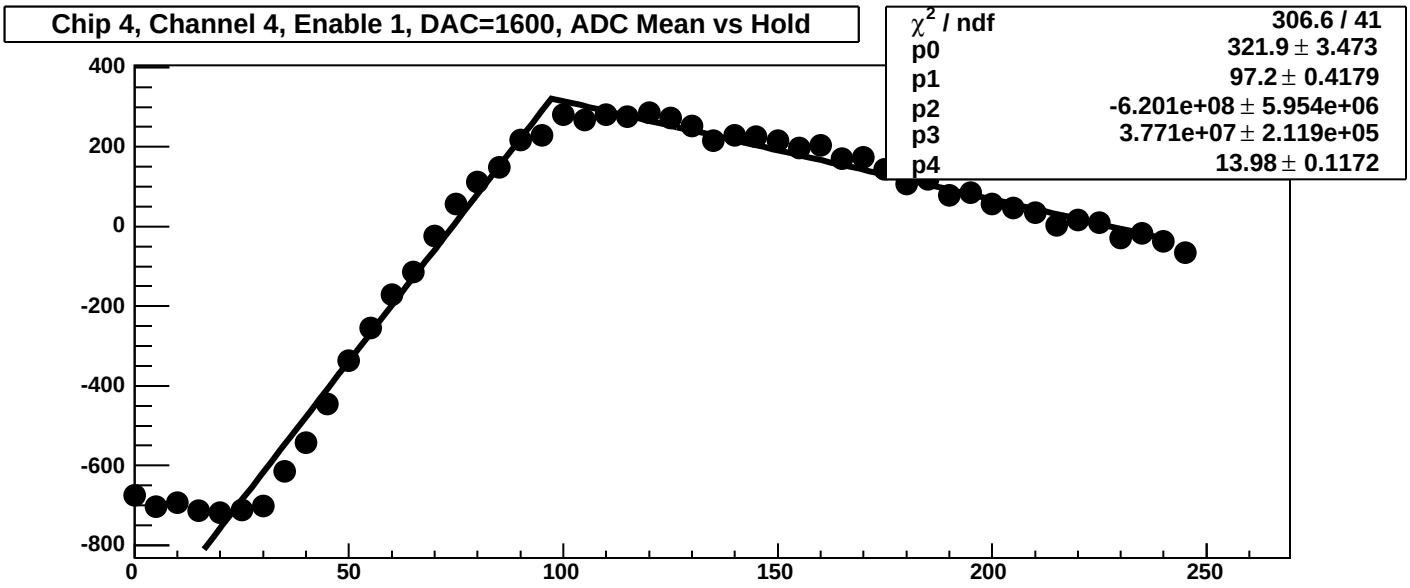


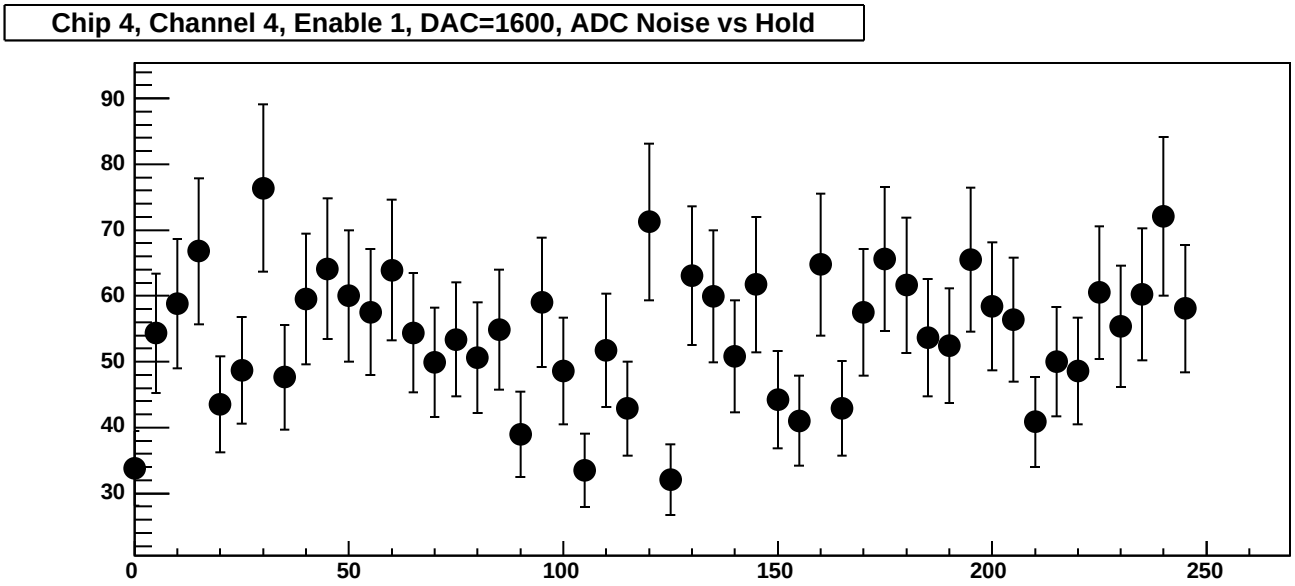
Chip 4, Channel 4, Enable 0, DAC=1600, ADC Noise vs Hold

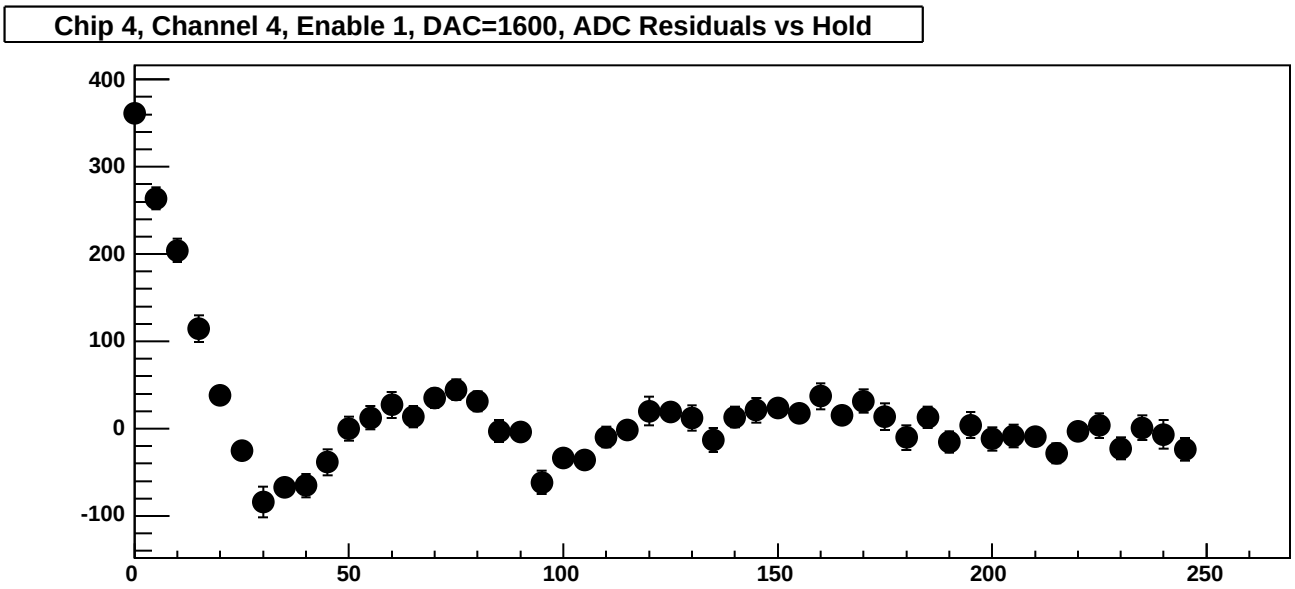


Chip 4, Channel 4, Enable 0, DAC=1600, ADC Residuals vs Hold

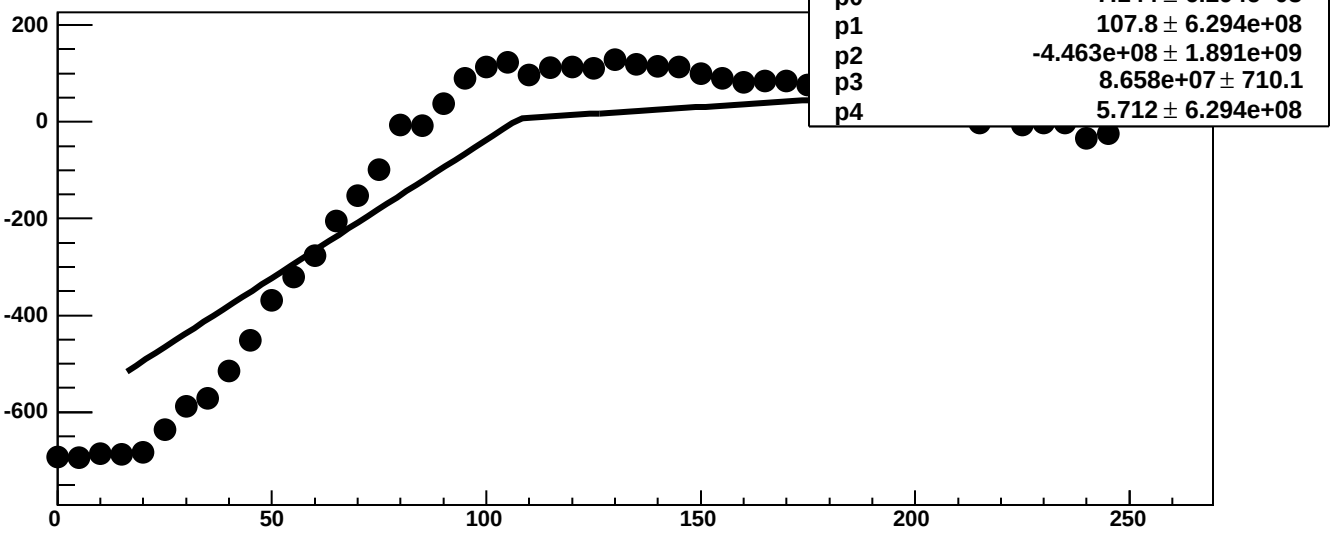



Chip 4, Channel 4, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 306.6 / 41
p0 321.9 ± 3.473
p1 97.2 ± 0.4179
p2 -6.201e+08 ± 5.954e+06
p3 3.771e+07 ± 2.119e+05
p4 13.98 ± 0.1172
400
200
0
-200
-400
-600
-800
0
50
100
150
200
250

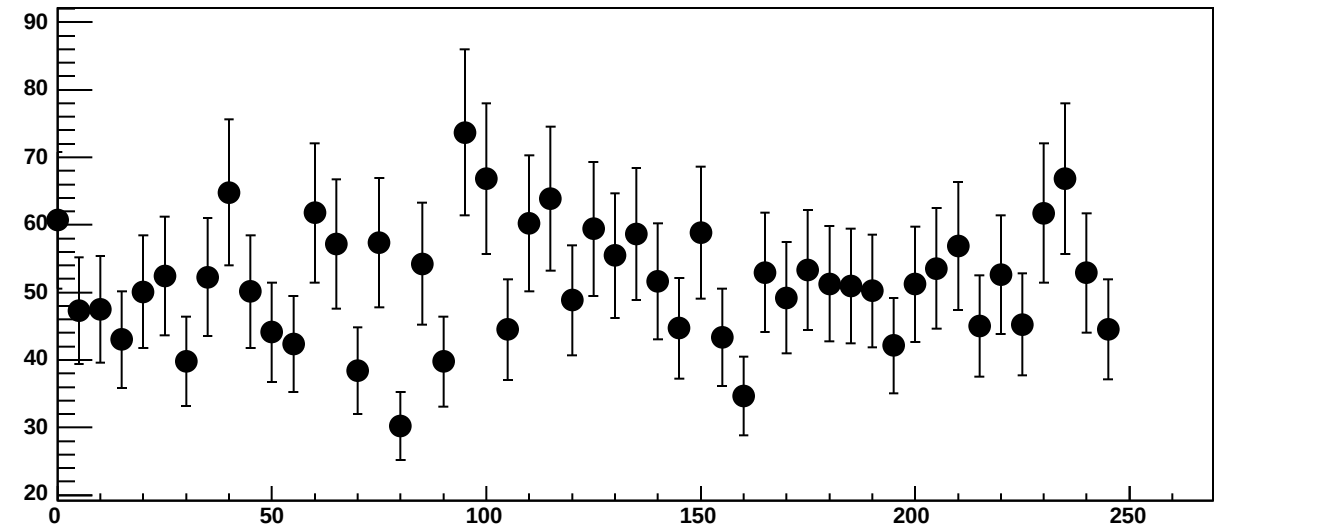

Chip 4, Channel 4, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 4, Channel 4, Enable 1, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

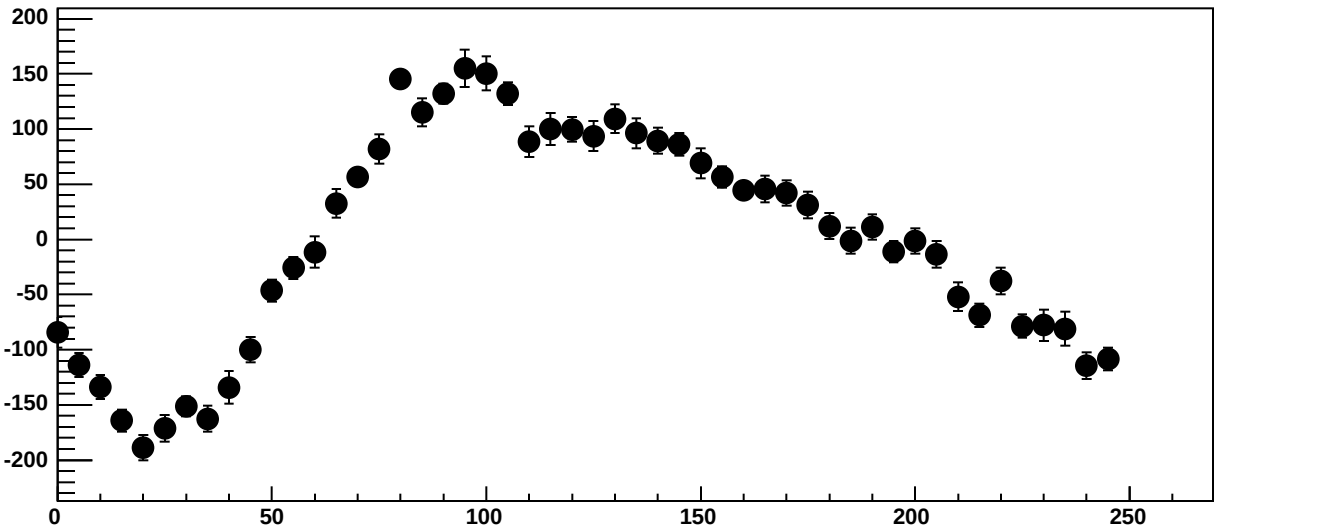
Chip 4, Channel 4, Enable 2, DAC=1600, ADC Mean vs Hold



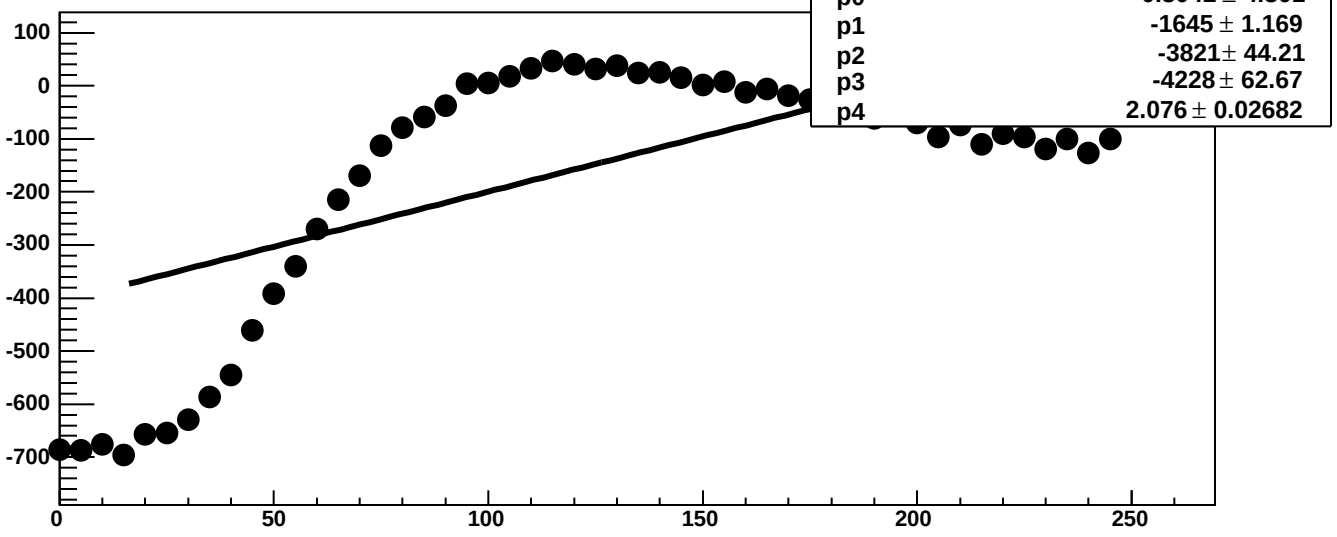
Chip 4, Channel 4, Enable 2, DAC=1600, ADC Noise vs Hold



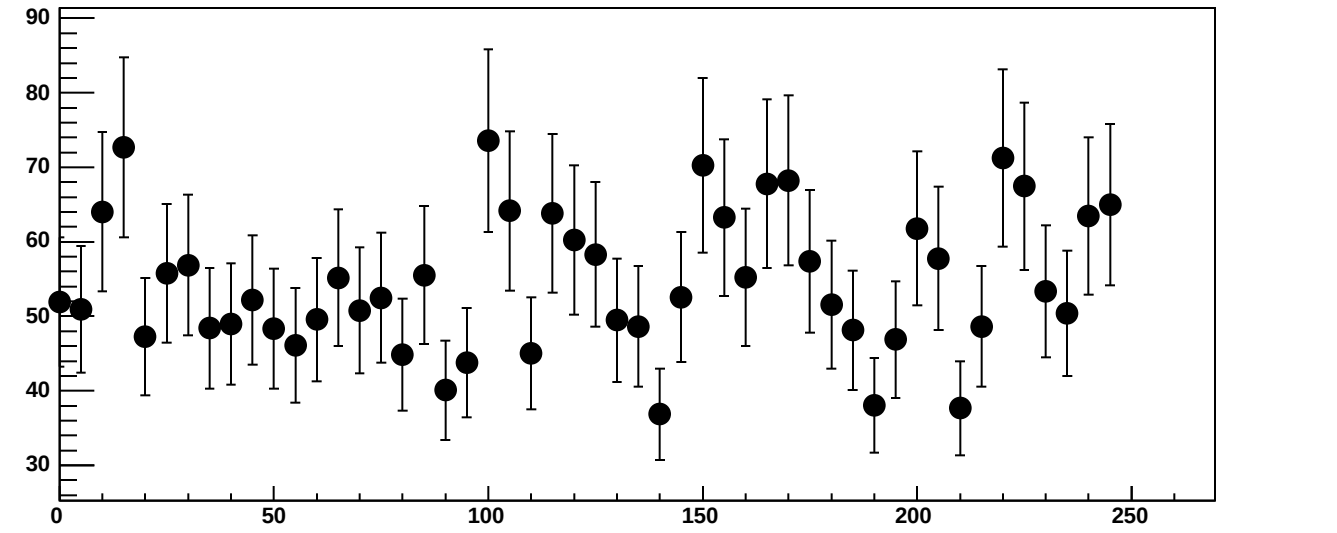
Chip 4, Channel 4, Enable 2, DAC=1600, ADC Residuals vs Hold



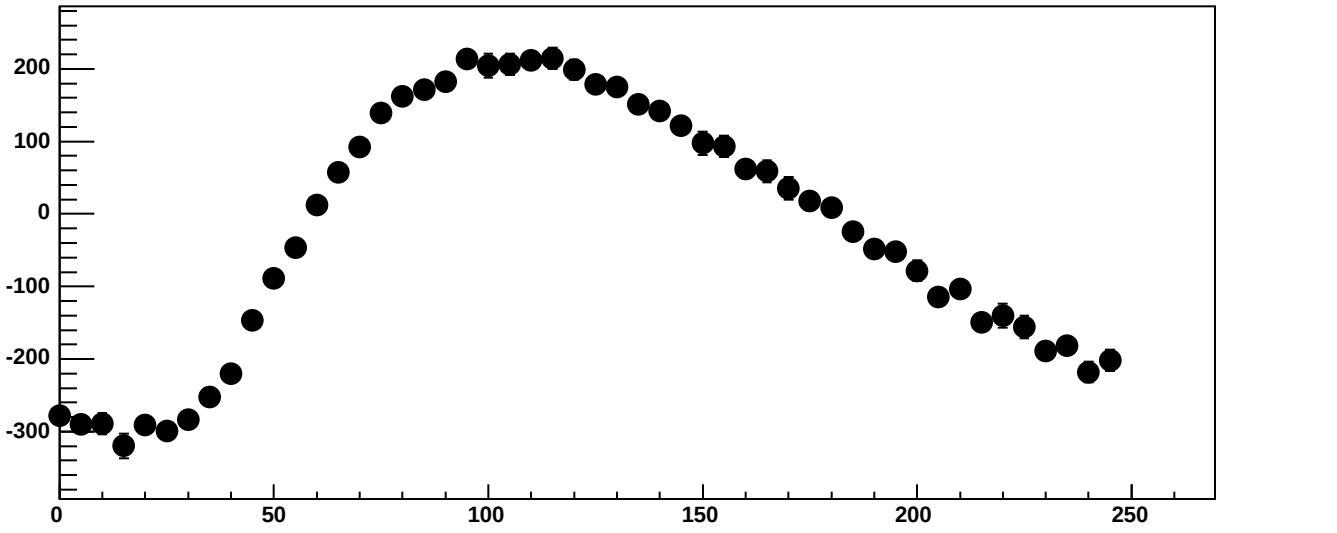
Chip 4, Channel 4, Enable 3, DAC=1600, ADC Mean vs Hold

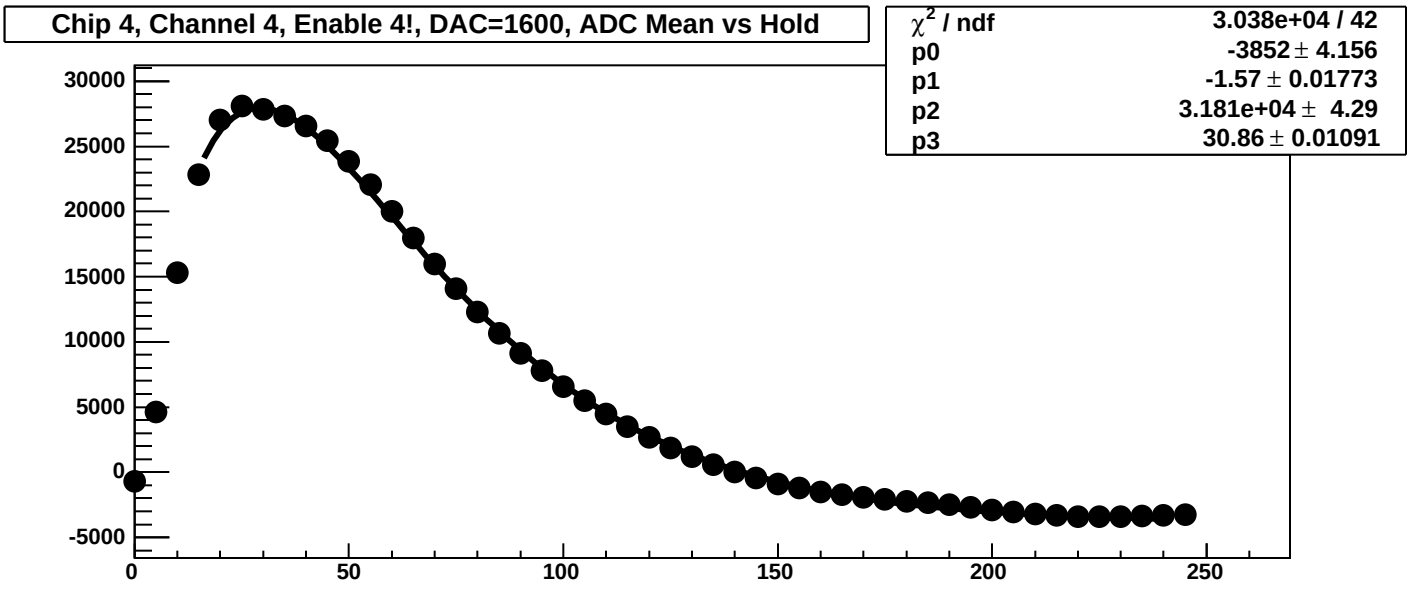


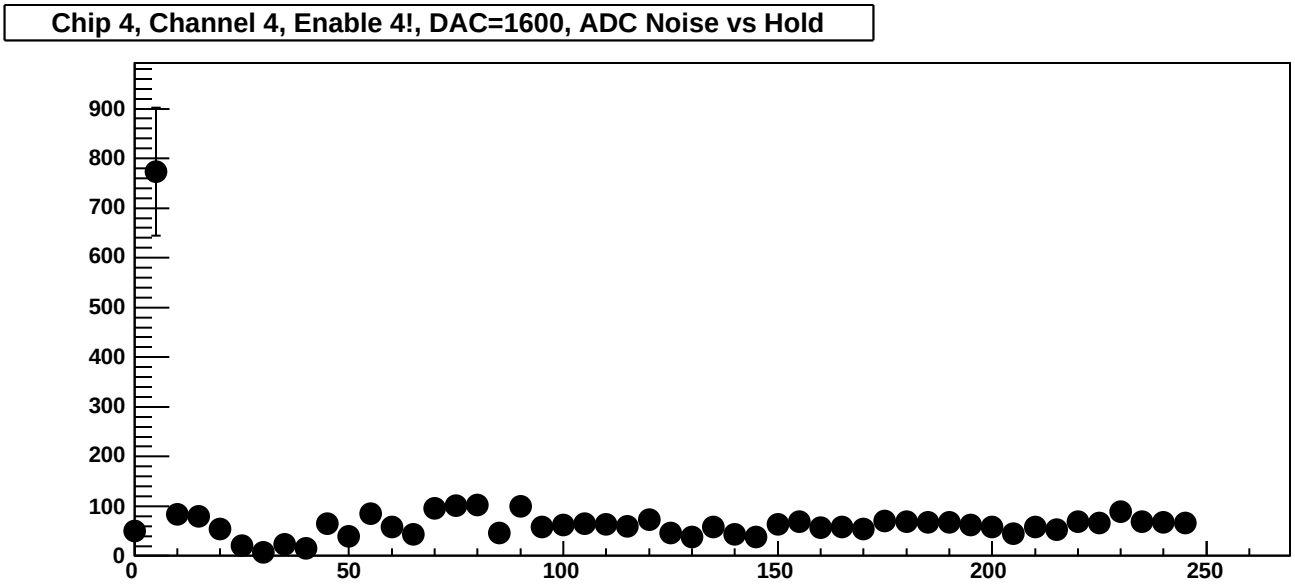
Chip 4, Channel 4, Enable 3, DAC=1600, ADC Noise vs Hold

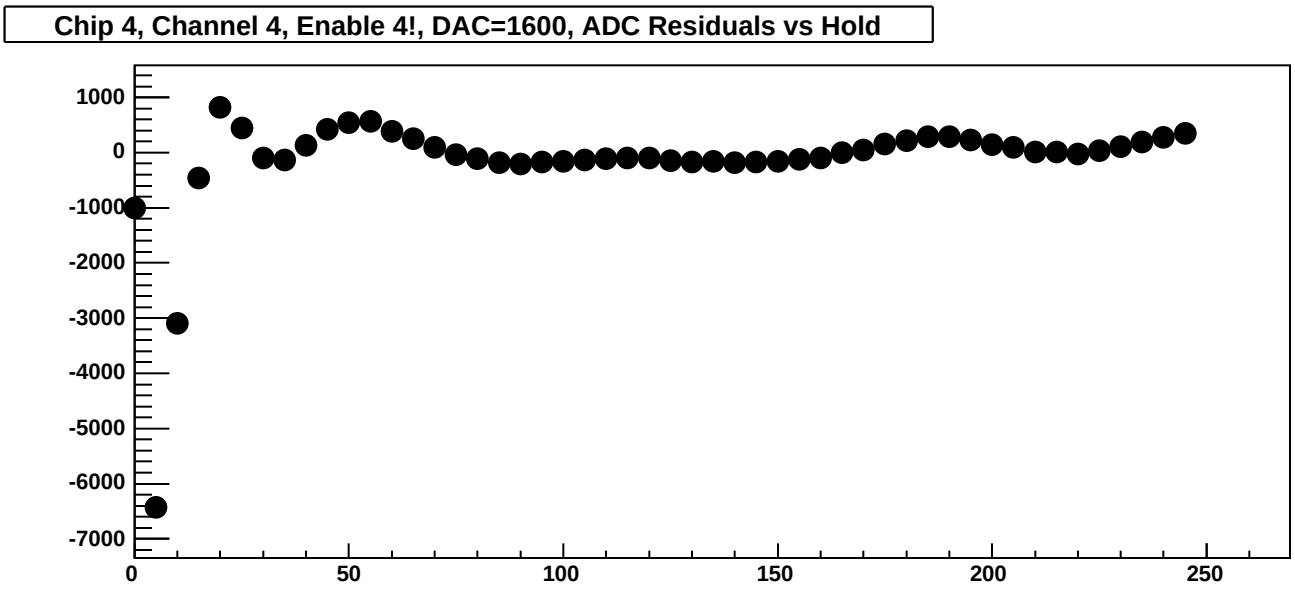


Chip 4, Channel 4, Enable 3, DAC=1600, ADC Residuals vs Hold

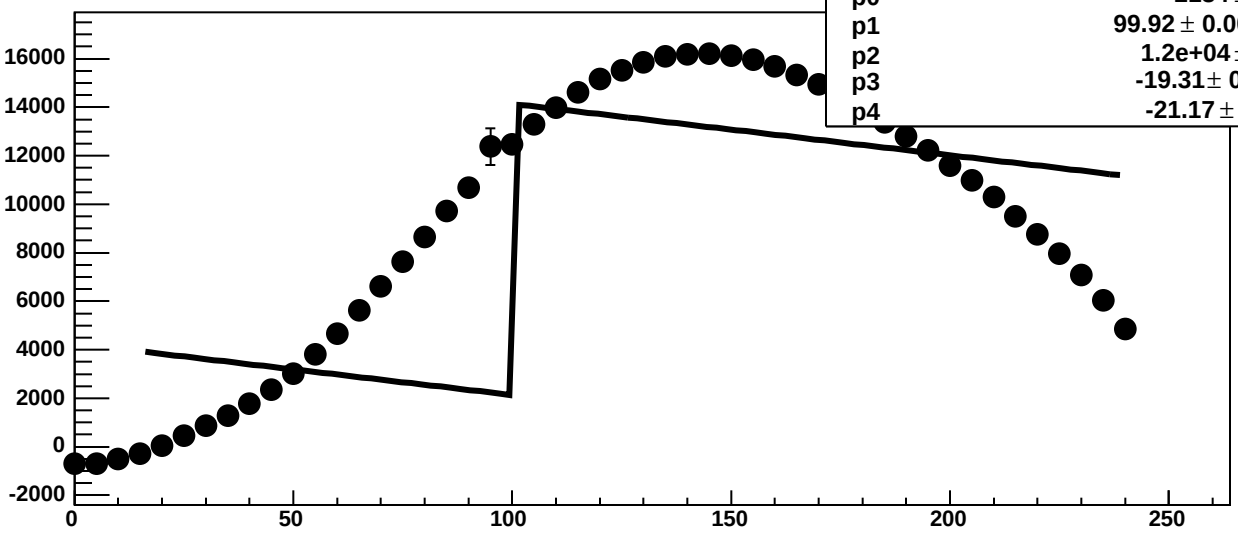



Chip 4, Channel 4, Enable 4!, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.038e+04 / 42
p0 -3852 ± 4.156
p1 -1.57 ± 0.01773
p2 3.181e+04 ± 4.29
p3 30.86 ± 0.01091
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

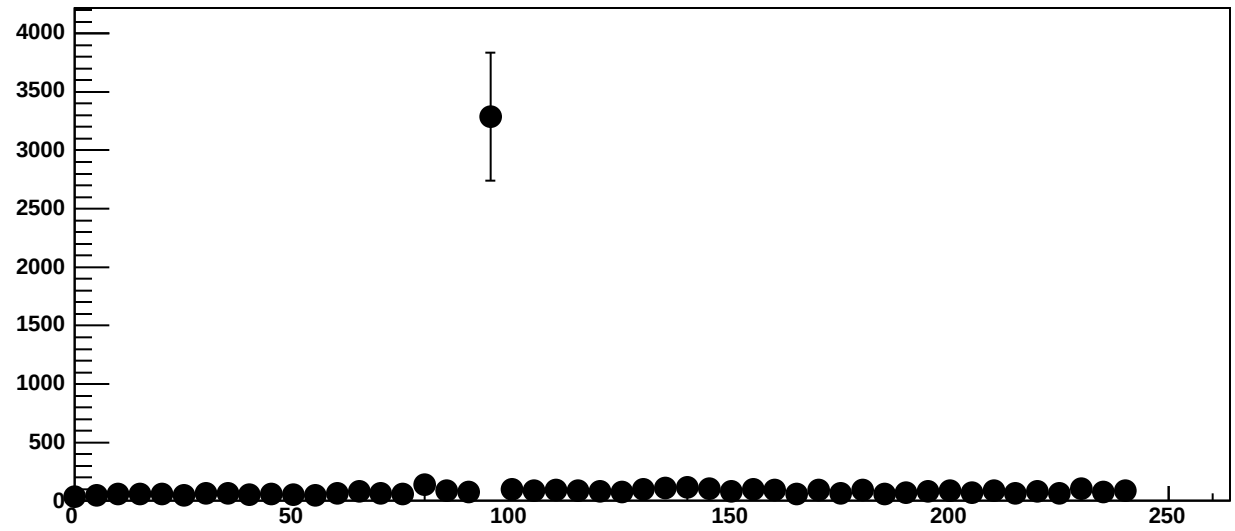

Chip 4, Channel 4, Enable 4!, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 4, Channel 4, Enable 4!, DAC=1600, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
-7000
0
50
100
150
200
250

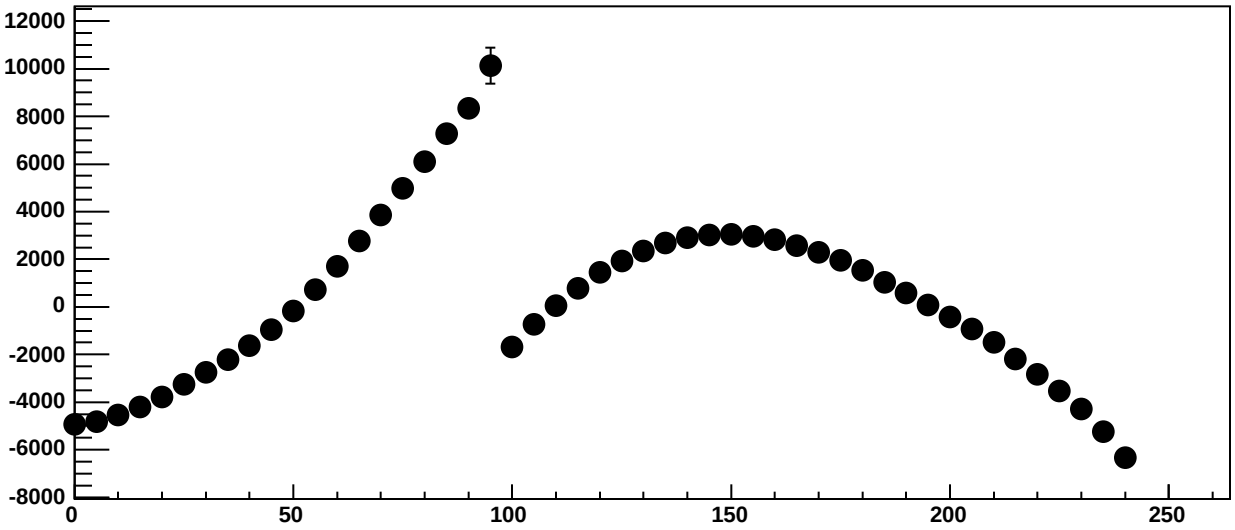
Chip 4, Channel 4, Enable 5, DAC=1600, ADC Mean vs Hold



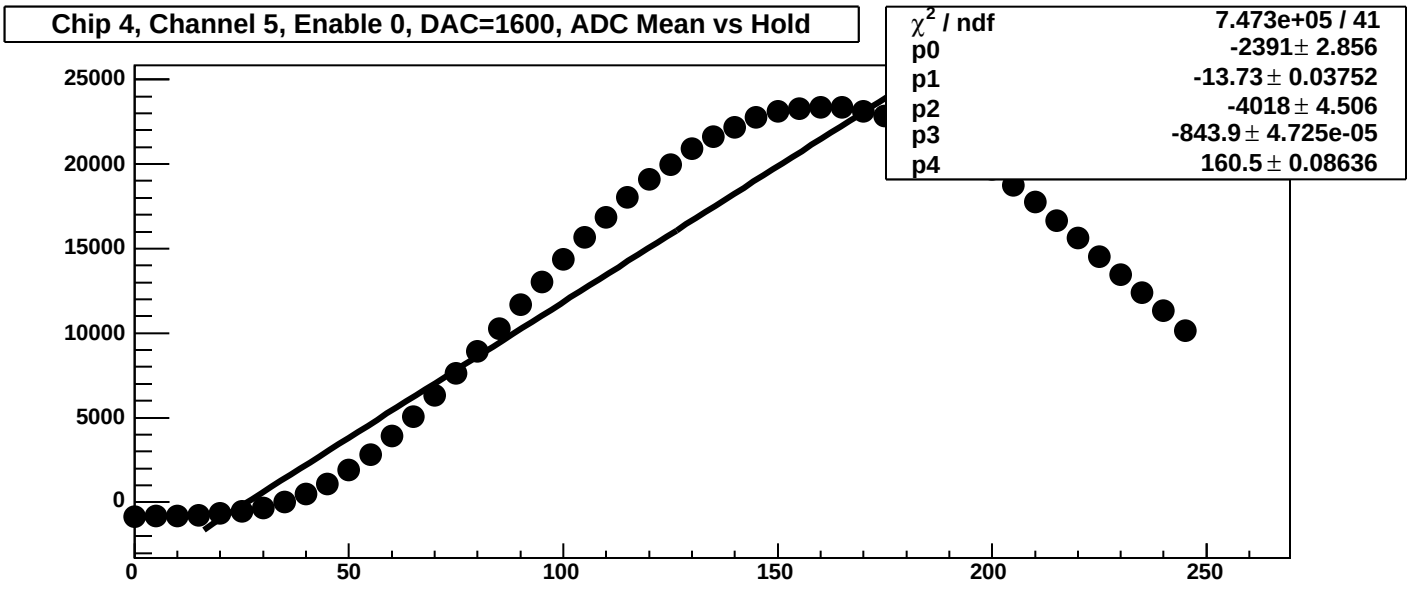
Chip 4, Channel 4, Enable 5, DAC=1600, ADC Noise vs Hold



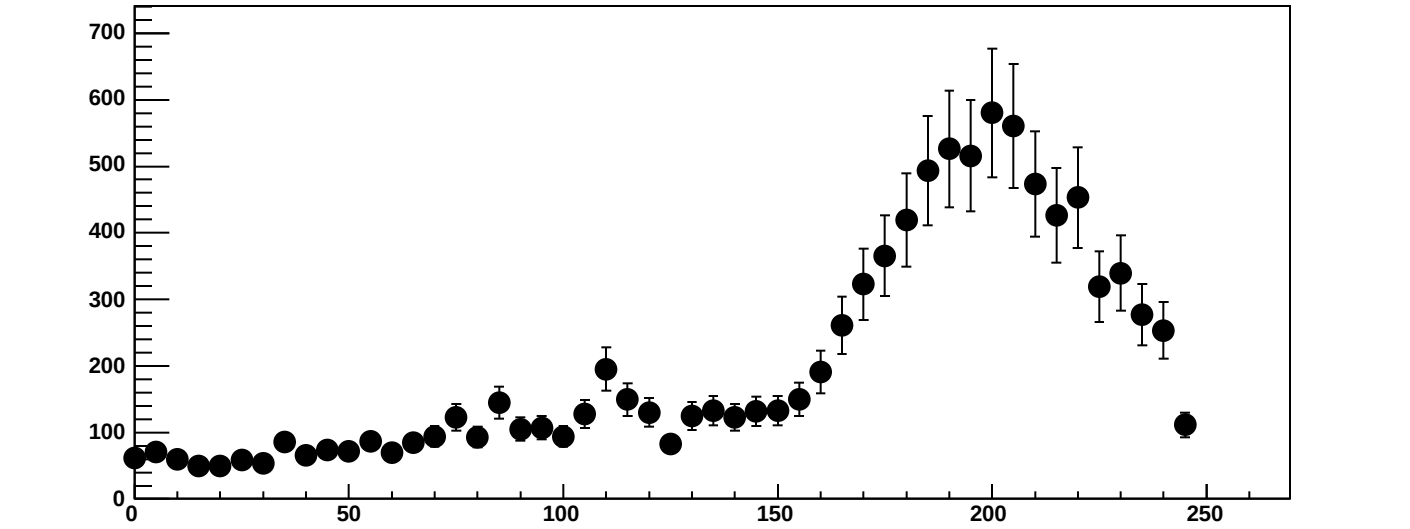
Chip 4, Channel 4, Enable 5, DAC=1600, ADC Residuals vs Hold



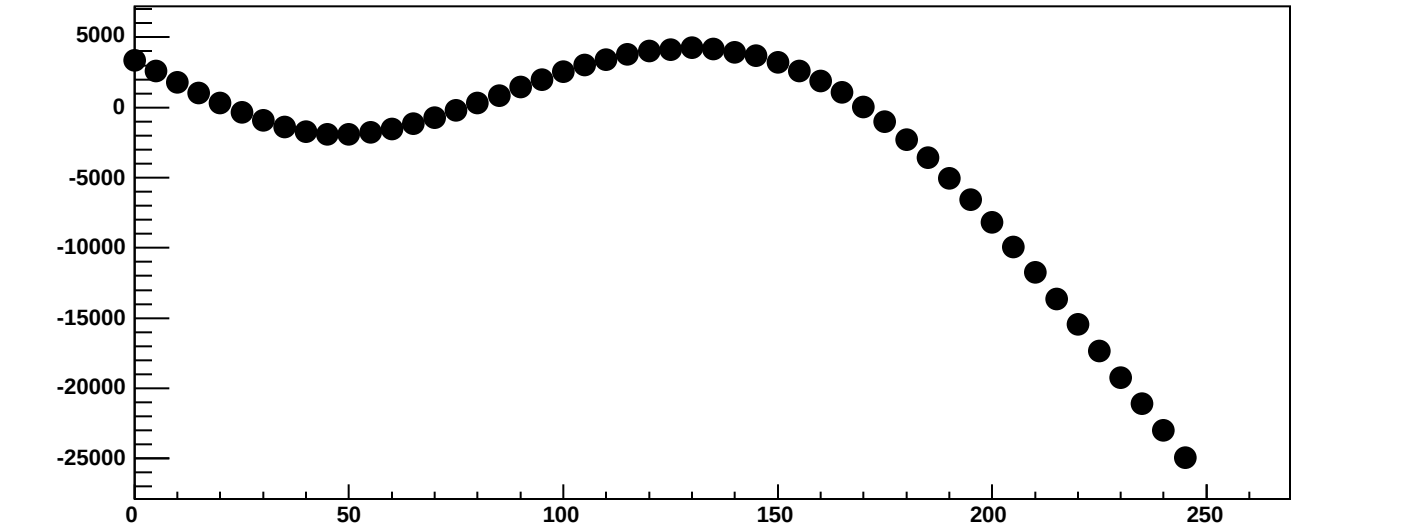
Chip 4, Channel 5, Enable 0, DAC=1600, ADC Mean vs Hold



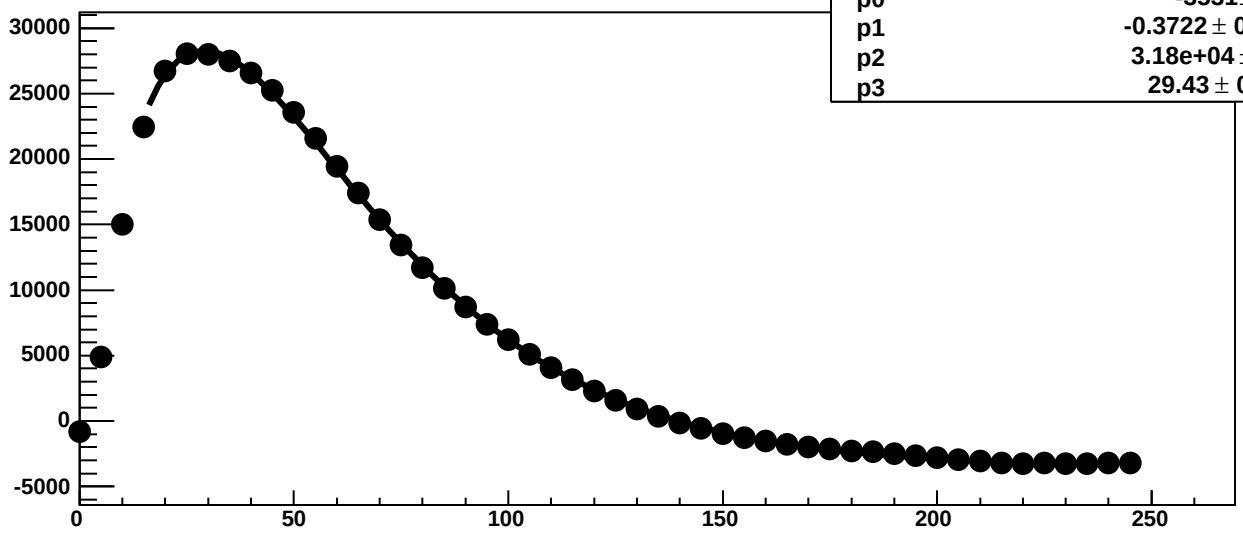
Chip 4, Channel 5, Enable 0, DAC=1600, ADC Noise vs Hold



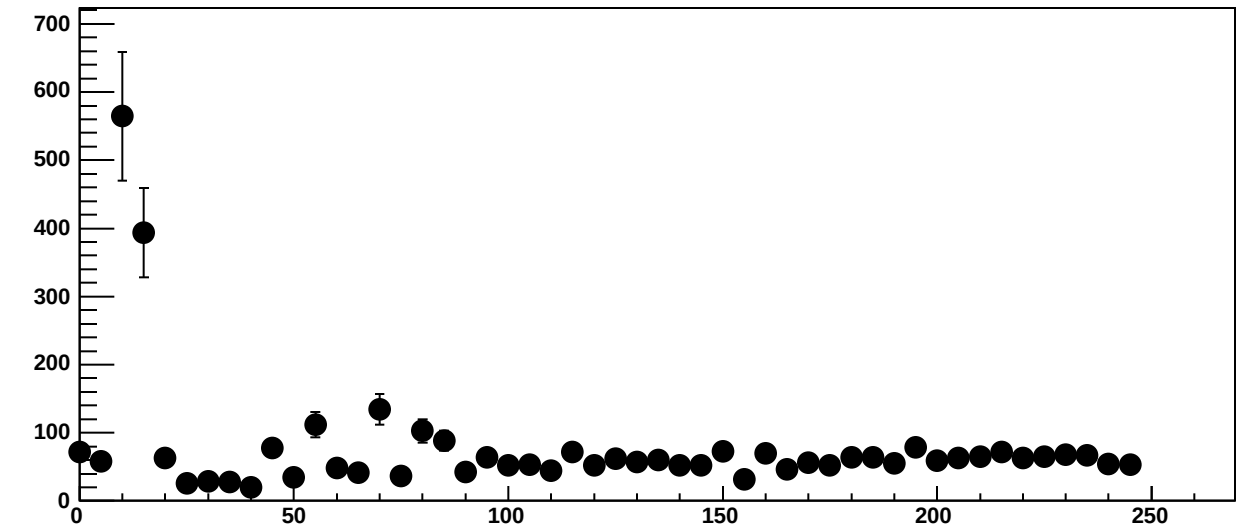
Chip 4, Channel 5, Enable 0, DAC=1600, ADC Residuals vs Hold



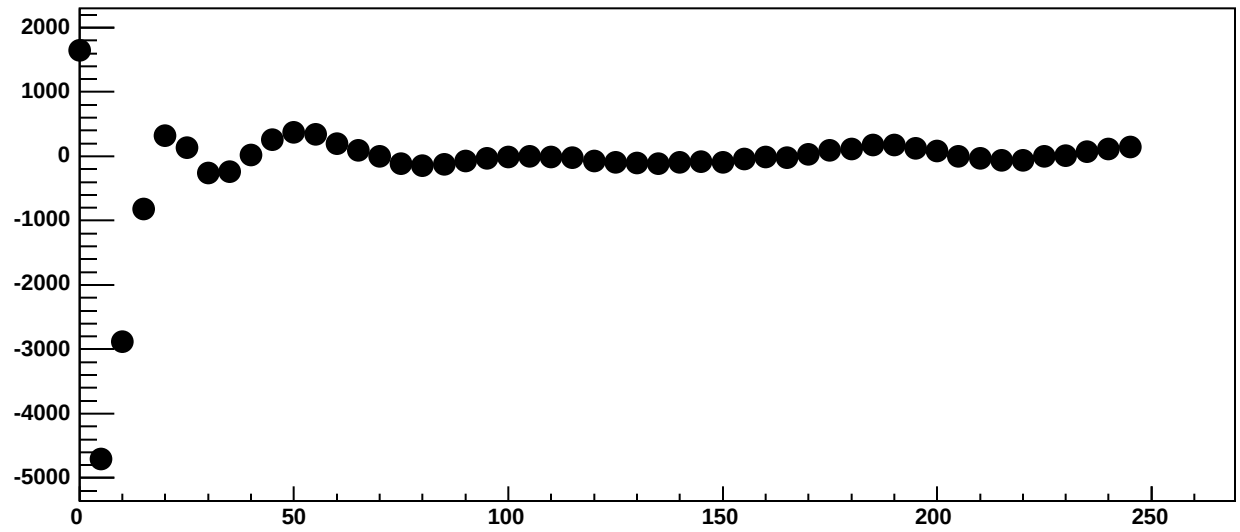
Chip 4, Channel 5, Enable 1!, DAC=1600, ADC Mean vs Hold



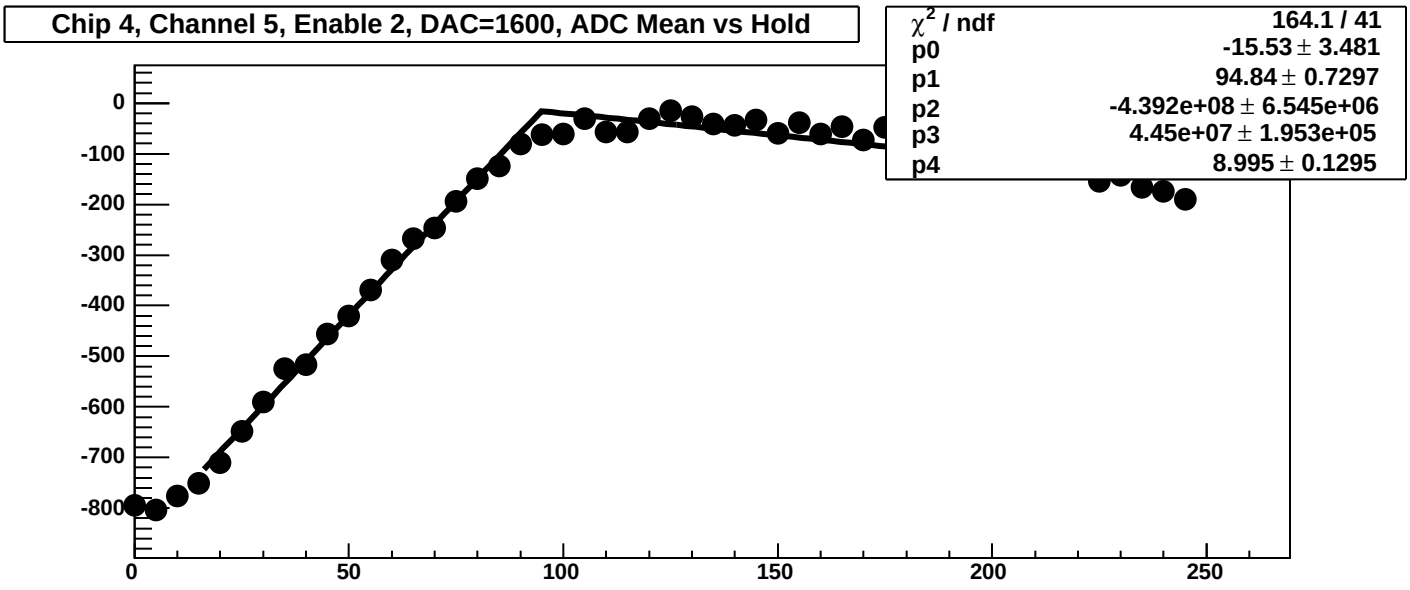
Chip 4, Channel 5, Enable 1!, DAC=1600, ADC Noise vs Hold



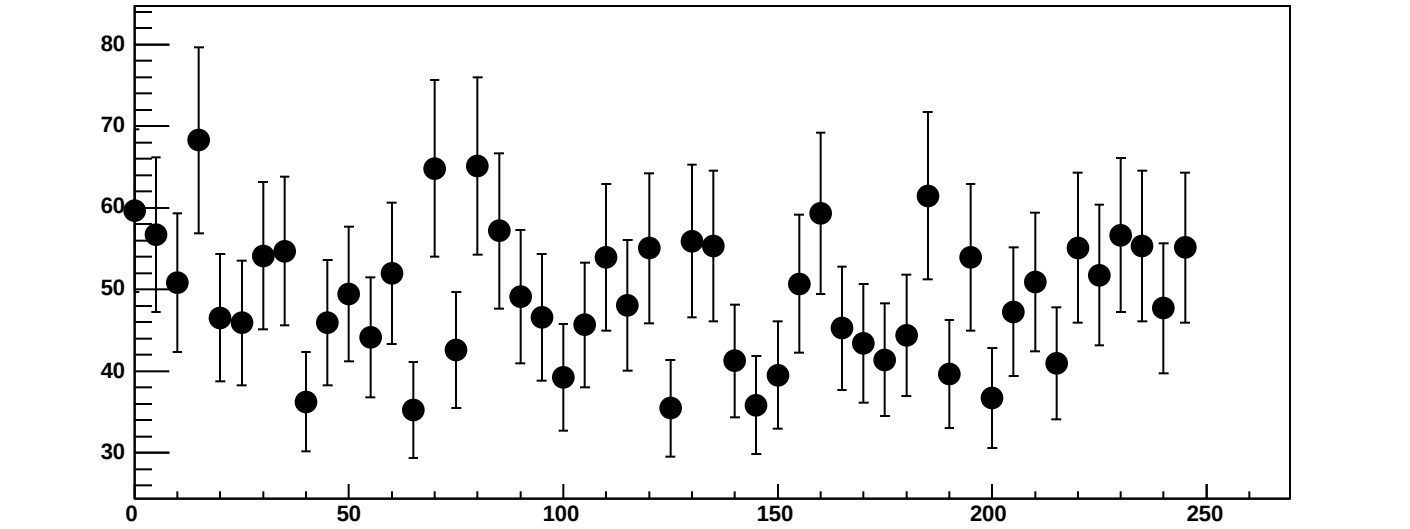
Chip 4, Channel 5, Enable 1!, DAC=1600, ADC Residuals vs Hold



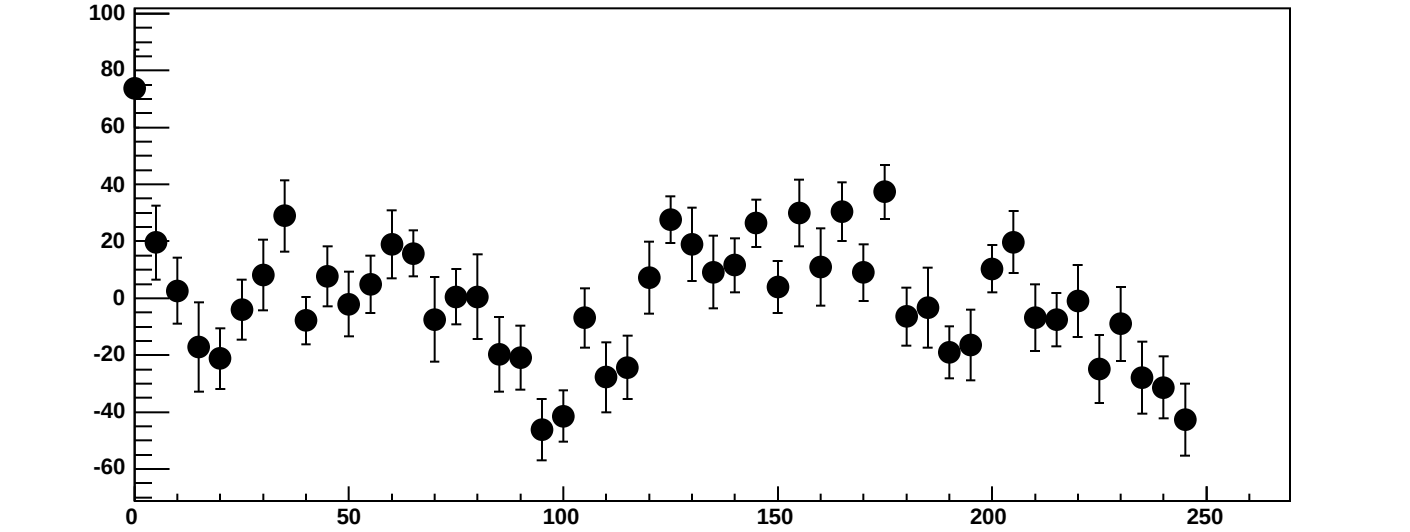
Chip 4, Channel 5, Enable 2, DAC=1600, ADC Mean vs Hold



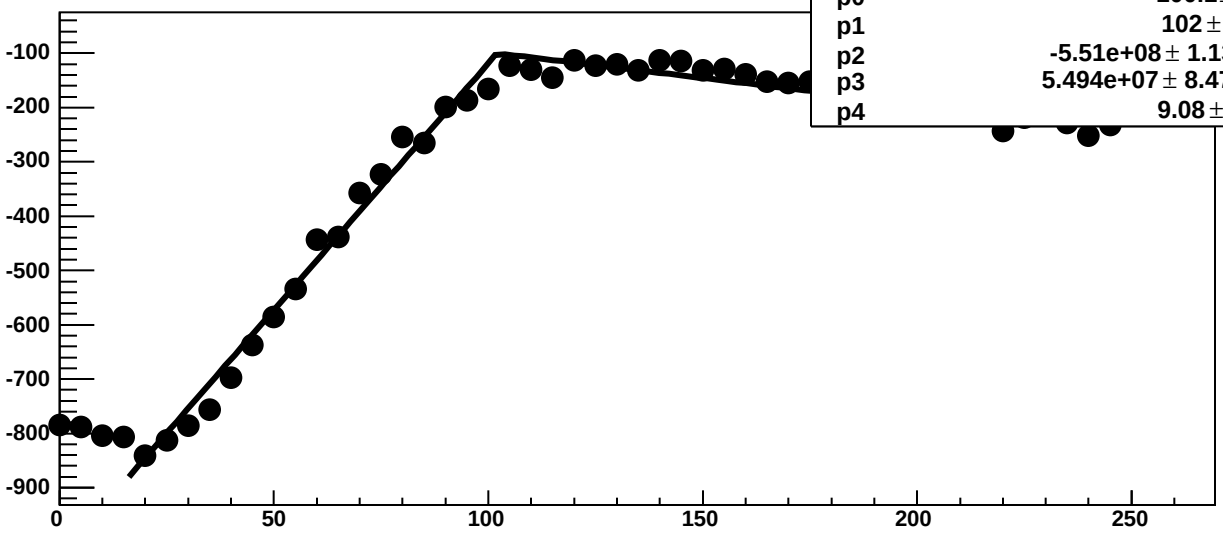
Chip 4, Channel 5, Enable 2, DAC=1600, ADC Noise vs Hold



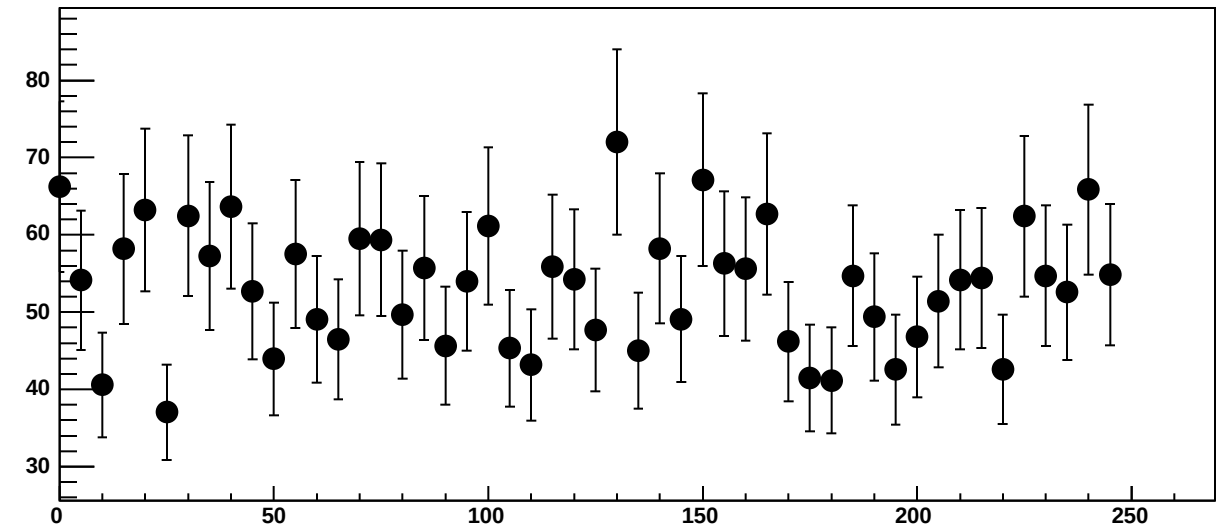
Chip 4, Channel 5, Enable 2, DAC=1600, ADC Residuals vs Hold



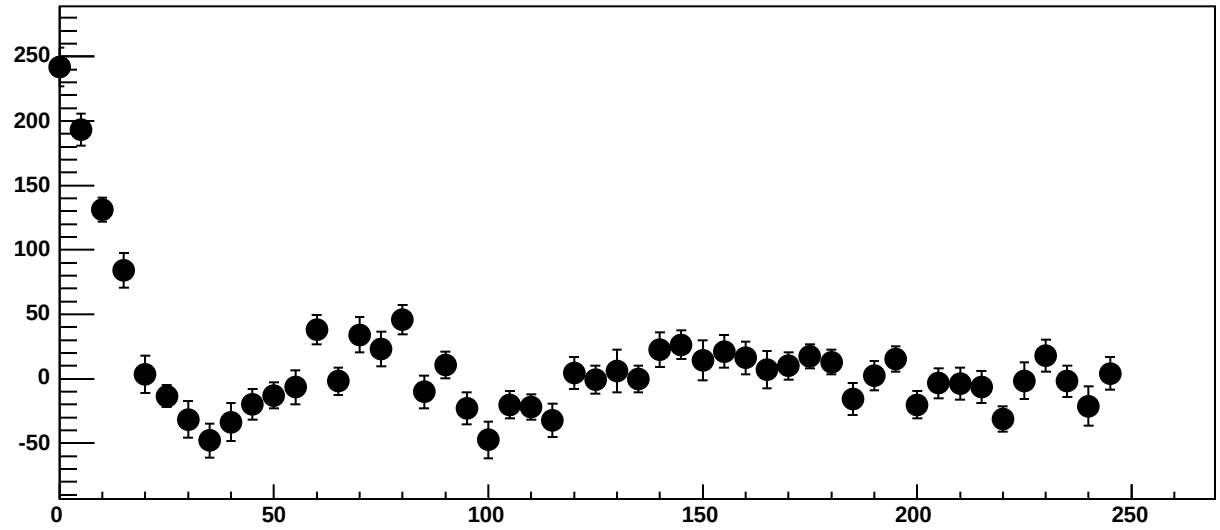
Chip 4, Channel 5, Enable 3, DAC=1600, ADC Mean vs Hold



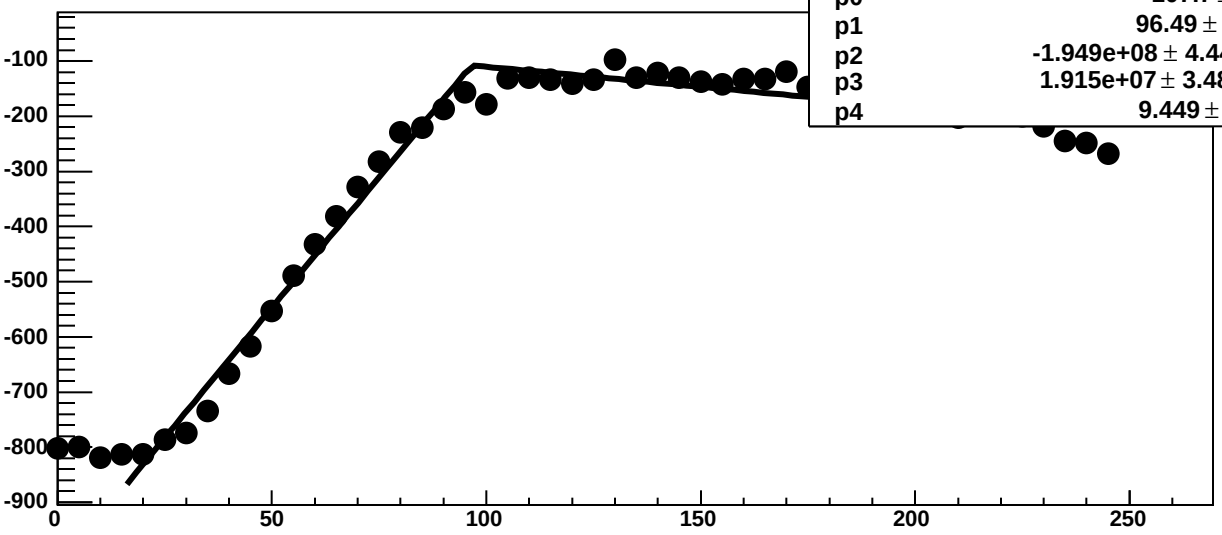
Chip 4, Channel 5, Enable 3, DAC=1600, ADC Noise vs Hold



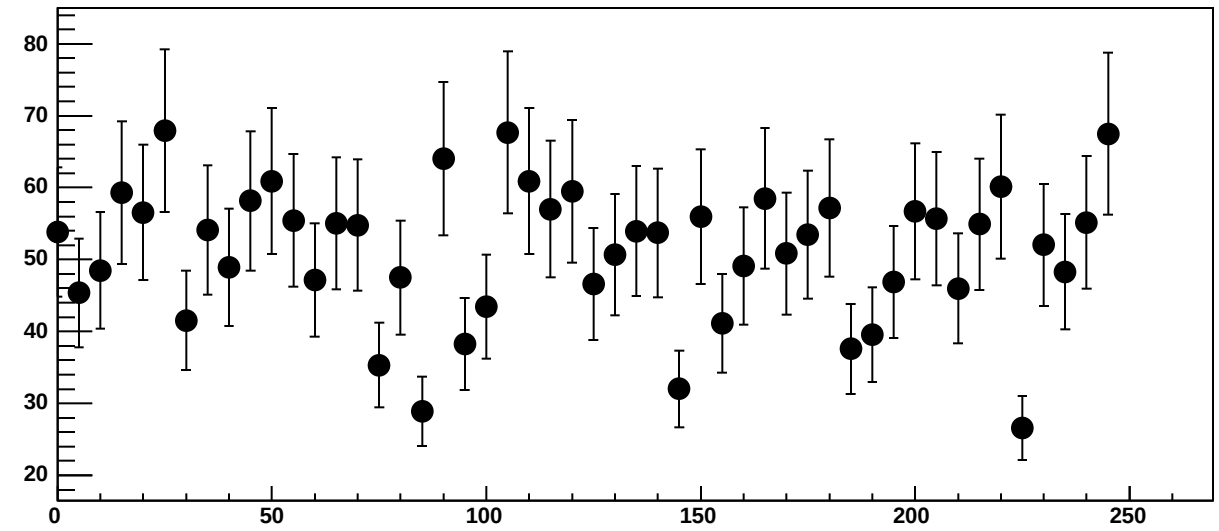
Chip 4, Channel 5, Enable 3, DAC=1600, ADC Residuals vs Hold



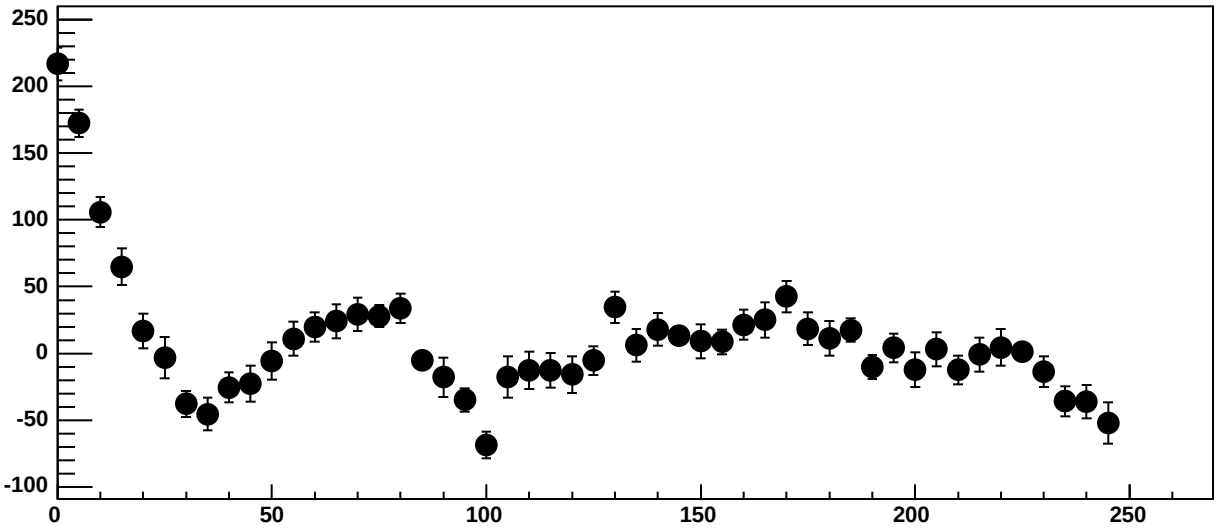
Chip 4, Channel 5, Enable 4, DAC=1600, ADC Mean vs Hold



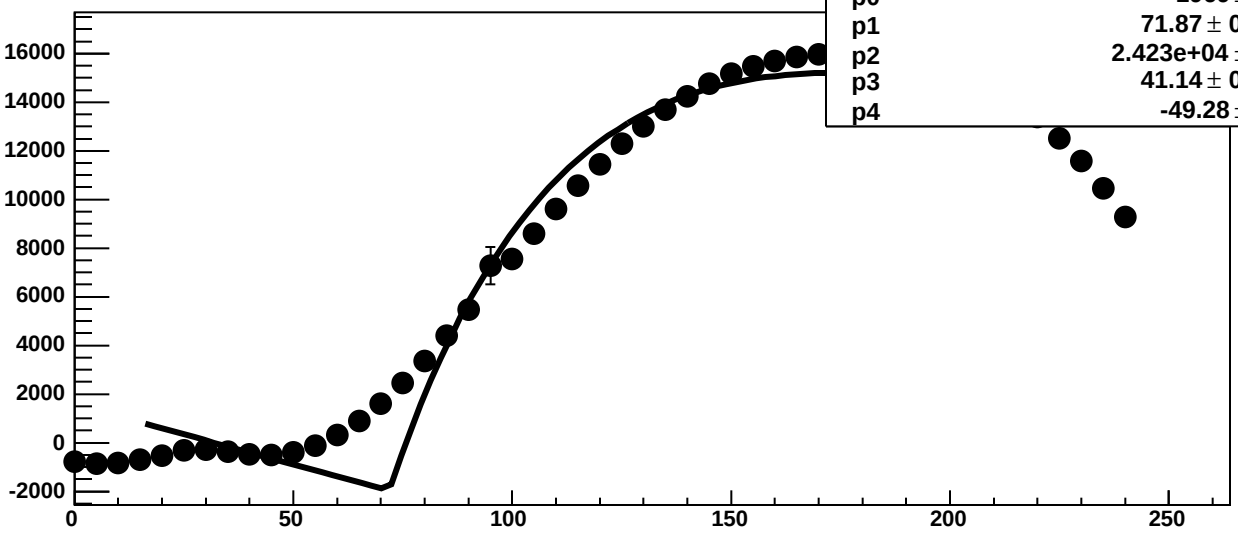
Chip 4, Channel 5, Enable 4, DAC=1600, ADC Noise vs Hold



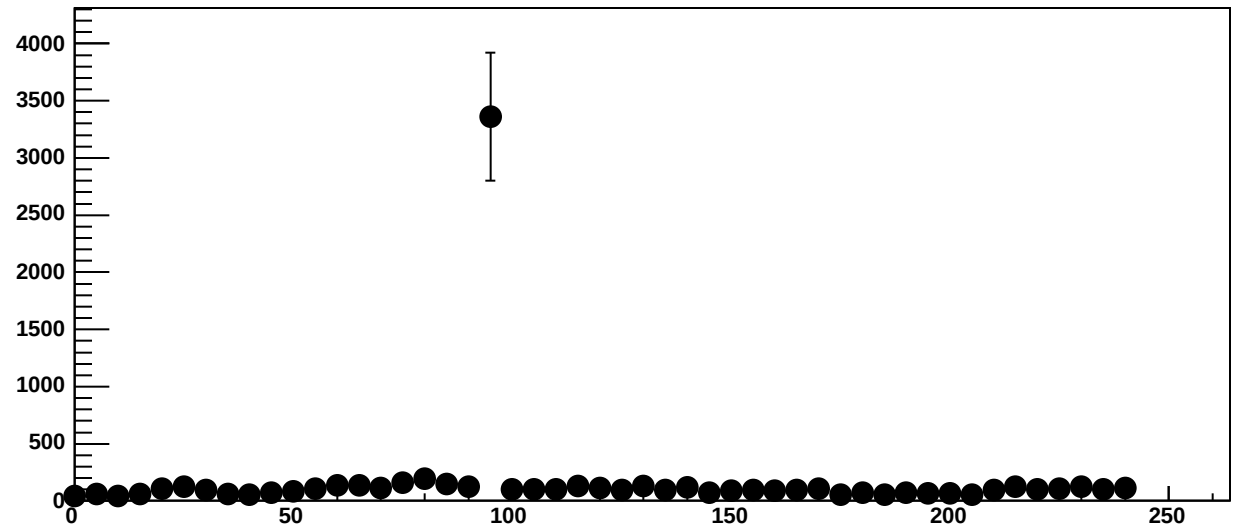
Chip 4, Channel 5, Enable 4, DAC=1600, ADC Residuals vs Hold



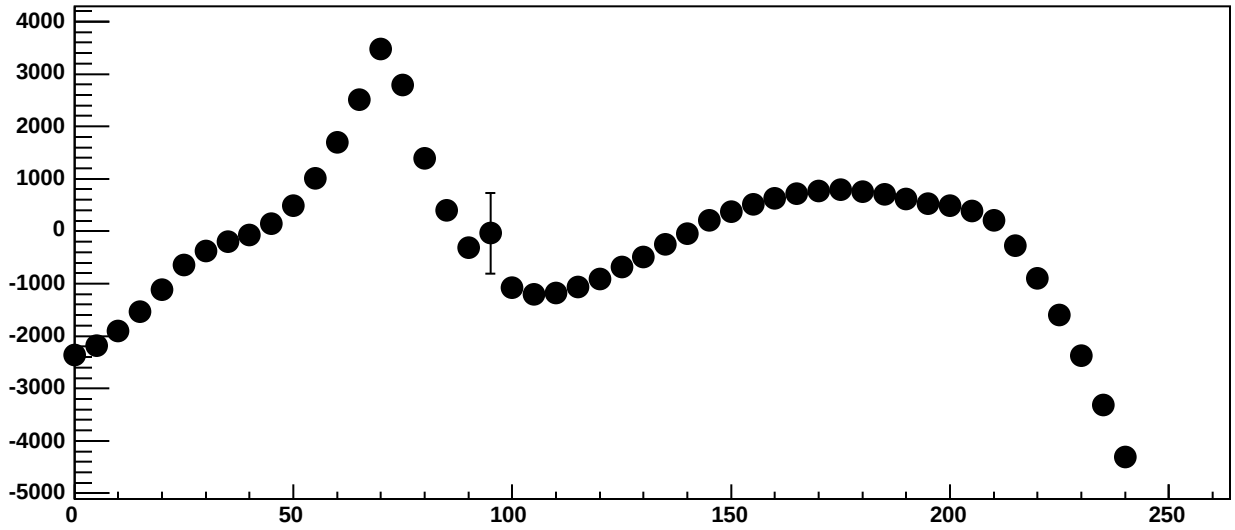
Chip 4, Channel 5, Enable 5, DAC=1600, ADC Mean vs Hold

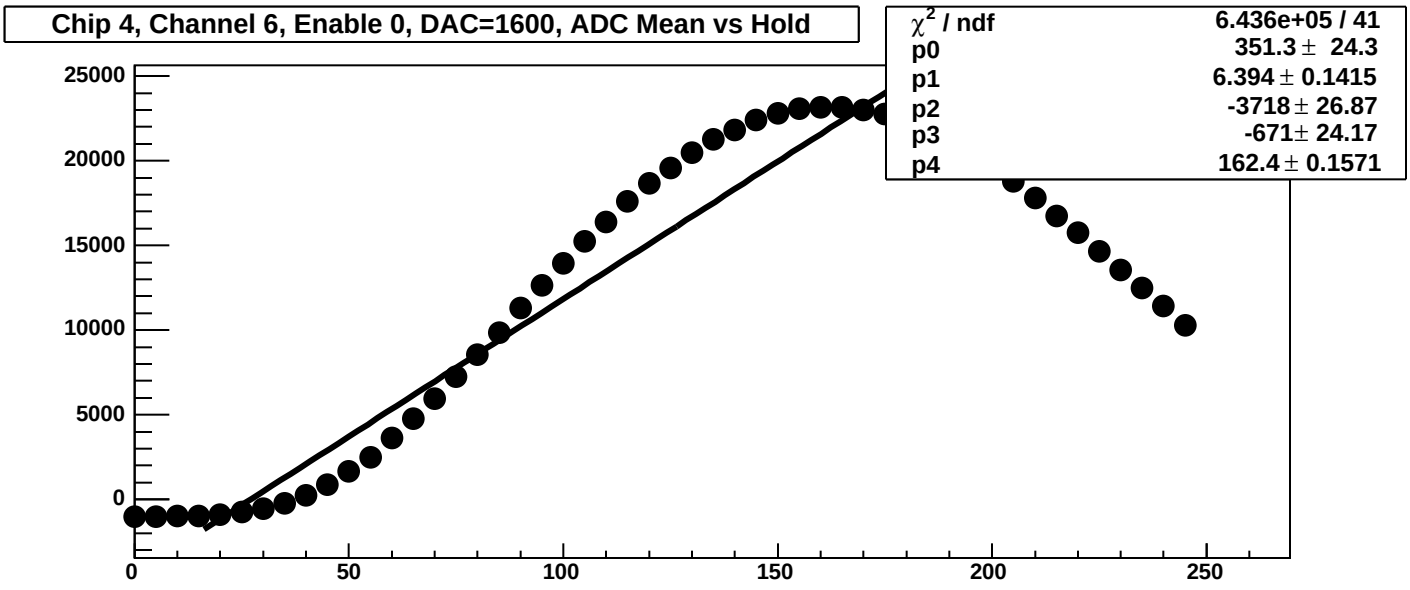


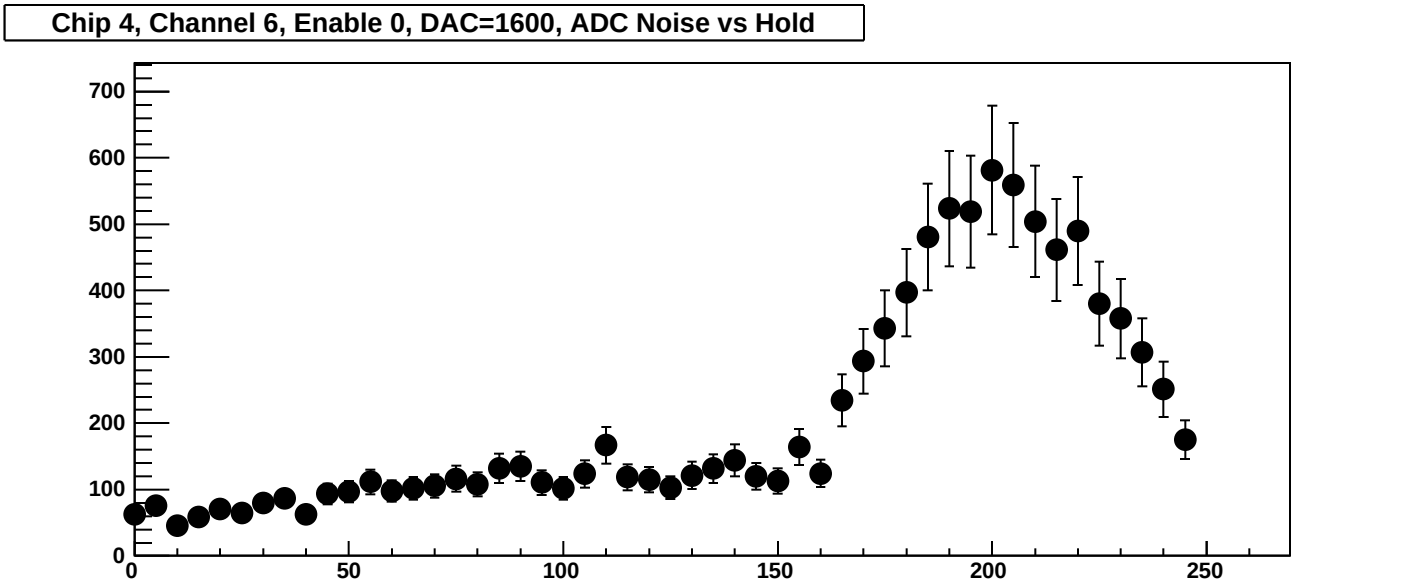
Chip 4, Channel 5, Enable 5, DAC=1600, ADC Noise vs Hold

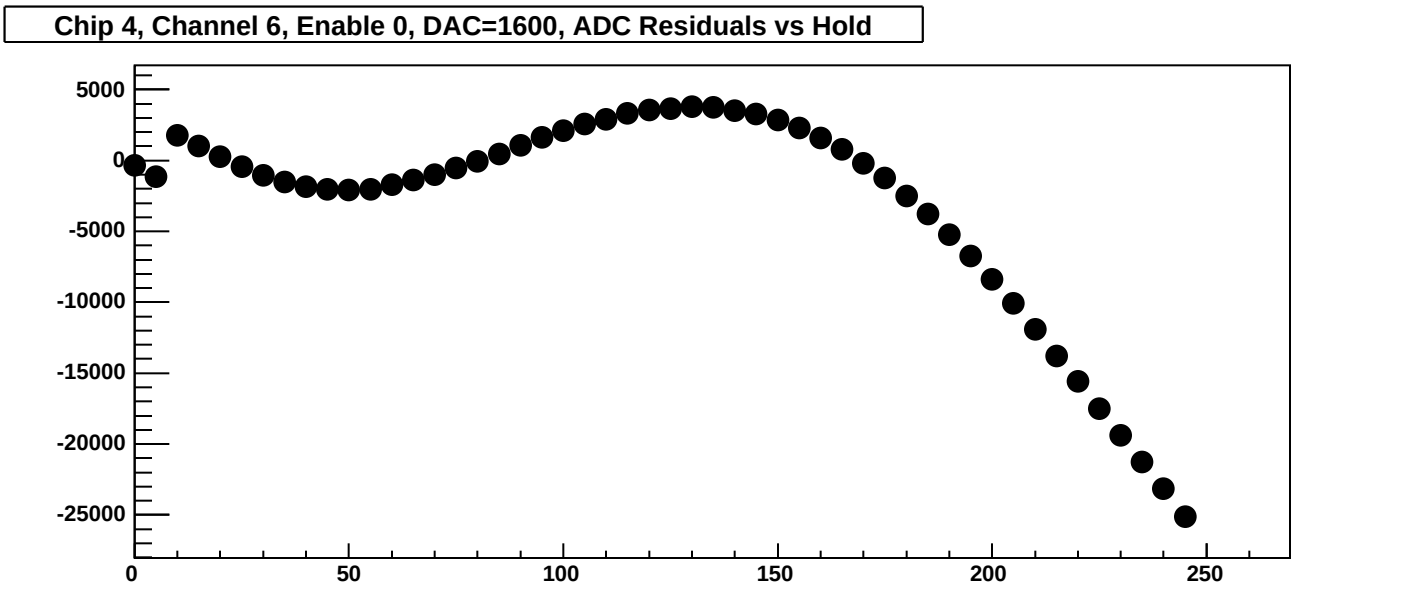


Chip 4, Channel 5, Enable 5, DAC=1600, ADC Residuals vs Hold

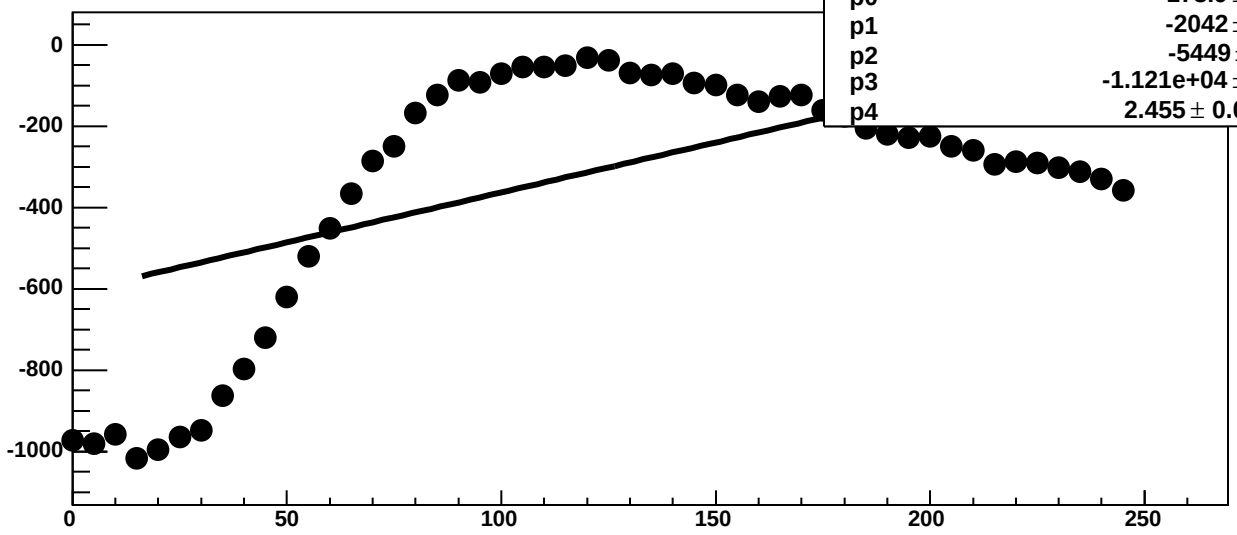



Chip 4, Channel 6, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 6.436e+05 / 41
p0 351.3 ± 24.3
p1 6.394 ± 0.1415
p2 -3718 ± 26.87
p3 -671 ± 24.17
p4 162.4 ± 0.1571
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

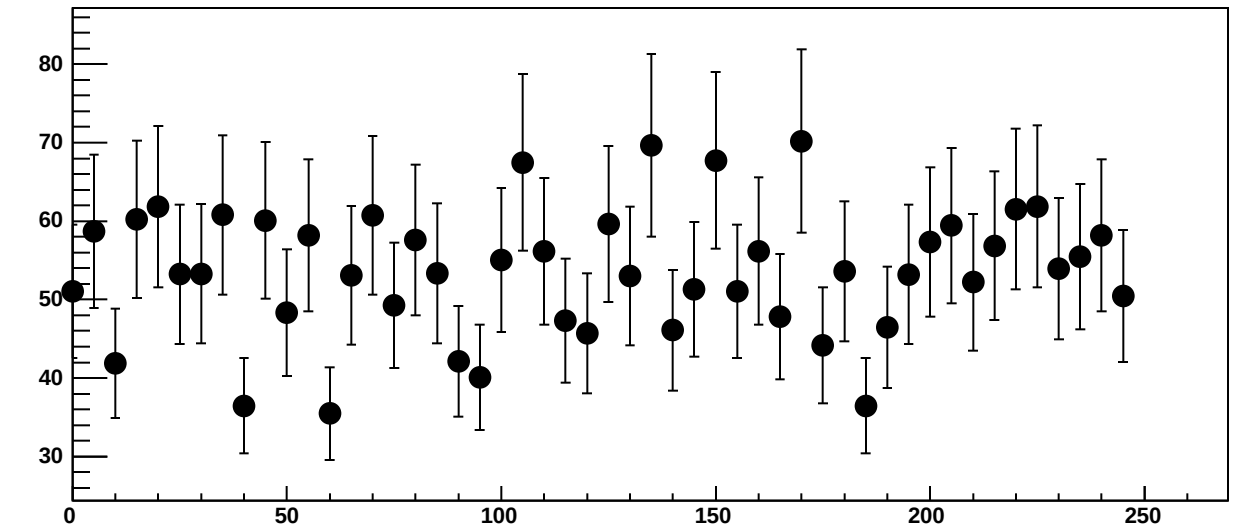

Chip 4, Channel 6, Enable 0, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 4, Channel 6, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
0
50
100
150
200
250

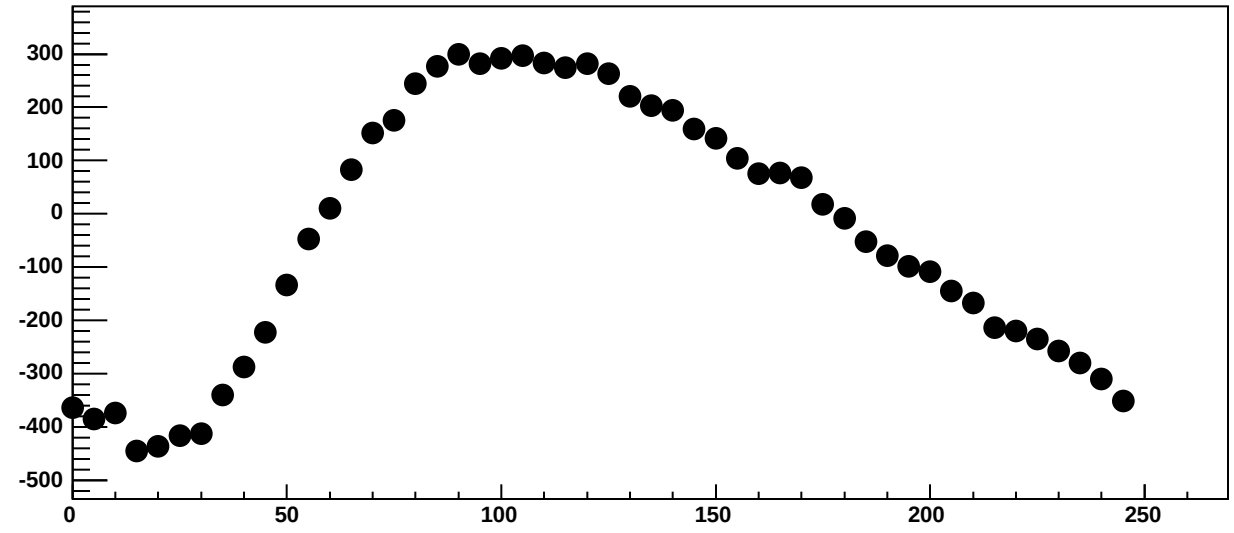
Chip 4, Channel 6, Enable 1, DAC=1600, ADC Mean vs Hold



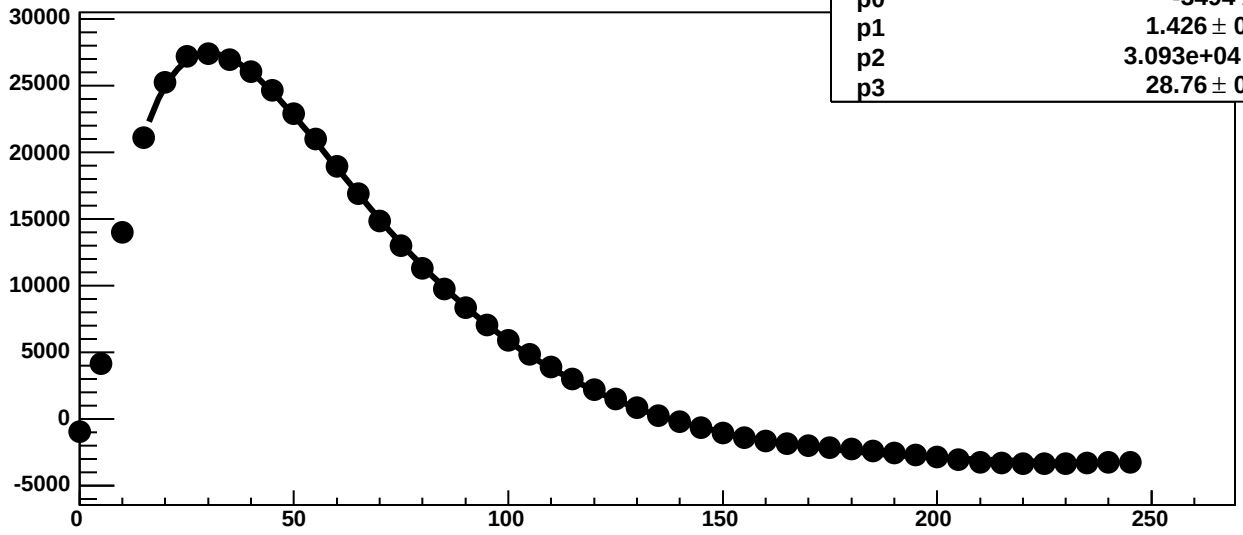
Chip 4, Channel 6, Enable 1, DAC=1600, ADC Noise vs Hold



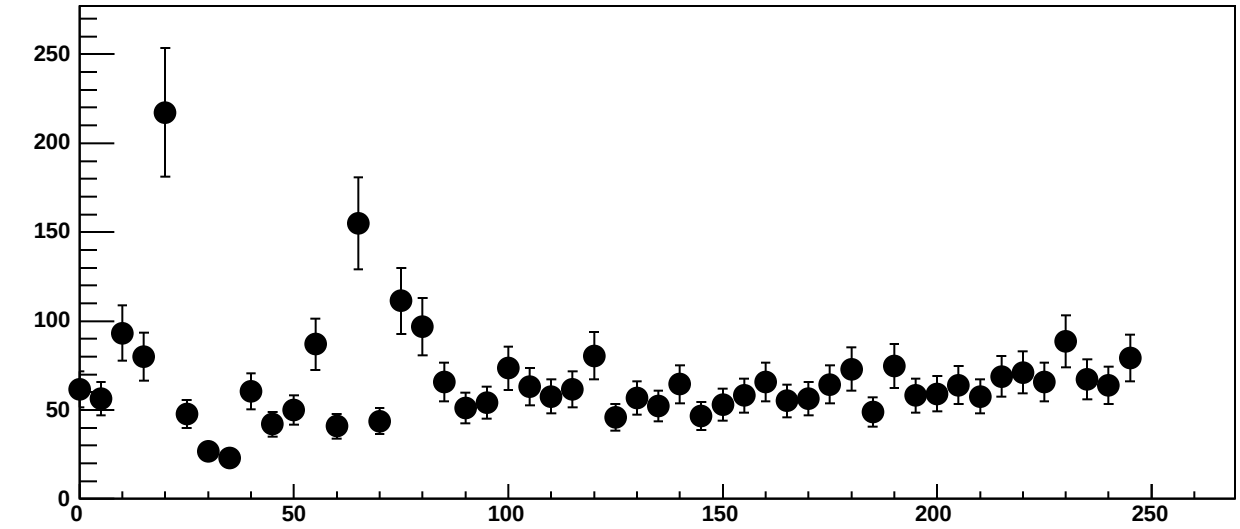
Chip 4, Channel 6, Enable 1, DAC=1600, ADC Residuals vs Hold



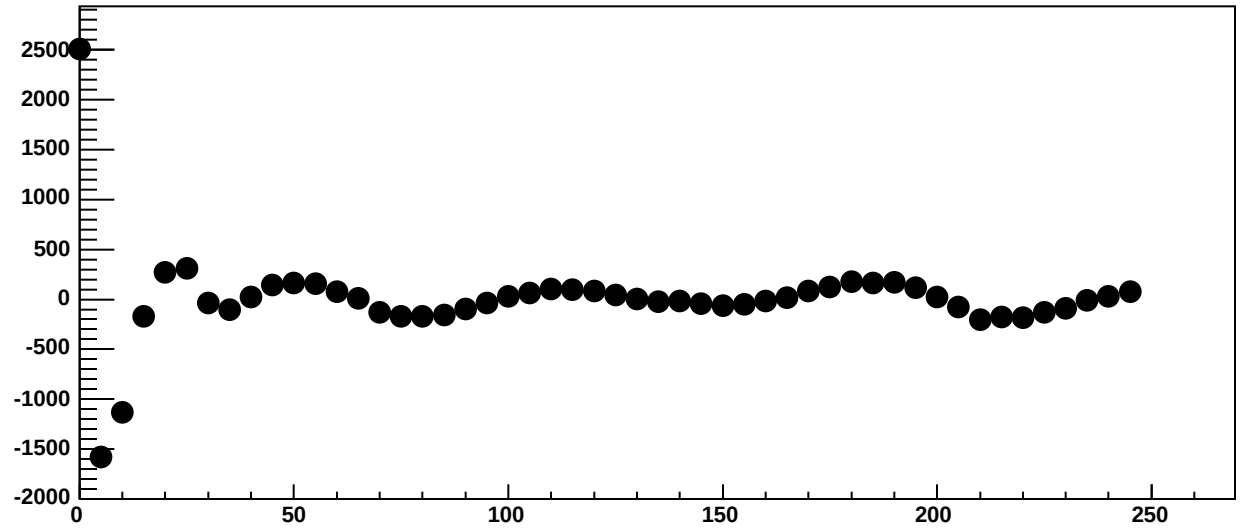
Chip 4, Channel 6, Enable 2!, DAC=1600, ADC Mean vs Hold



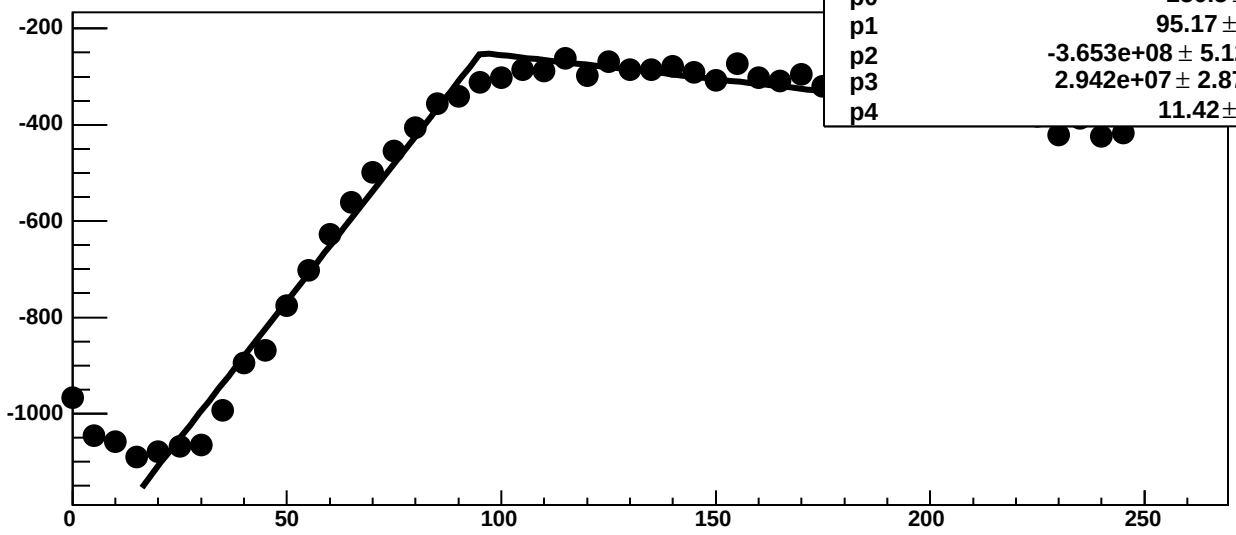
Chip 4, Channel 6, Enable 2!, DAC=1600, ADC Noise vs Hold



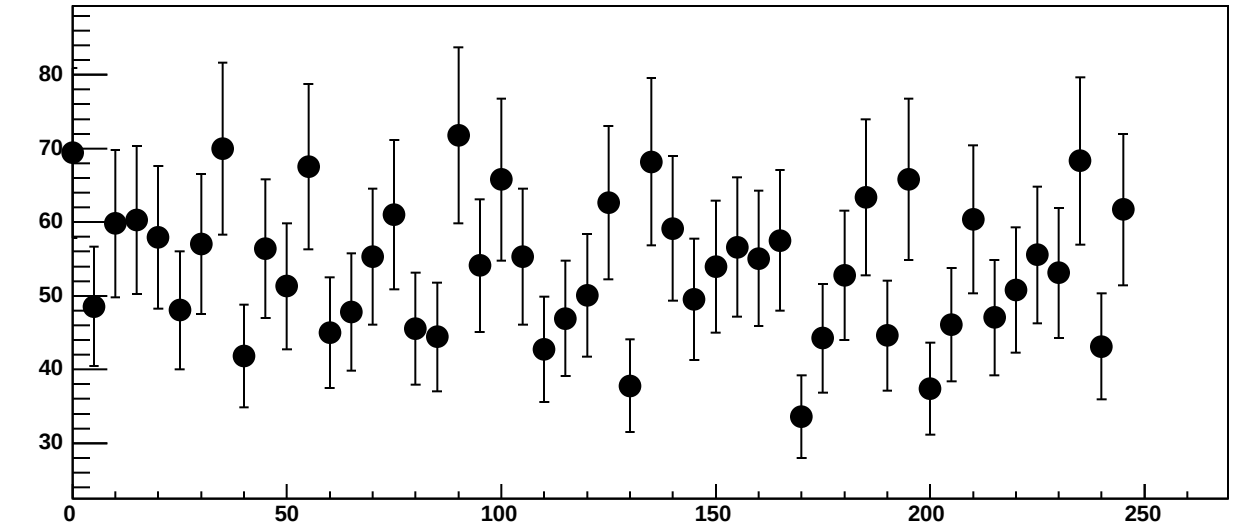
Chip 4, Channel 6, Enable 2!, DAC=1600, ADC Residuals vs Hold



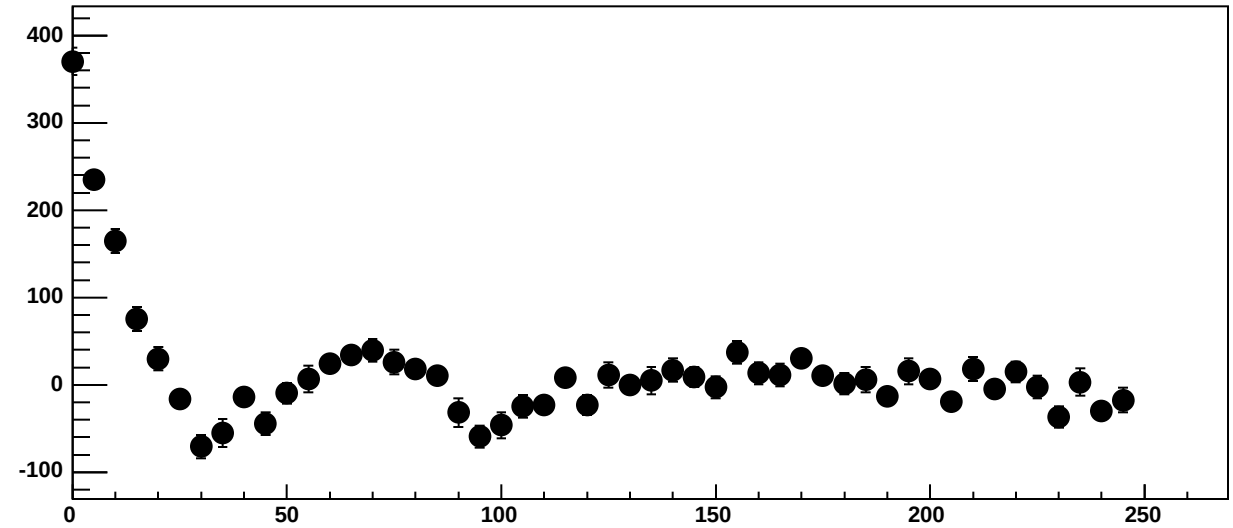
Chip 4, Channel 6, Enable 3, DAC=1600, ADC Mean vs Hold

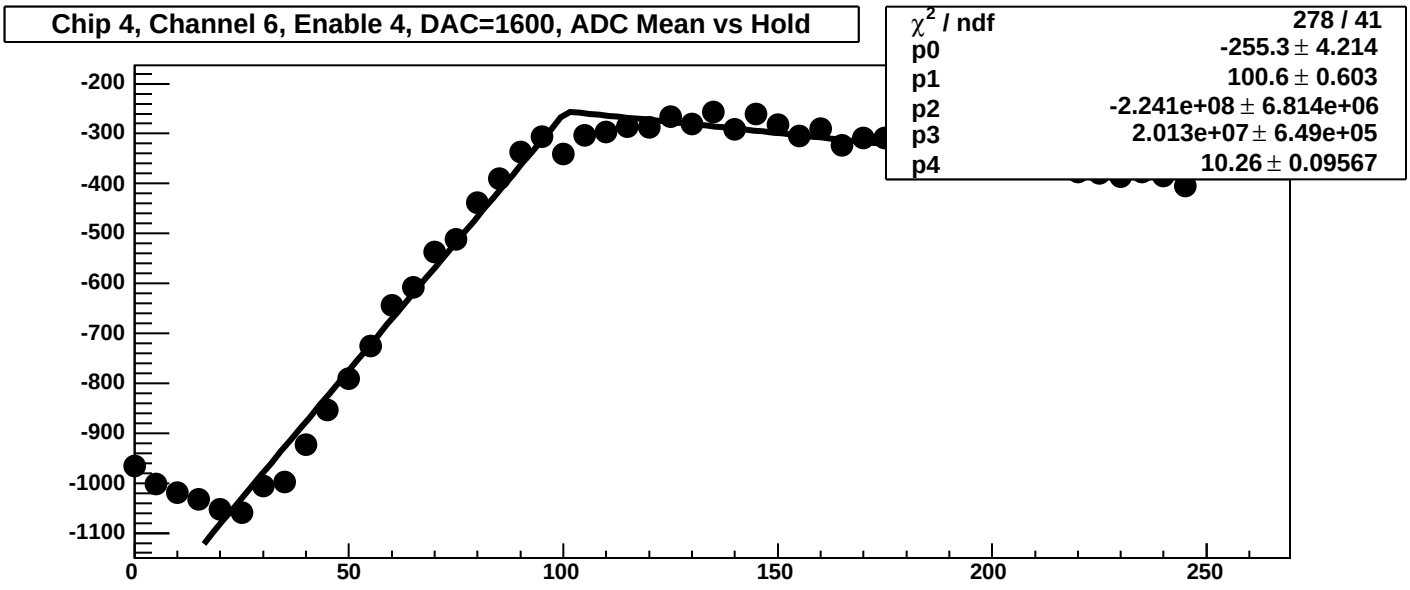


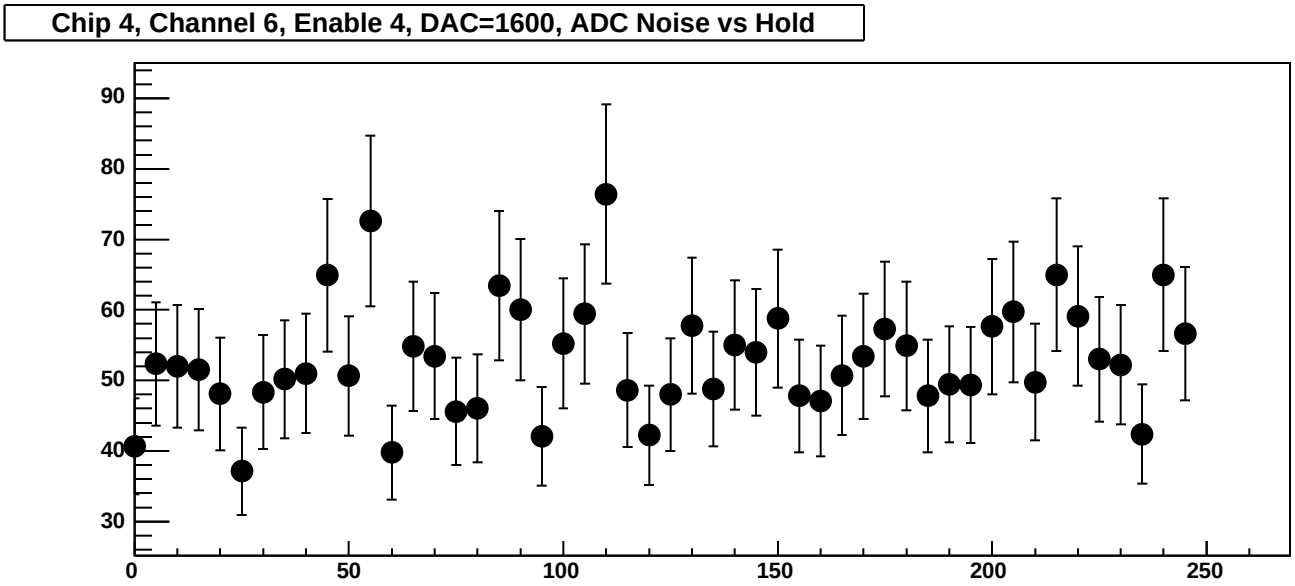
Chip 4, Channel 6, Enable 3, DAC=1600, ADC Noise vs Hold

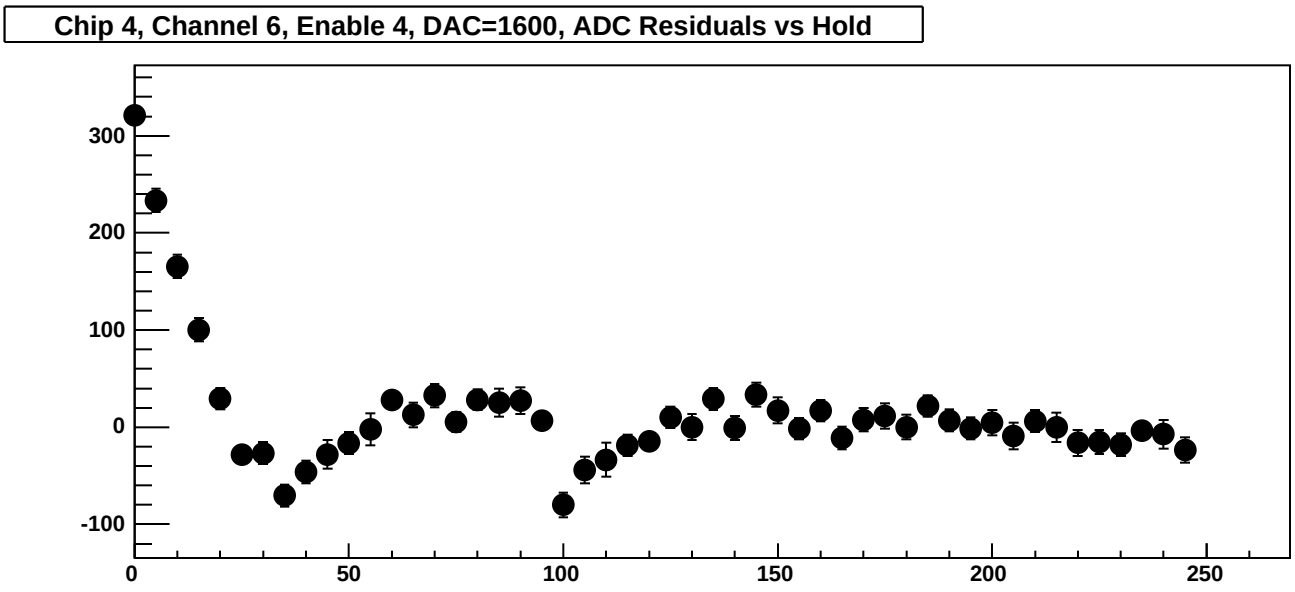


Chip 4, Channel 6, Enable 3, DAC=1600, ADC Residuals vs Hold

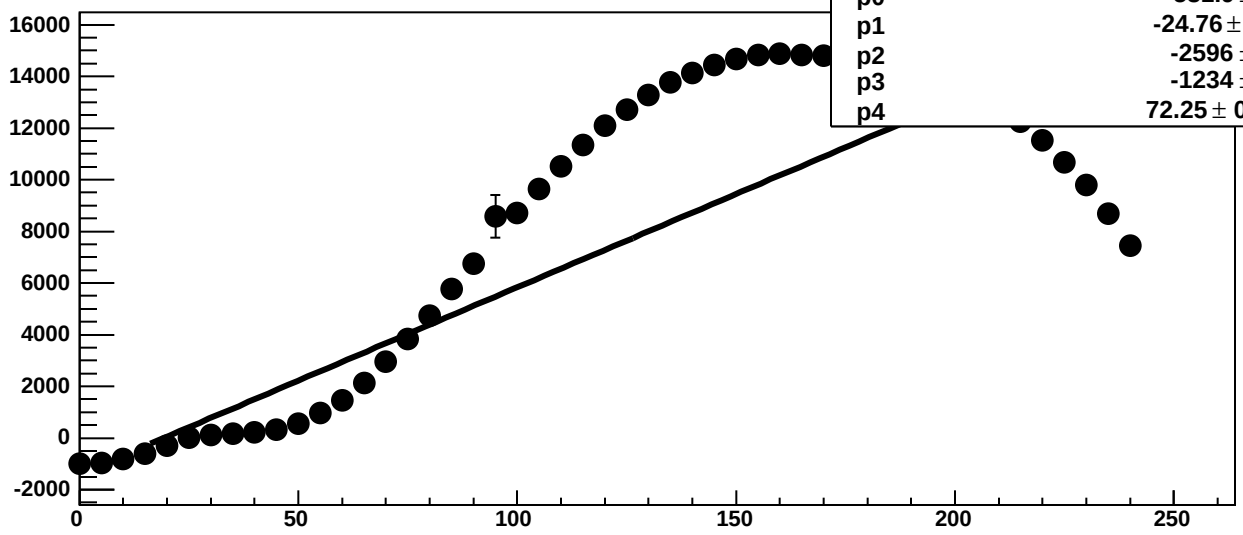



Chip 4, Channel 6, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 278 / 41
p0 -255.3 ± 4.214
p1 100.6 ± 0.603
p2 -2.241e+08 ± 6.814e+06
p3 2.013e+07 ± 6.49e+05
p4 10.26 ± 0.09567
-200
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
50
100
150
200
250

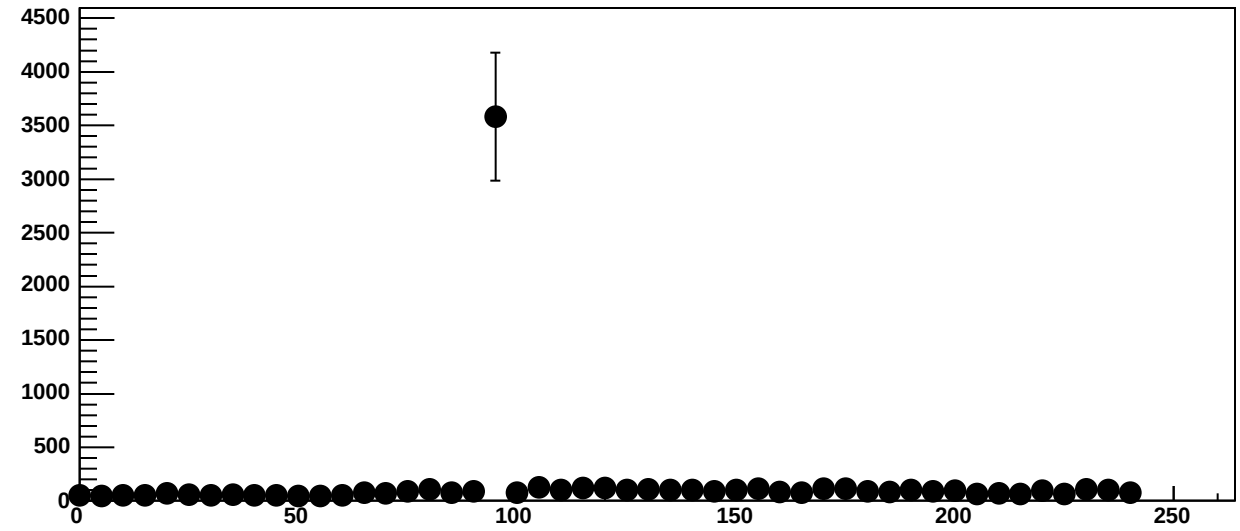

Chip 4, Channel 6, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 4, Channel 6, Enable 4, DAC=1600, ADC Residuals vs Hold
300
200
100
0
-100
0
50
100
150
200
250

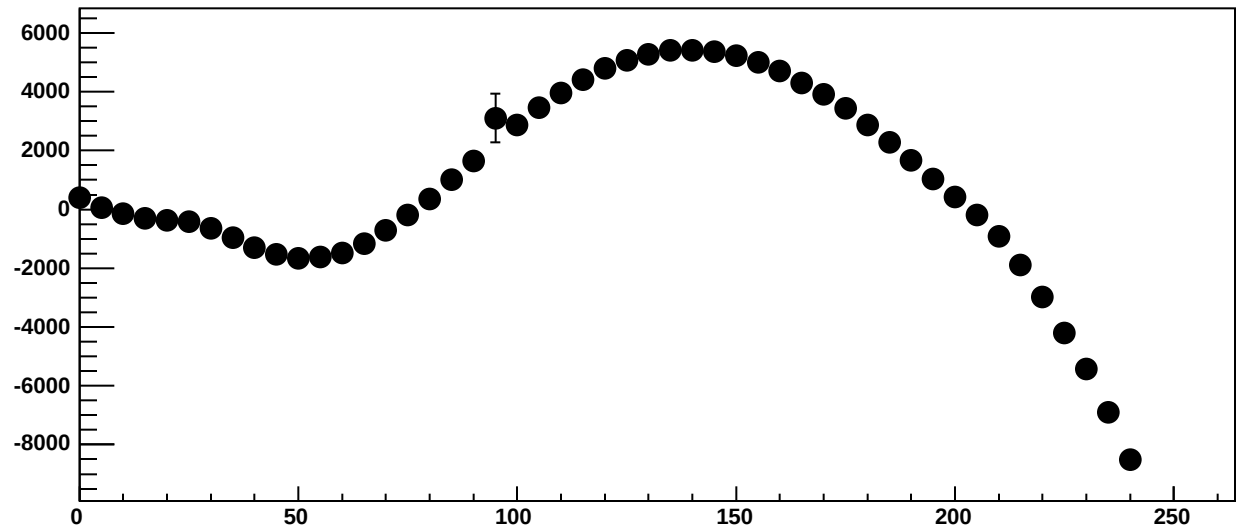
Chip 4, Channel 6, Enable 5, DAC=1600, ADC Mean vs Hold

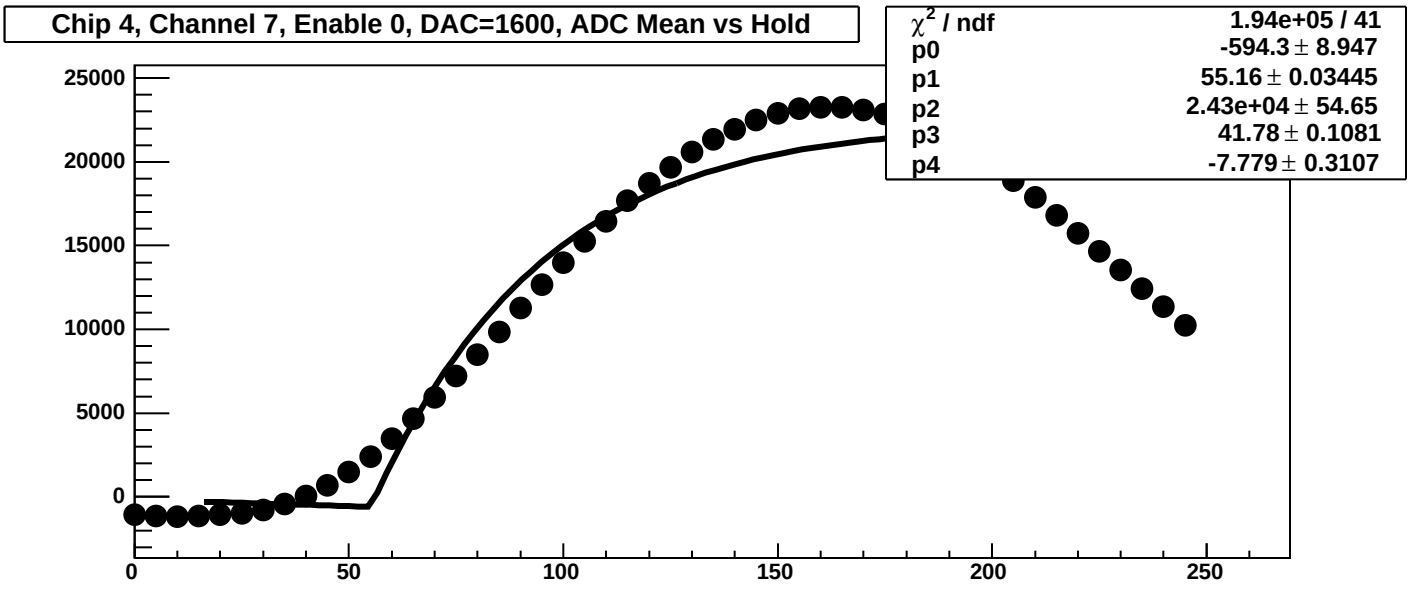


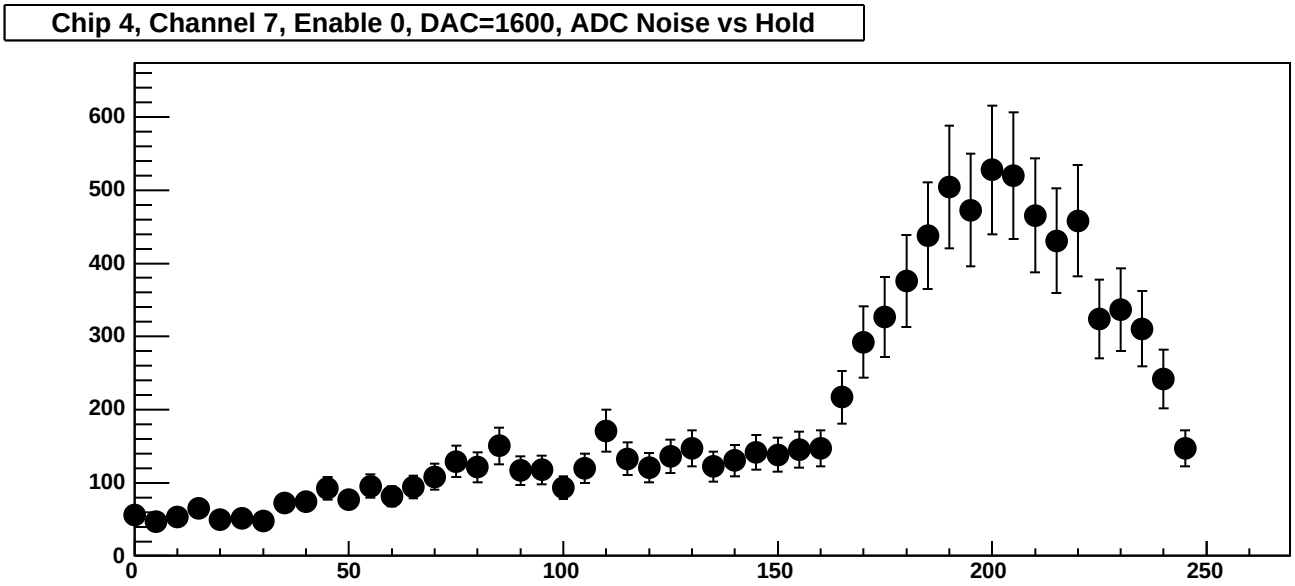
Chip 4, Channel 6, Enable 5, DAC=1600, ADC Noise vs Hold

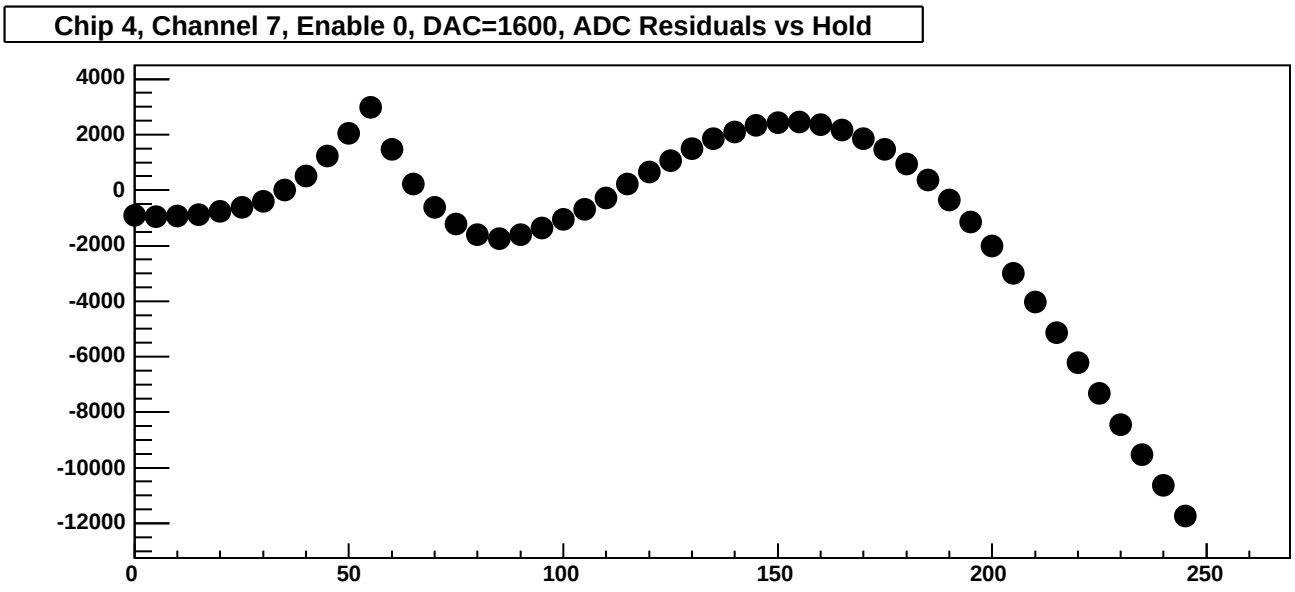


Chip 4, Channel 6, Enable 5, DAC=1600, ADC Residuals vs Hold

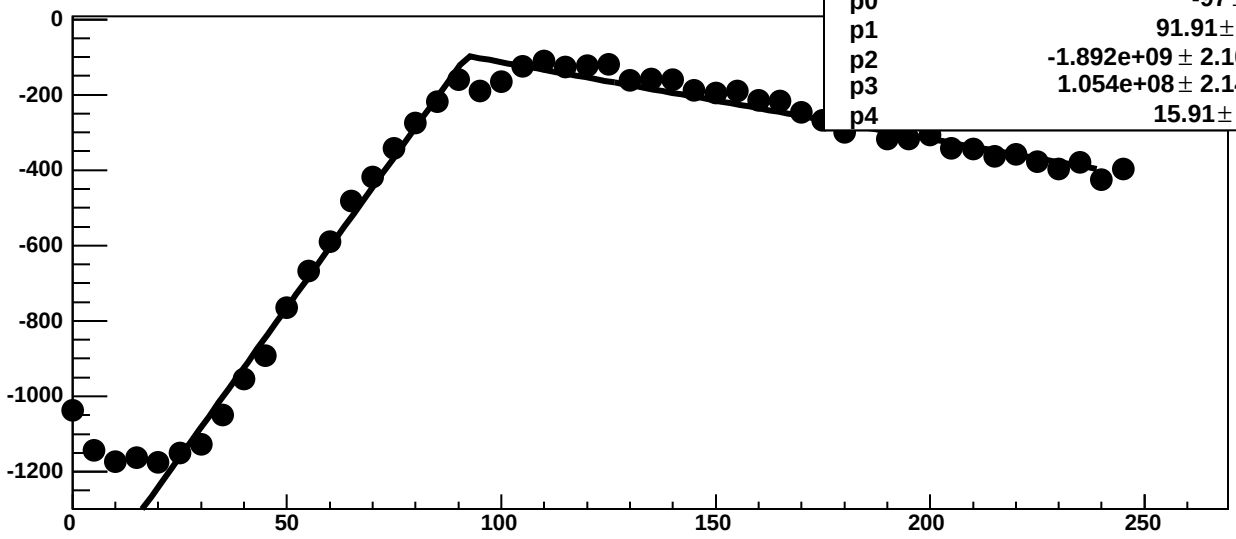



Chip 4, Channel 7, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.94e+05 / 41
p0 -594.3 ± 8.947
p1 55.16 ± 0.03445
p2 2.43e+04 ± 54.65
p3 41.78 ± 0.1081
p4 -7.779 ± 0.3107
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

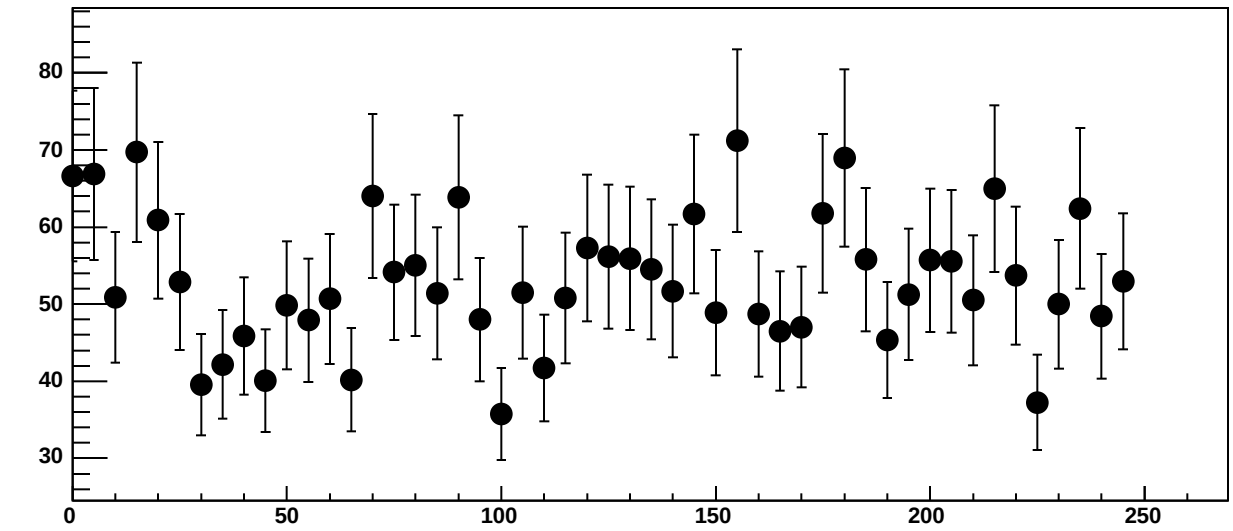

Chip 4, Channel 7, Enable 0, DAC=1600, ADC Noise vs Hold
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 4, Channel 7, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
0
50
100
150
200
250

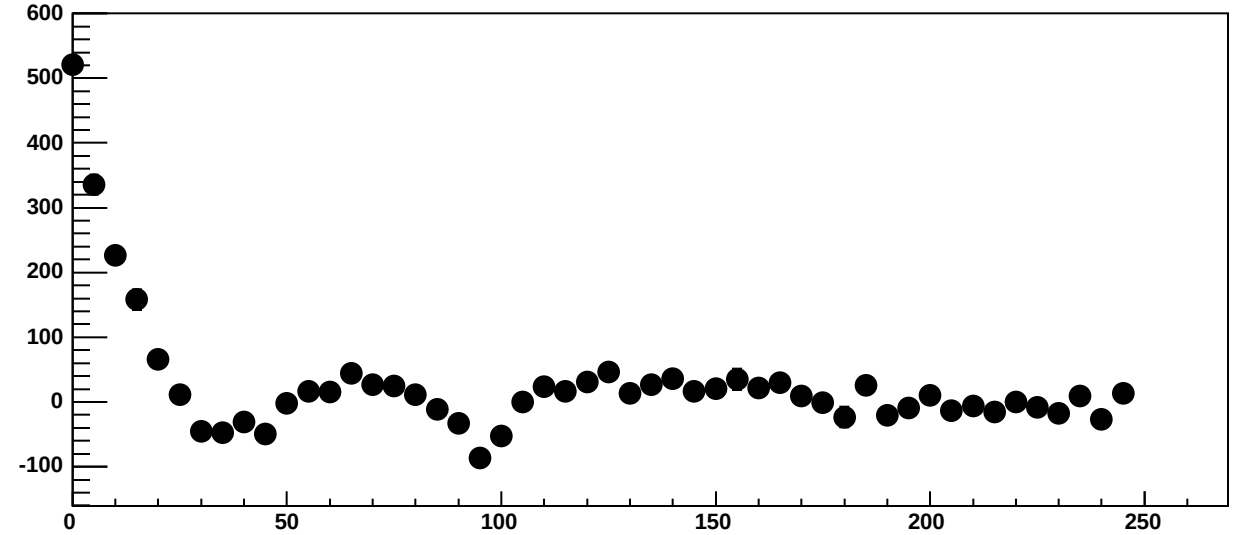
Chip 4, Channel 7, Enable 1, DAC=1600, ADC Mean vs Hold



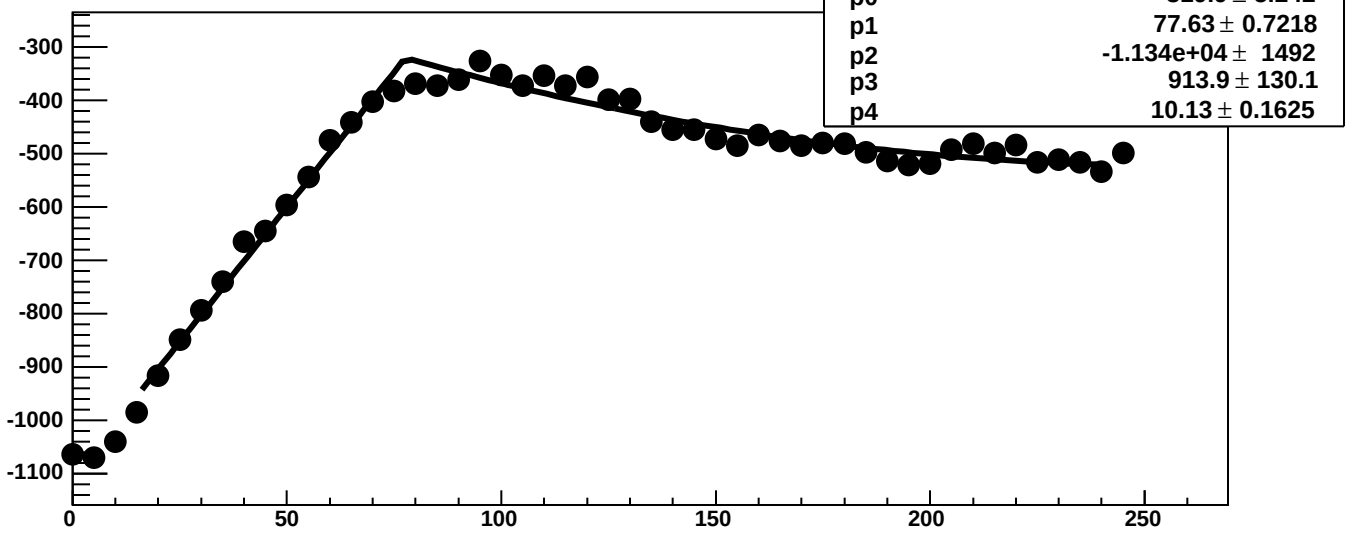
Chip 4, Channel 7, Enable 1, DAC=1600, ADC Noise vs Hold



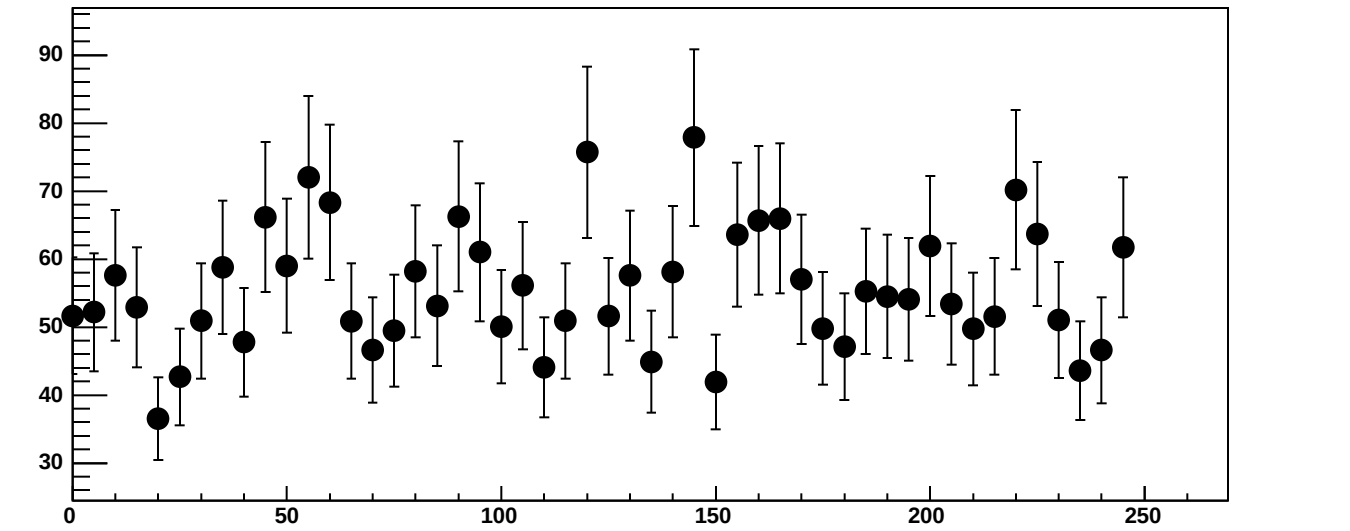
Chip 4, Channel 7, Enable 1, DAC=1600, ADC Residuals vs Hold



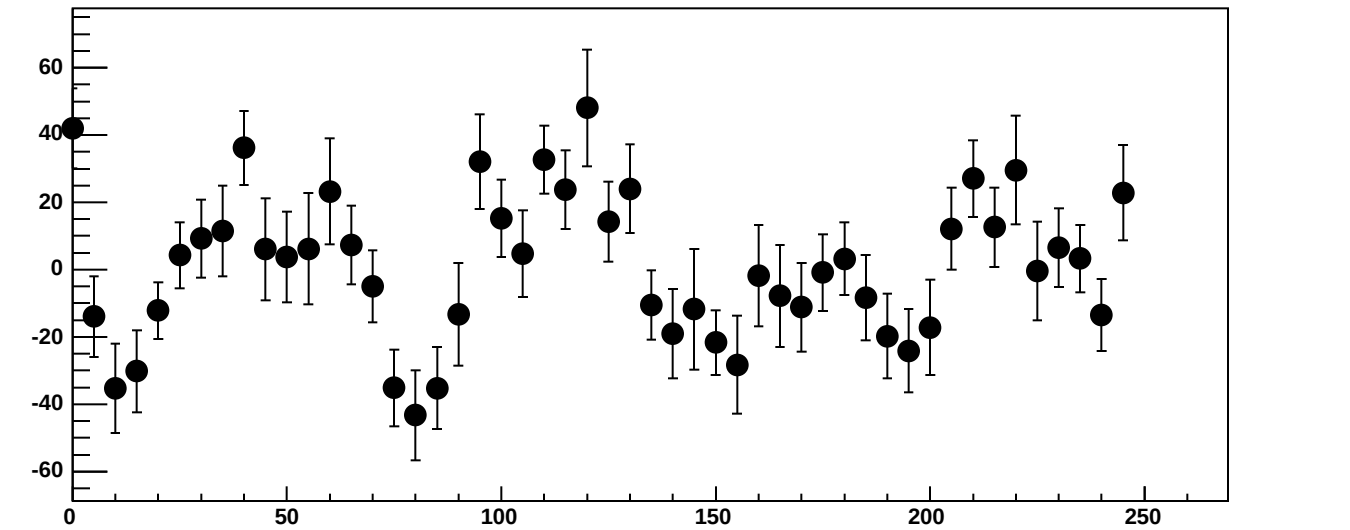
Chip 4, Channel 7, Enable 2, DAC=1600, ADC Mean vs Hold

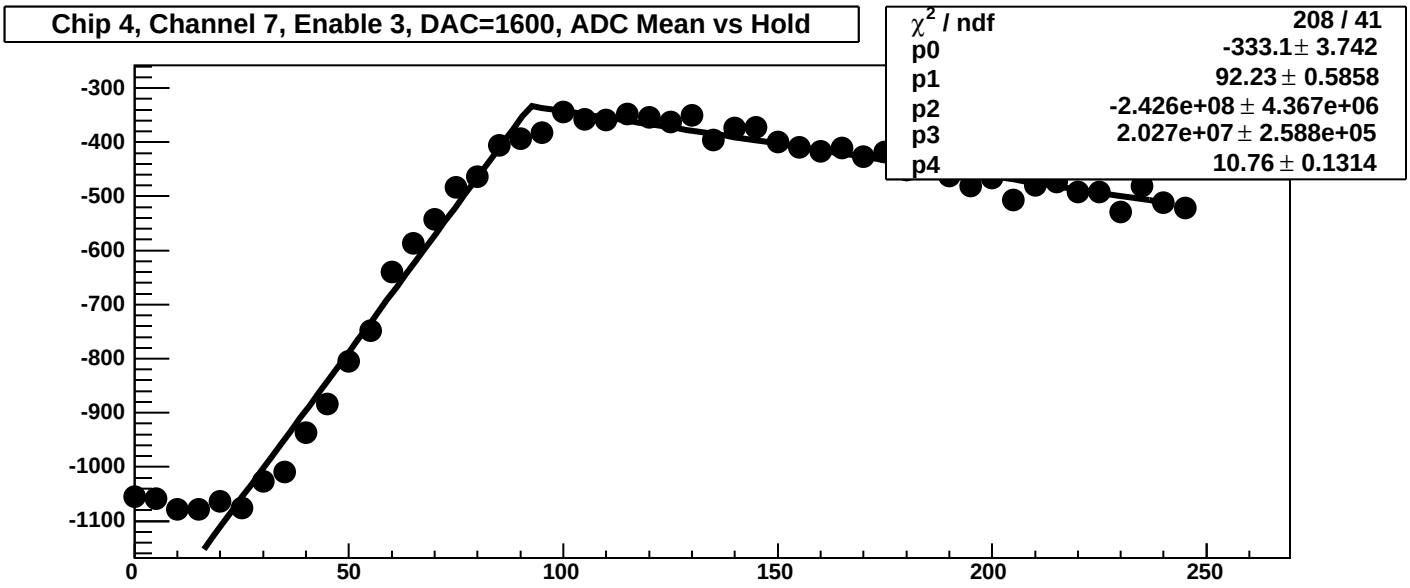


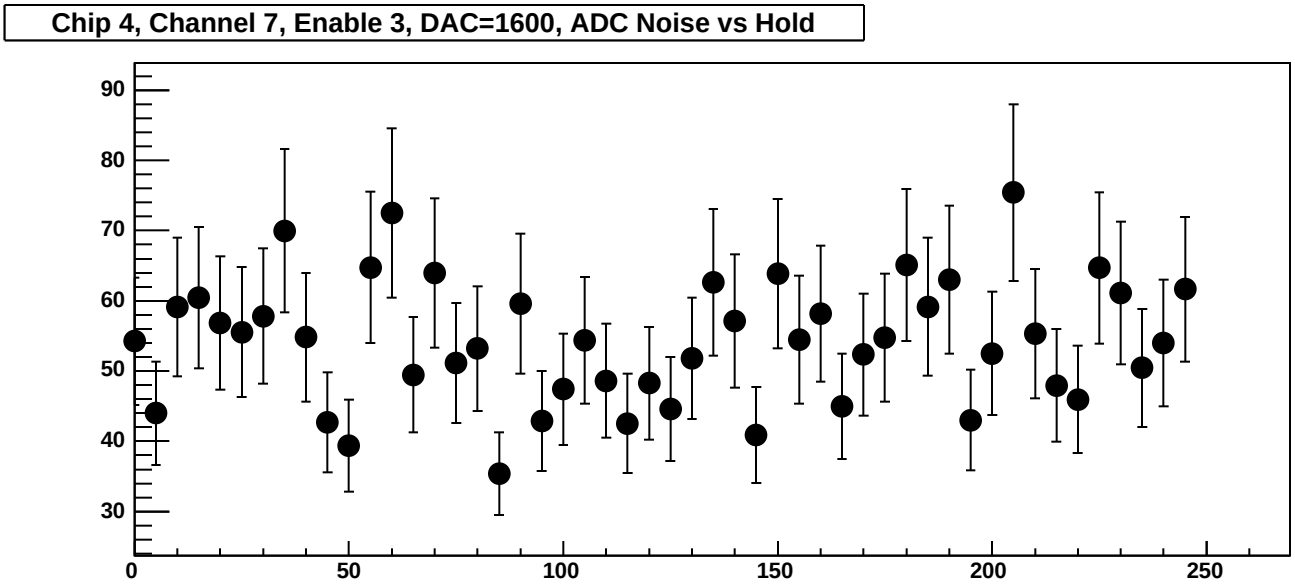
Chip 4, Channel 7, Enable 2, DAC=1600, ADC Noise vs Hold

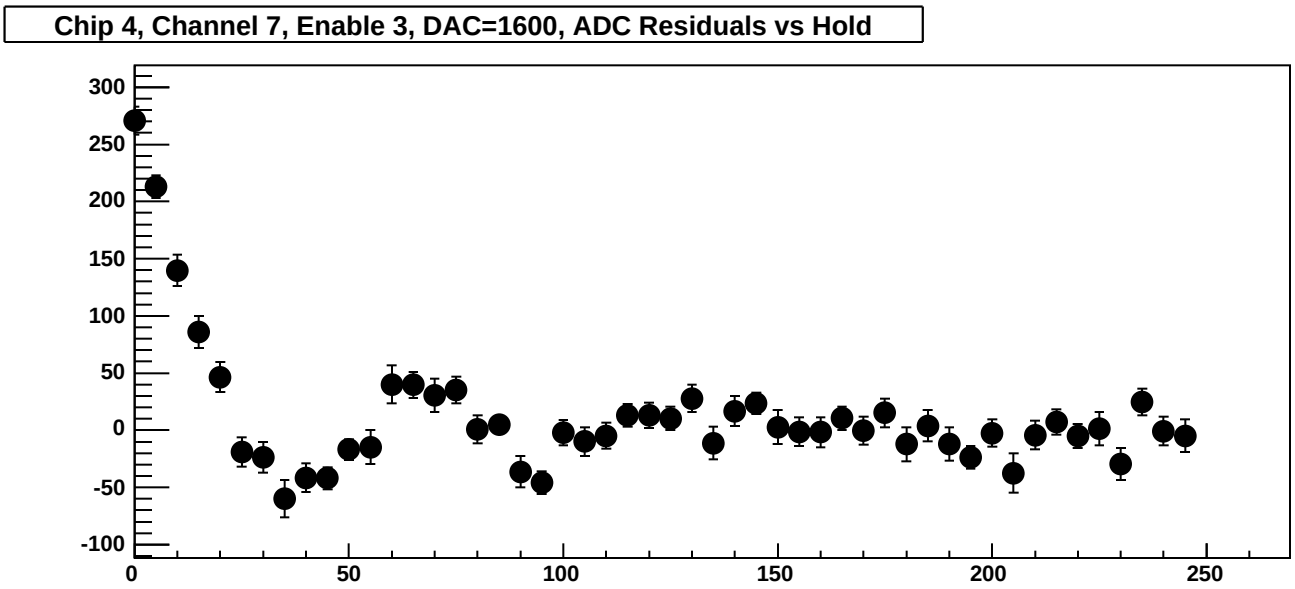


Chip 4, Channel 7, Enable 2, DAC=1600, ADC Residuals vs Hold

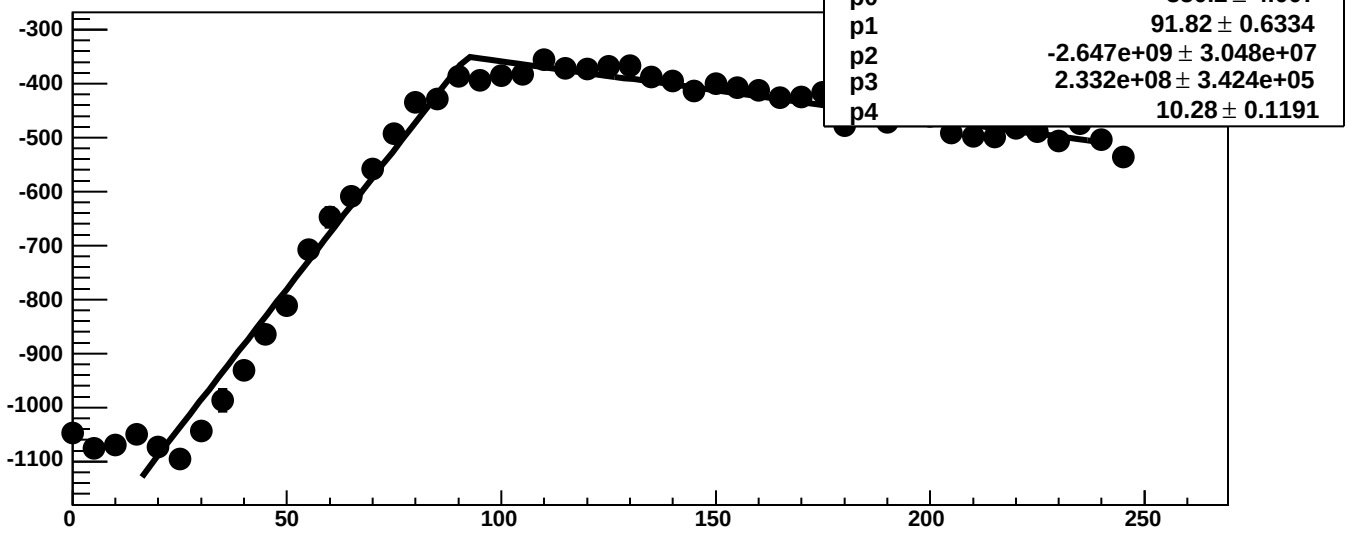



Chip 4, Channel 7, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 208 / 41
p0 -333.1 ± 3.742
p1 92.23 ± 0.5858
p2 -2.426e+08 ± 4.367e+06
p3 2.027e+07 ± 2.588e+05
p4 10.76 ± 0.1314
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
50
100
150
200
250

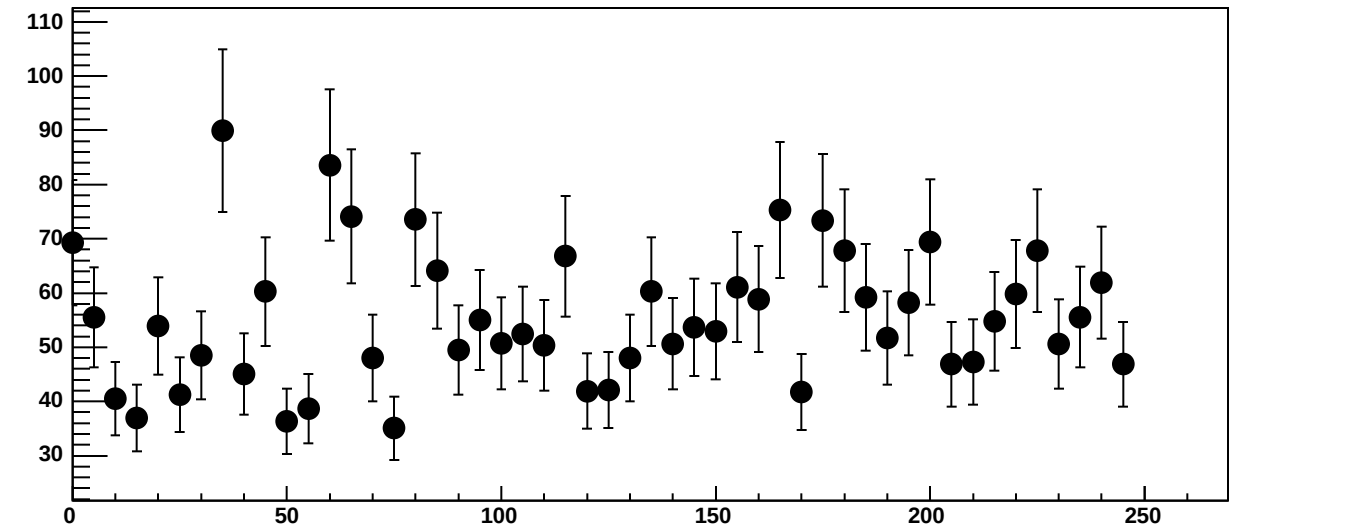

Chip 4, Channel 7, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 4, Channel 7, Enable 3, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

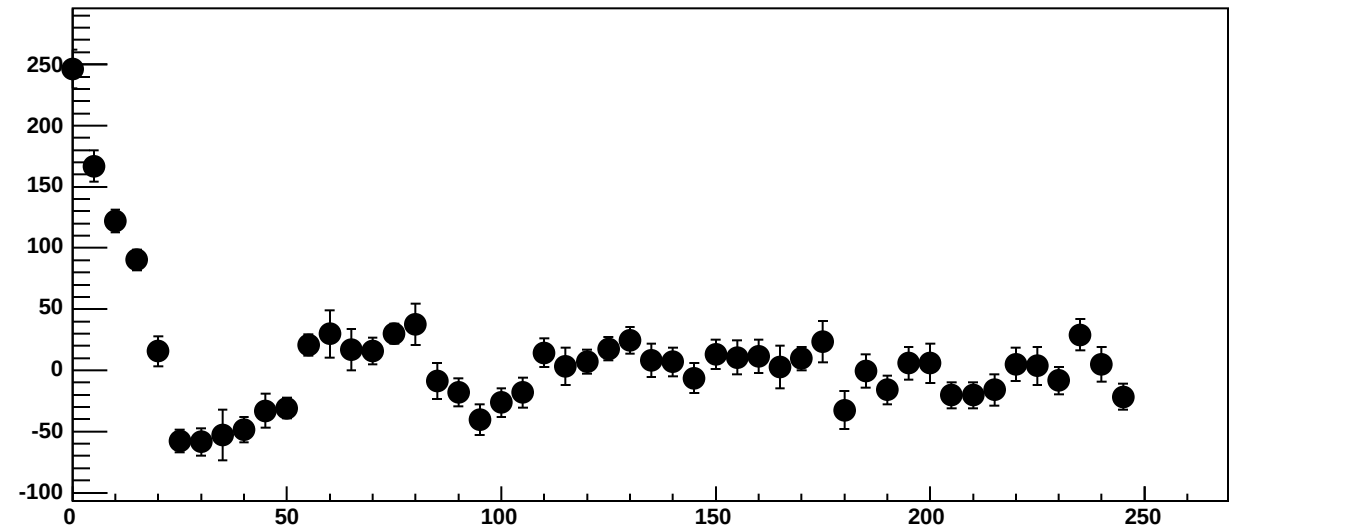
Chip 4, Channel 7, Enable 4, DAC=1600, ADC Mean vs Hold



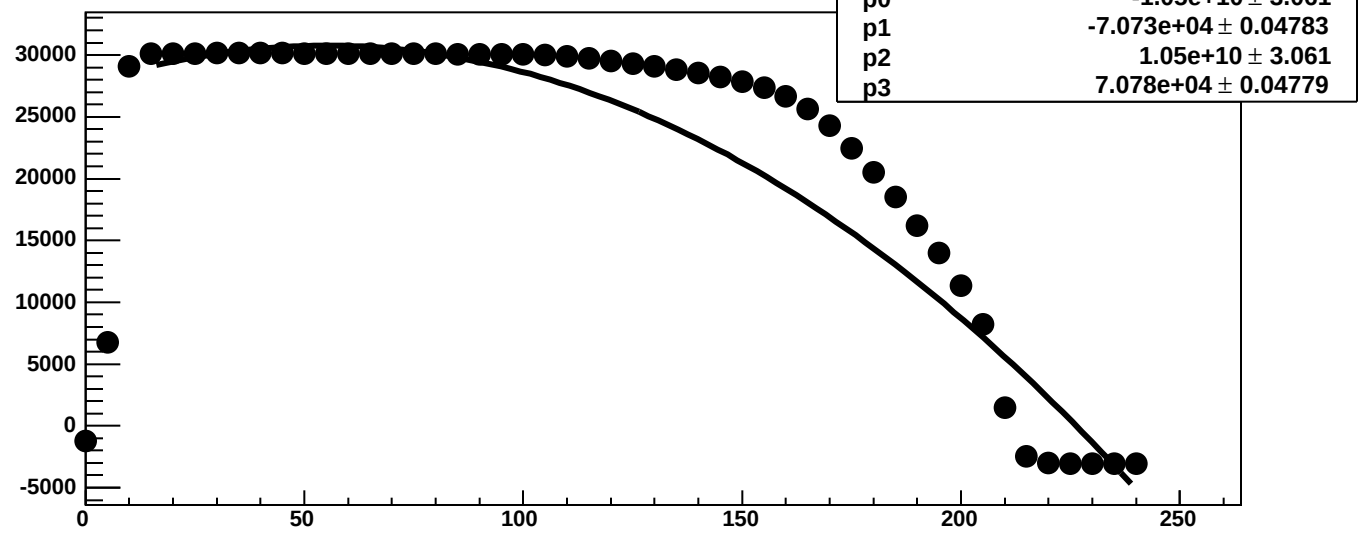
Chip 4, Channel 7, Enable 4, DAC=1600, ADC Noise vs Hold



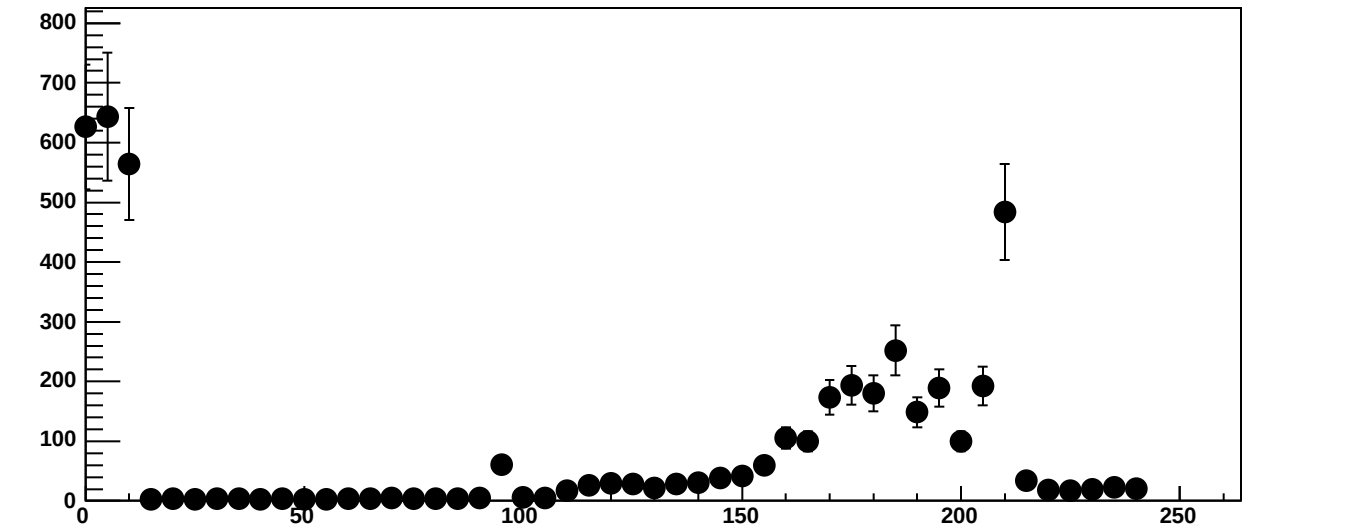
Chip 4, Channel 7, Enable 4, DAC=1600, ADC Residuals vs Hold



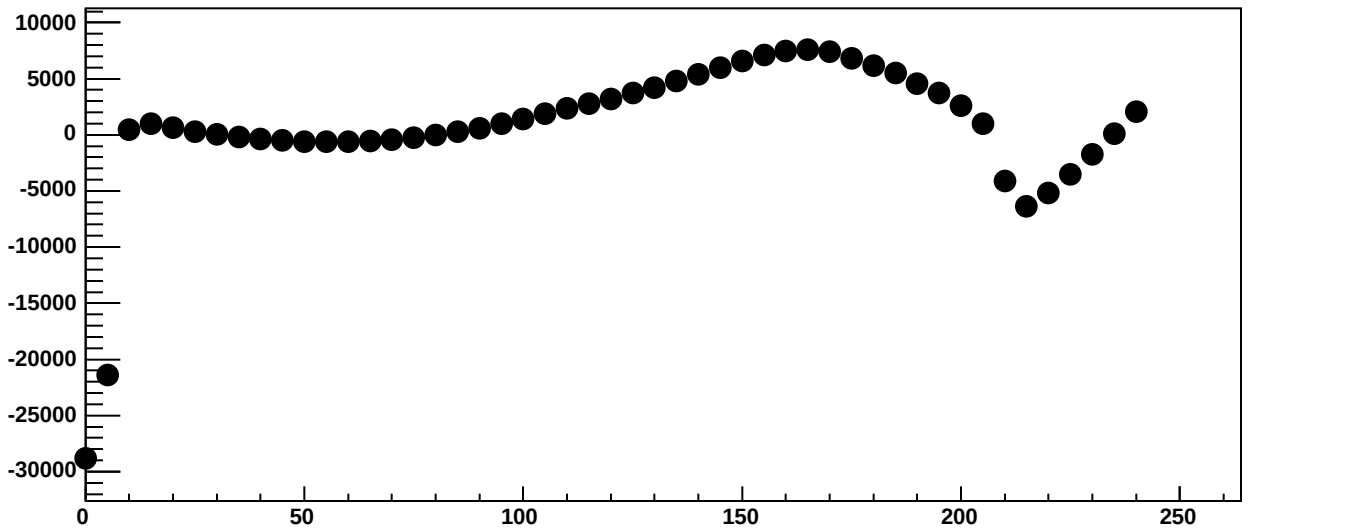
Chip 4, Channel 7, Enable 5!, DAC=1600, ADC Mean vs Hold

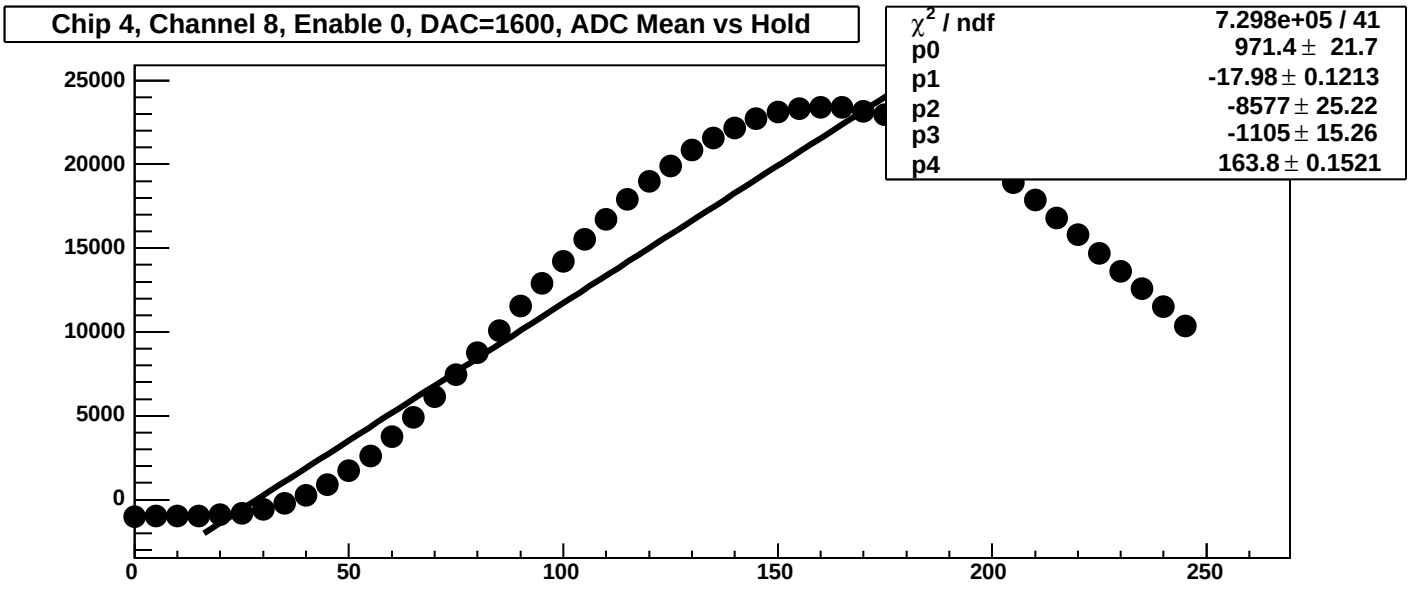


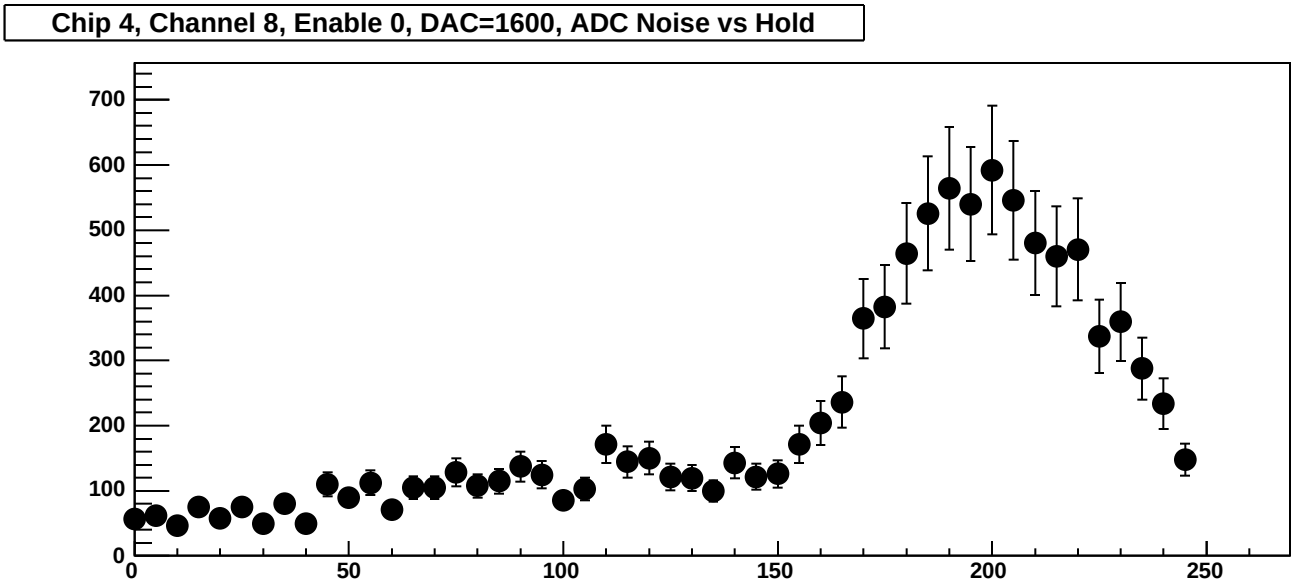
Chip 4, Channel 7, Enable 5!, DAC=1600, ADC Noise vs Hold

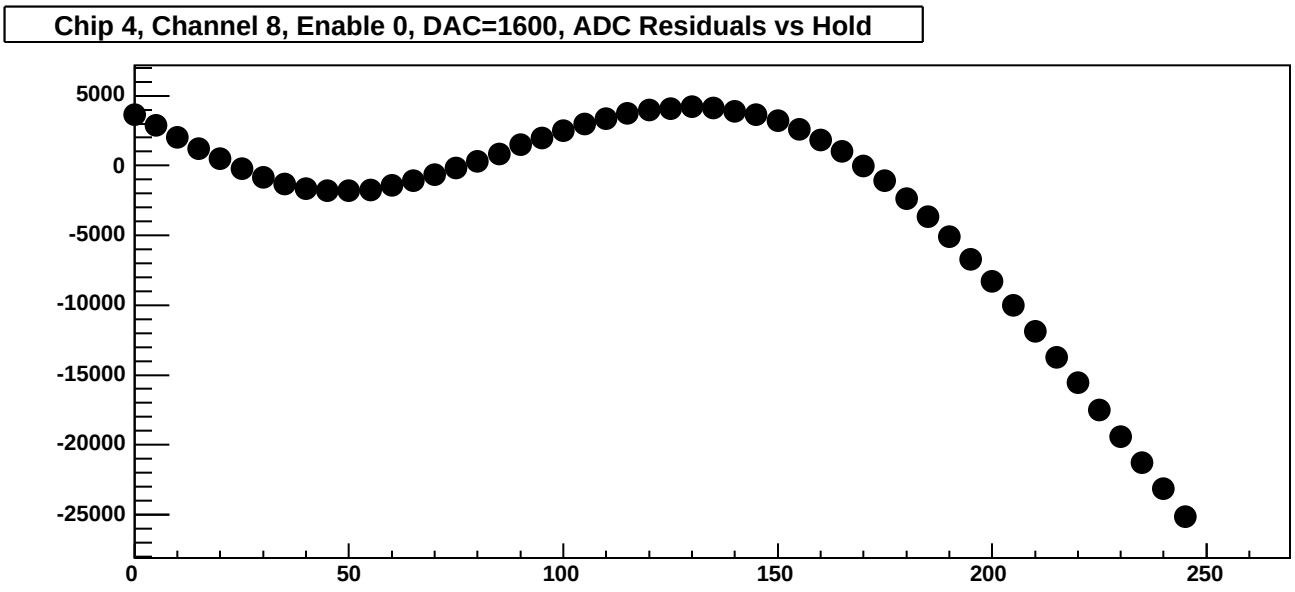


Chip 4, Channel 7, Enable 5!, DAC=1600, ADC Residuals vs Hold

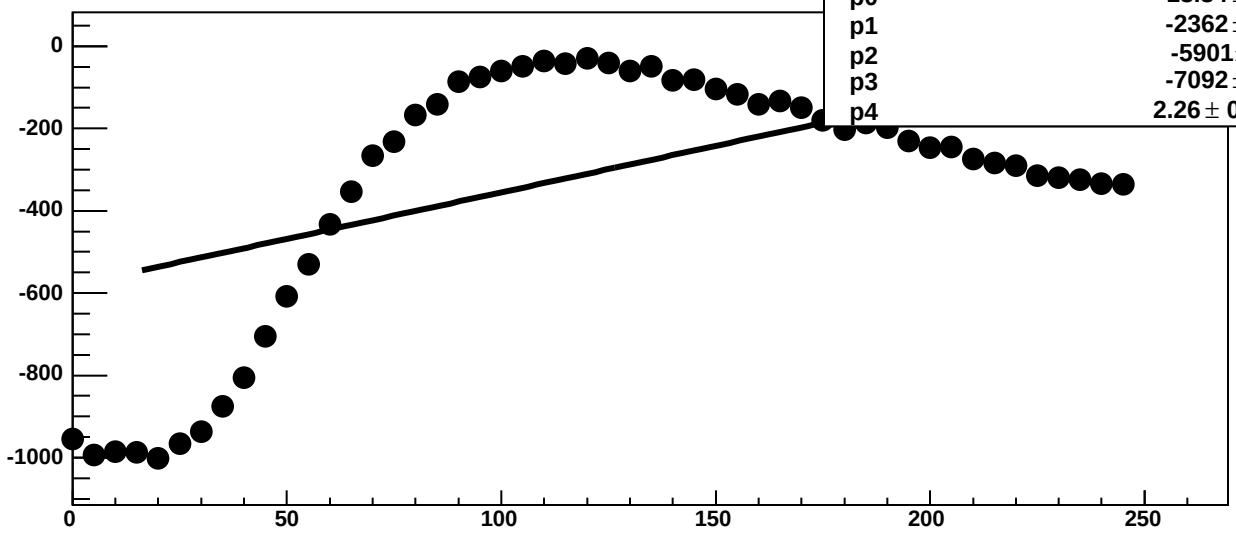



Chip 4, Channel 8, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.298e+05 / 41
p0 971.4 ± 21.7
p1 -17.98 ± 0.1213
p2 -8577 ± 25.22
p3 -1105 ± 15.26
p4 163.8 ± 0.1521
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

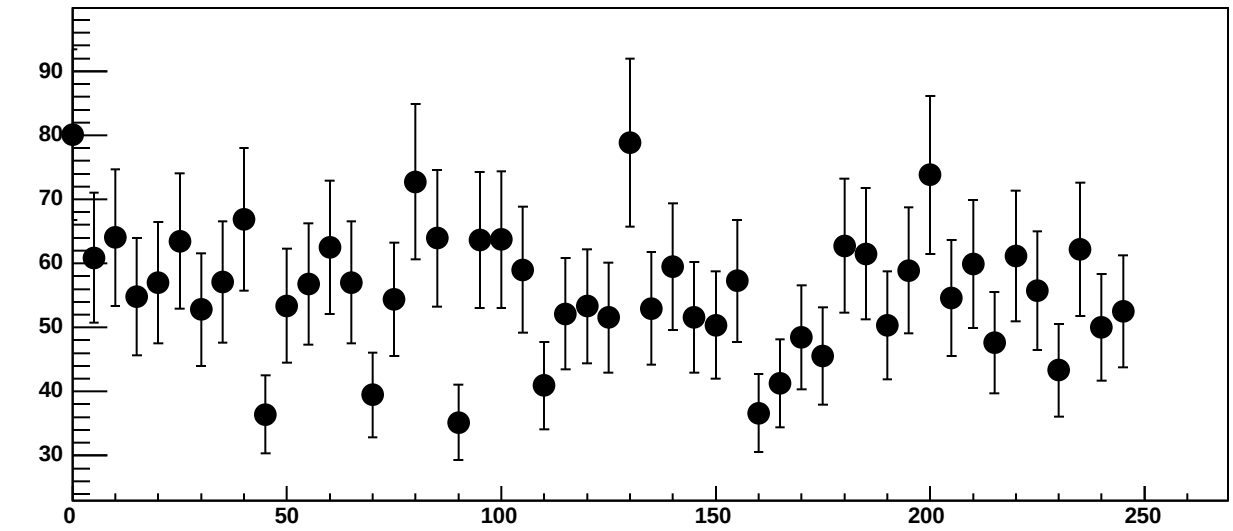

Chip 4, Channel 8, Enable 0, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 4, Channel 8, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
0
50
100
150
200
250

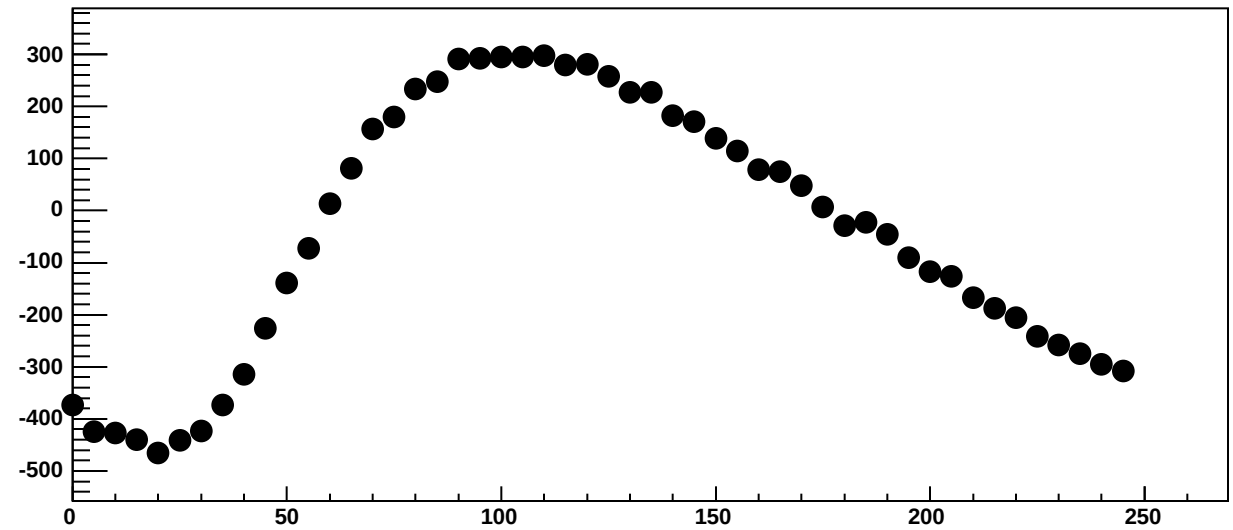
Chip 4, Channel 8, Enable 1, DAC=1600, ADC Mean vs Hold



Chip 4, Channel 8, Enable 1, DAC=1600, ADC Noise vs Hold

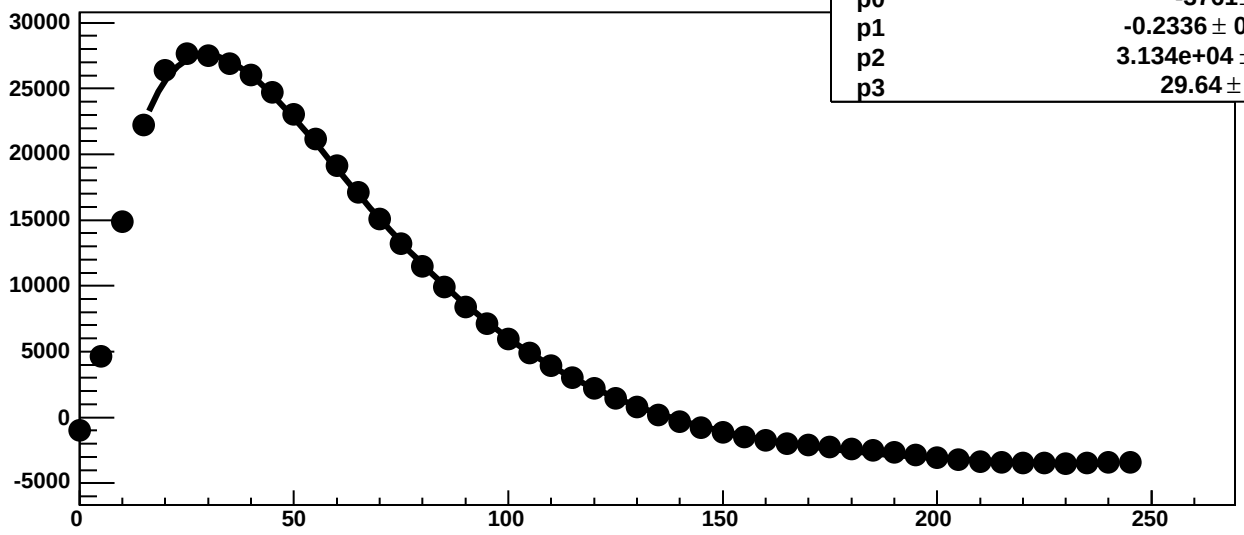


Chip 4, Channel 8, Enable 1, DAC=1600, ADC Residuals vs Hold

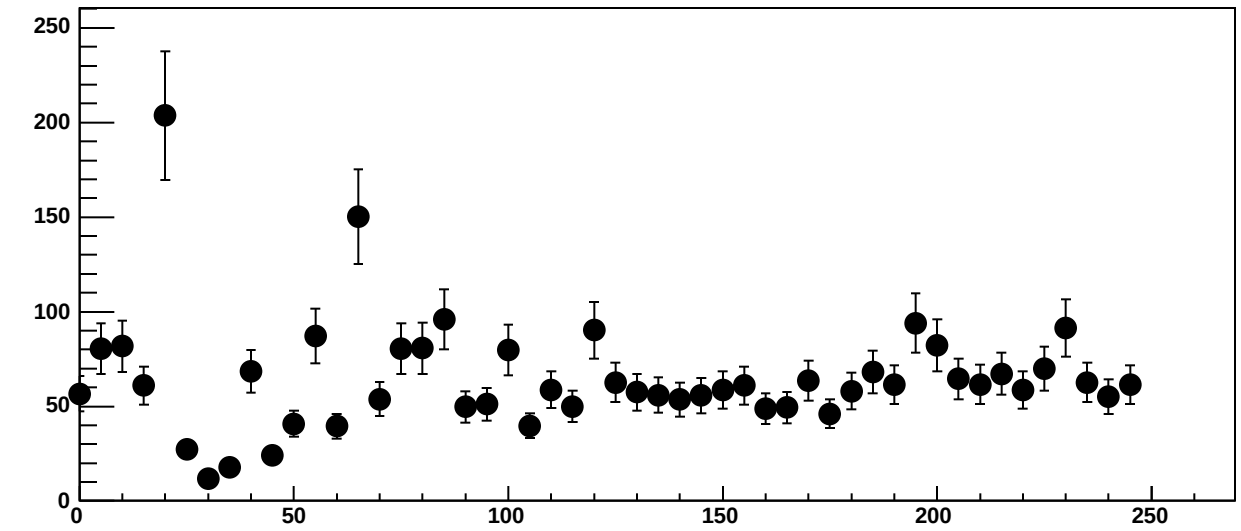


Chip 4, Channel 8, Enable 2!, DAC=1600, ADC Mean vs Hold

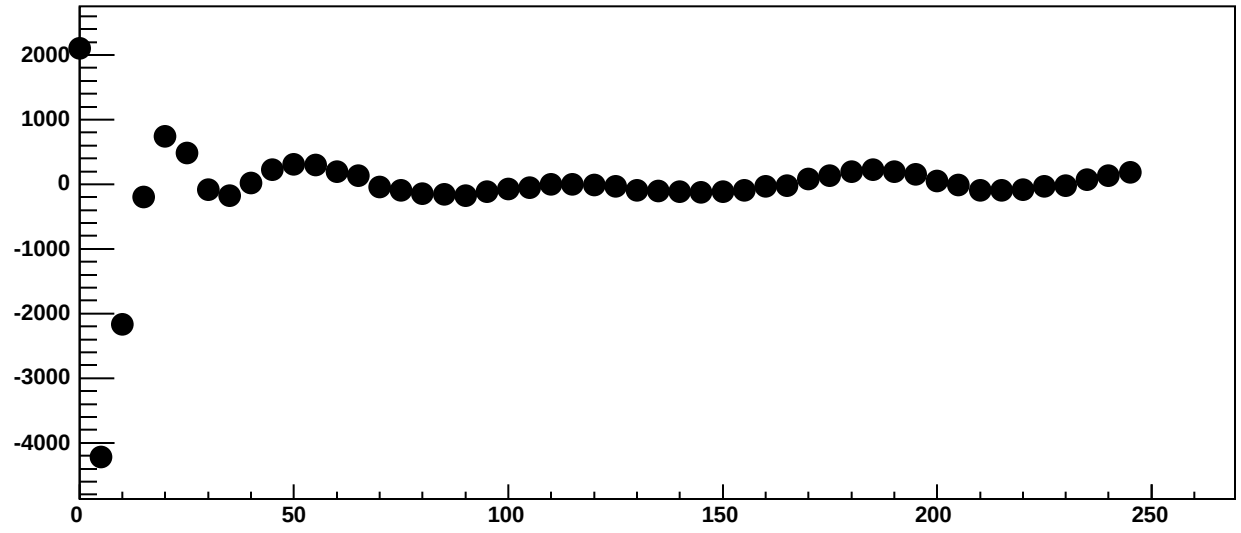
χ^2 / ndf	1.49e+04 / 42
p0	-3761 ± 4.045
p1	-0.2336 ± 0.01692
p2	3.134e+04 ± 4.334
p3	29.64 ± 0.0103



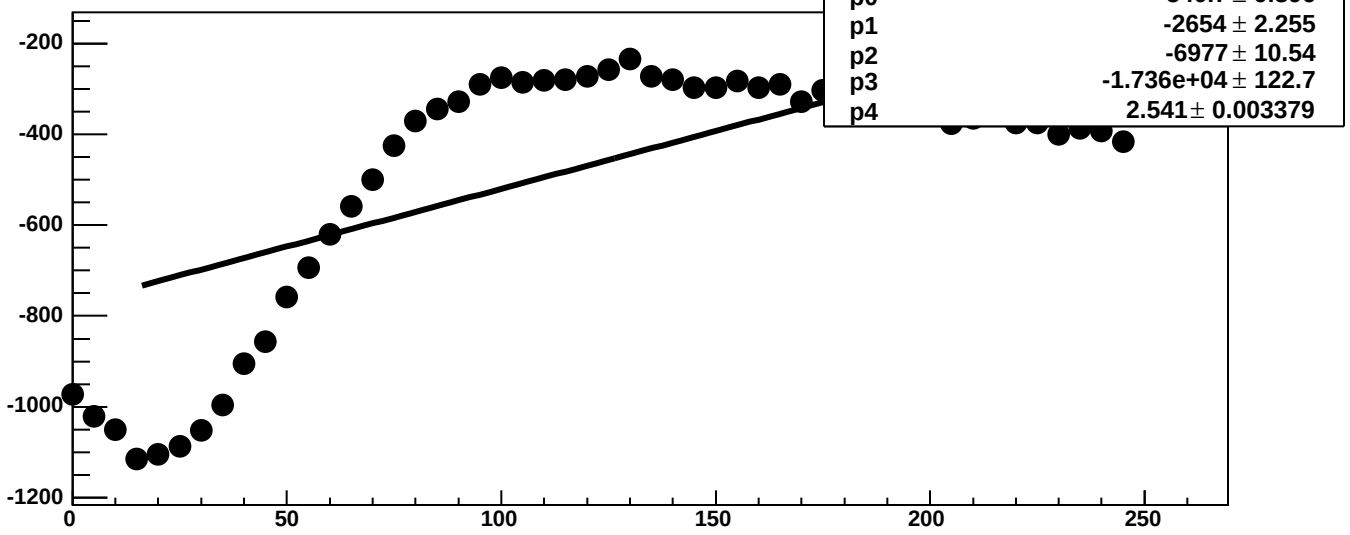
Chip 4, Channel 8, Enable 2!, DAC=1600, ADC Noise vs Hold



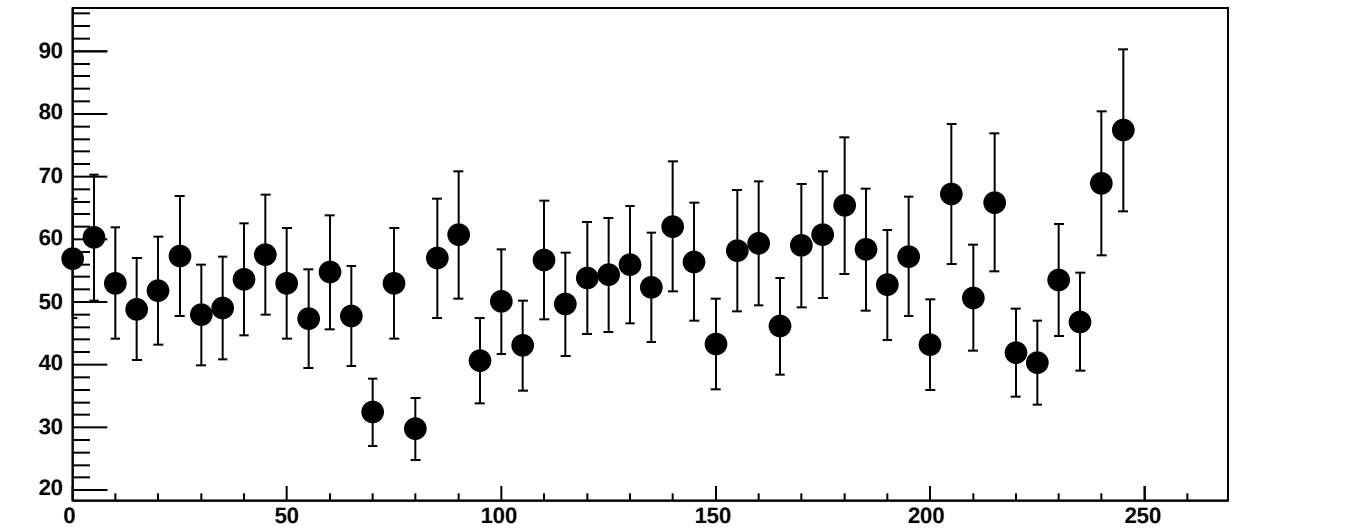
Chip 4, Channel 8, Enable 2!, DAC=1600, ADC Residuals vs Hold



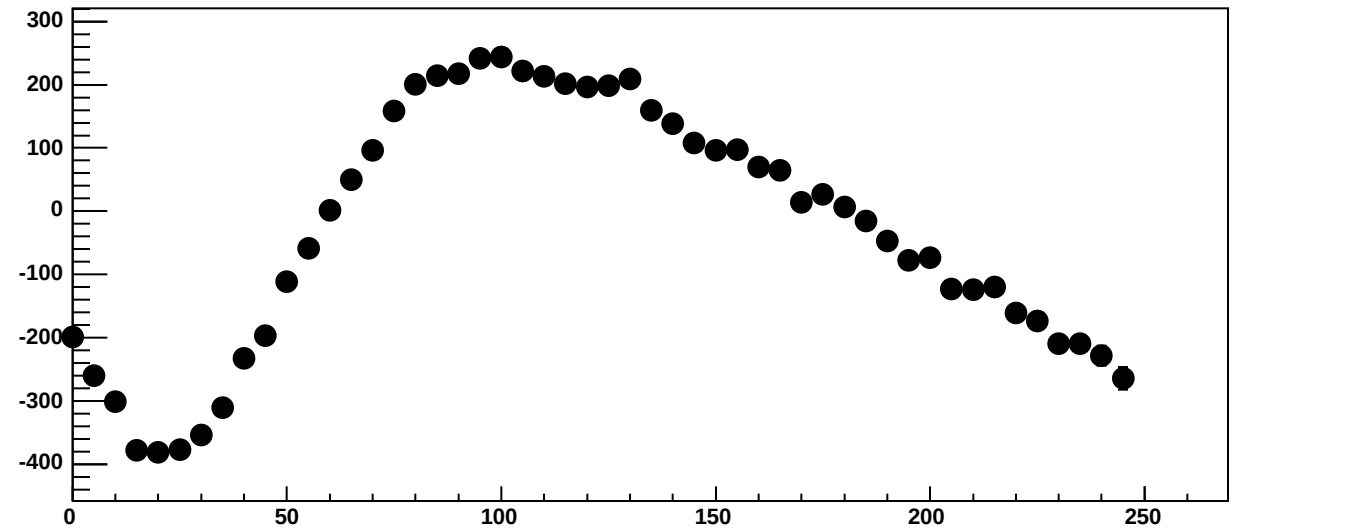
Chip 4, Channel 8, Enable 3, DAC=1600, ADC Mean vs Hold



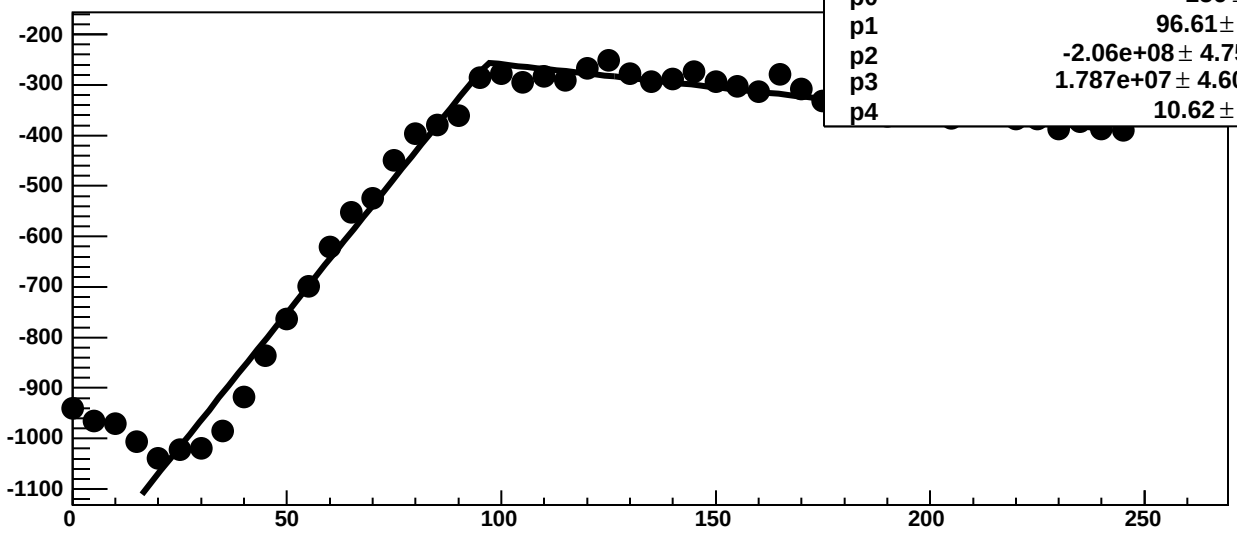
Chip 4, Channel 8, Enable 3, DAC=1600, ADC Noise vs Hold



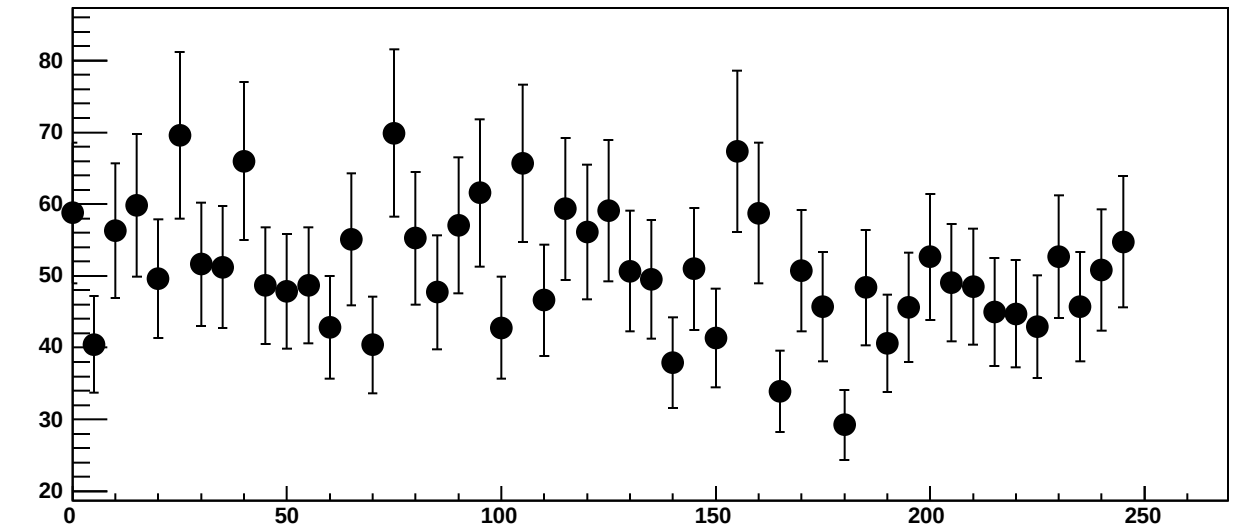
Chip 4, Channel 8, Enable 3, DAC=1600, ADC Residuals vs Hold



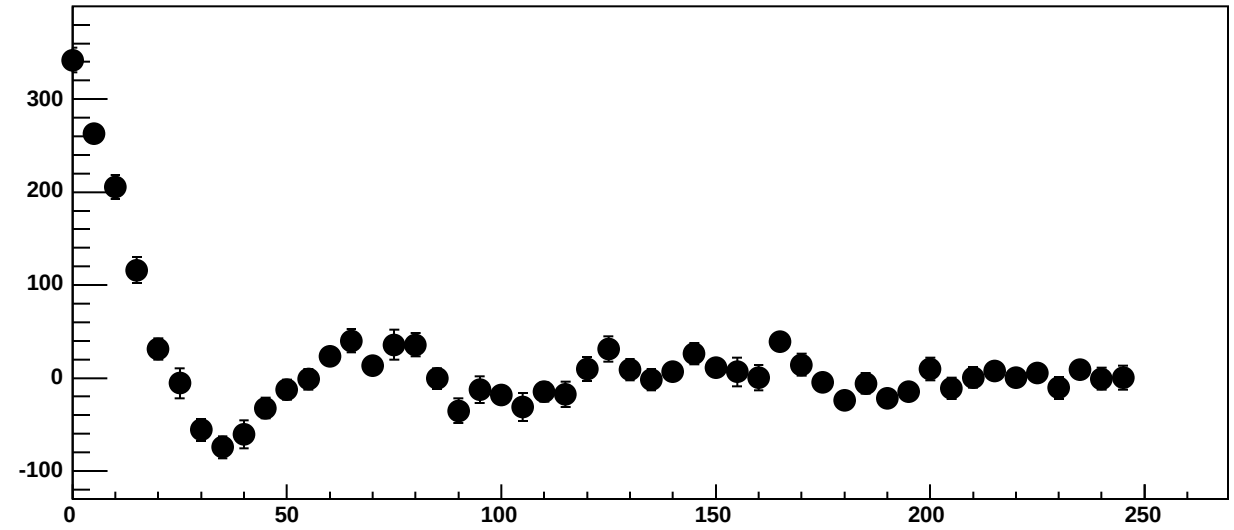
Chip 4, Channel 8, Enable 4, DAC=1600, ADC Mean vs Hold

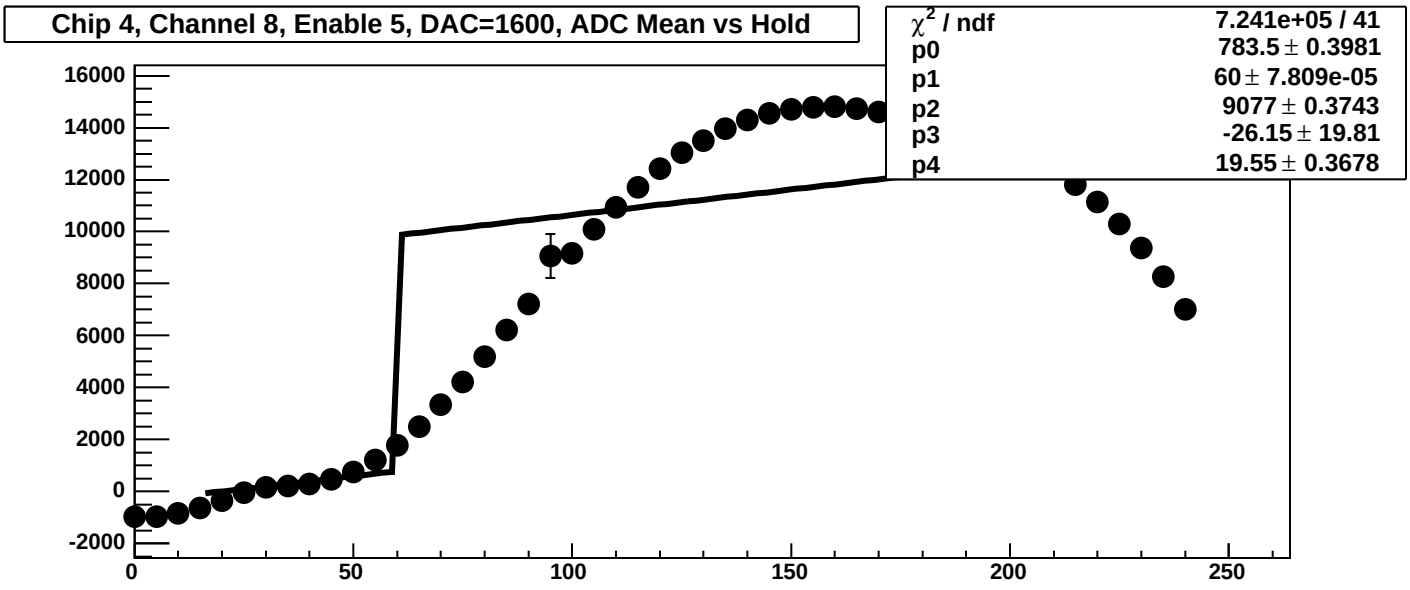


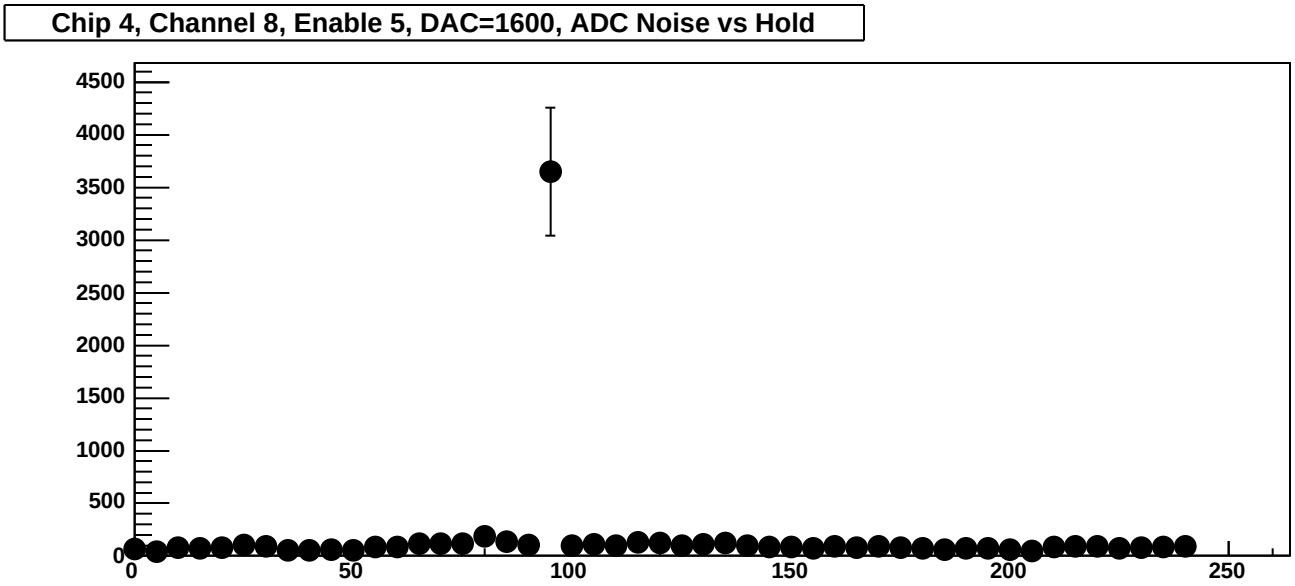
Chip 4, Channel 8, Enable 4, DAC=1600, ADC Noise vs Hold

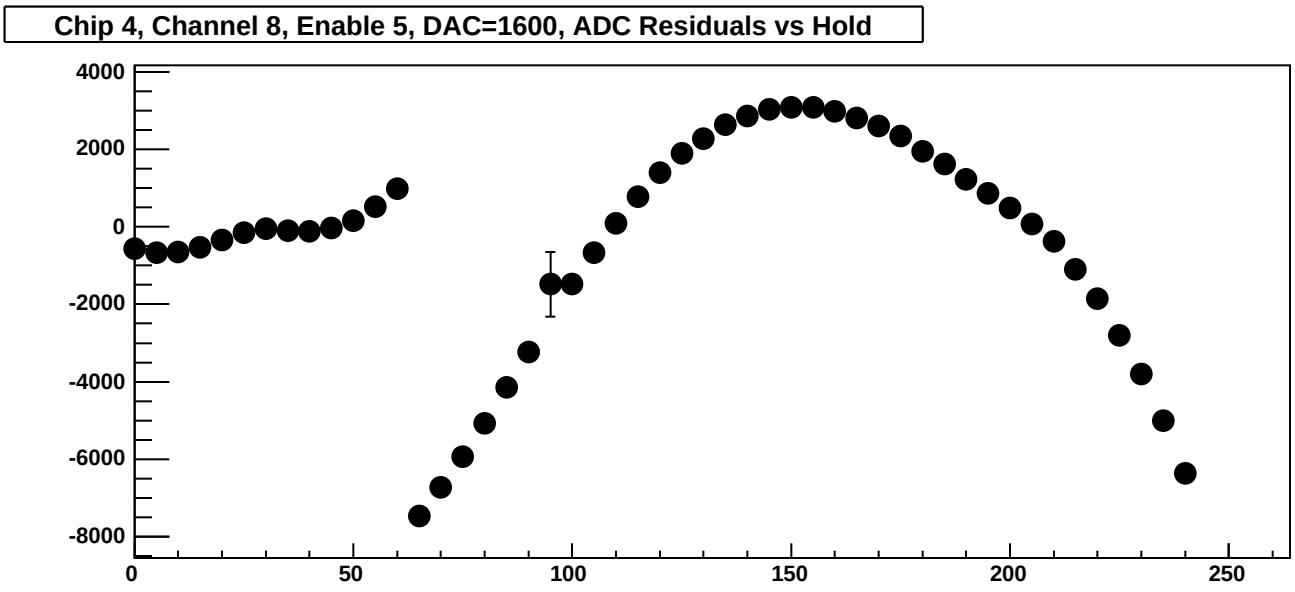


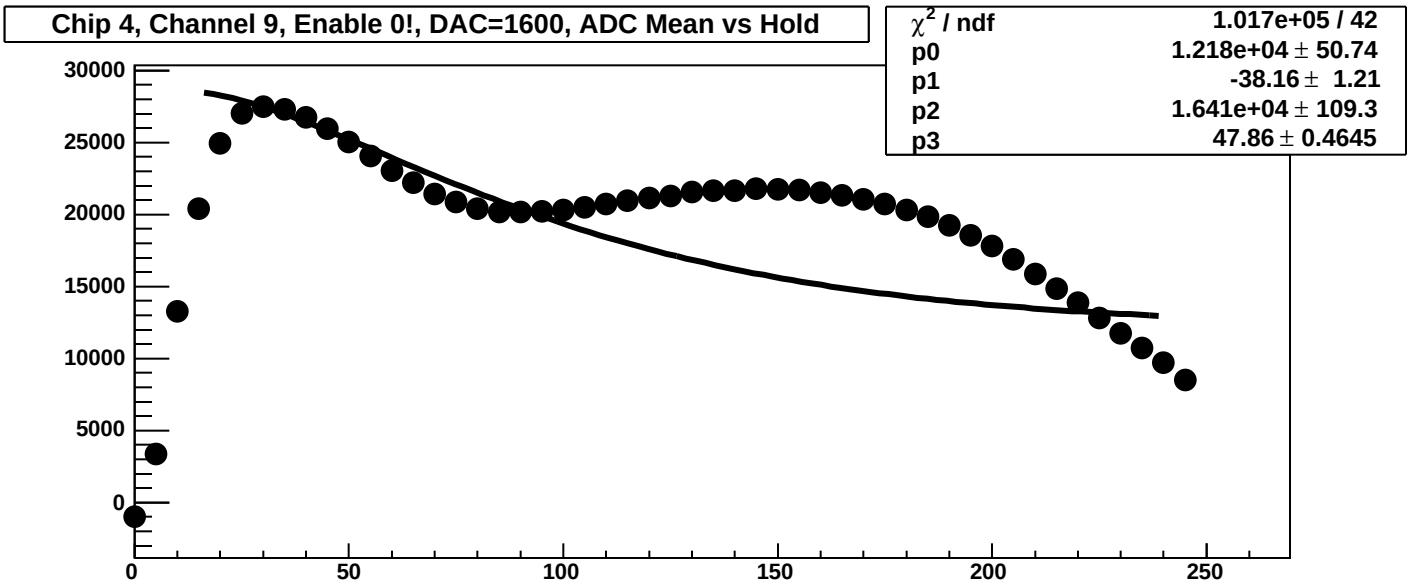
Chip 4, Channel 8, Enable 4, DAC=1600, ADC Residuals vs Hold

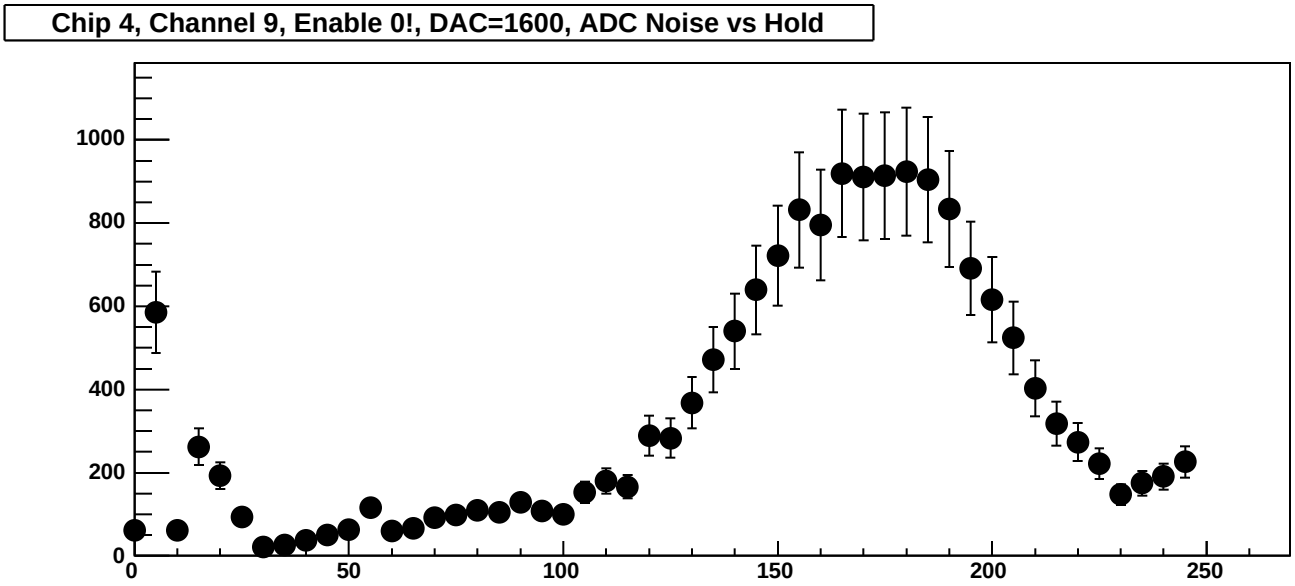


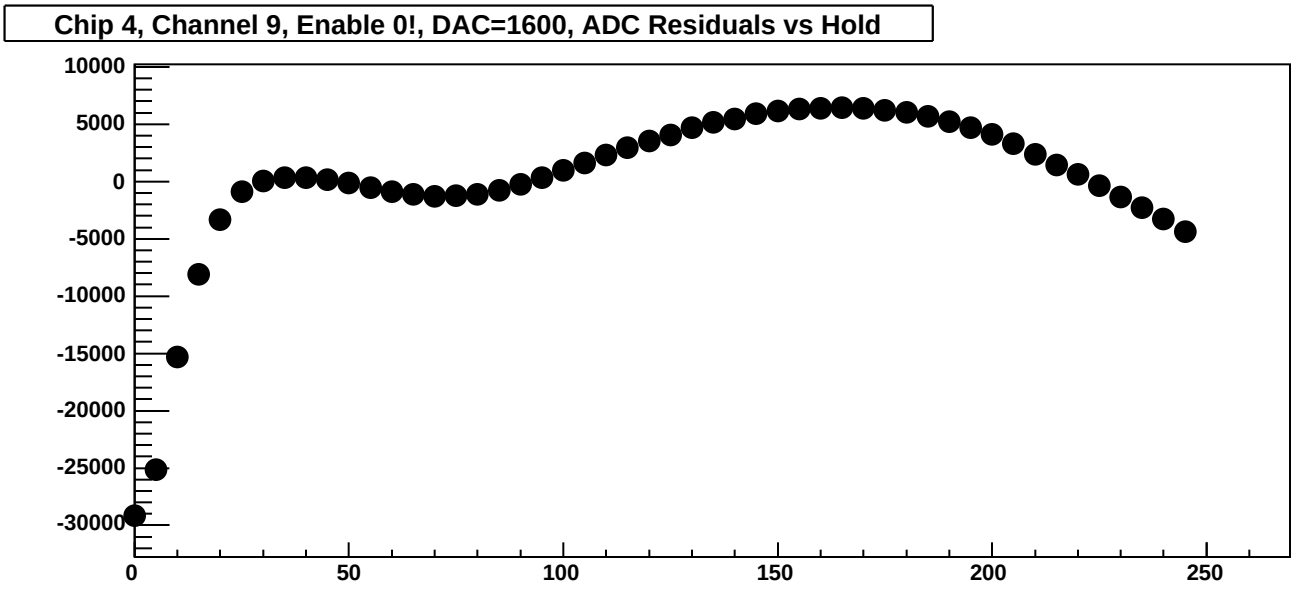

Chip 4, Channel 8, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.241e+05 / 41
p0 783.5 ± 0.3981
p1 60 ± 7.809e-05
p2 9077 ± 0.3743
p3 -26.15 ± 19.81
p4 19.55 ± 0.3678
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

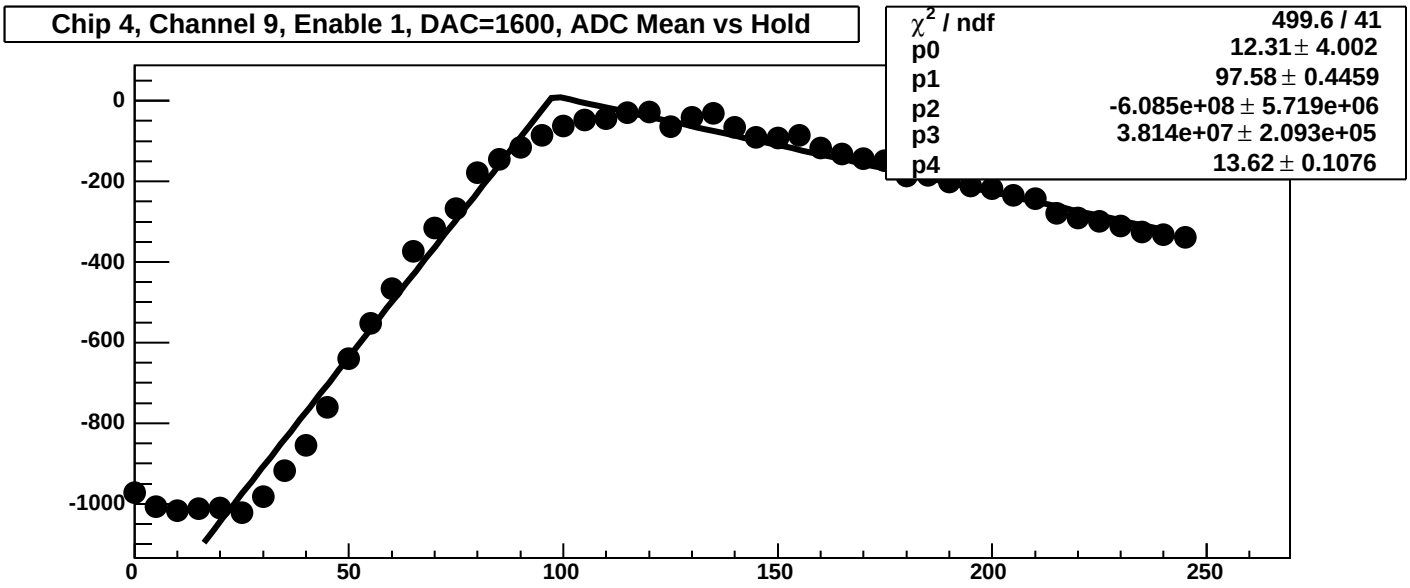

Chip 4, Channel 8, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250

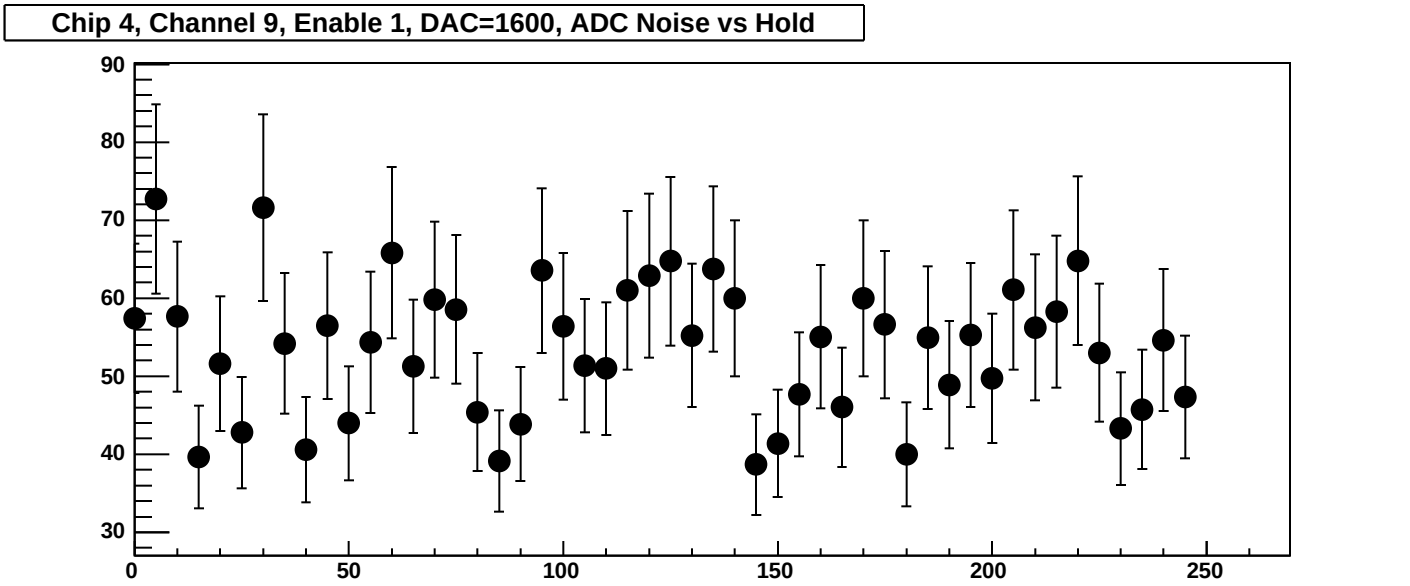

Chip 4, Channel 8, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250

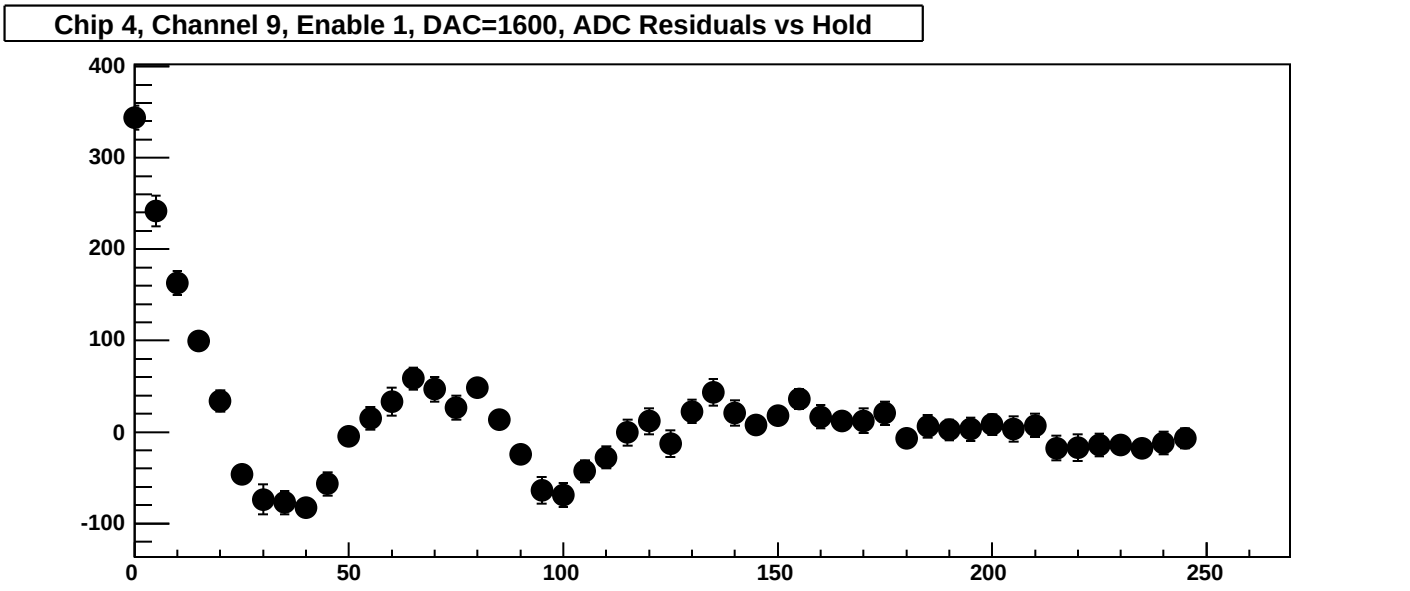

Chip 4, Channel 9, Enable 0!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.017e+05 / 42
p0 1.218e+04 ± 50.74
p1 -38.16 ± 1.21
p2 1.641e+04 ± 109.3
p3 47.86 ± 0.4645
30000
25000
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 4, Channel 9, Enable 0!, DAC=1600, ADC Noise vs Hold
1000
800
600
400
200
0
0
50
100
150
200
250

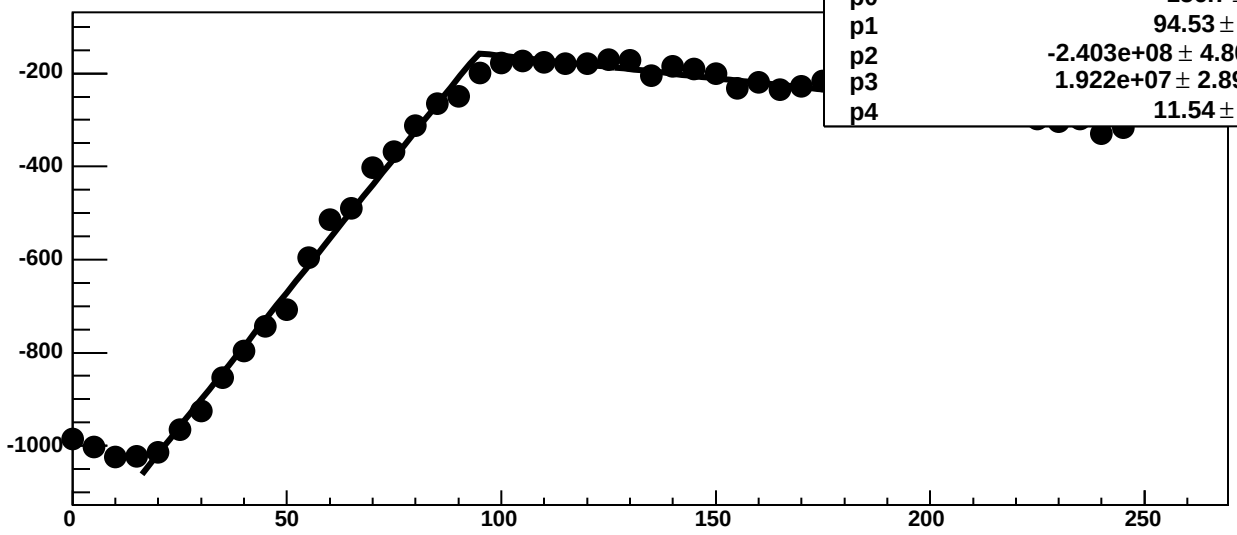

Chip 4, Channel 9, Enable 0!, DAC=1600, ADC Residuals vs Hold
10000
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
0
50
100
150
200
250


Chip 4, Channel 9, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 499.6 / 41
p0 12.31 ± 4.002
p1 97.58 ± 0.4459
p2 -6.085e+08 ± 5.719e+06
p3 3.814e+07 ± 2.093e+05
p4 13.62 ± 0.1076
0
-200
-400
-600
-800
-1000
0
50
100
150
200
250

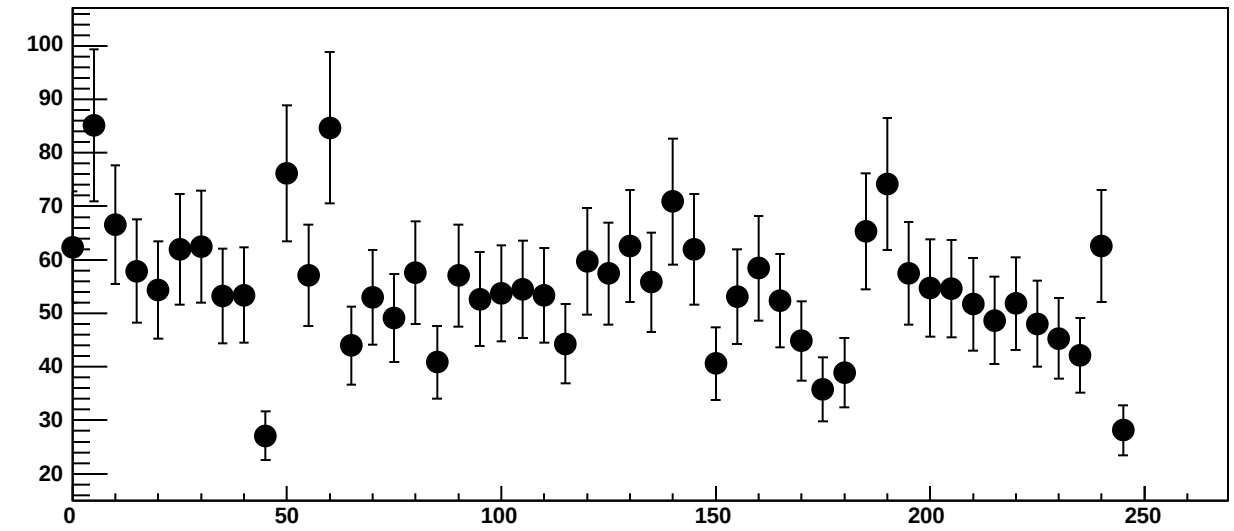

Chip 4, Channel 9, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 4, Channel 9, Enable 1, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

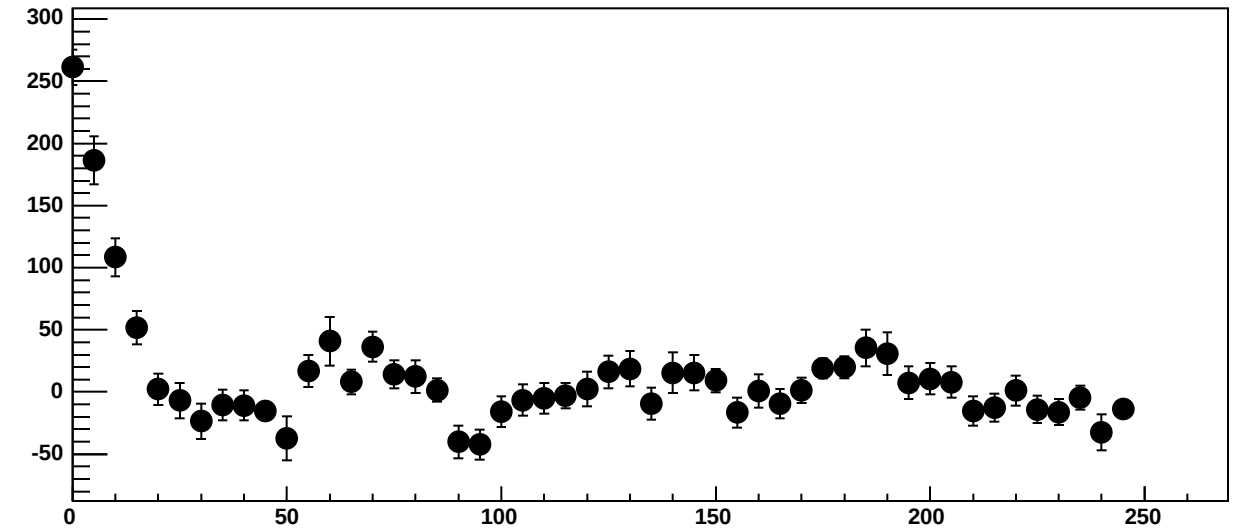
Chip 4, Channel 9, Enable 2, DAC=1600, ADC Mean vs Hold



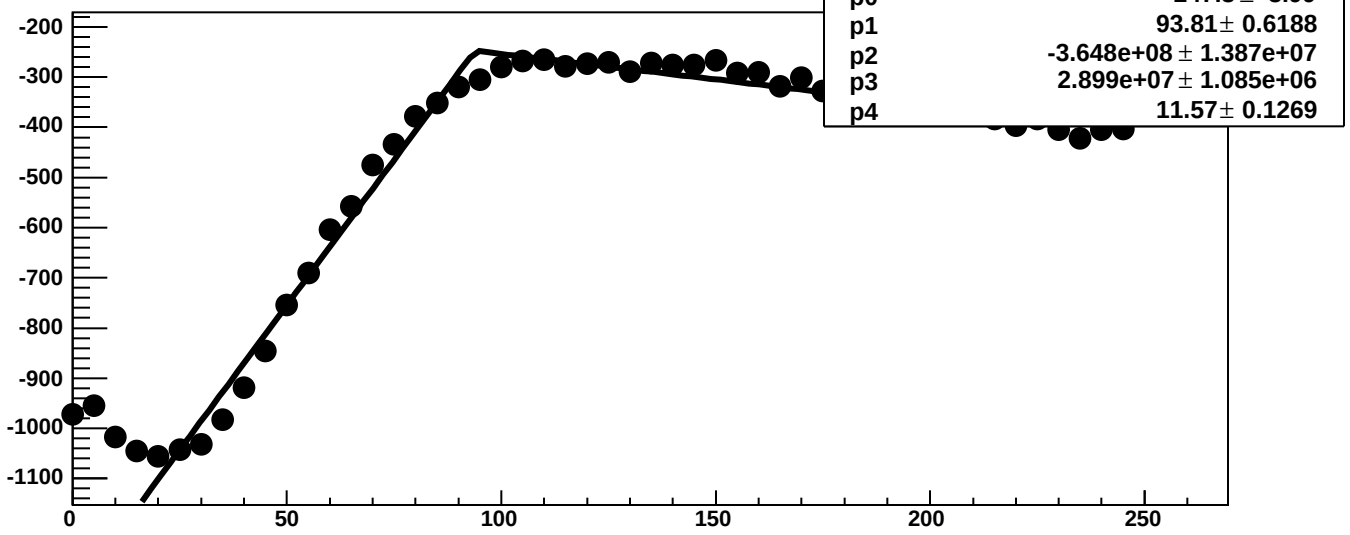
Chip 4, Channel 9, Enable 2, DAC=1600, ADC Noise vs Hold



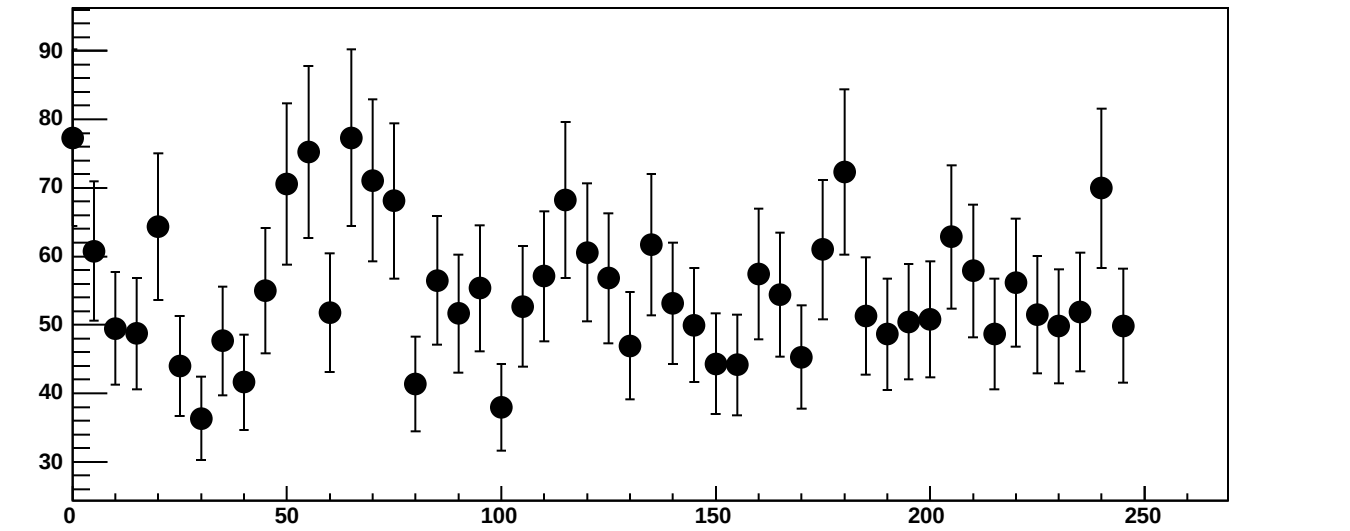
Chip 4, Channel 9, Enable 2, DAC=1600, ADC Residuals vs Hold



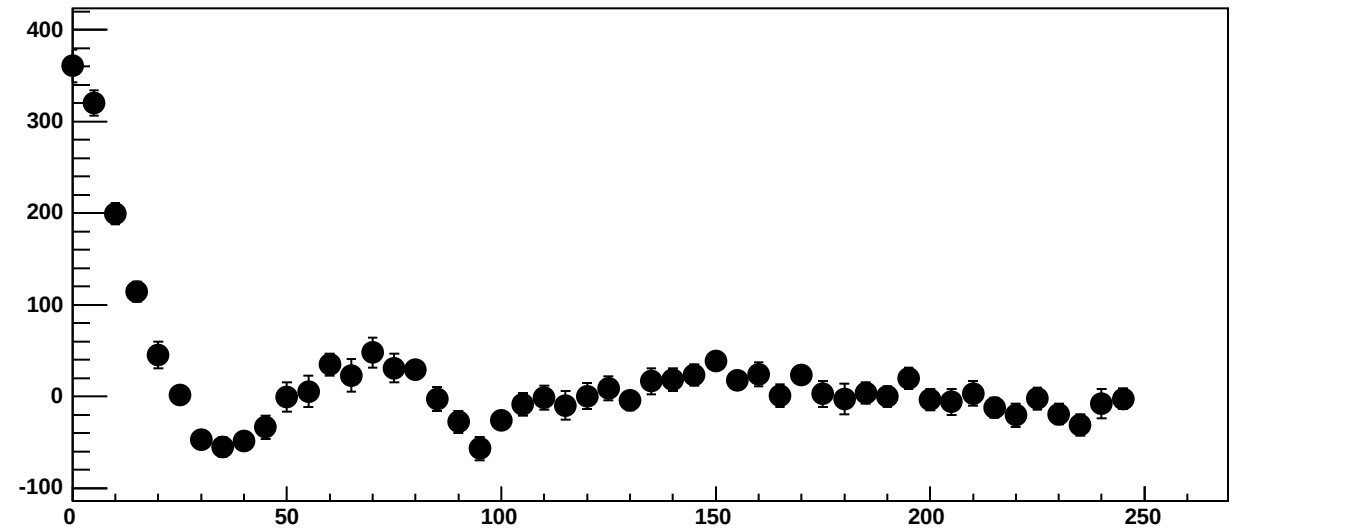
Chip 4, Channel 9, Enable 3, DAC=1600, ADC Mean vs Hold



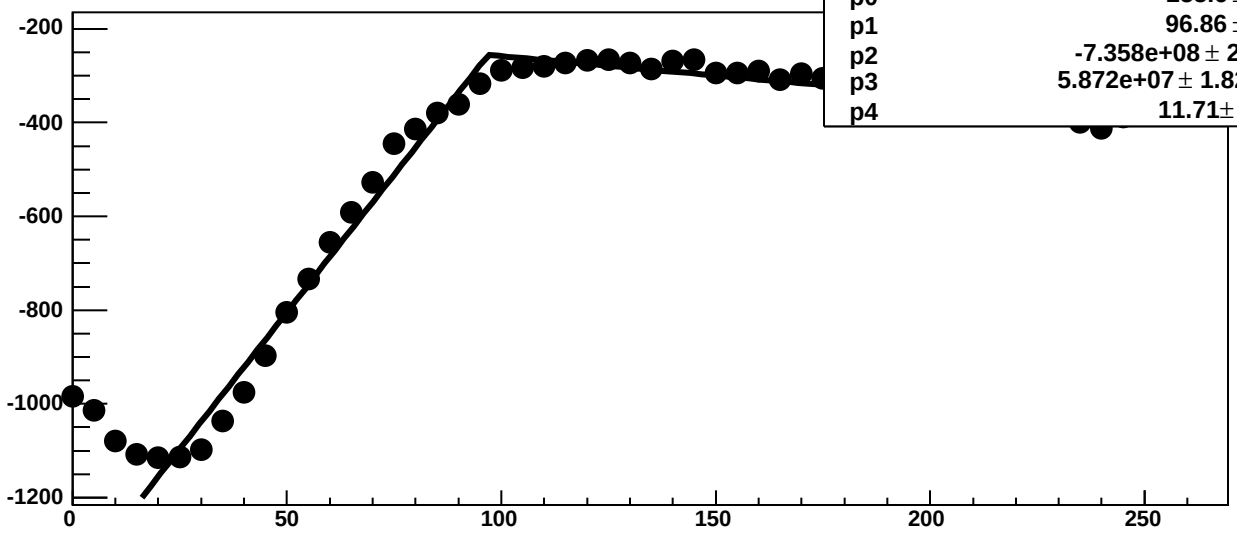
Chip 4, Channel 9, Enable 3, DAC=1600, ADC Noise vs Hold



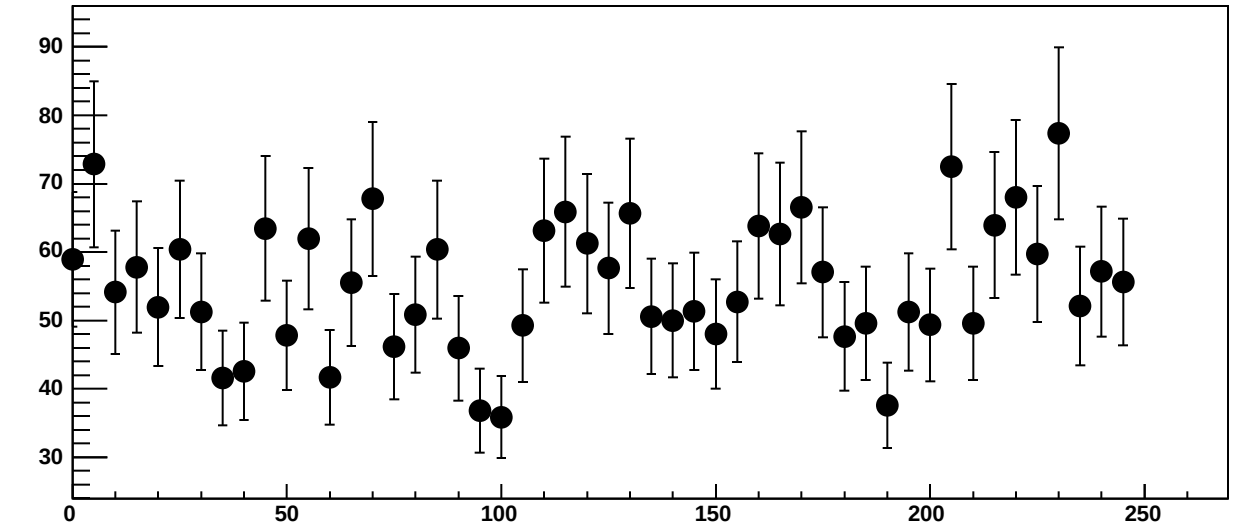
Chip 4, Channel 9, Enable 3, DAC=1600, ADC Residuals vs Hold



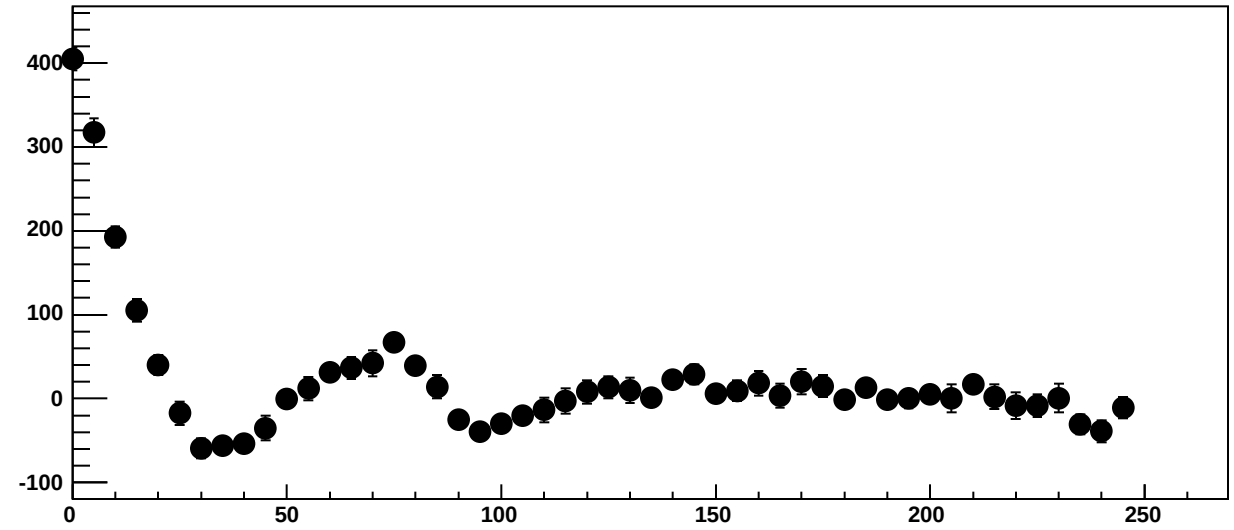
Chip 4, Channel 9, Enable 4, DAC=1600, ADC Mean vs Hold



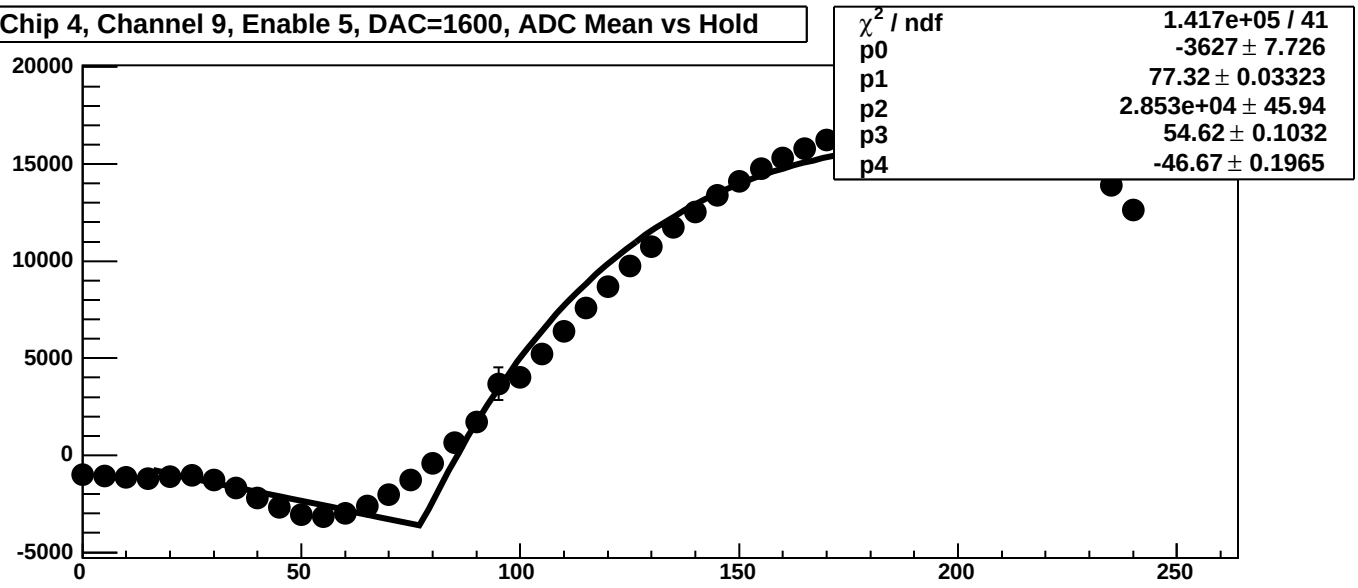
Chip 4, Channel 9, Enable 4, DAC=1600, ADC Noise vs Hold



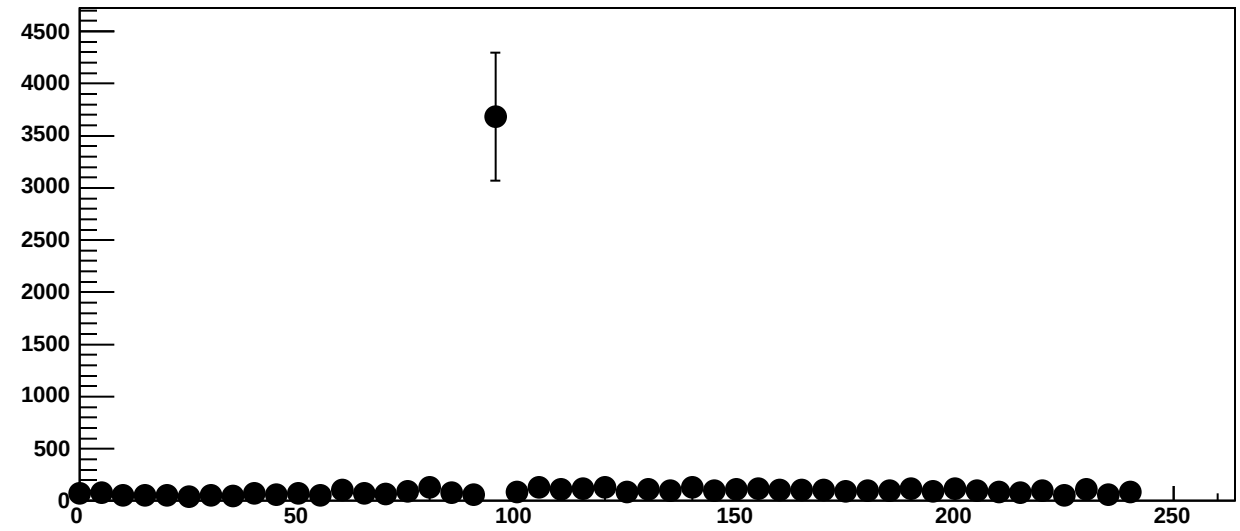
Chip 4, Channel 9, Enable 4, DAC=1600, ADC Residuals vs Hold



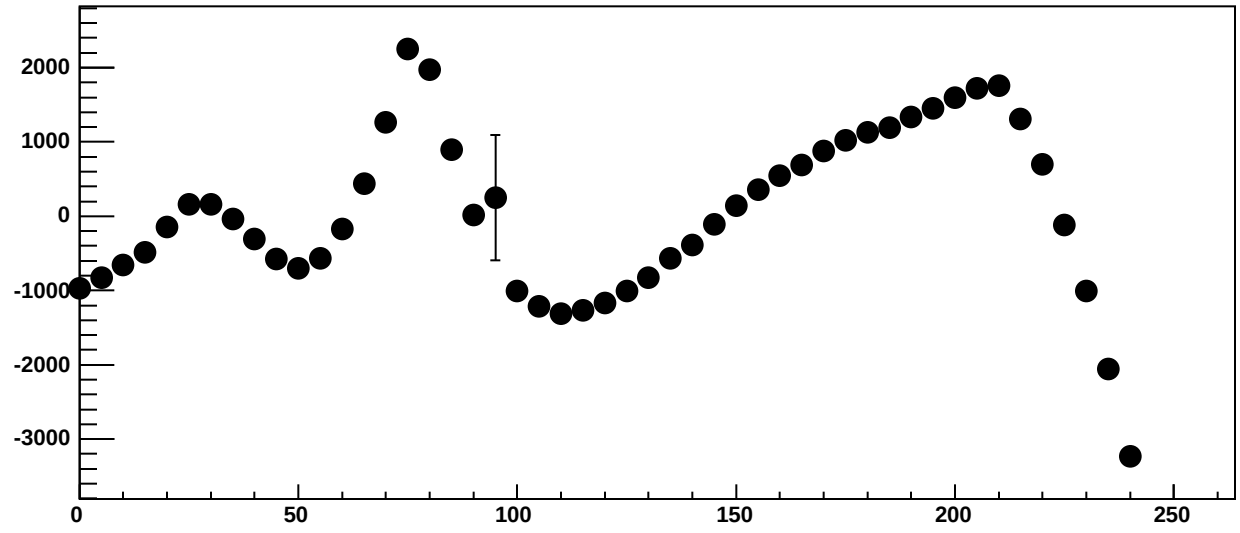
Chip 4, Channel 9, Enable 5, DAC=1600, ADC Mean vs Hold

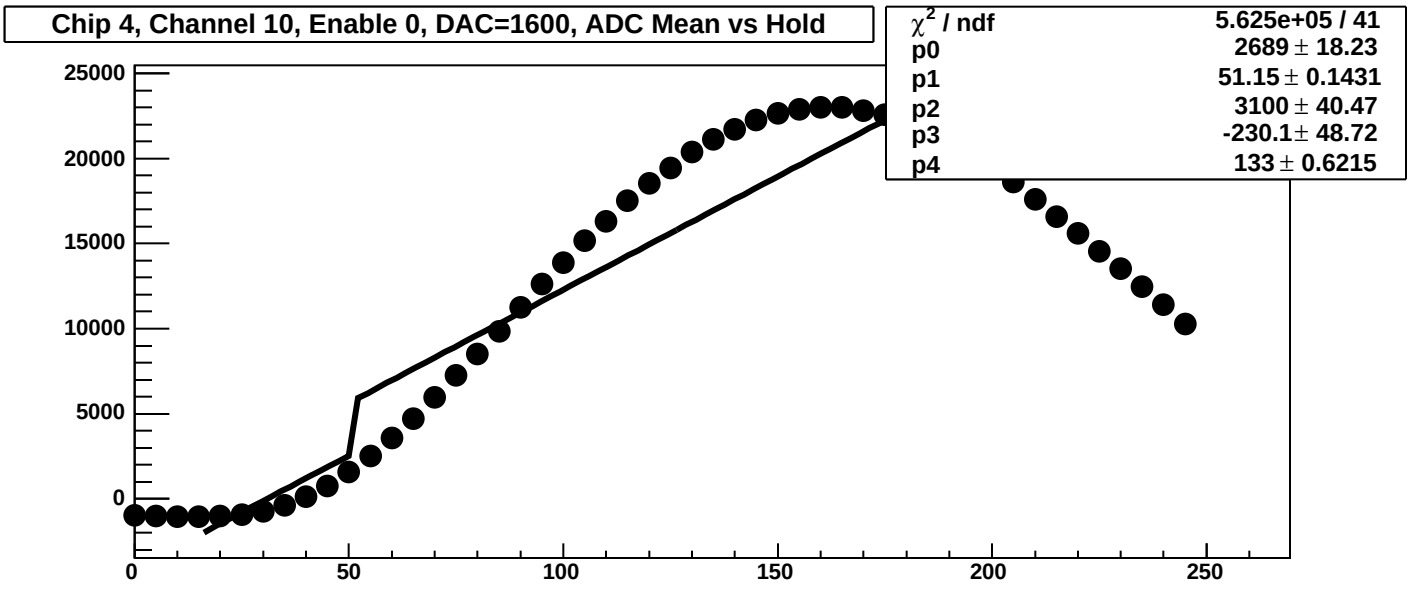


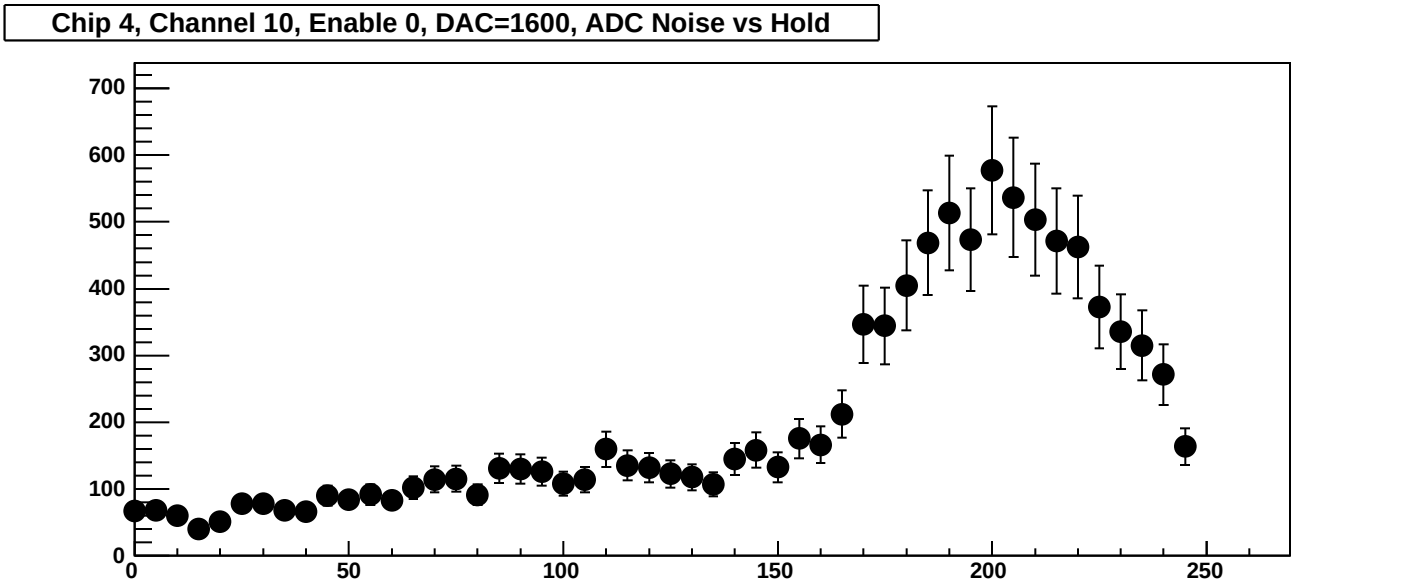
Chip 4, Channel 9, Enable 5, DAC=1600, ADC Noise vs Hold

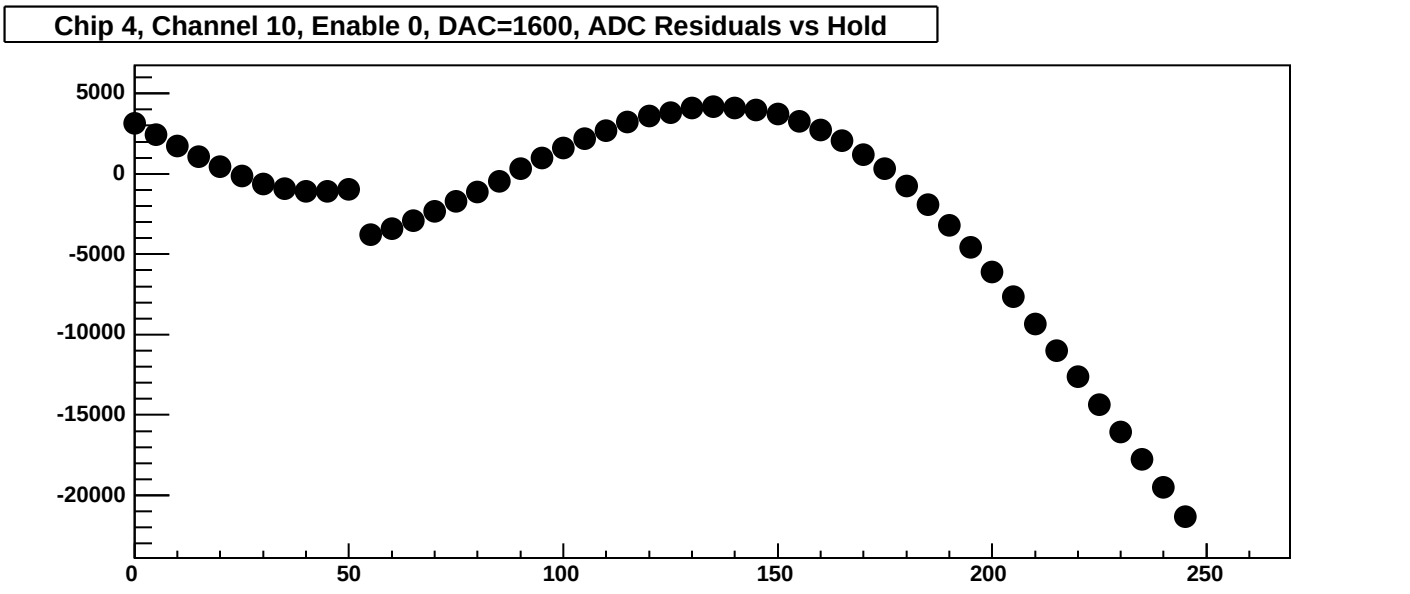


Chip 4, Channel 9, Enable 5, DAC=1600, ADC Residuals vs Hold

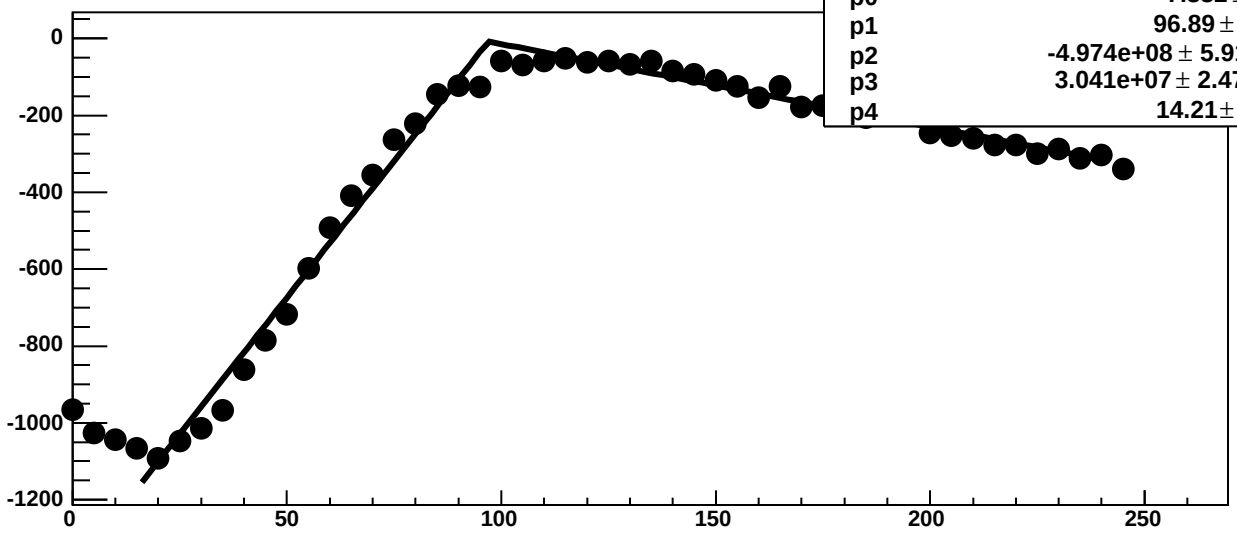



Chip 4, Channel 10, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.625e+05 / 41
p0 2689 ± 18.23
p1 51.15 ± 0.1431
p2 3100 ± 40.47
p3 -230.1 ± 48.72
p4 133 ± 0.6215
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

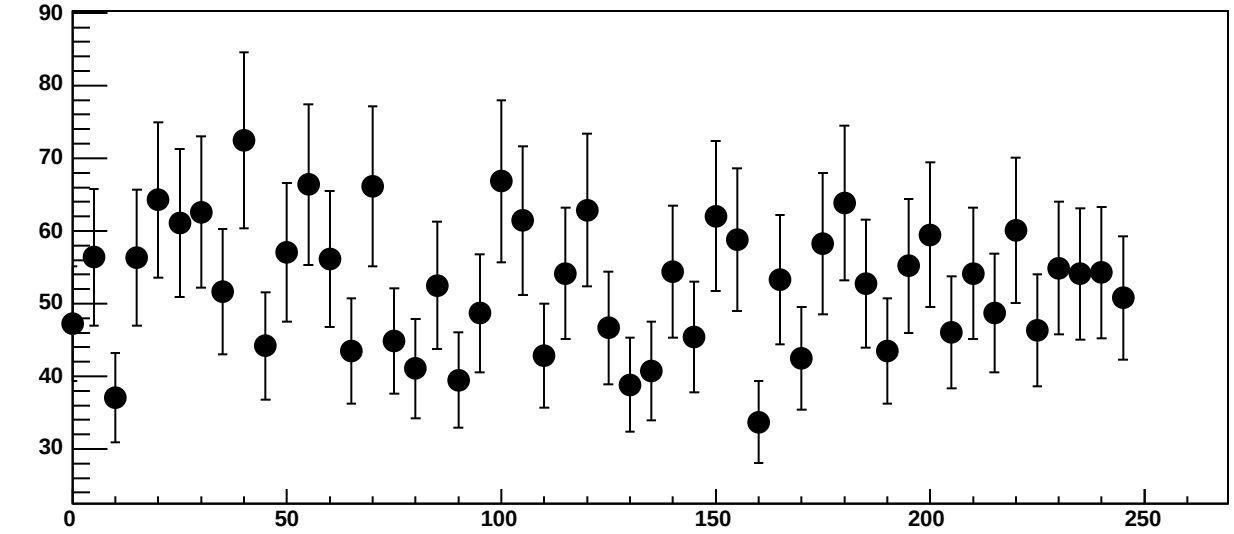

Chip 4, Channel 10, Enable 0, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 4, Channel 10, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

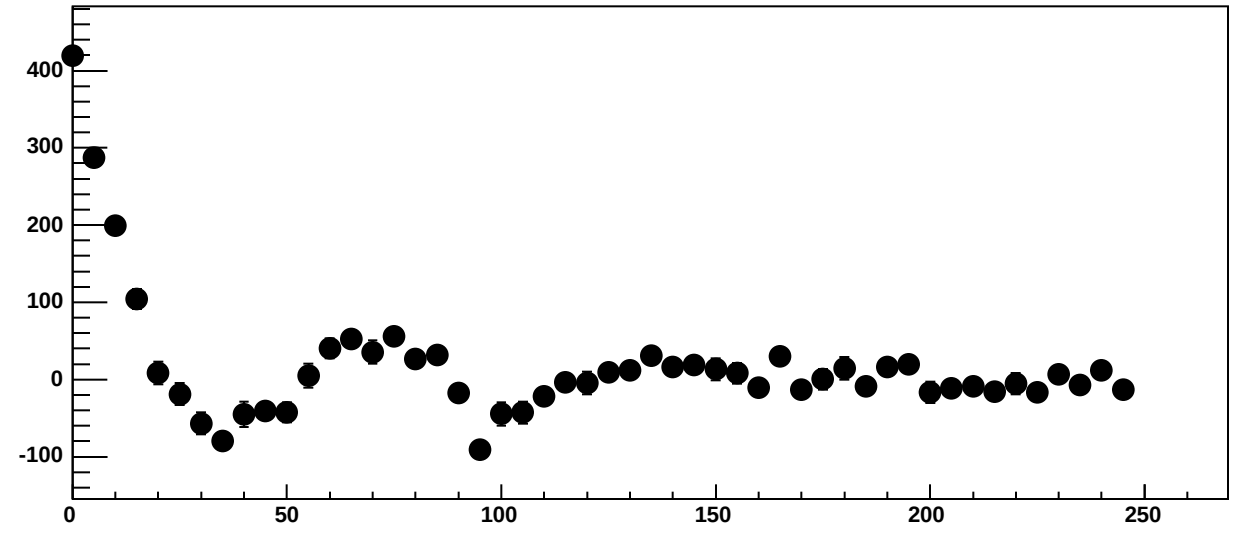
Chip 4, Channel 10, Enable 1, DAC=1600, ADC Mean vs Hold



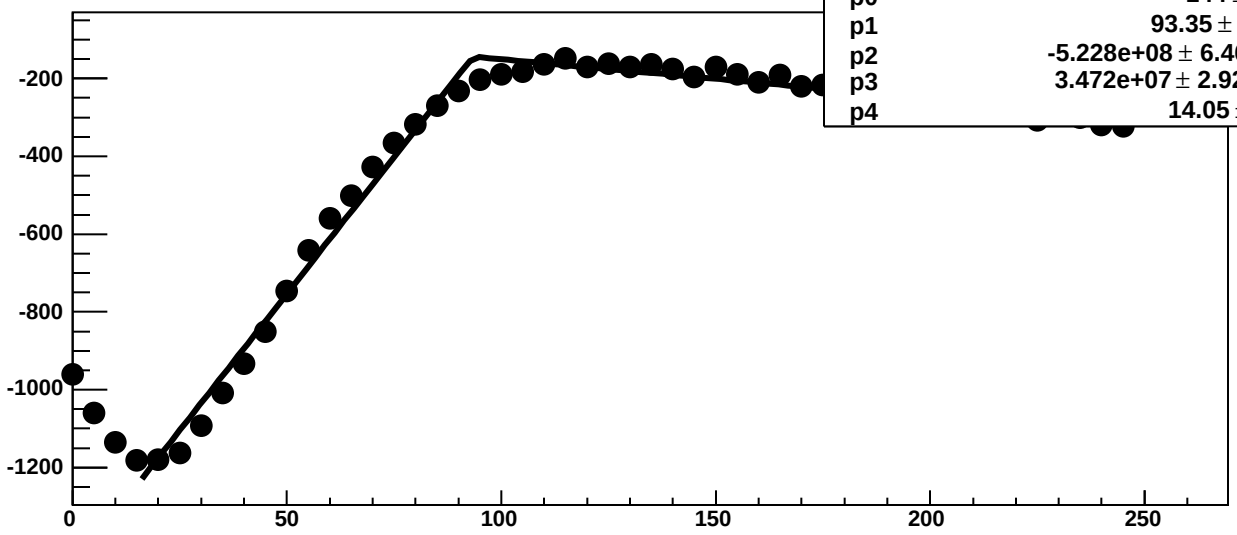
Chip 4, Channel 10, Enable 1, DAC=1600, ADC Noise vs Hold



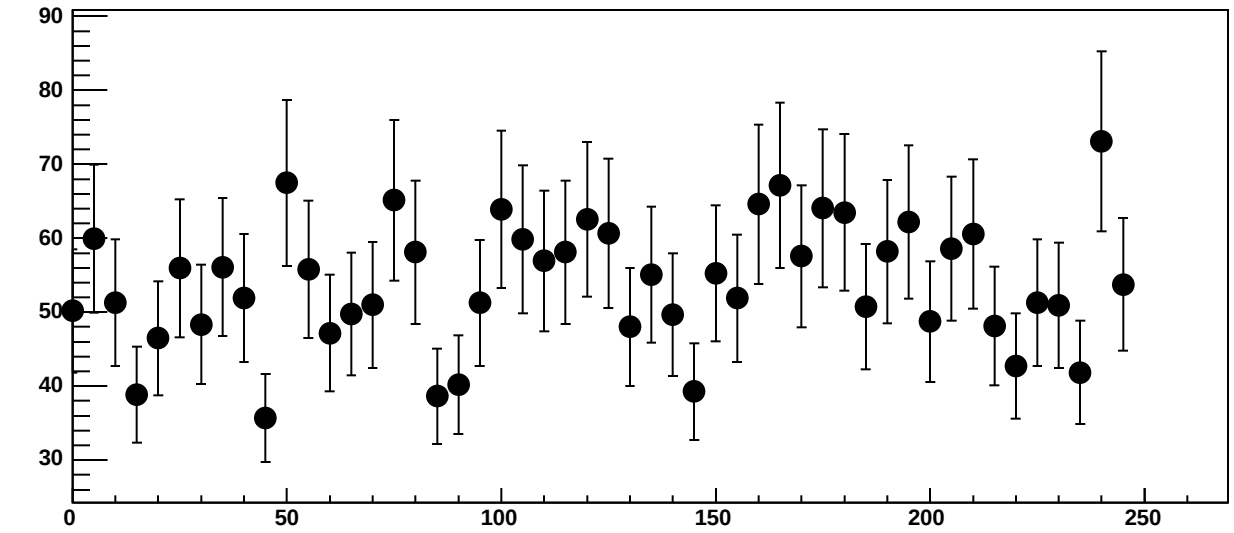
Chip 4, Channel 10, Enable 1, DAC=1600, ADC Residuals vs Hold



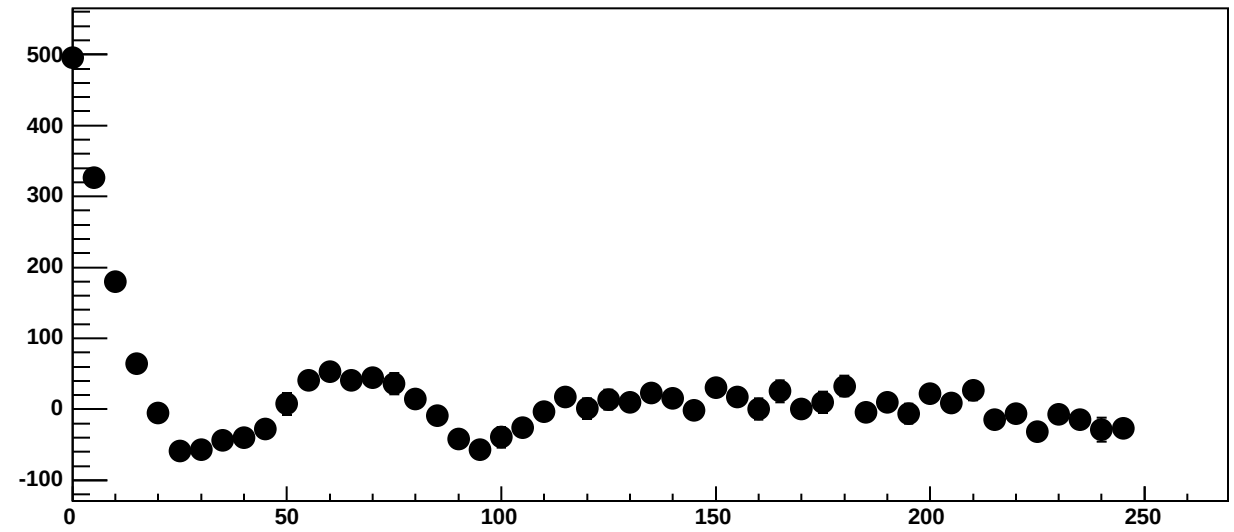
Chip 4, Channel 10, Enable 2, DAC=1600, ADC Mean vs Hold



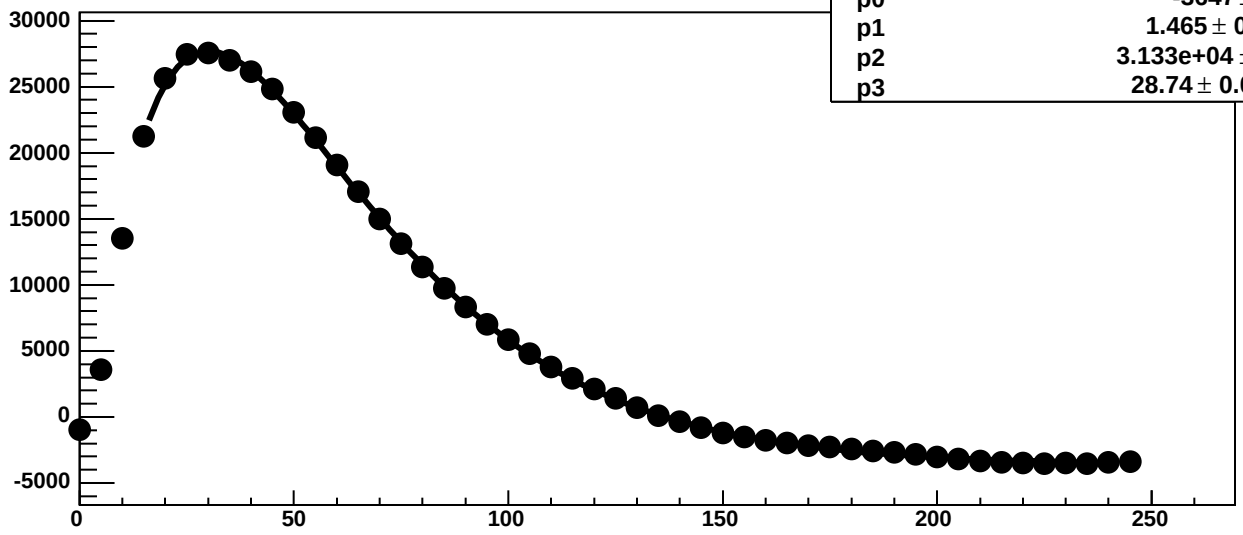
Chip 4, Channel 10, Enable 2, DAC=1600, ADC Noise vs Hold



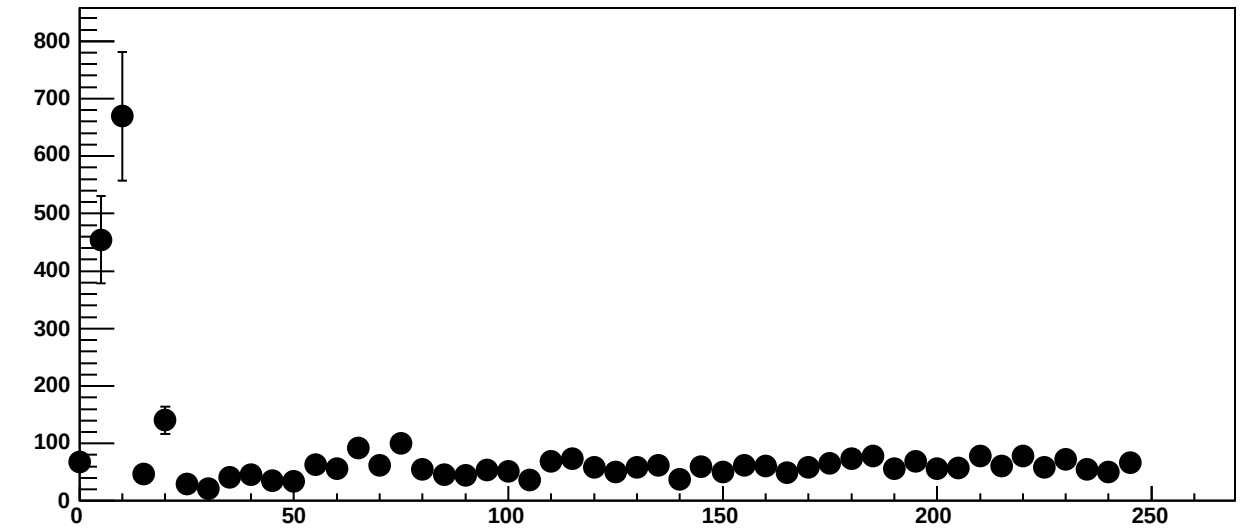
Chip 4, Channel 10, Enable 2, DAC=1600, ADC Residuals vs Hold



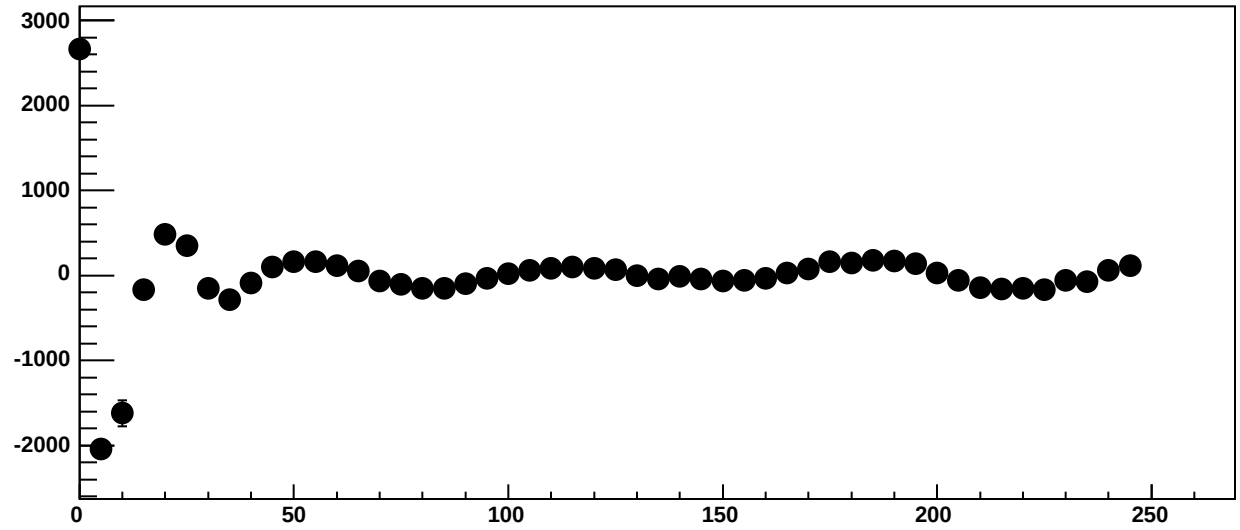
Chip 4, Channel 10, Enable 3!, DAC=1600, ADC Mean vs Hold



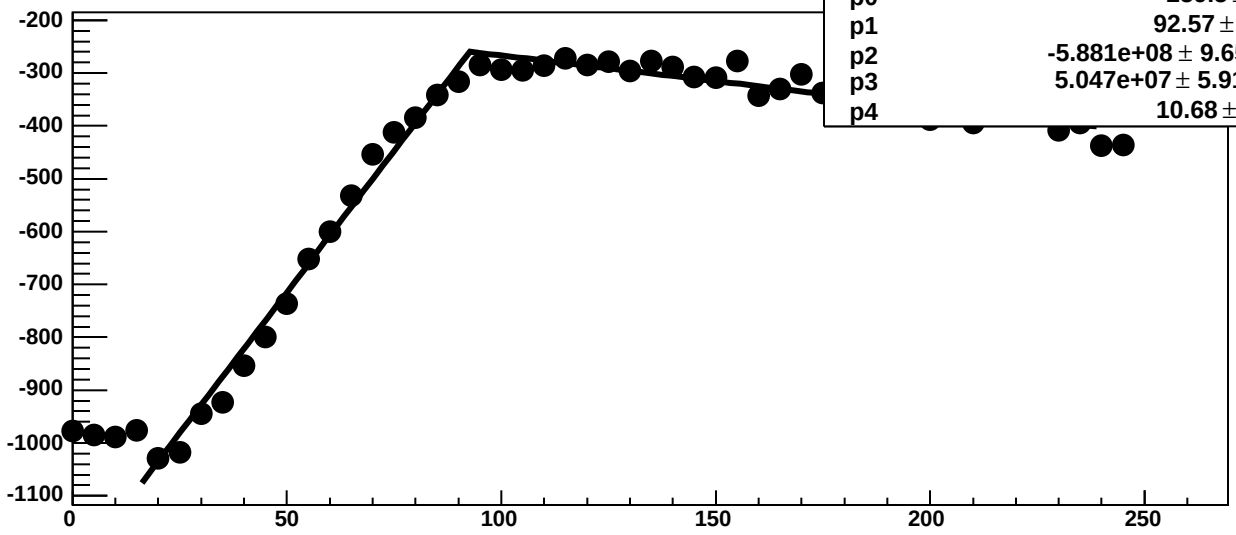
Chip 4, Channel 10, Enable 3!, DAC=1600, ADC Noise vs Hold



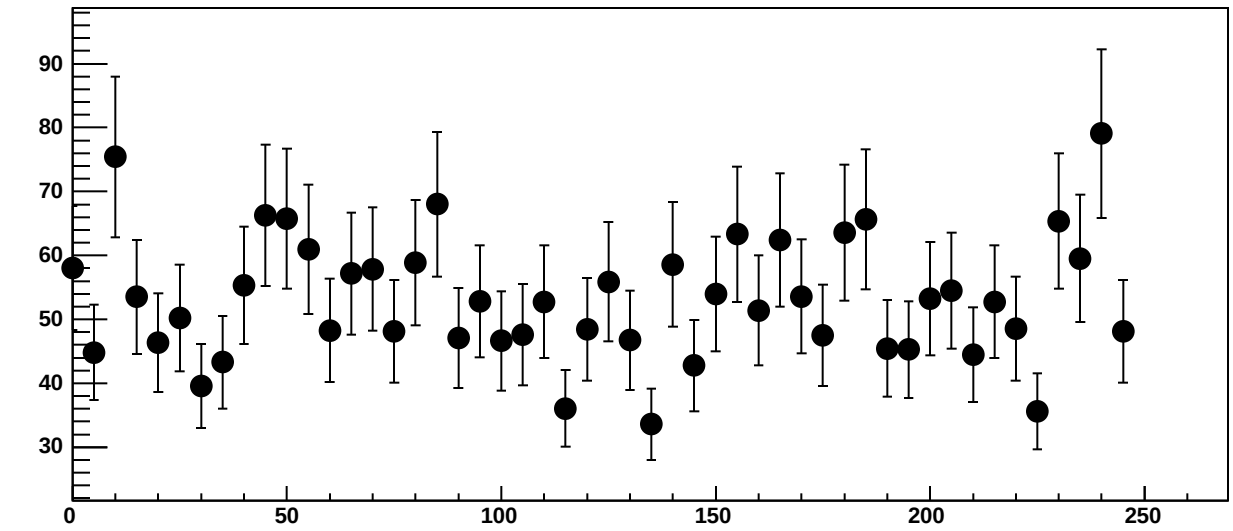
Chip 4, Channel 10, Enable 3!, DAC=1600, ADC Residuals vs Hold



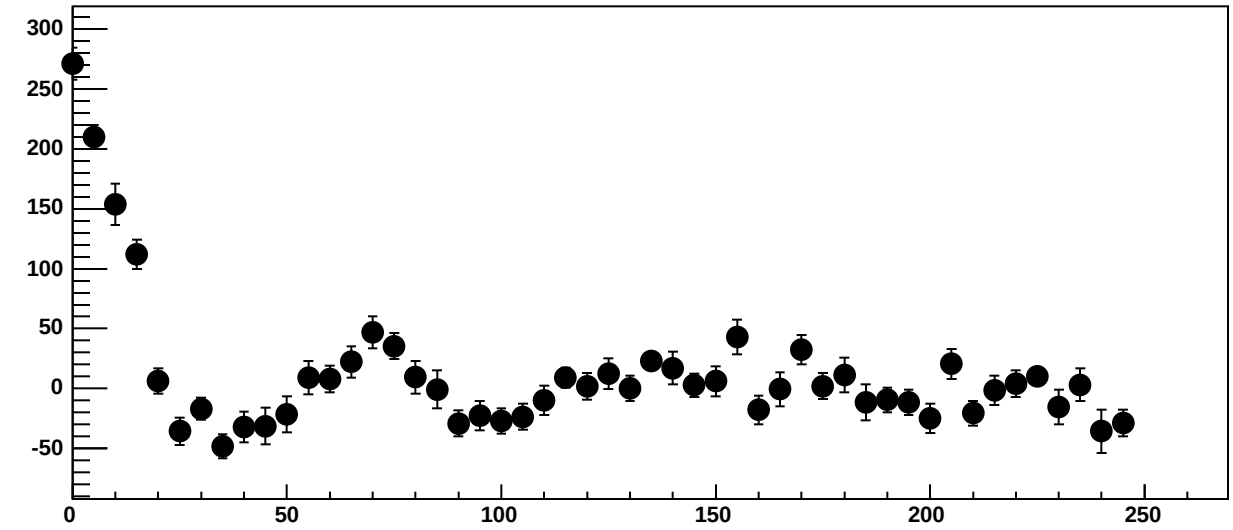
Chip 4, Channel 10, Enable 4, DAC=1600, ADC Mean vs Hold

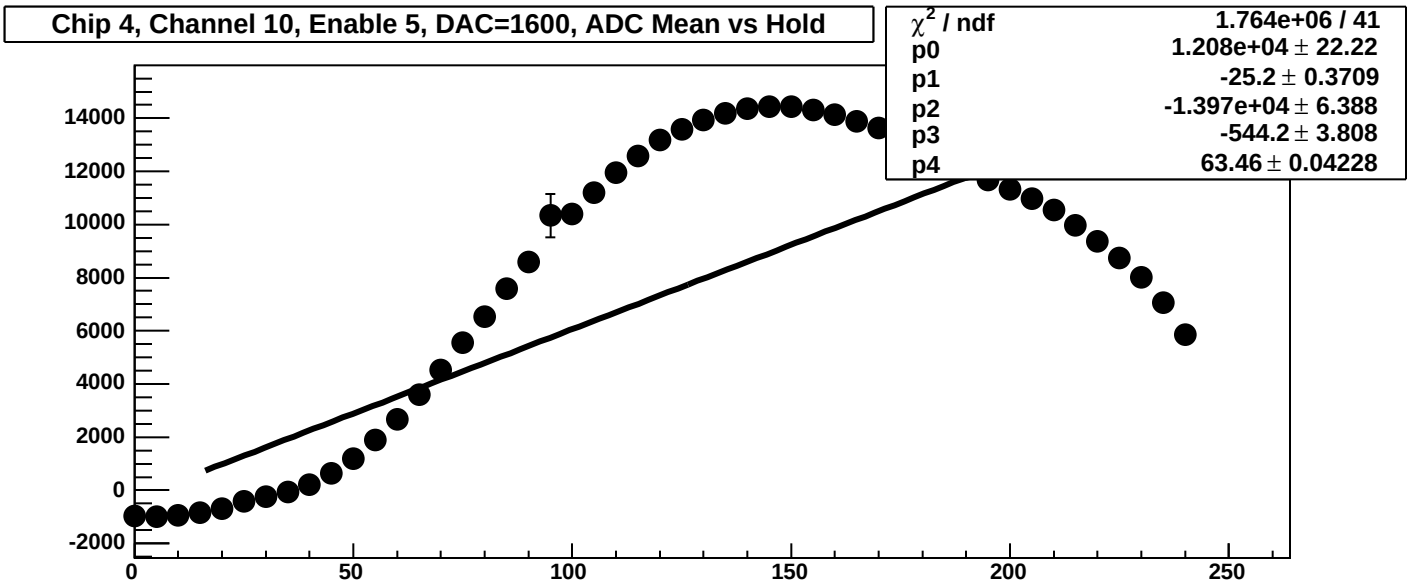


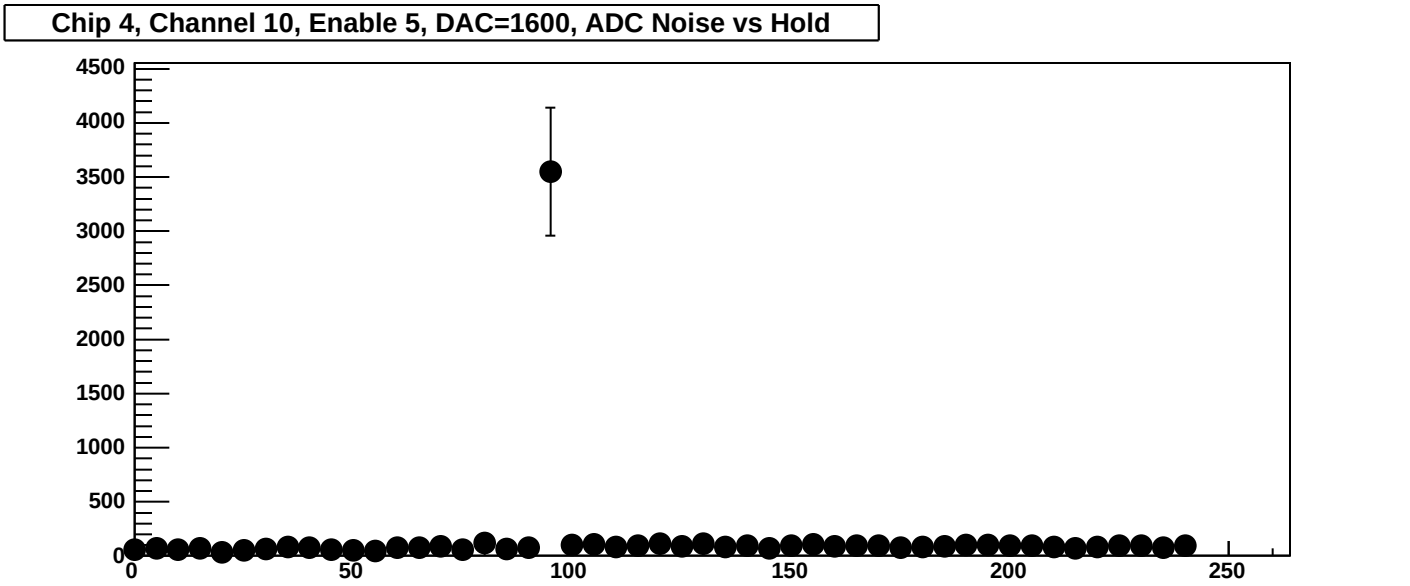
Chip 4, Channel 10, Enable 4, DAC=1600, ADC Noise vs Hold

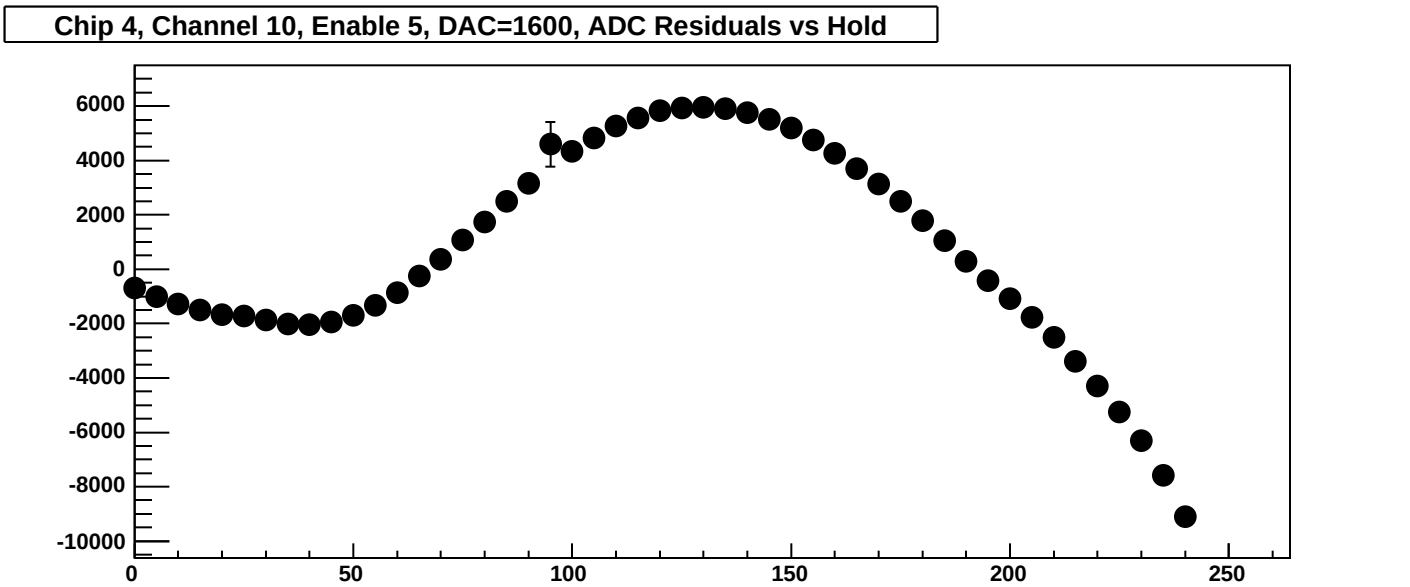


Chip 4, Channel 10, Enable 4, DAC=1600, ADC Residuals vs Hold

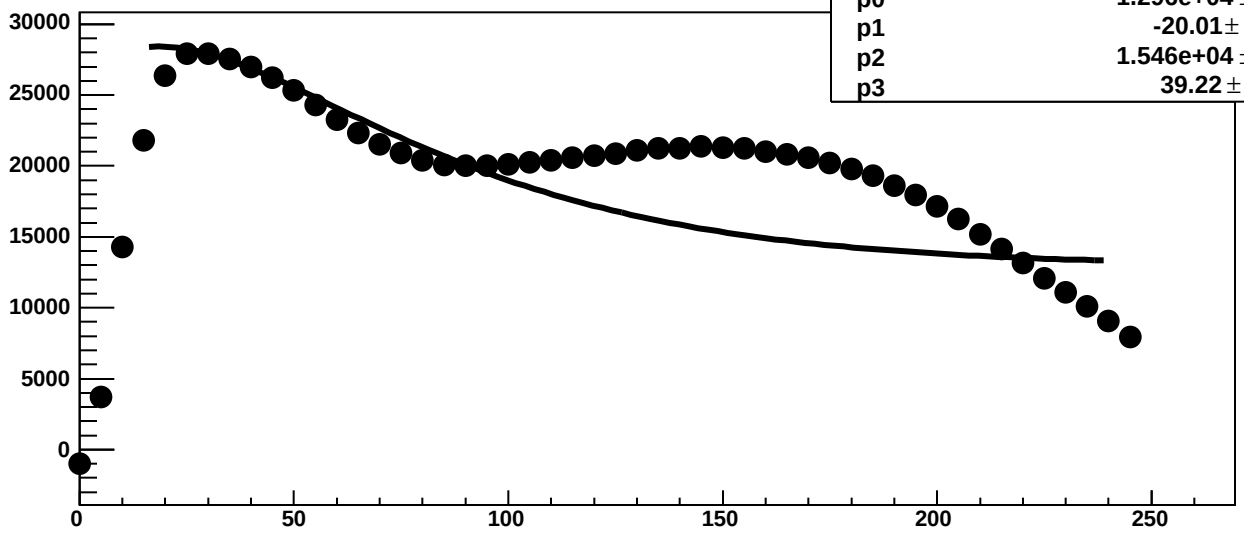



Chip 4, Channel 10, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.764e+06 / 41
p0 1.208e+04 ± 22.22
p1 -25.2 ± 0.3709
p2 -1.397e+04 ± 6.388
p3 -544.2 ± 3.808
p4 63.46 ± 0.04228
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

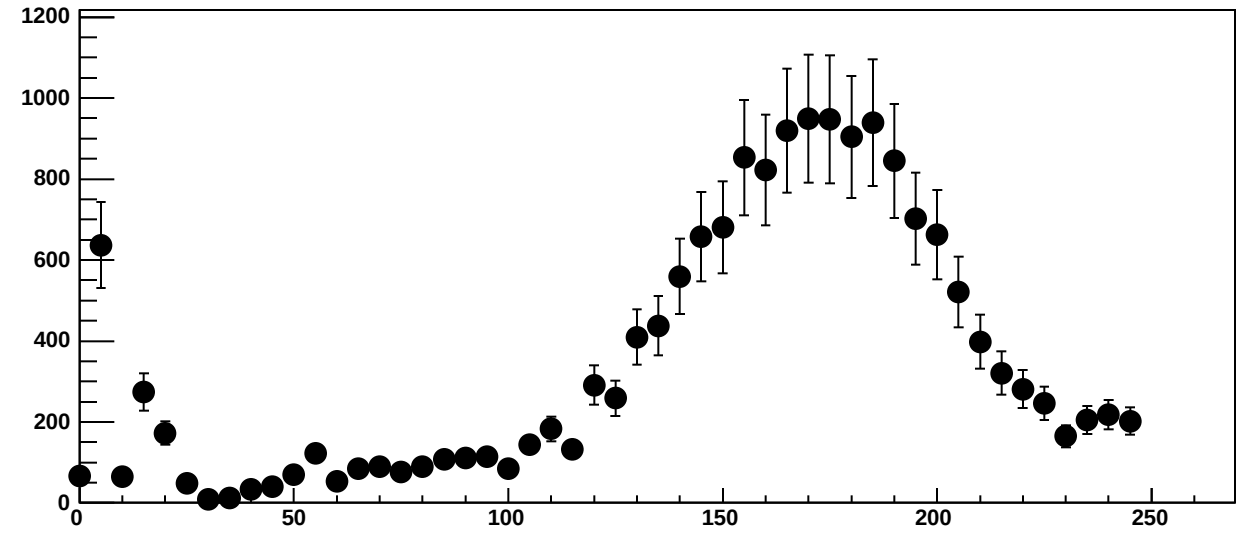

Chip 4, Channel 10, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 4, Channel 10, Enable 5, DAC=1600, ADC Residuals vs Hold
6000
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

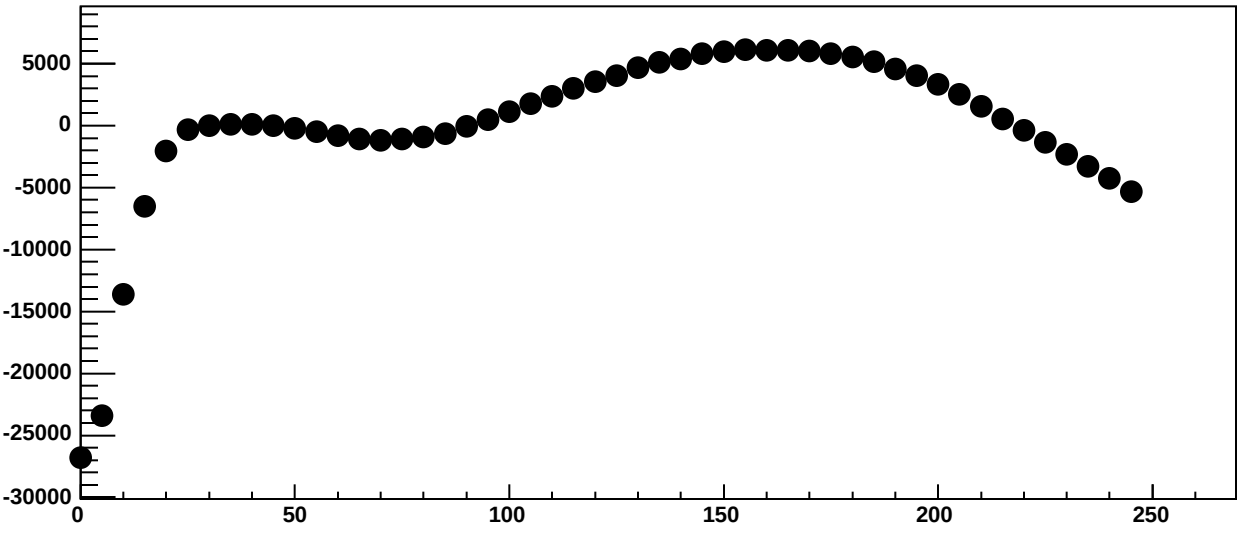
Chip 4, Channel 11, Enable 0!, DAC=1600, ADC Mean vs Hold

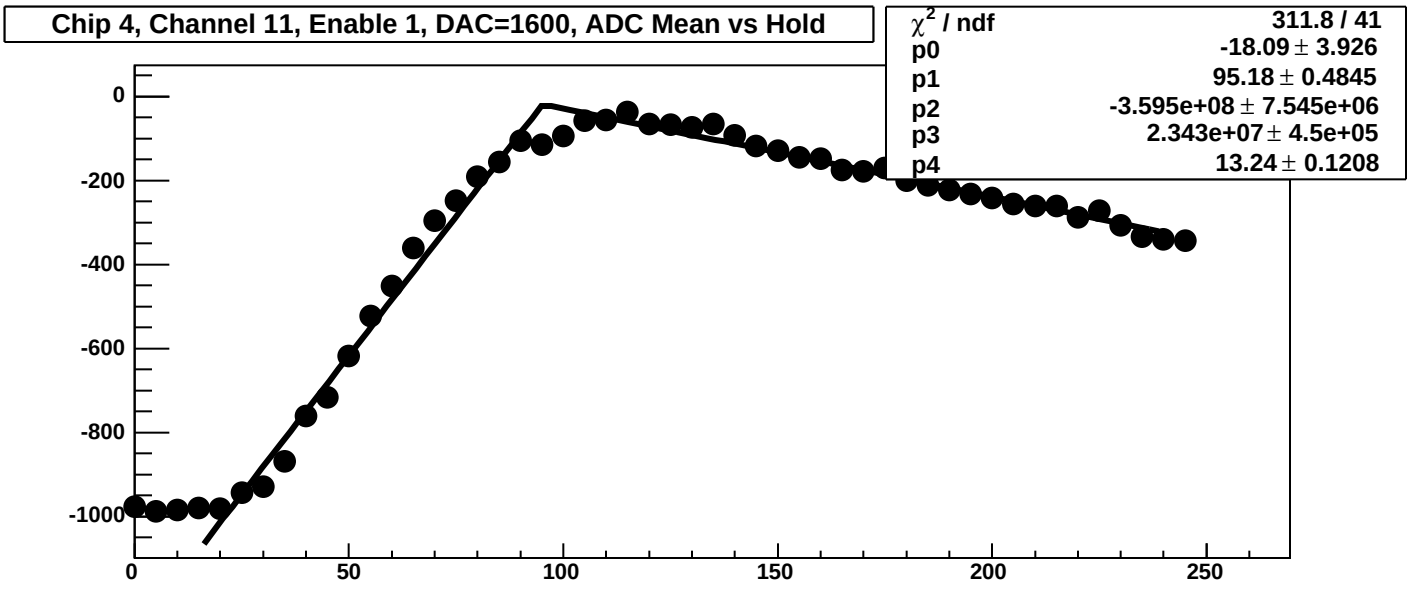


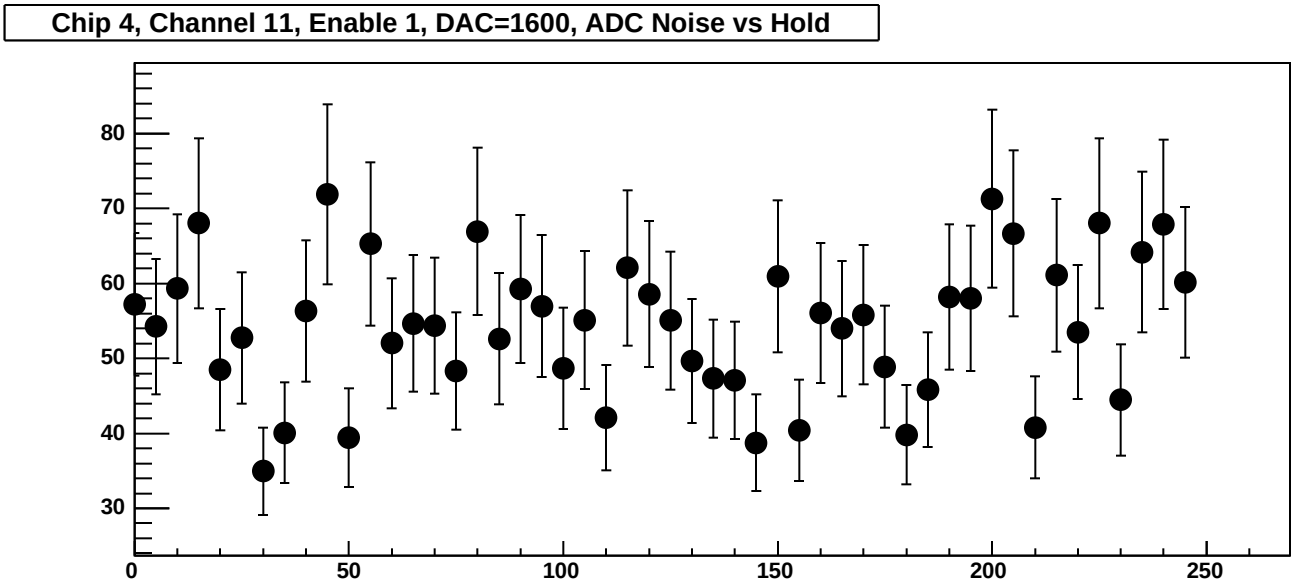
Chip 4, Channel 11, Enable 0!, DAC=1600, ADC Noise vs Hold

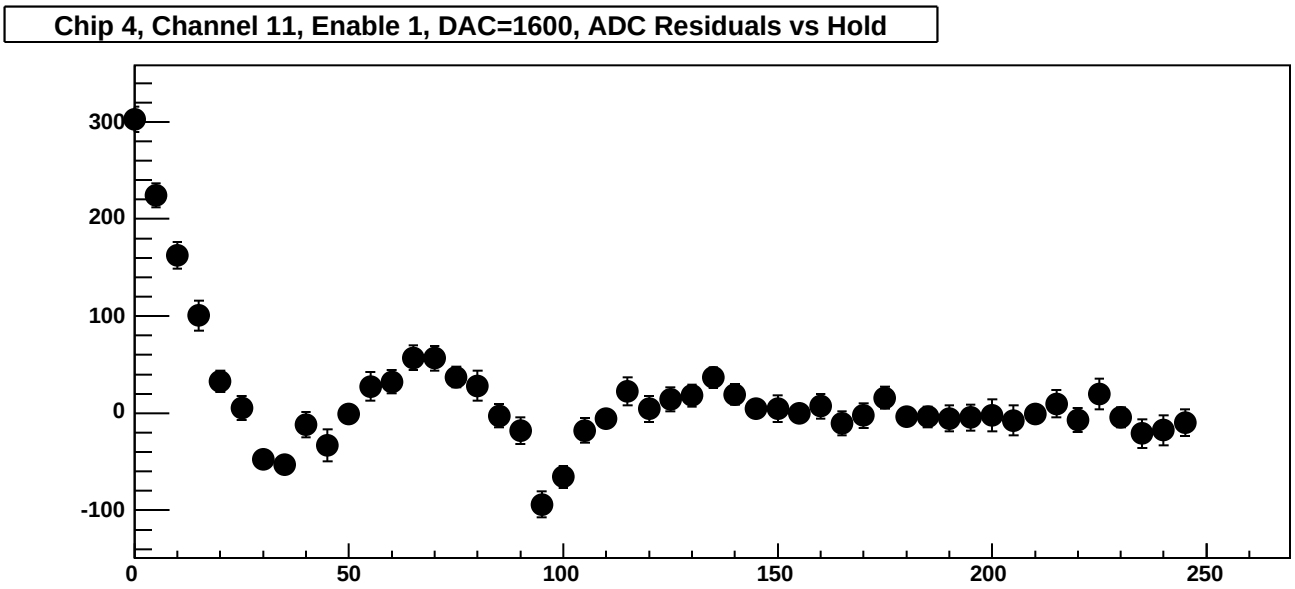


Chip 4, Channel 11, Enable 0!, DAC=1600, ADC Residuals vs Hold

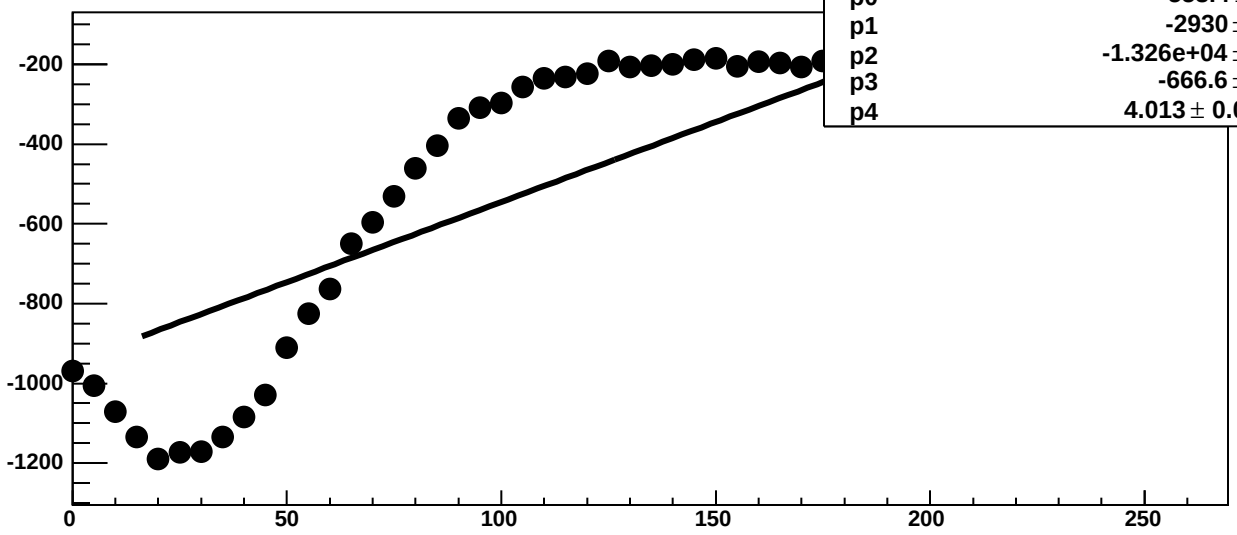



Chip 4, Channel 11, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 311.8 / 41
p0 -18.09 ± 3.926
p1 95.18 ± 0.4845
p2 -3.595e+08 ± 7.545e+06
p3 2.343e+07 ± 4.5e+05
p4 13.24 ± 0.1208
0
-200
-400
-600
-800
-1000
0
50
100
150
200
250

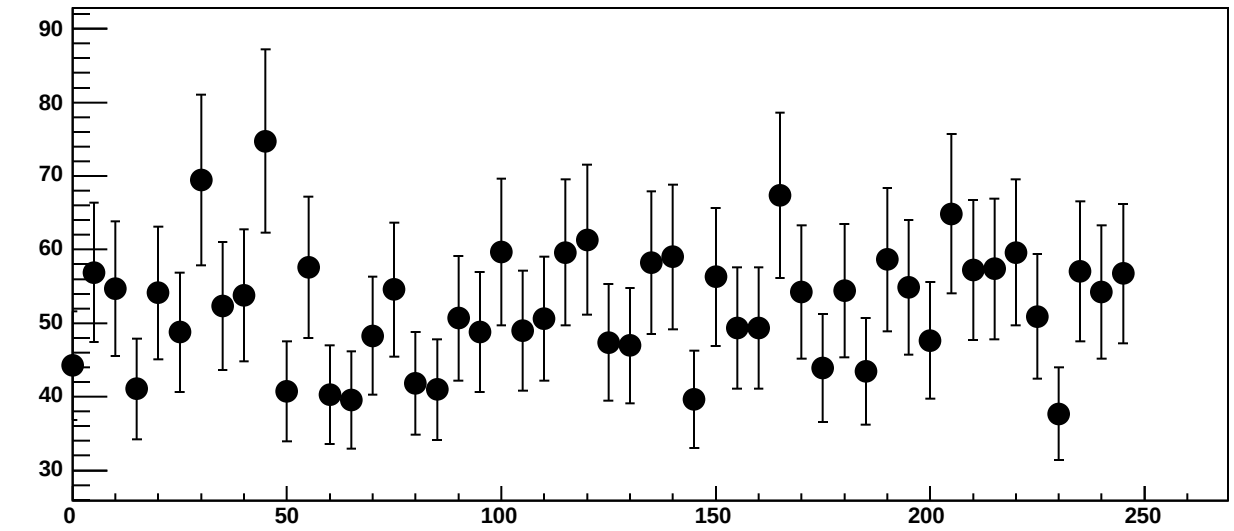

Chip 4, Channel 11, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 4, Channel 11, Enable 1, DAC=1600, ADC Residuals vs Hold
300
200
100
0
-100
0
50
100
150
200
250

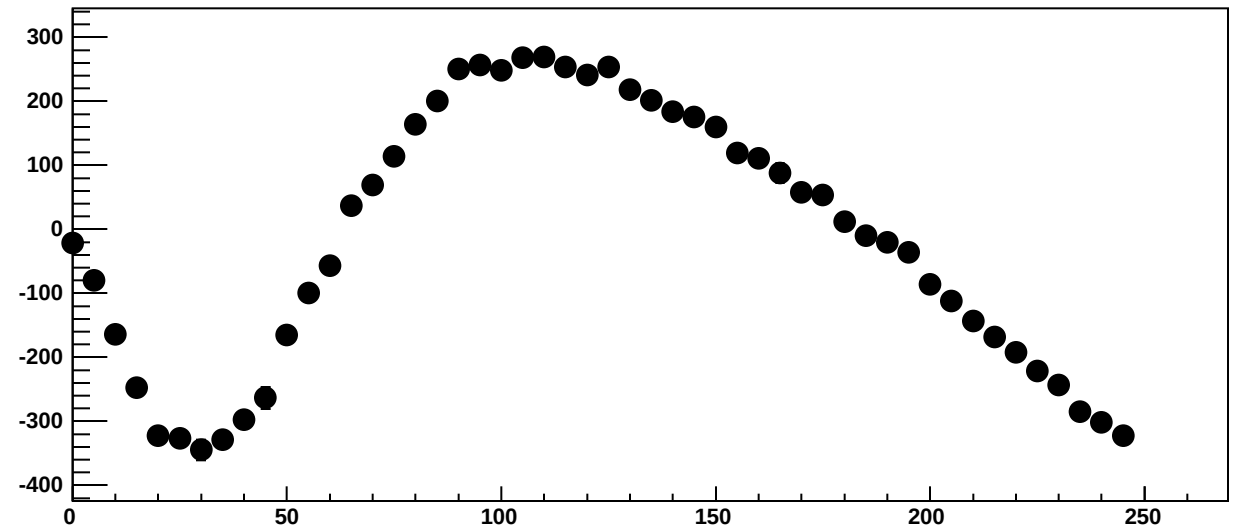
Chip 4, Channel 11, Enable 2, DAC=1600, ADC Mean vs Hold

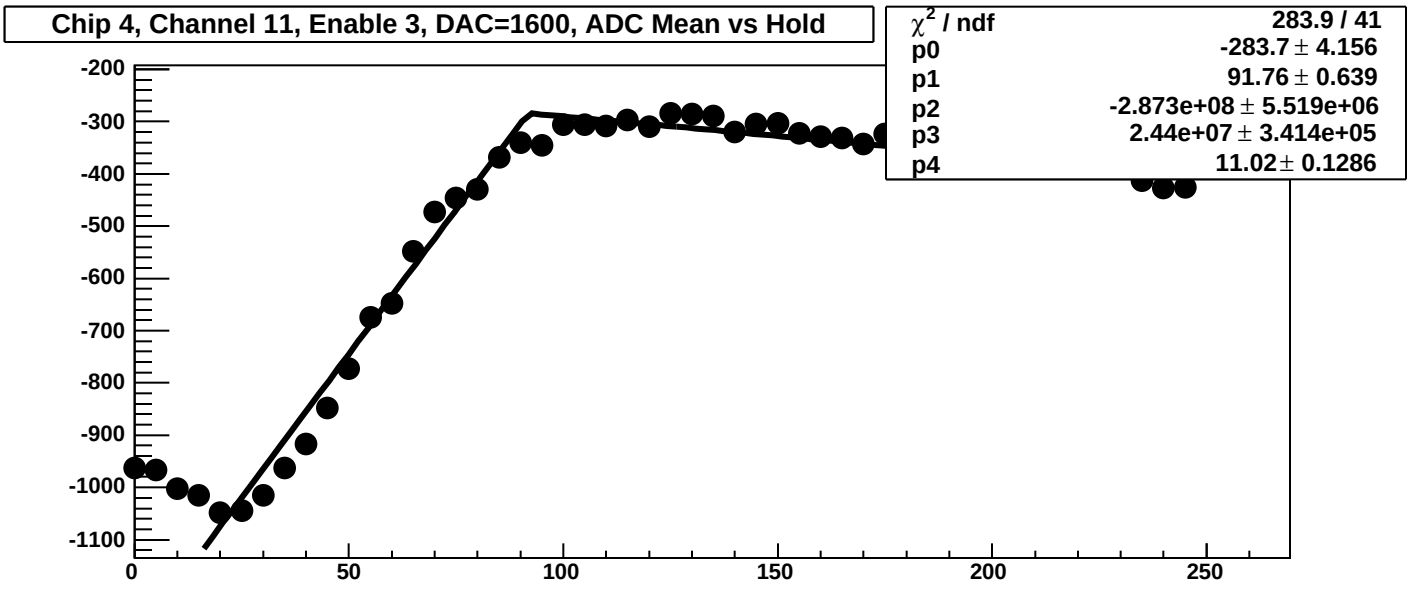


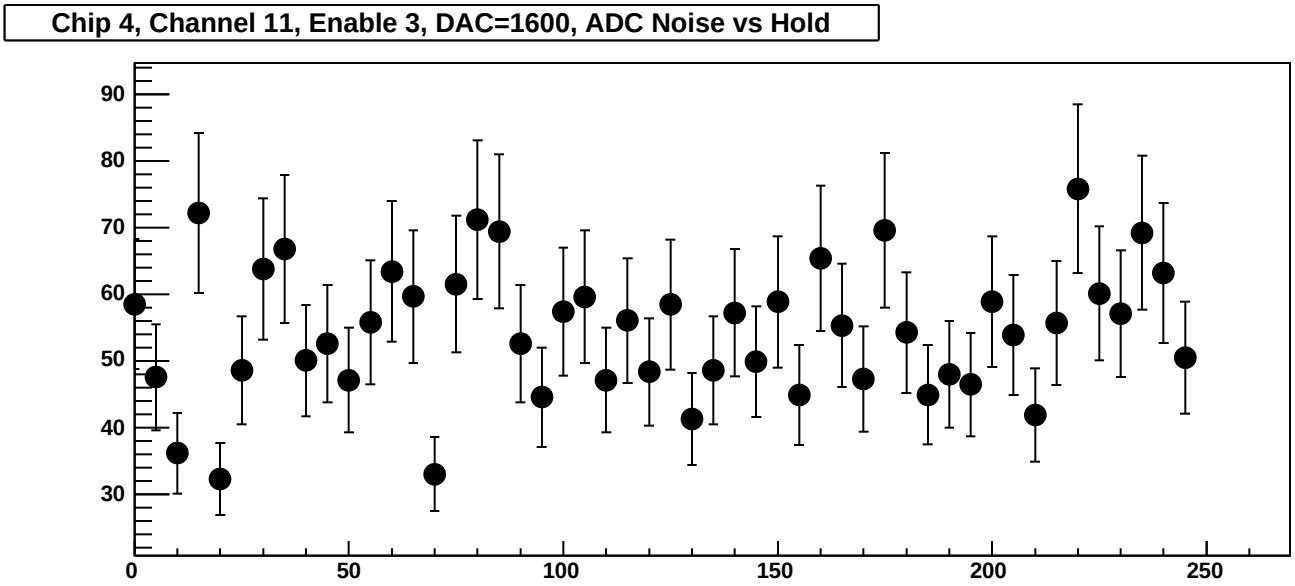
Chip 4, Channel 11, Enable 2, DAC=1600, ADC Noise vs Hold

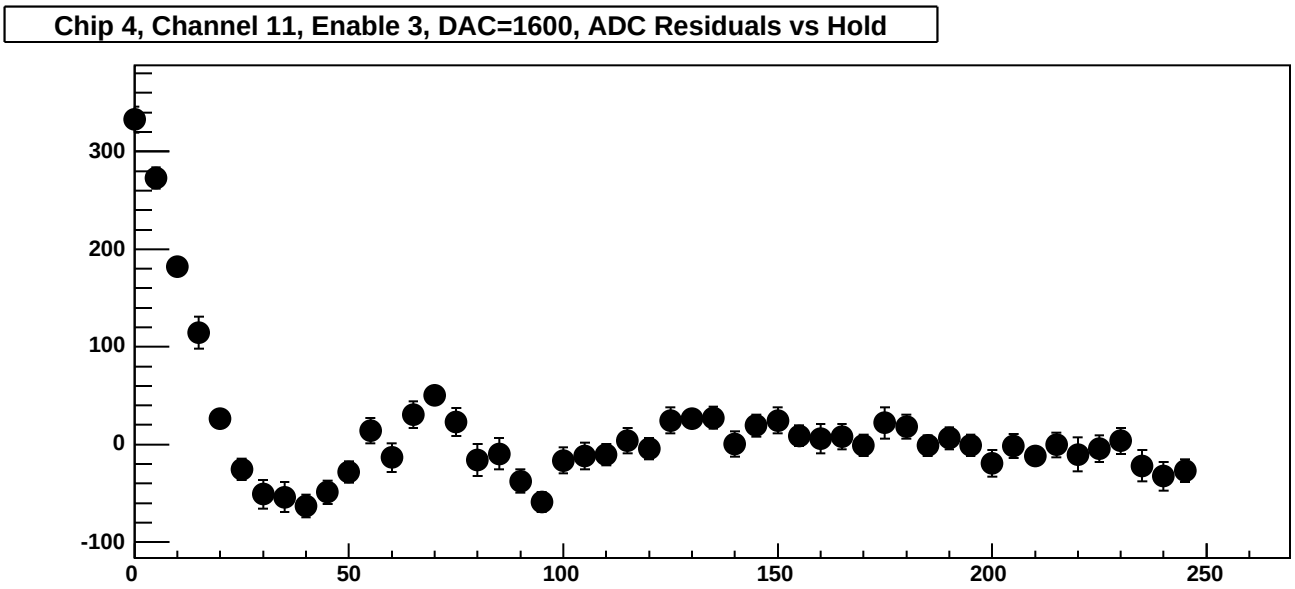


Chip 4, Channel 11, Enable 2, DAC=1600, ADC Residuals vs Hold

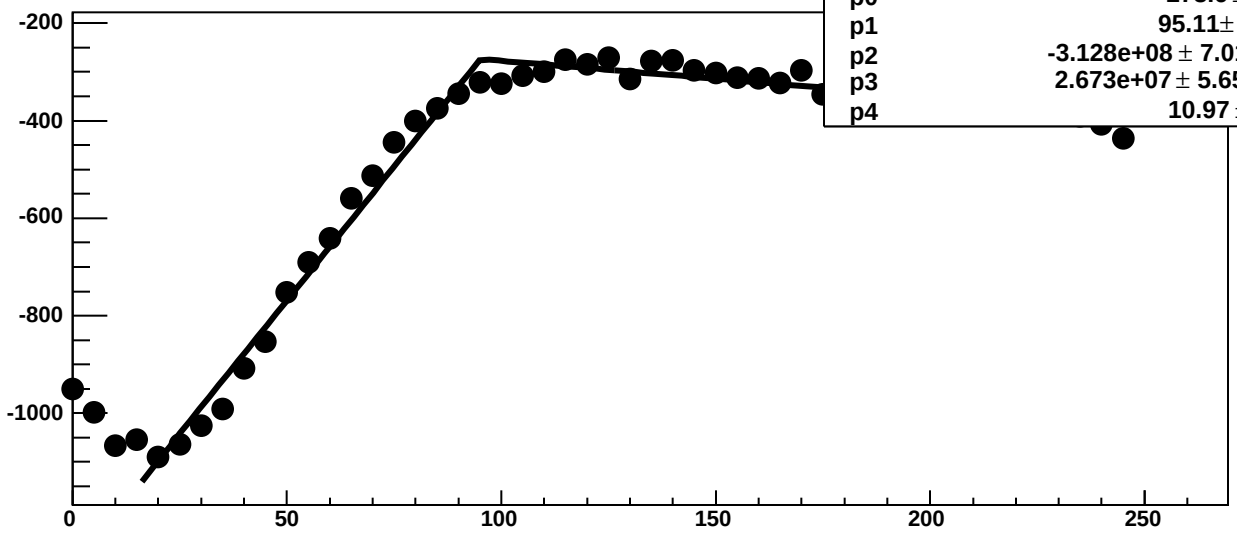



Chip 4, Channel 11, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 283.9 / 41
p0 -283.7 ± 4.156
p1 91.76 ± 0.639
p2 -2.873e+08 ± 5.519e+06
p3 2.44e+07 ± 3.414e+05
p4 11.02 ± 0.1286
-200
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
50
100
150
200
250

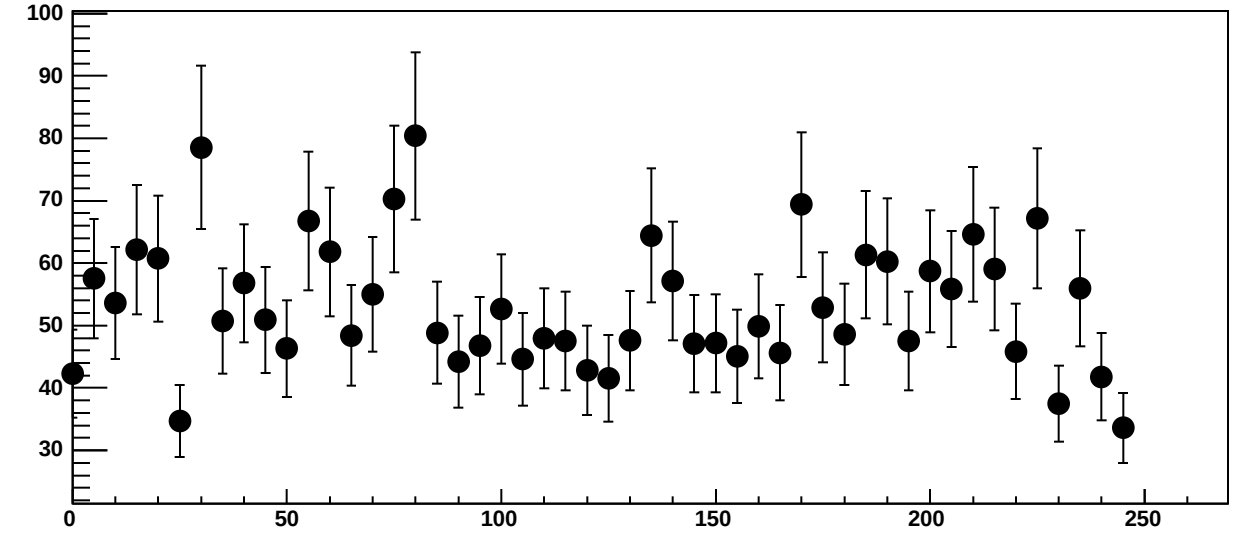

Chip 4, Channel 11, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 4, Channel 11, Enable 3, DAC=1600, ADC Residuals vs Hold
300
200
100
0
-100
0
50
100
150
200
250

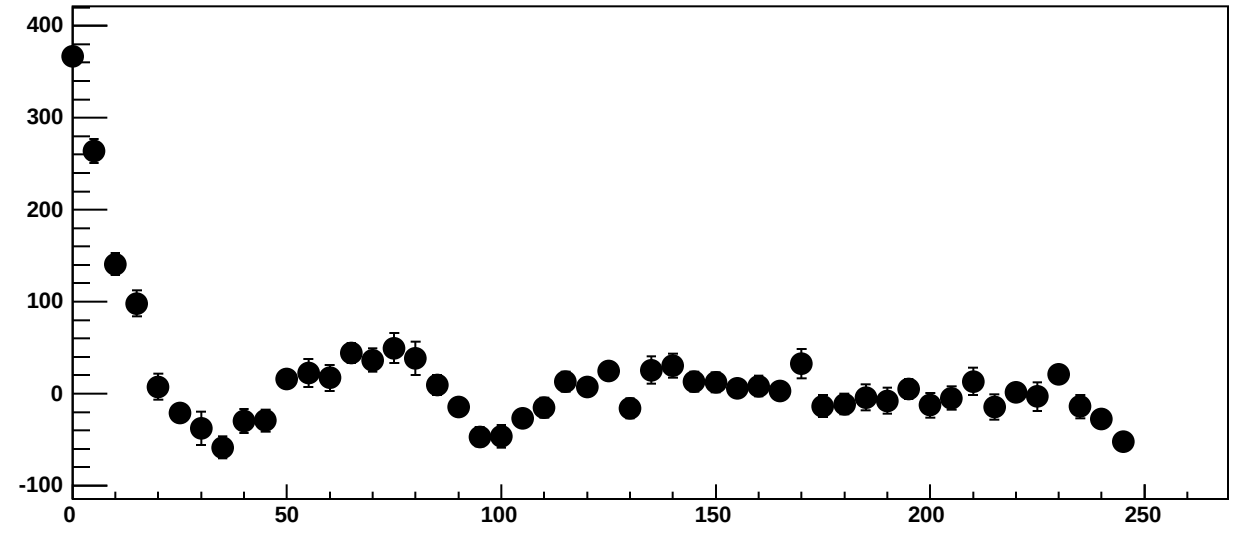
Chip 4, Channel 11, Enable 4, DAC=1600, ADC Mean vs Hold

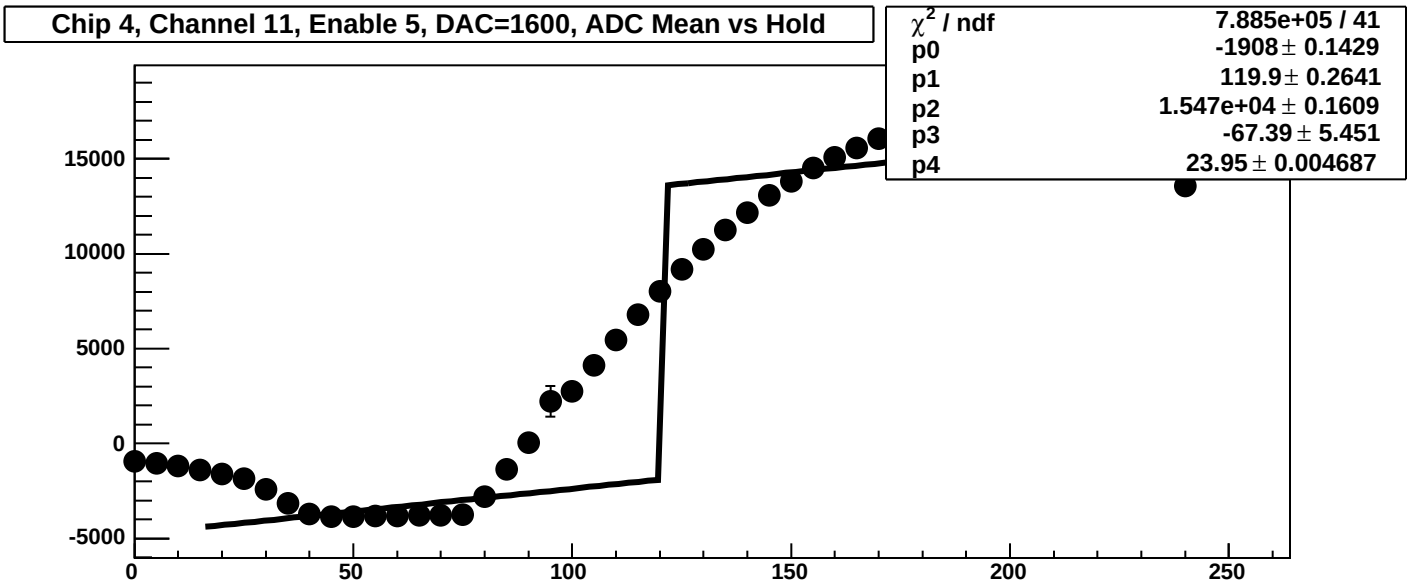


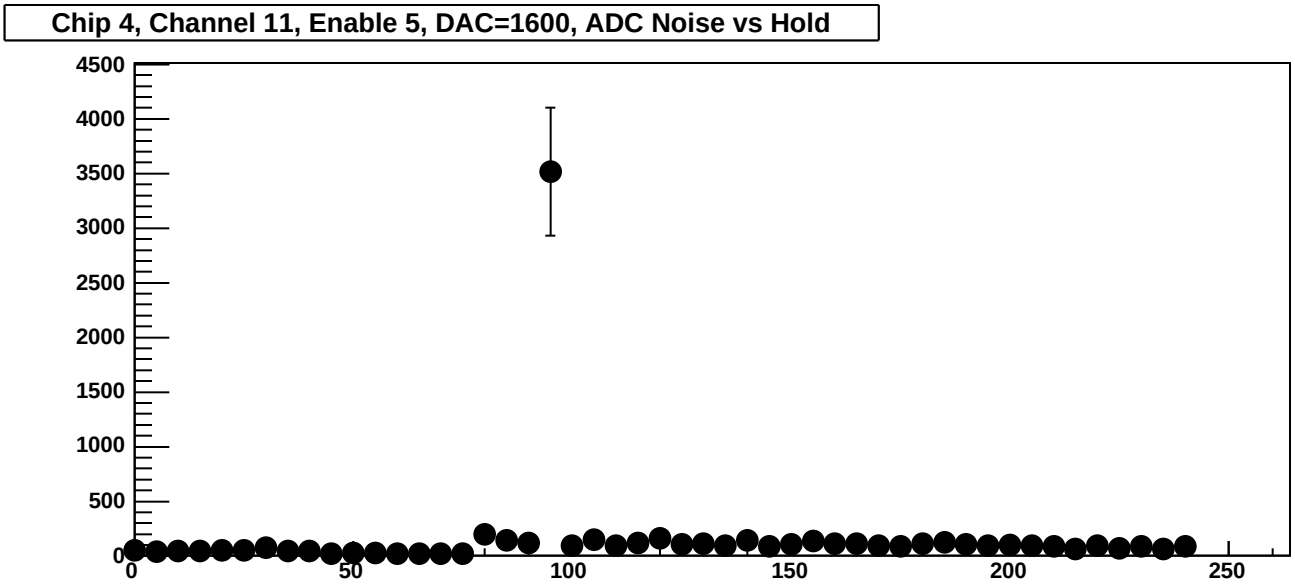
Chip 4, Channel 11, Enable 4, DAC=1600, ADC Noise vs Hold

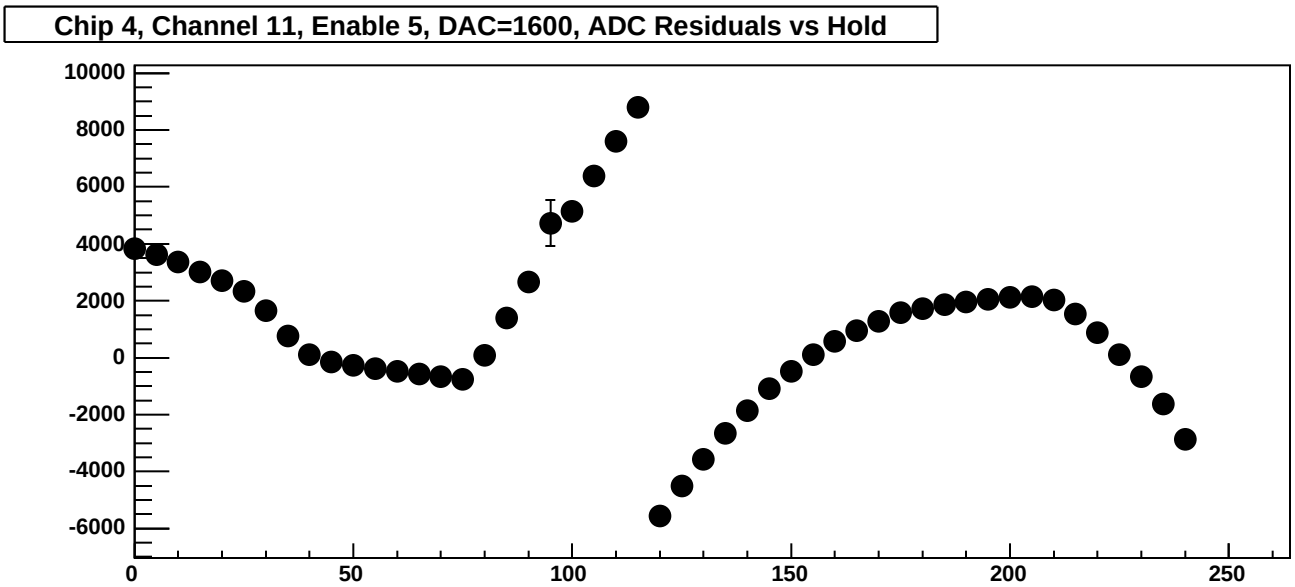


Chip 4, Channel 11, Enable 4, DAC=1600, ADC Residuals vs Hold

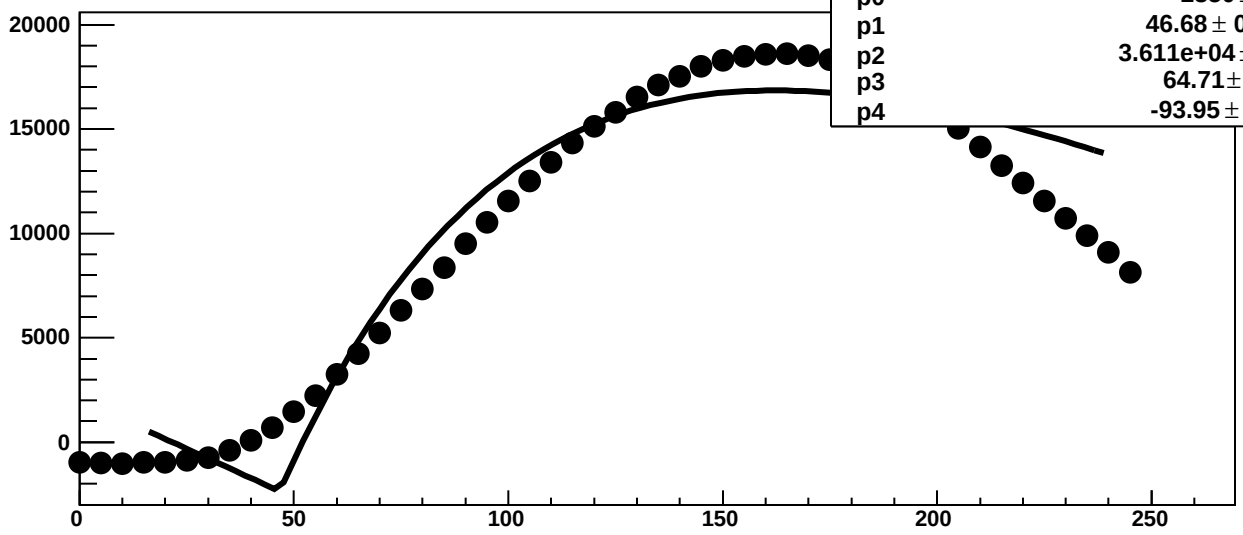



Chip 4, Channel 11, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.885e+05 / 41
p0 -1908 ± 0.1429
p1 119.9 ± 0.2641
p2 1.547e+04 ± 0.1609
p3 -67.39 ± 5.451
p4 23.95 ± 0.004687
15000
10000
5000
0
-5000
0
50
100
150
200
250

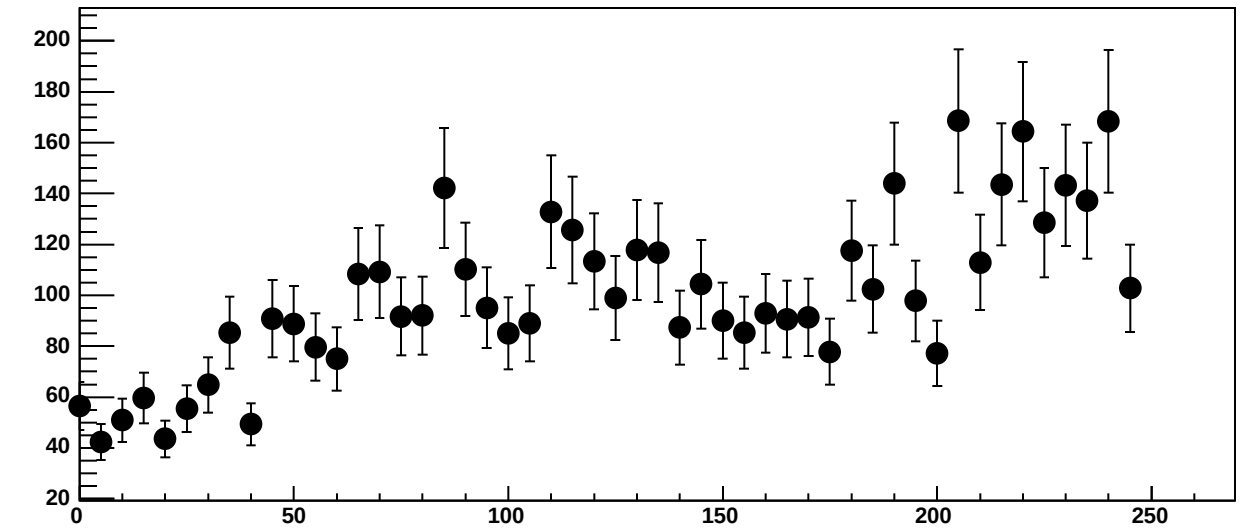

Chip 4, Channel 11, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 4, Channel 11, Enable 5, DAC=1600, ADC Residuals vs Hold
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

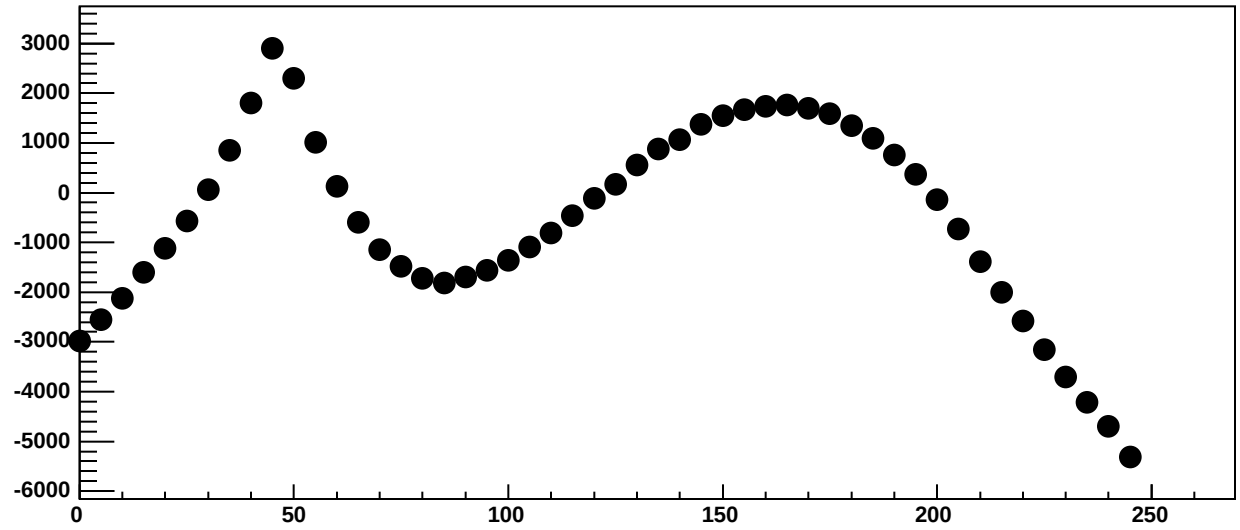
Chip 4, Channel 12, Enable 0, DAC=1600, ADC Mean vs Hold



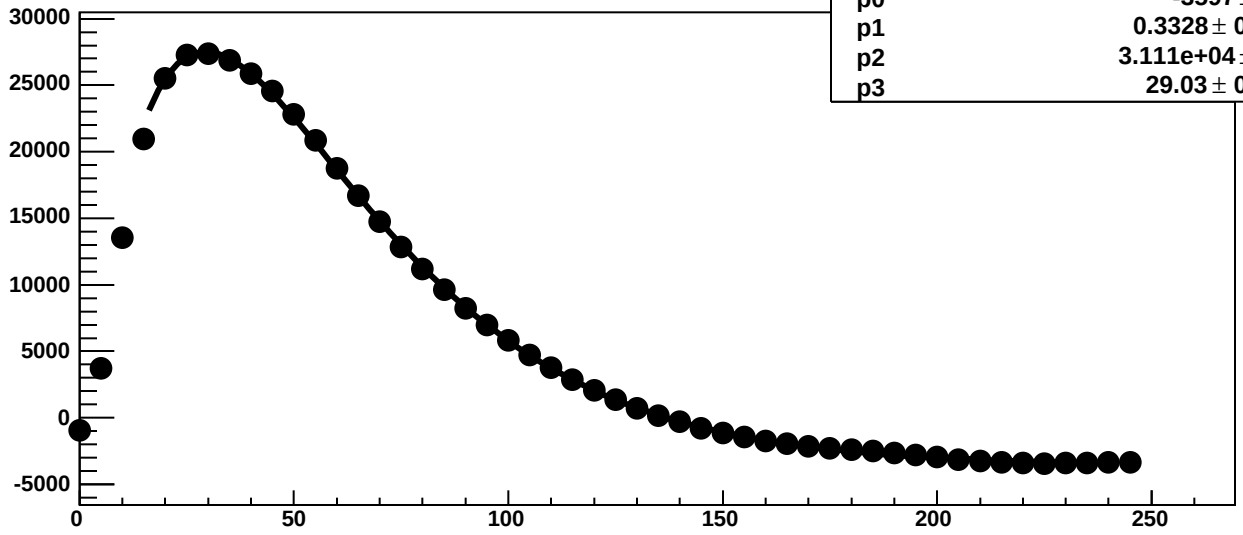
Chip 4, Channel 12, Enable 0, DAC=1600, ADC Noise vs Hold



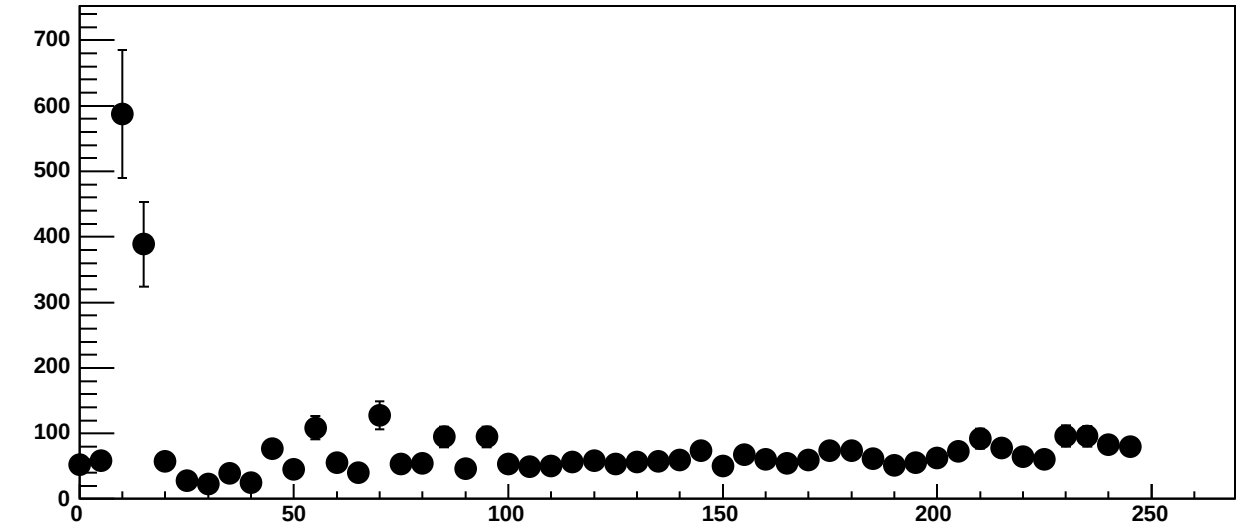
Chip 4, Channel 12, Enable 0, DAC=1600, ADC Residuals vs Hold



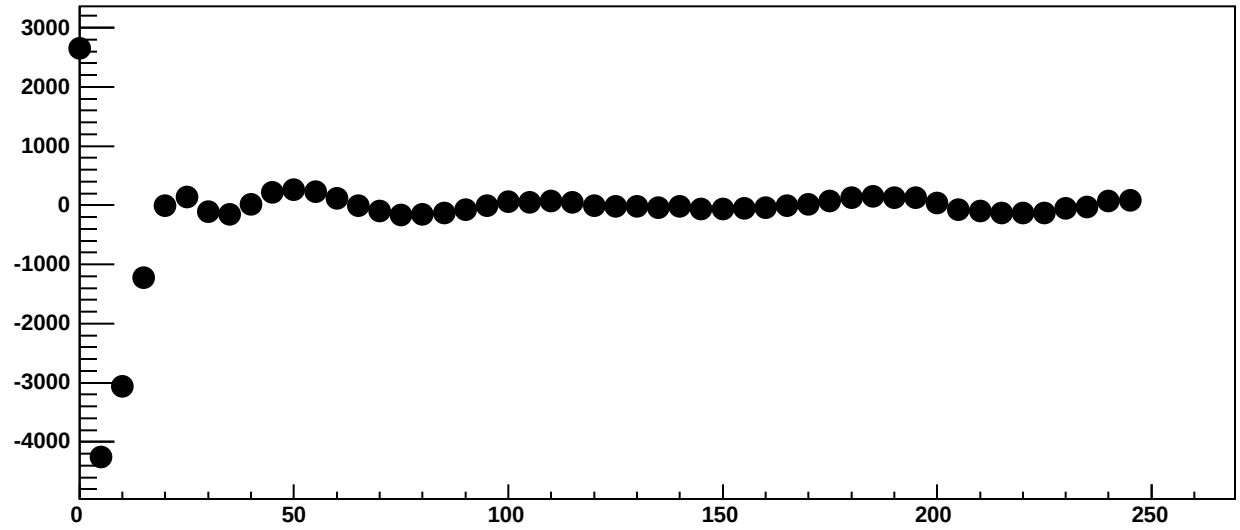
Chip 4, Channel 12, Enable 1!, DAC=1600, ADC Mean vs Hold

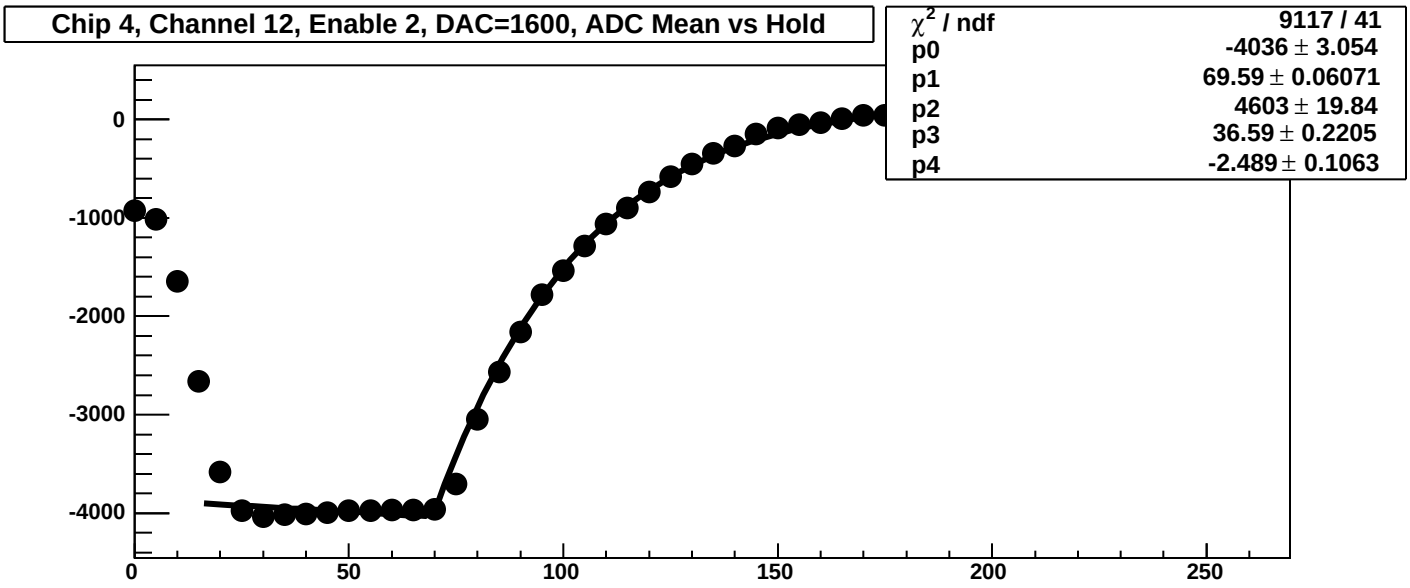


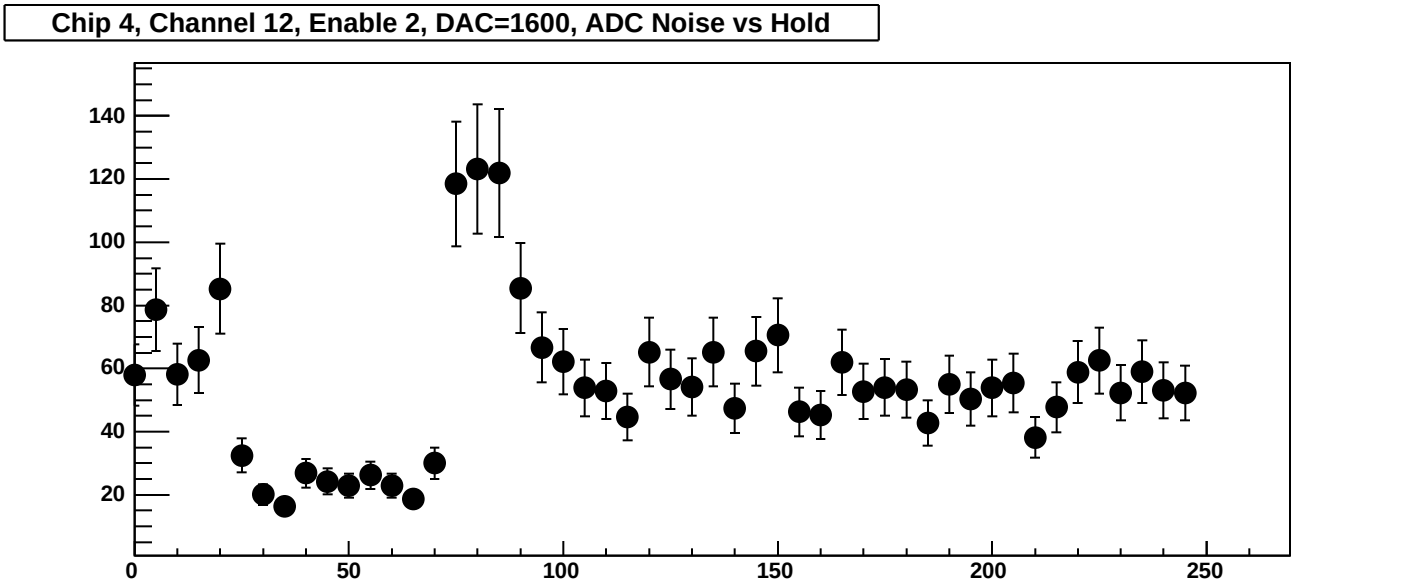
Chip 4, Channel 12, Enable 1!, DAC=1600, ADC Noise vs Hold

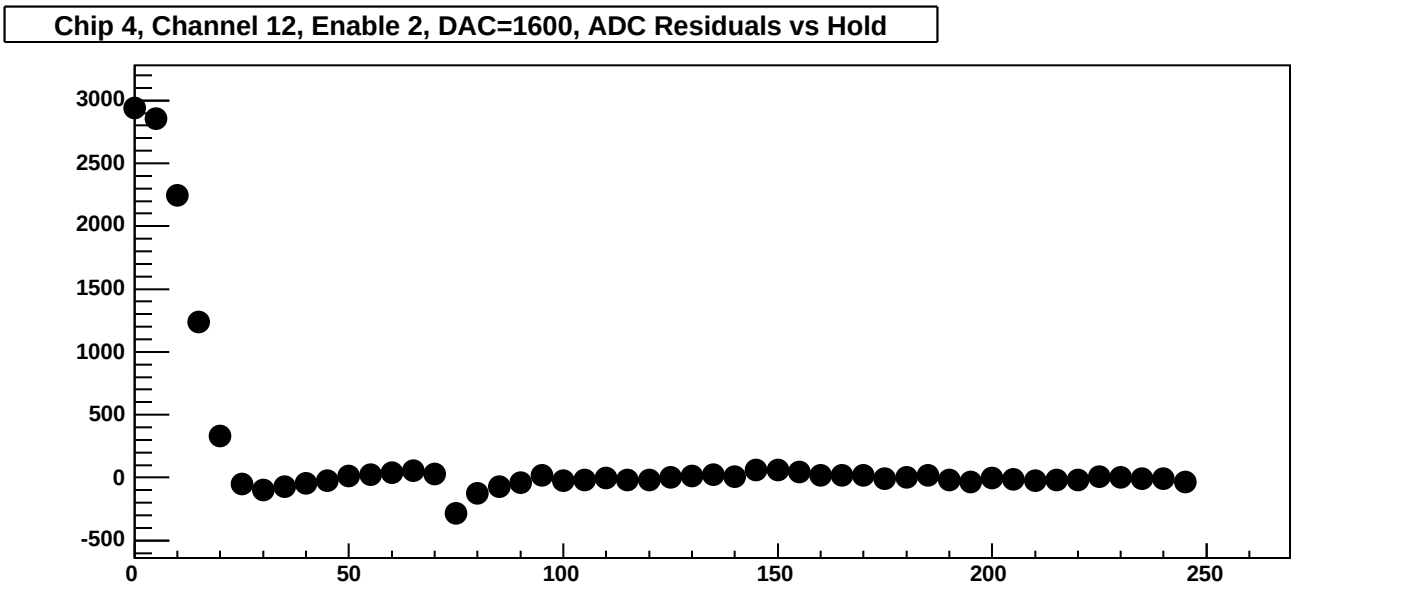


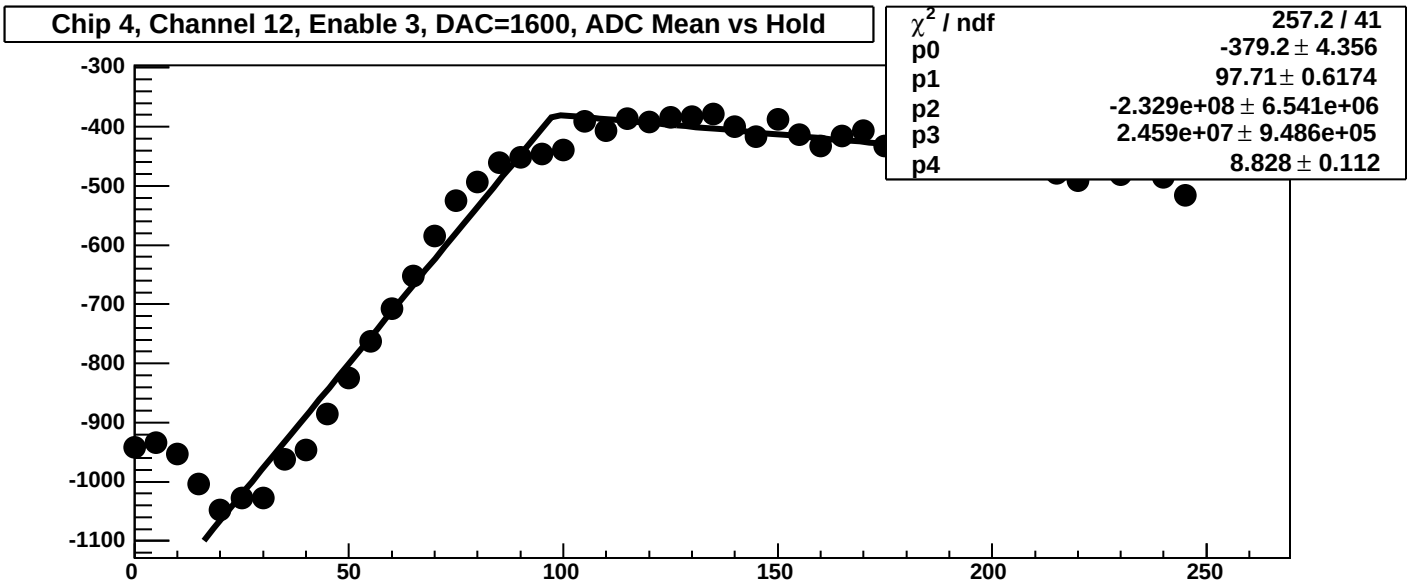
Chip 4, Channel 12, Enable 1!, DAC=1600, ADC Residuals vs Hold

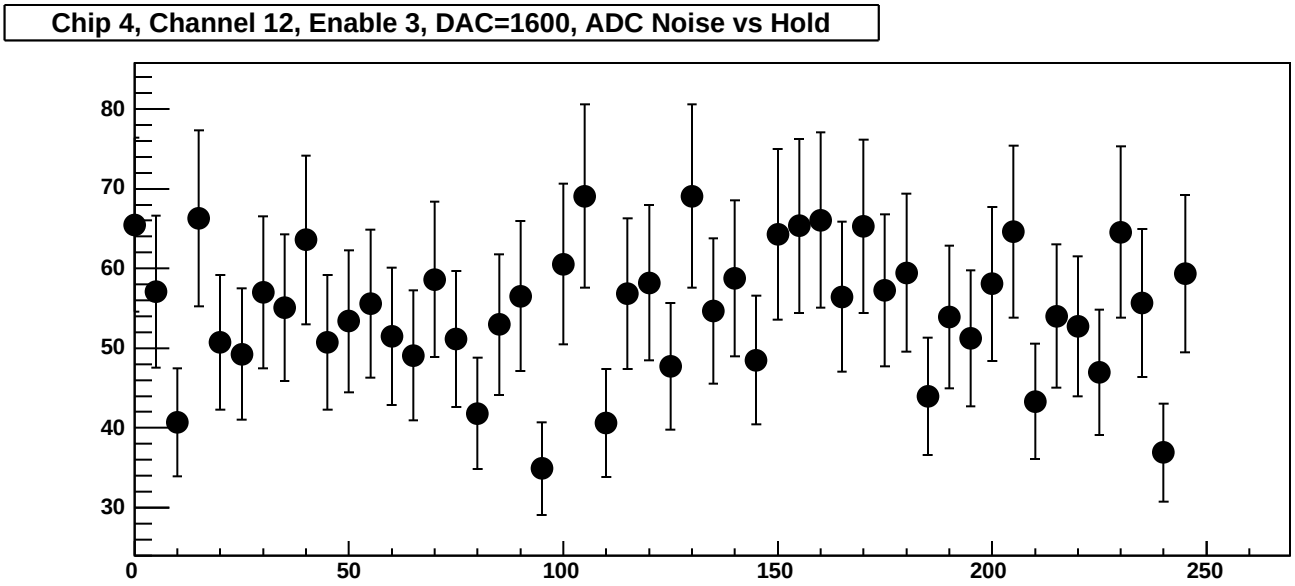


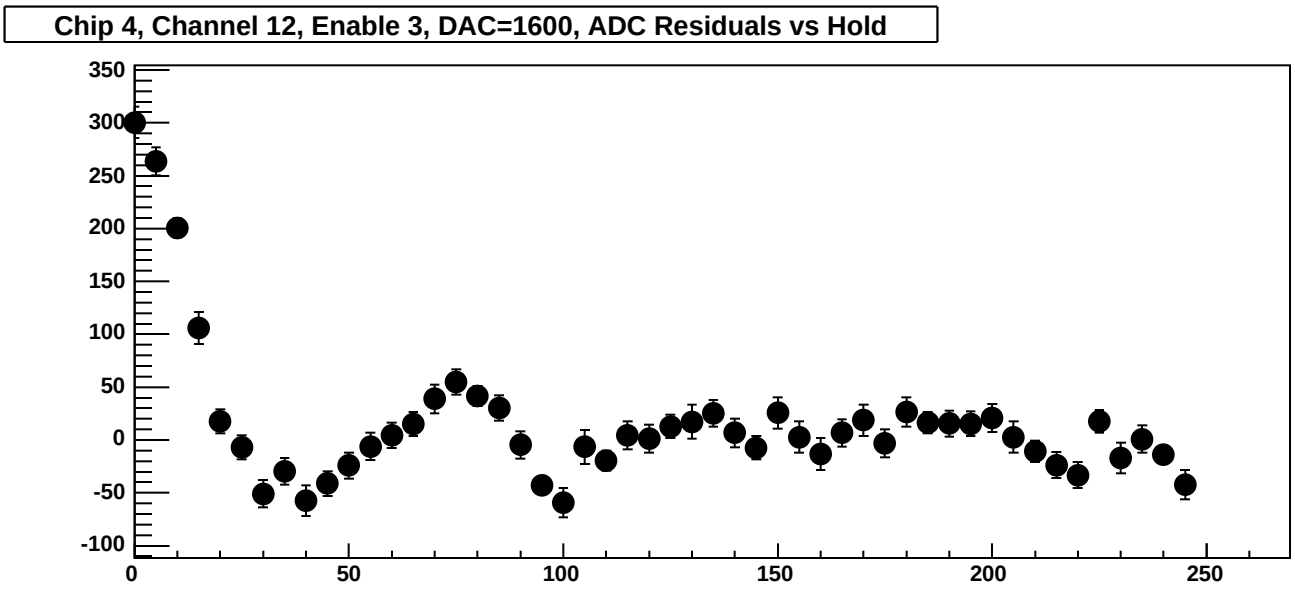

Chip 4, Channel 12, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 9117 / 41
p0 -4036 ± 3.054
p1 69.59 ± 0.06071
p2 4603 ± 19.84
p3 36.59 ± 0.2205
p4 -2.489 ± 0.1063
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250


Chip 4, Channel 12, Enable 2, DAC=1600, ADC Noise vs Hold
140
120
100
80
60
40
20
0
50
100
150
200
250

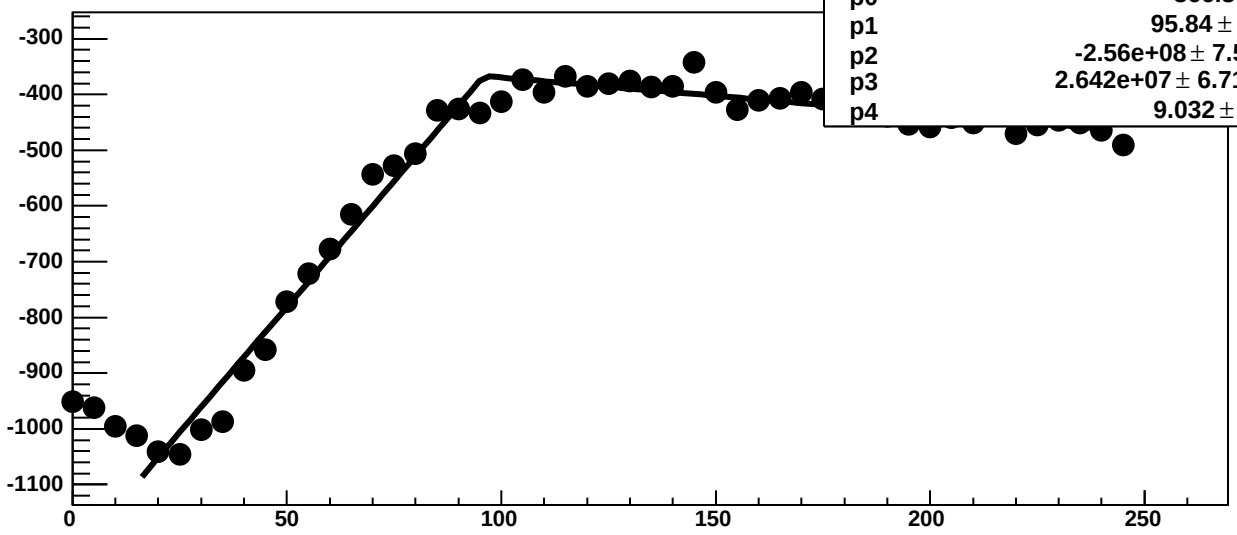

Chip 4, Channel 12, Enable 2, DAC=1600, ADC Residuals vs Hold
3000
2500
2000
1500
1000
500
0
-500
0
50
100
150
200
250


Chip 4, Channel 12, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 257.2 / 41
p0 -379.2 ± 4.356
p1 97.71 ± 0.6174
p2 -2.329e+08 ± 6.541e+06
p3 2.459e+07 ± 9.486e+05
p4 8.828 ± 0.112
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
50
100
150
200
250

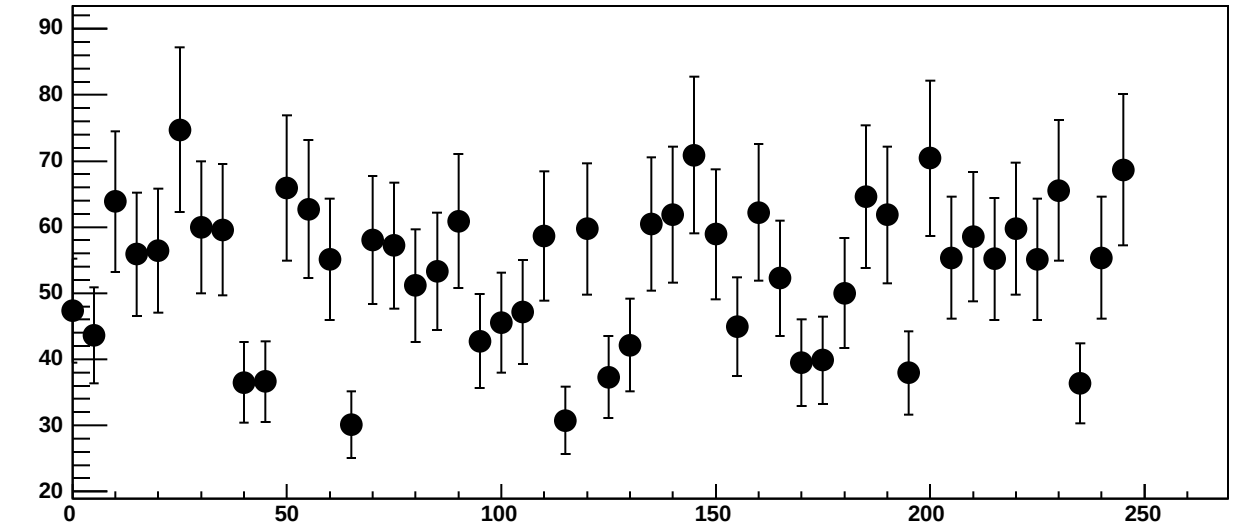

Chip 4, Channel 12, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 4, Channel 12, Enable 3, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

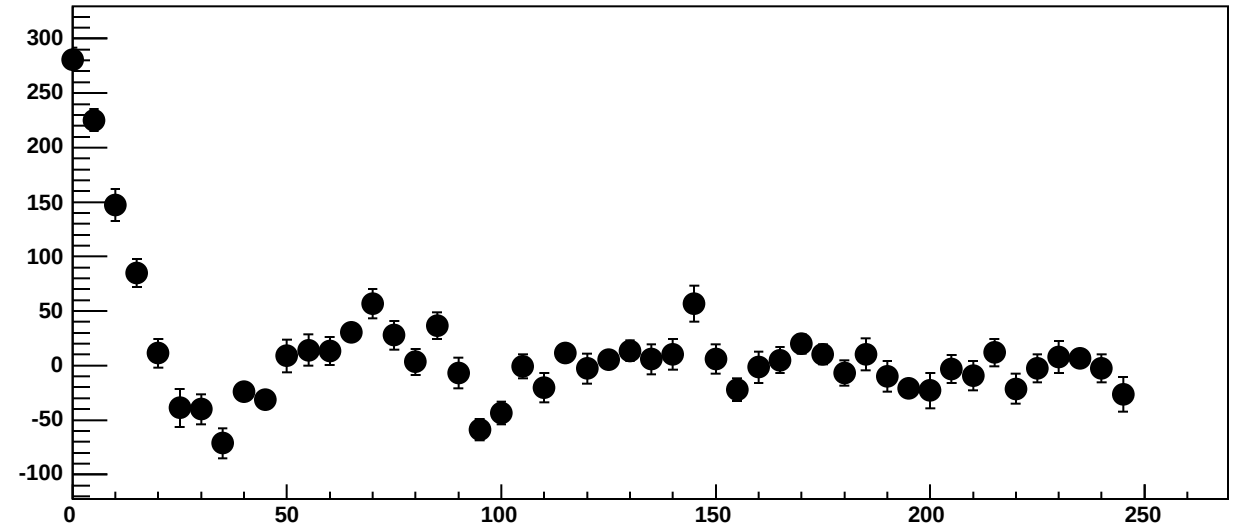
Chip 4, Channel 12, Enable 4, DAC=1600, ADC Mean vs Hold



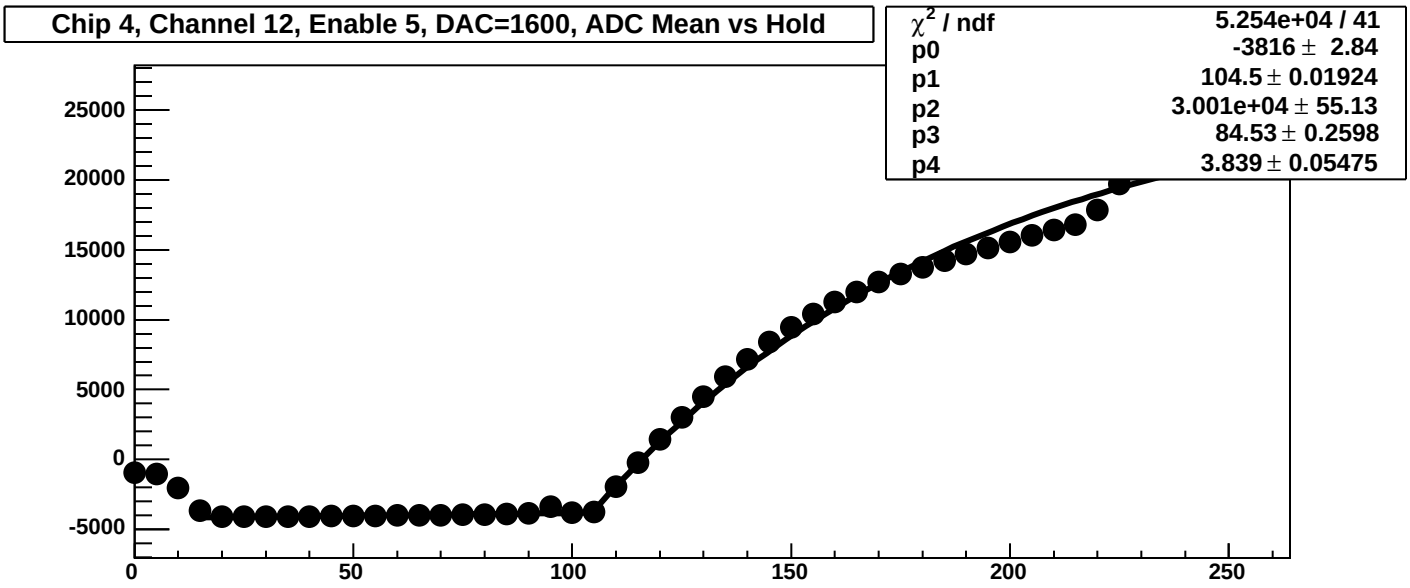
Chip 4, Channel 12, Enable 4, DAC=1600, ADC Noise vs Hold



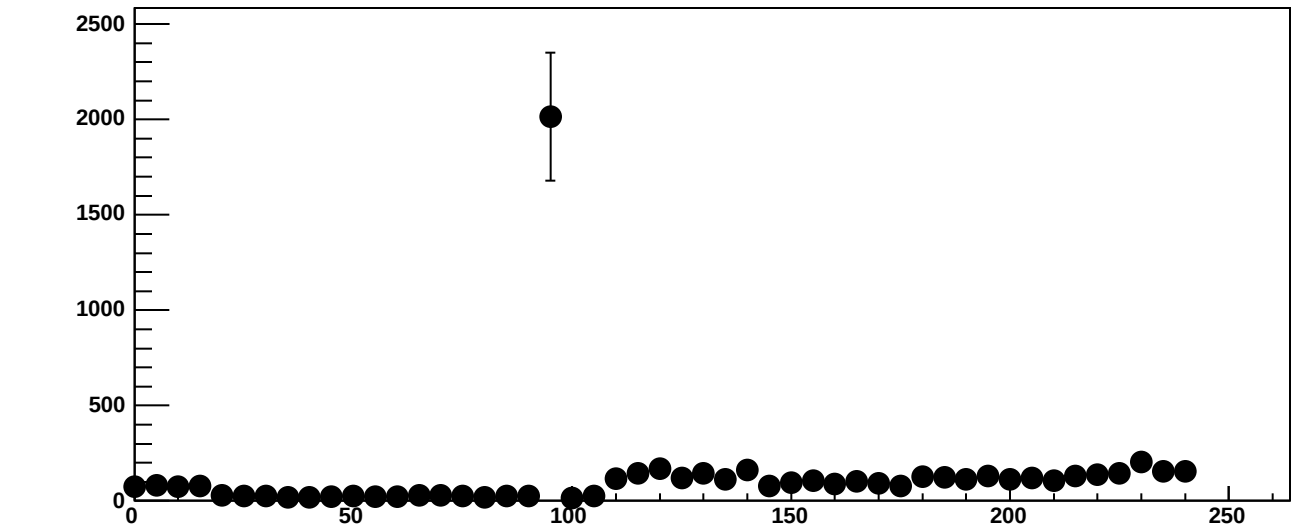
Chip 4, Channel 12, Enable 4, DAC=1600, ADC Residuals vs Hold



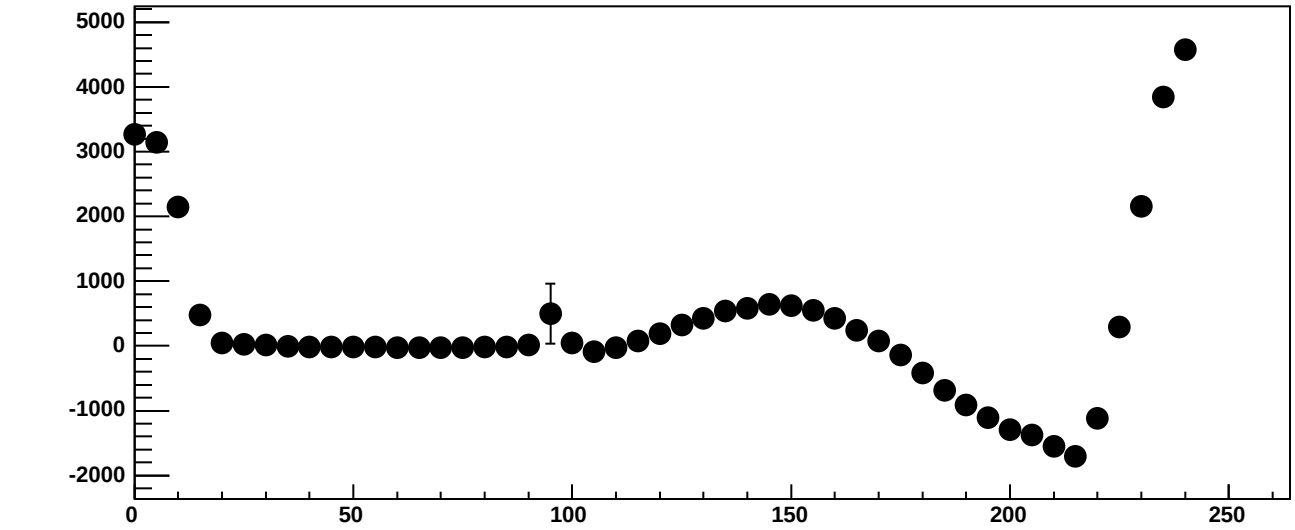
Chip 4, Channel 12, Enable 5, DAC=1600, ADC Mean vs Hold

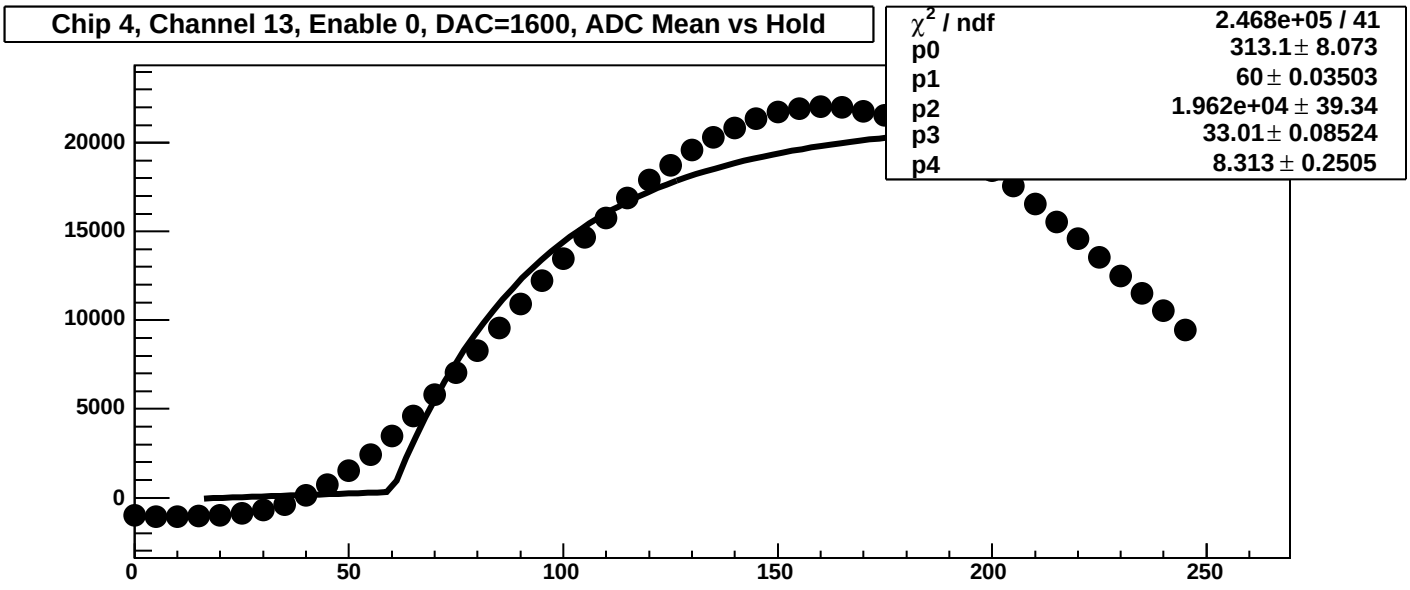


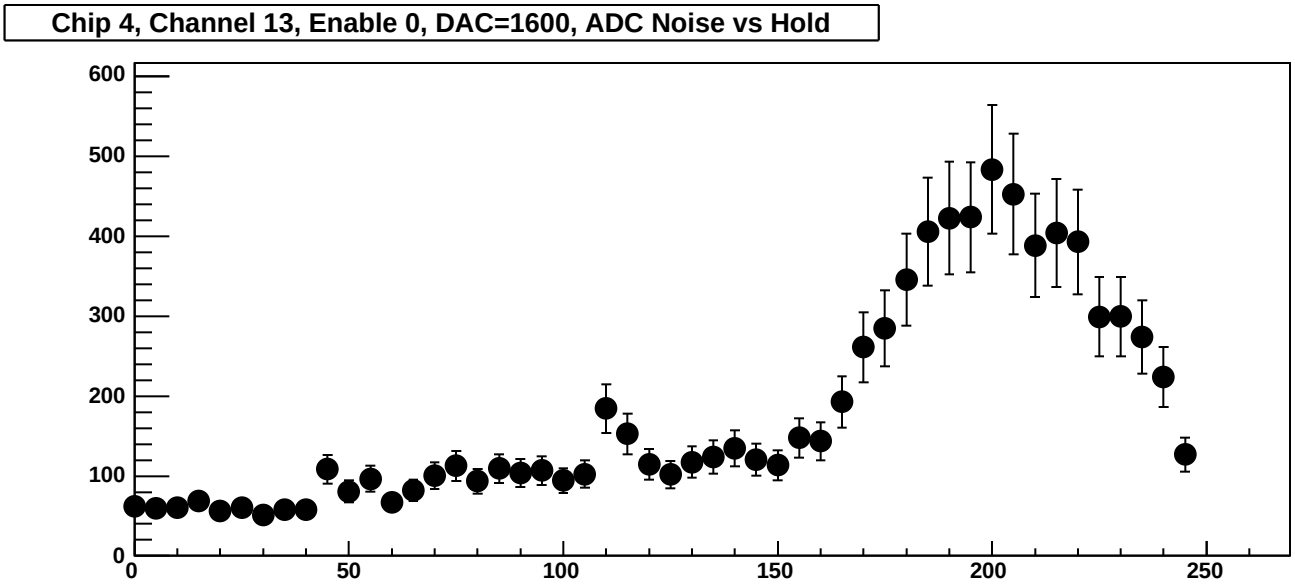
Chip 4, Channel 12, Enable 5, DAC=1600, ADC Noise vs Hold

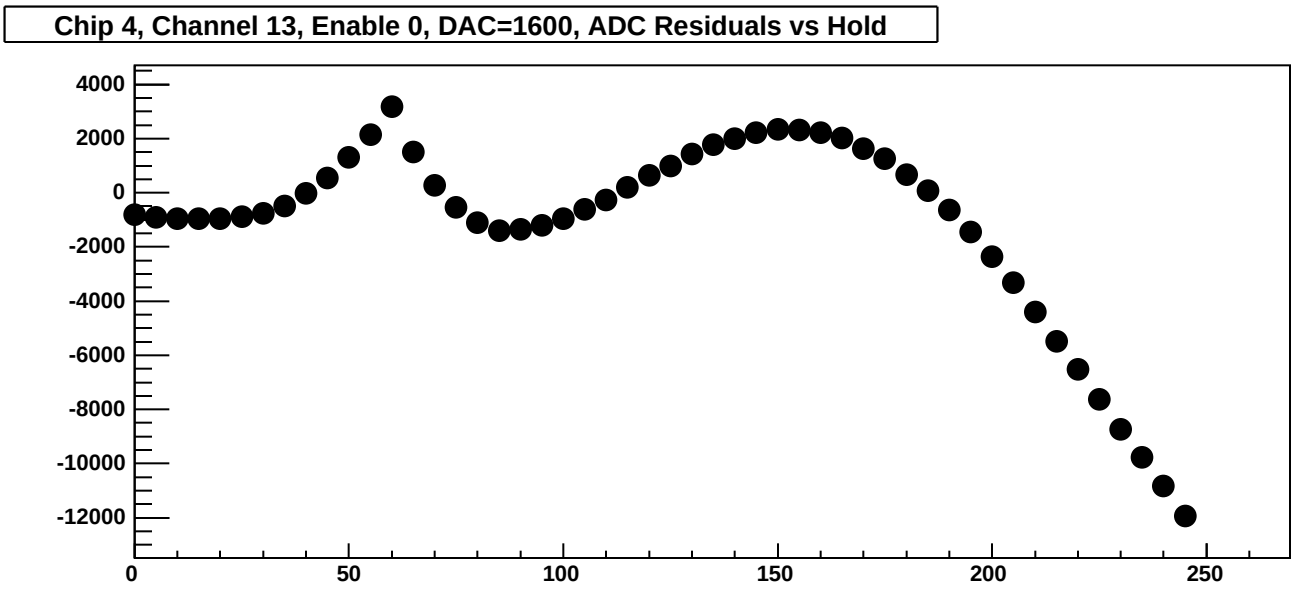


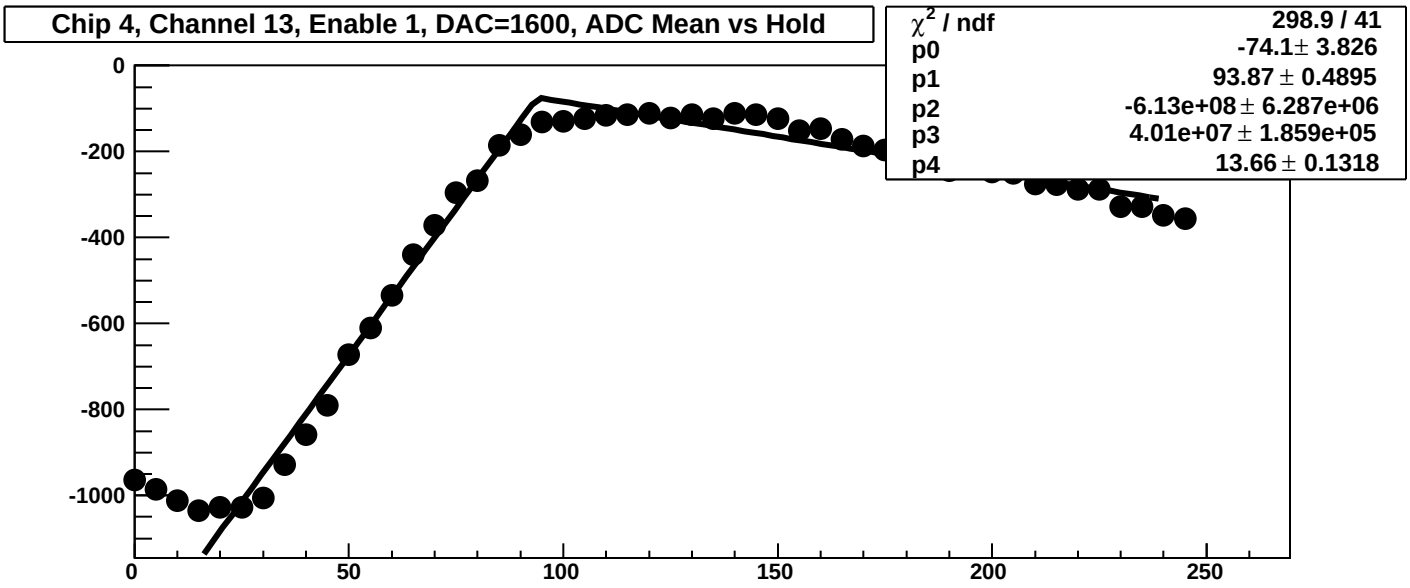
Chip 4, Channel 12, Enable 5, DAC=1600, ADC Residuals vs Hold

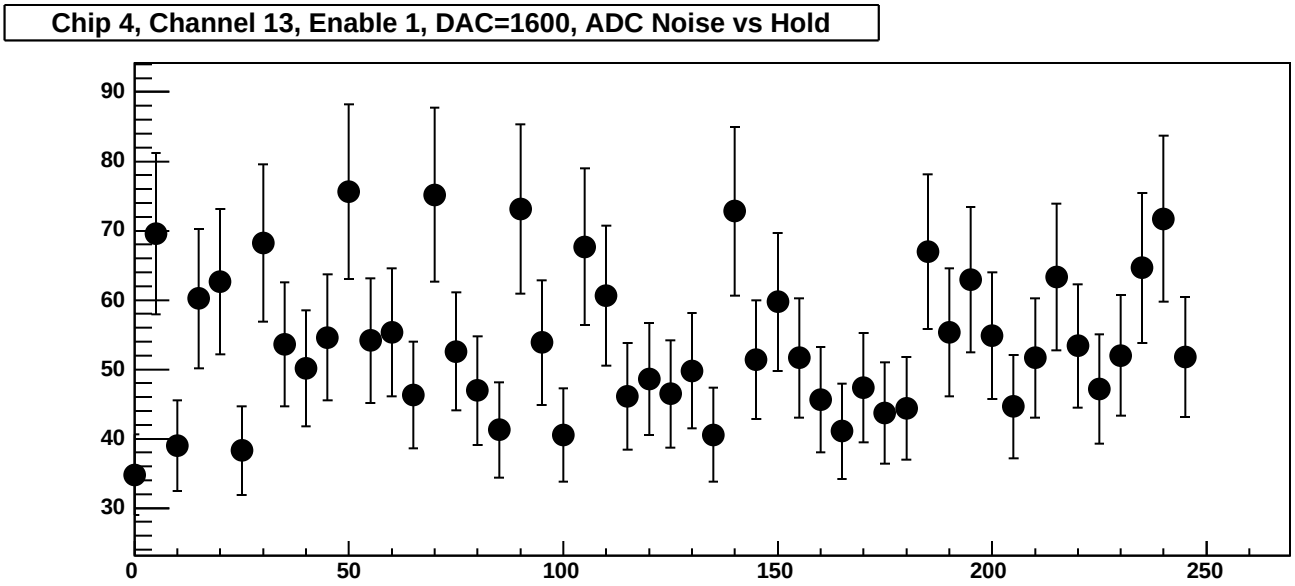


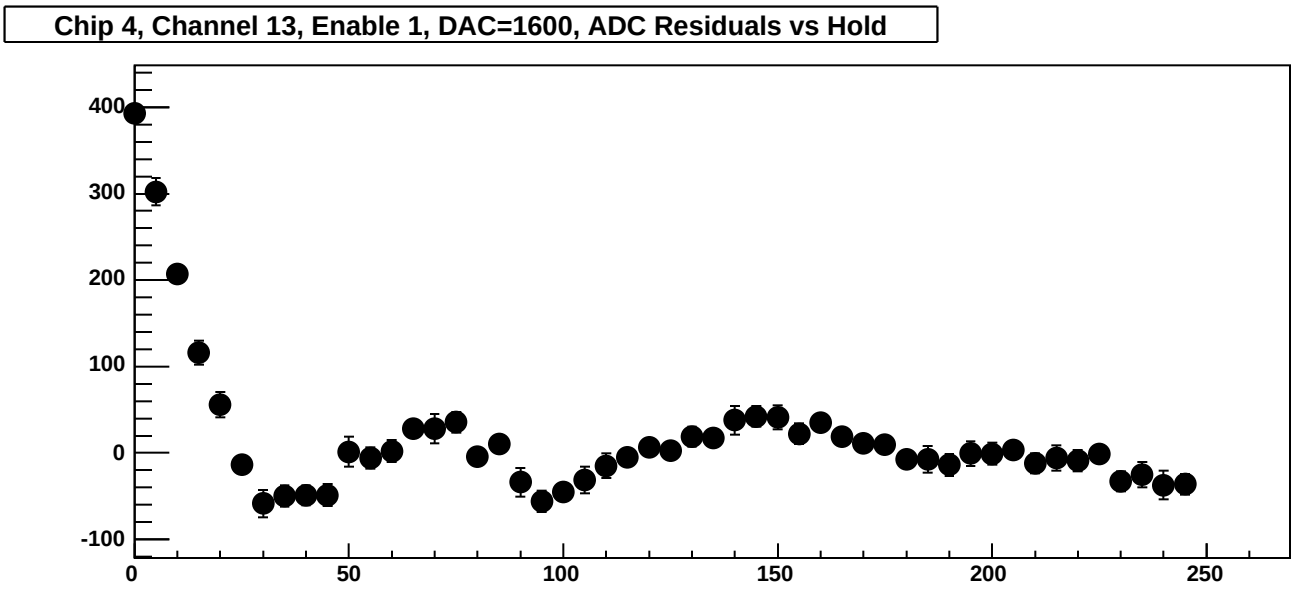

Chip 4, Channel 13, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.468e+05 / 41
p0 313.1 ± 8.073
p1 60 ± 0.03503
p2 1.962e+04 ± 39.34
p3 33.01 ± 0.08524
p4 8.313 ± 0.2505
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 4, Channel 13, Enable 0, DAC=1600, ADC Noise vs Hold
600
500
400
300
200
100
0
0
50
100
150
200
250

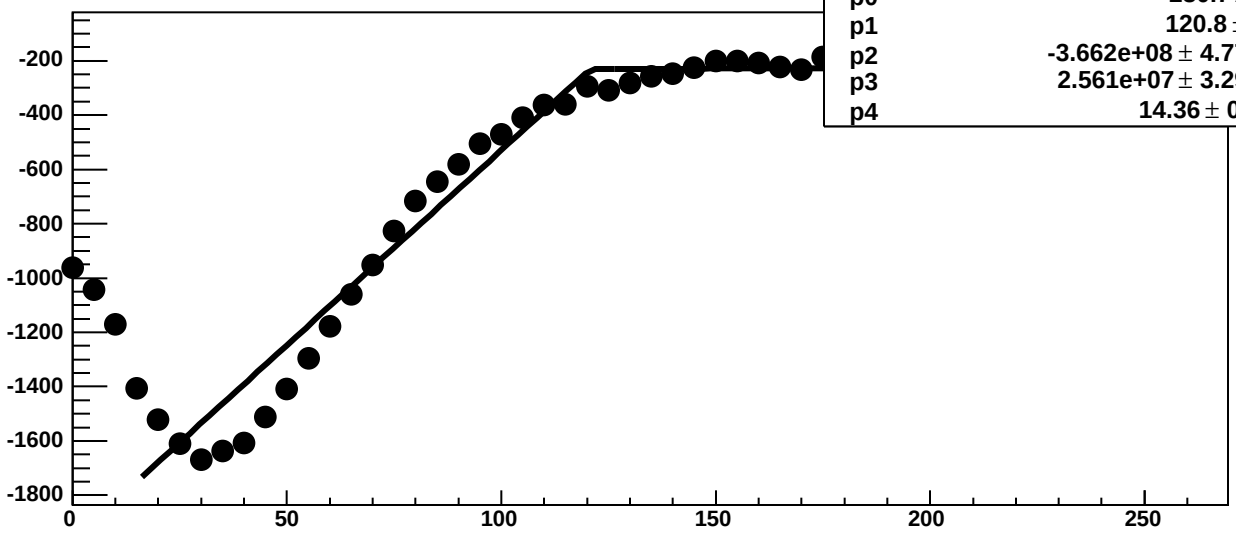

Chip 4, Channel 13, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
0
50
100
150
200
250


Chip 4, Channel 13, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 298.9 / 41
p0 -74.1 ± 3.826
p1 93.87 ± 0.4895
p2 -6.13e+08 ± 6.287e+06
p3 4.01e+07 ± 1.859e+05
p4 13.66 ± 0.1318
0
-200
-400
-600
-800
-1000
0
50
100
150
200
250

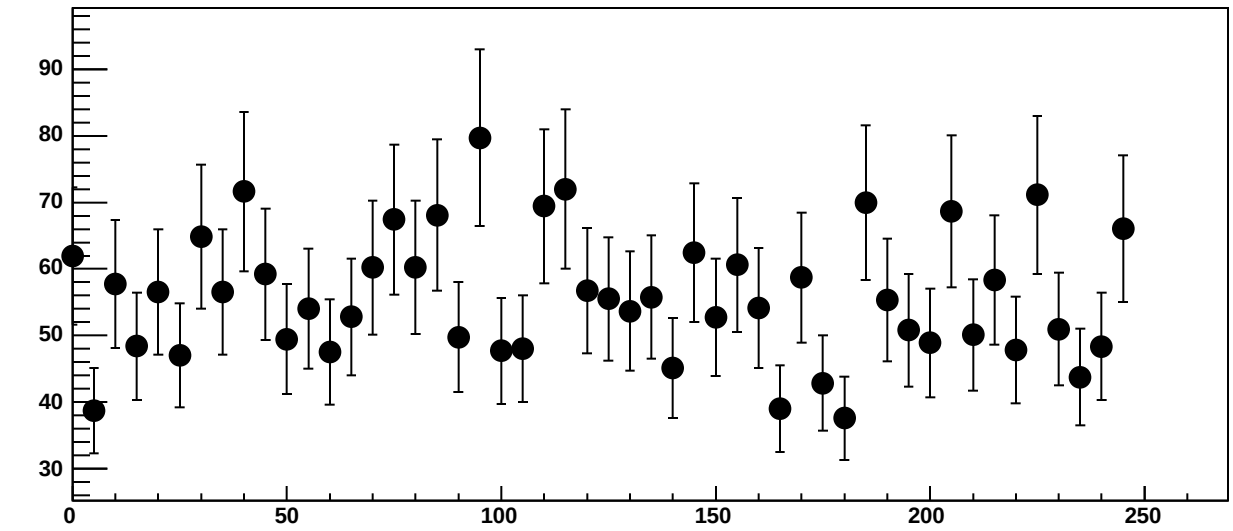

Chip 4, Channel 13, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 4, Channel 13, Enable 1, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

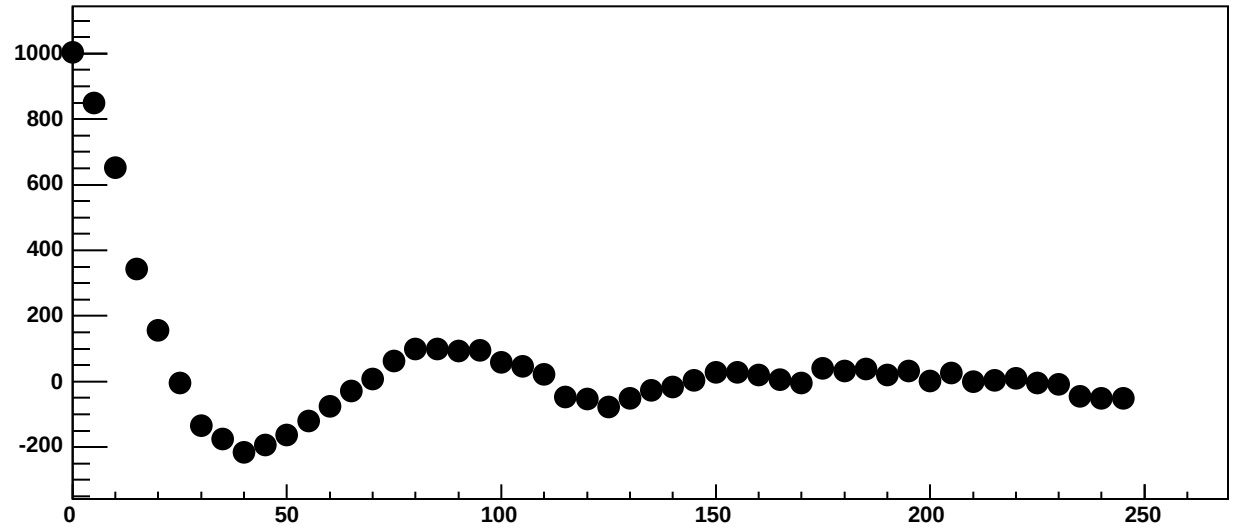
Chip 4, Channel 13, Enable 2, DAC=1600, ADC Mean vs Hold

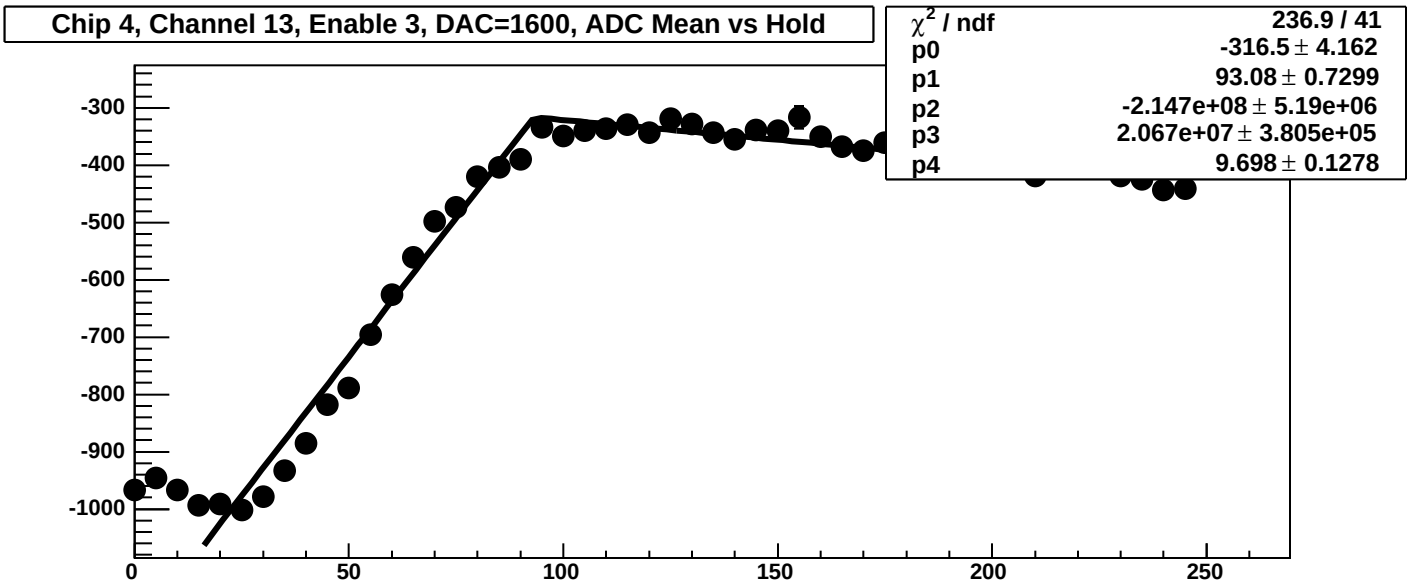


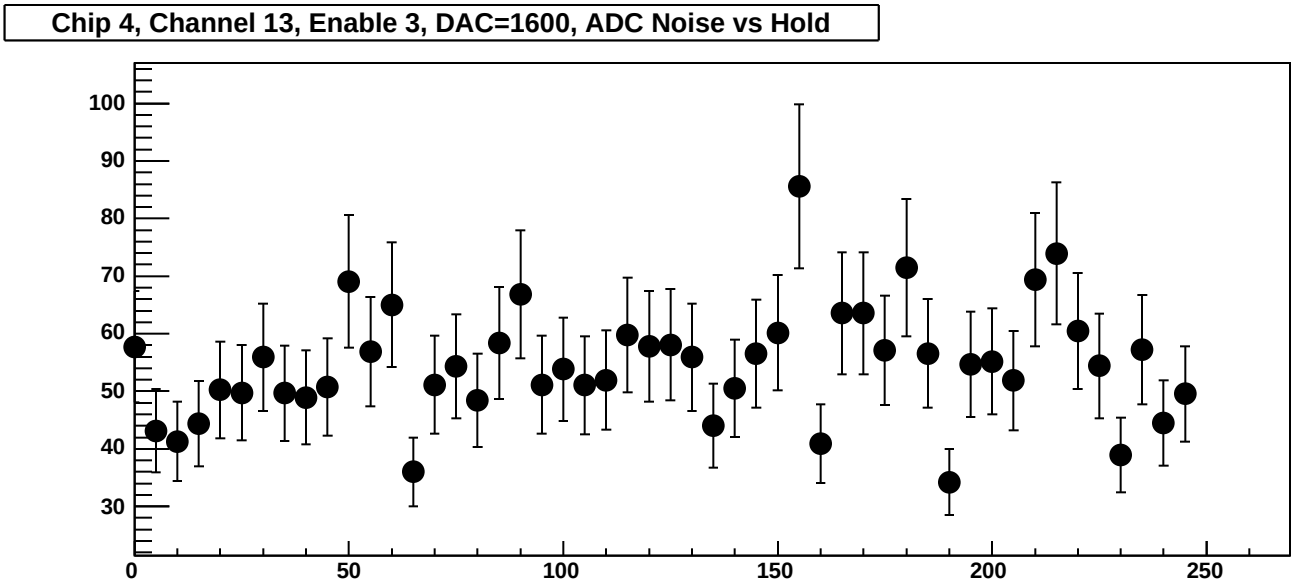
Chip 4, Channel 13, Enable 2, DAC=1600, ADC Noise vs Hold

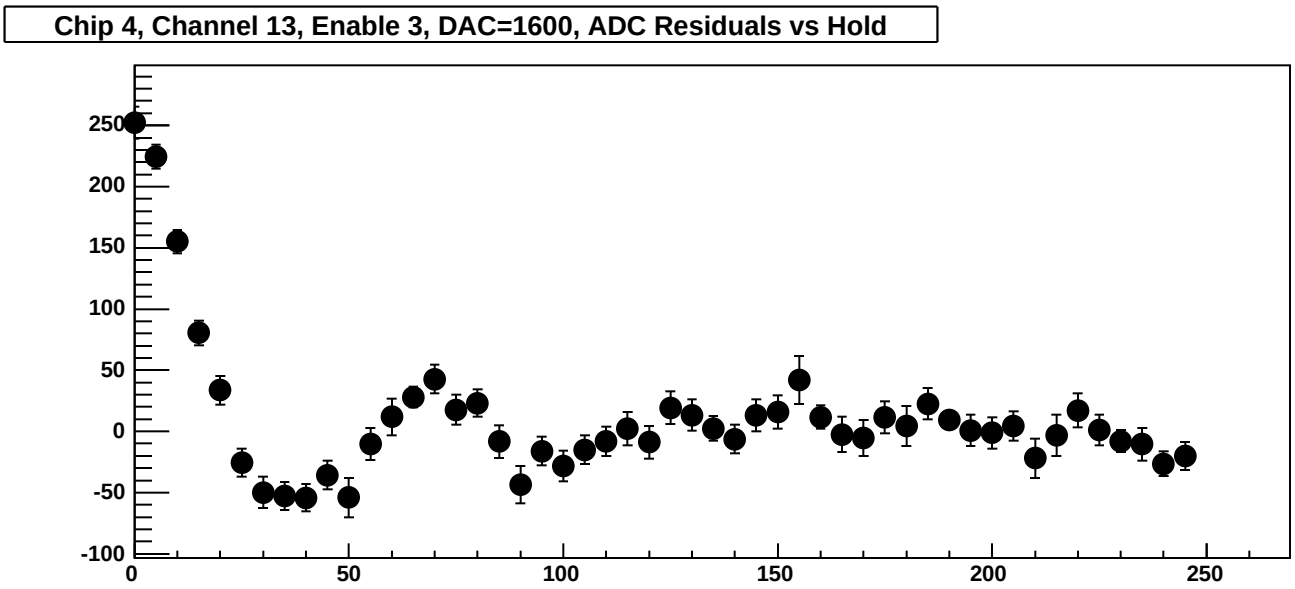


Chip 4, Channel 13, Enable 2, DAC=1600, ADC Residuals vs Hold



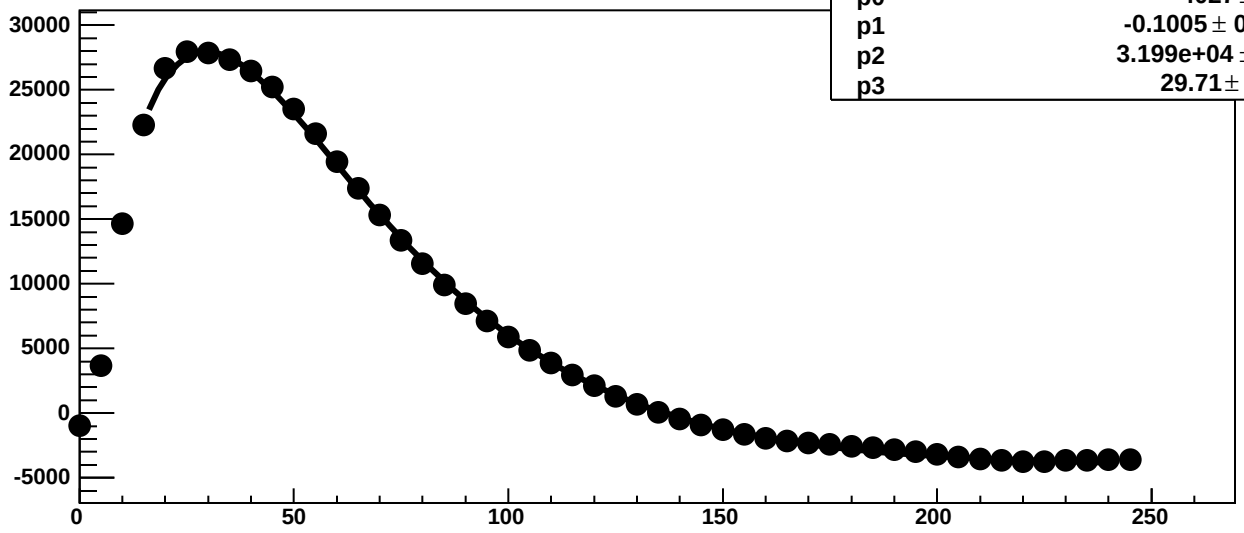

Chip 4, Channel 13, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 236.9 / 41
p0 -316.5 ± 4.162
p1 93.08 ± 0.7299
p2 -2.147e+08 ± 5.19e+06
p3 2.067e+07 ± 3.805e+05
p4 9.698 ± 0.1278
-300
-400
-500
-600
-700
-800
-900
-1000
0
50
100
150
200
250


Chip 4, Channel 13, Enable 3, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250

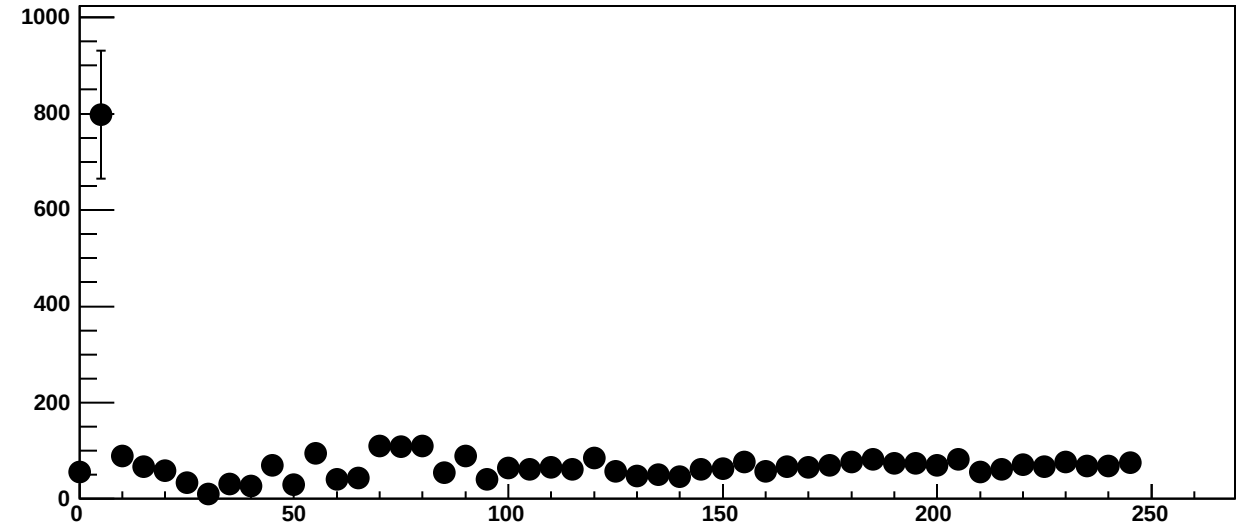

Chip 4, Channel 13, Enable 3, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

Chip 4, Channel 13, Enable 4!, DAC=1600, ADC Mean vs Hold

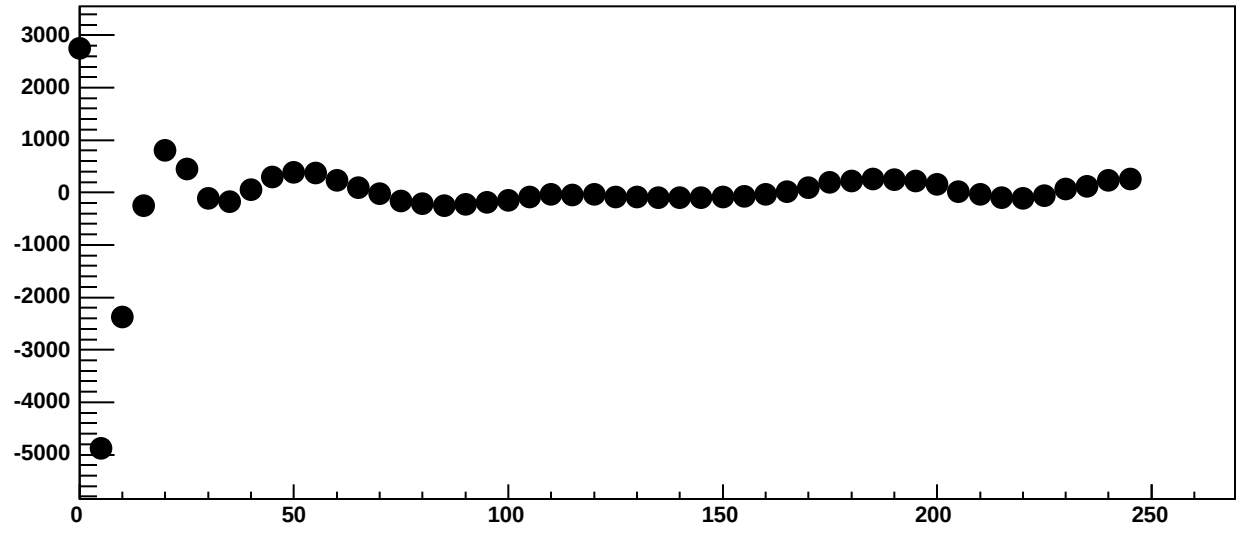
χ^2 / ndf	1.679e+04 / 42
p0	-4027 ± 4.329
p1	-0.1005 ± 0.01694
p2	3.199e+04 ± 4.579
p3	29.71 ± 0.0104

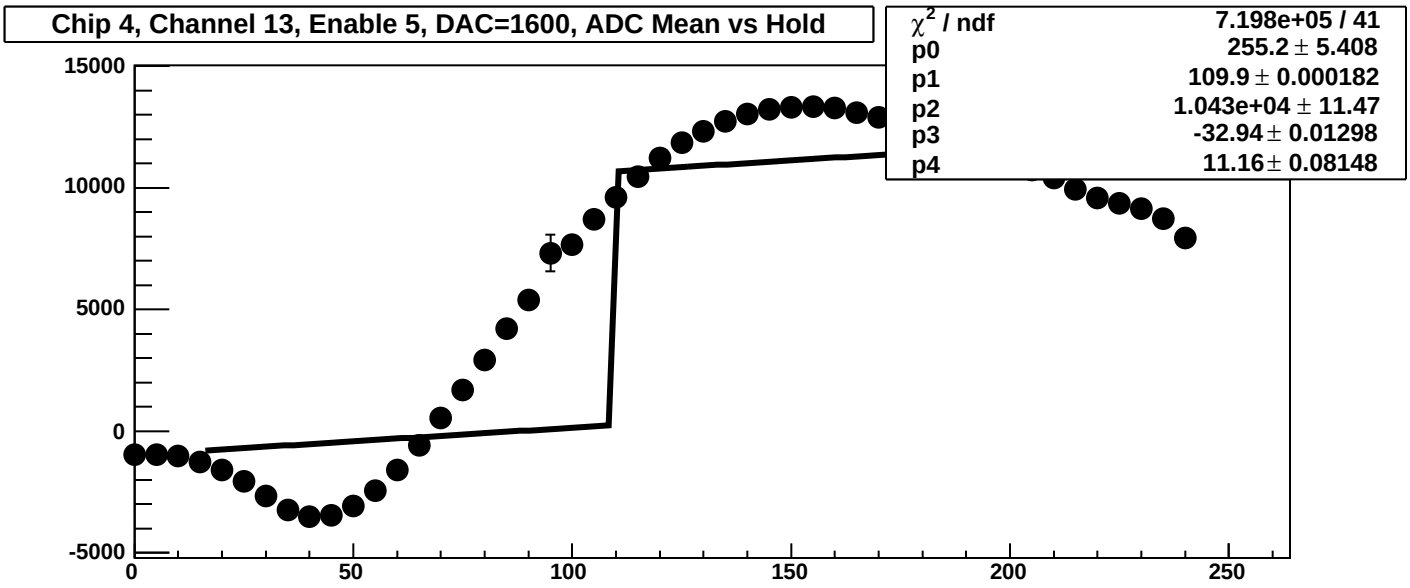


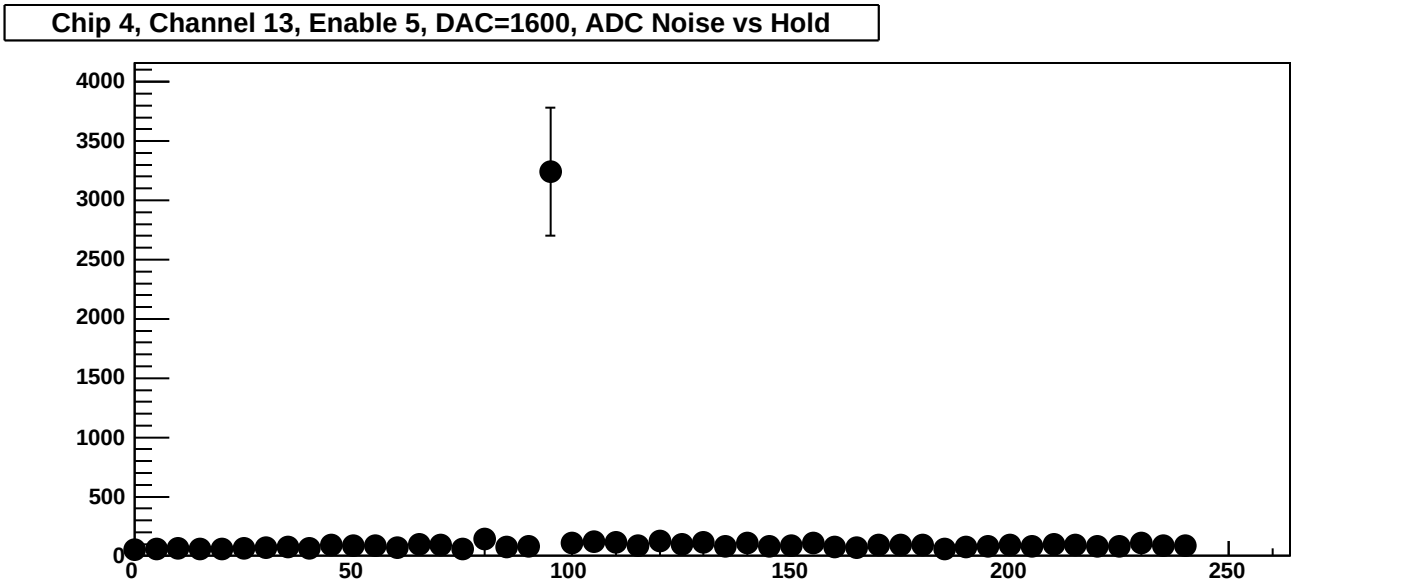
Chip 4, Channel 13, Enable 4!, DAC=1600, ADC Noise vs Hold

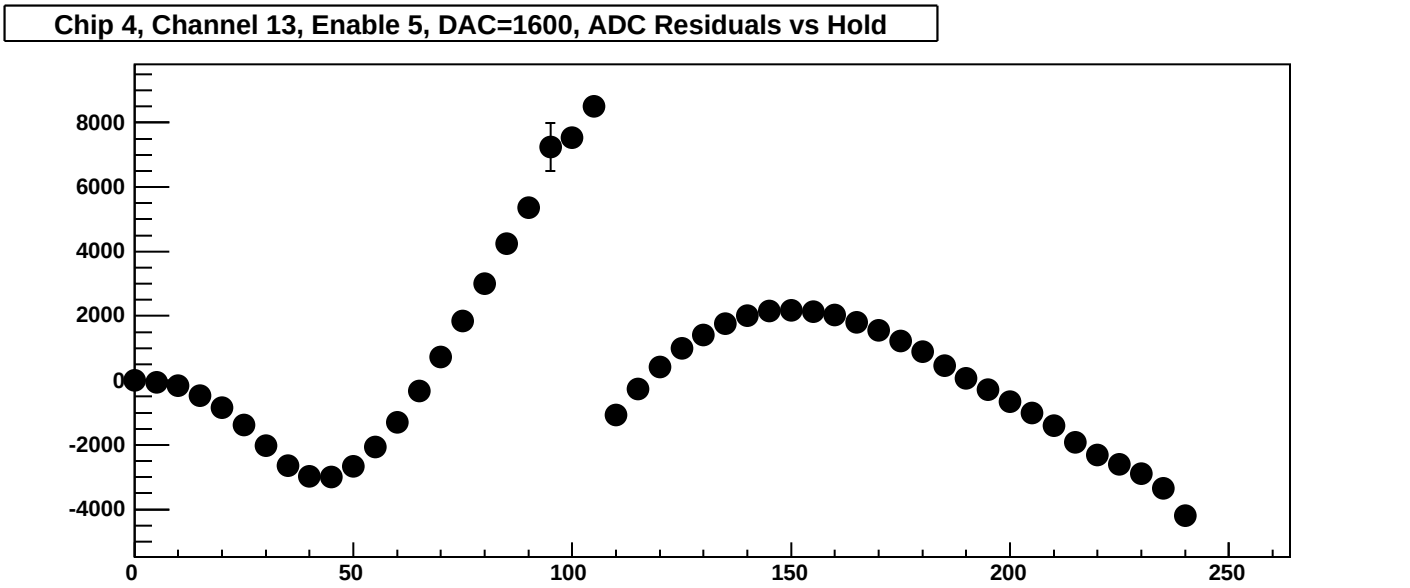


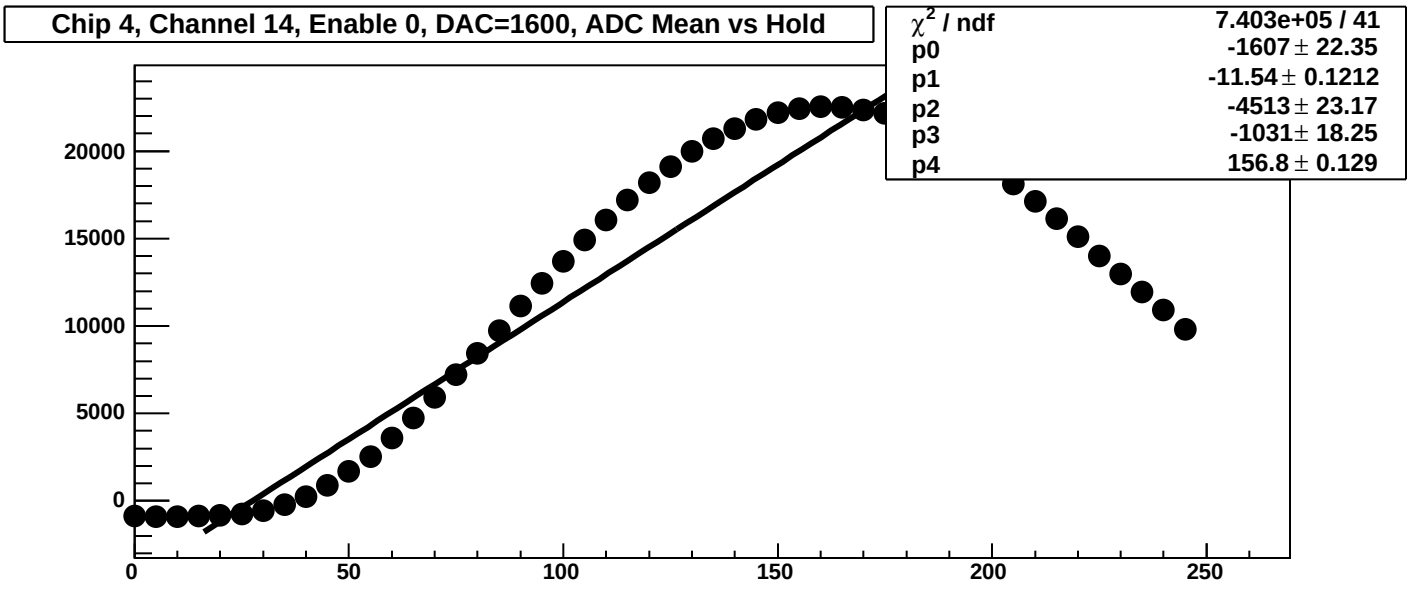
Chip 4, Channel 13, Enable 4!, DAC=1600, ADC Residuals vs Hold

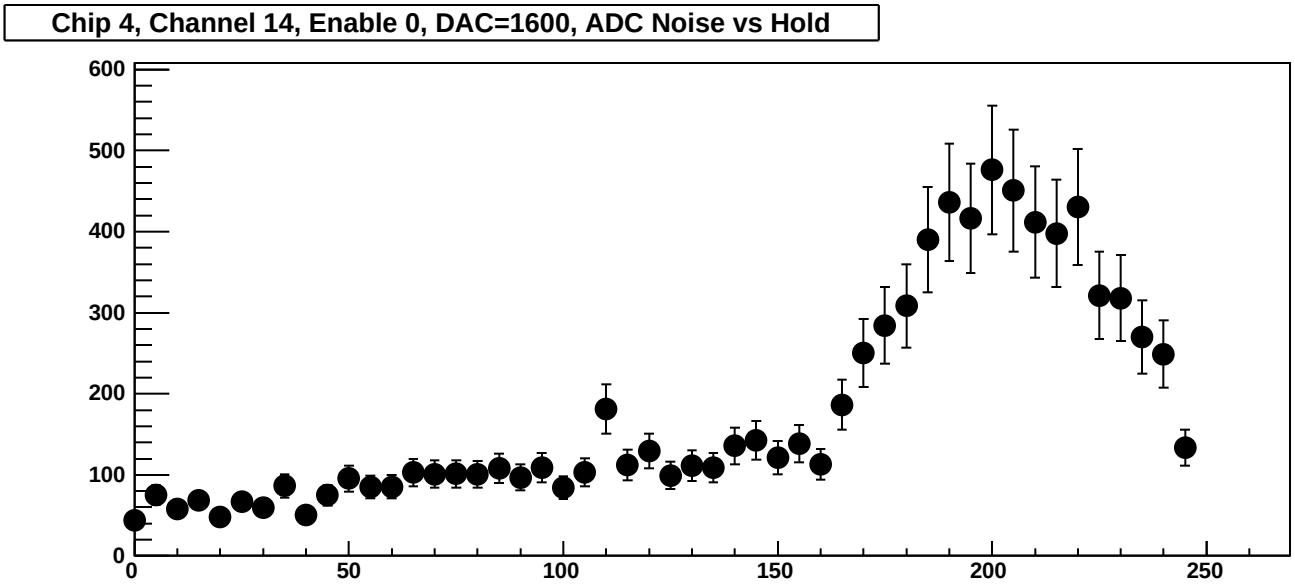


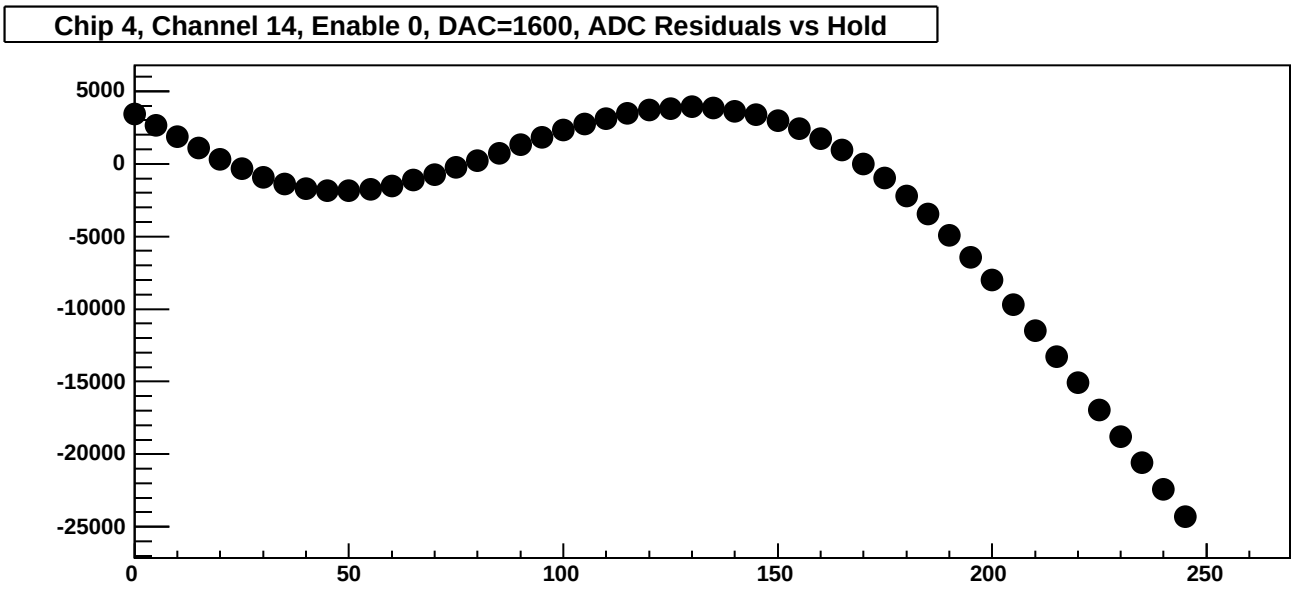

Chip 4, Channel 13, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.198e+05 / 41
p0 255.2 ± 5.408
p1 109.9 ± 0.000182
p2 1.043e+04 ± 11.47
p3 -32.94 ± 0.01298
p4 11.16 ± 0.08148
15000
10000
5000
0
-5000
0
50
100
150
200
250

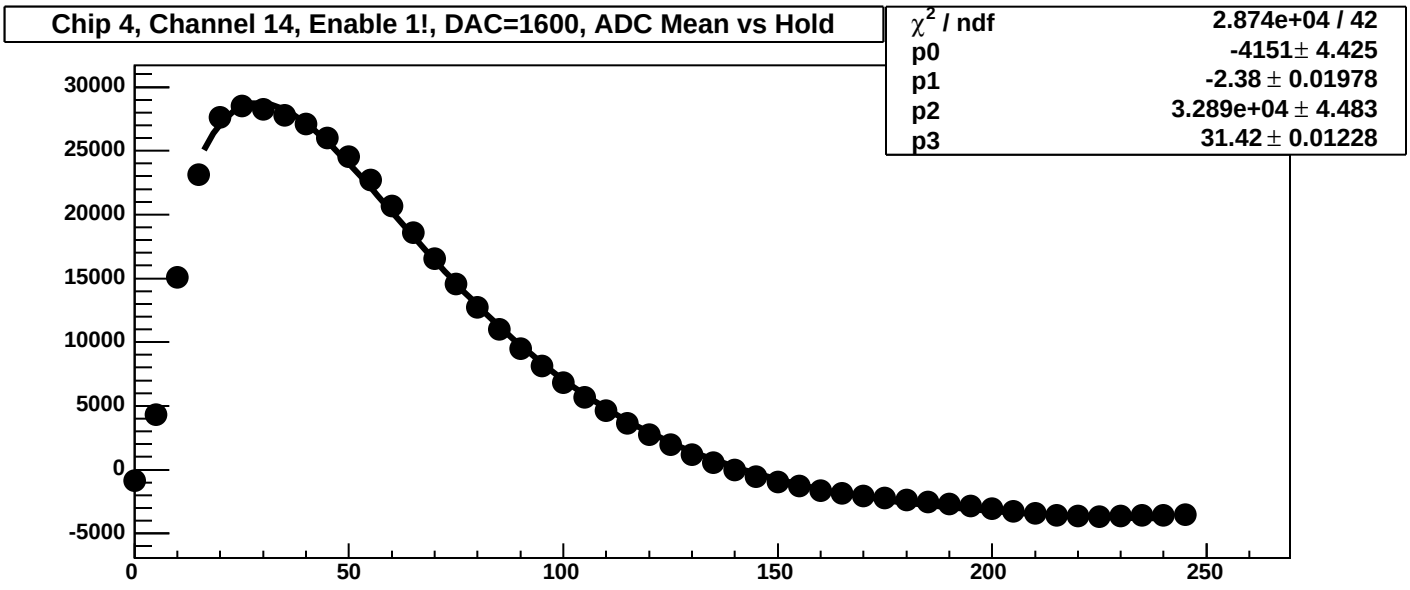

Chip 4, Channel 13, Enable 5, DAC=1600, ADC Noise vs Hold
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250

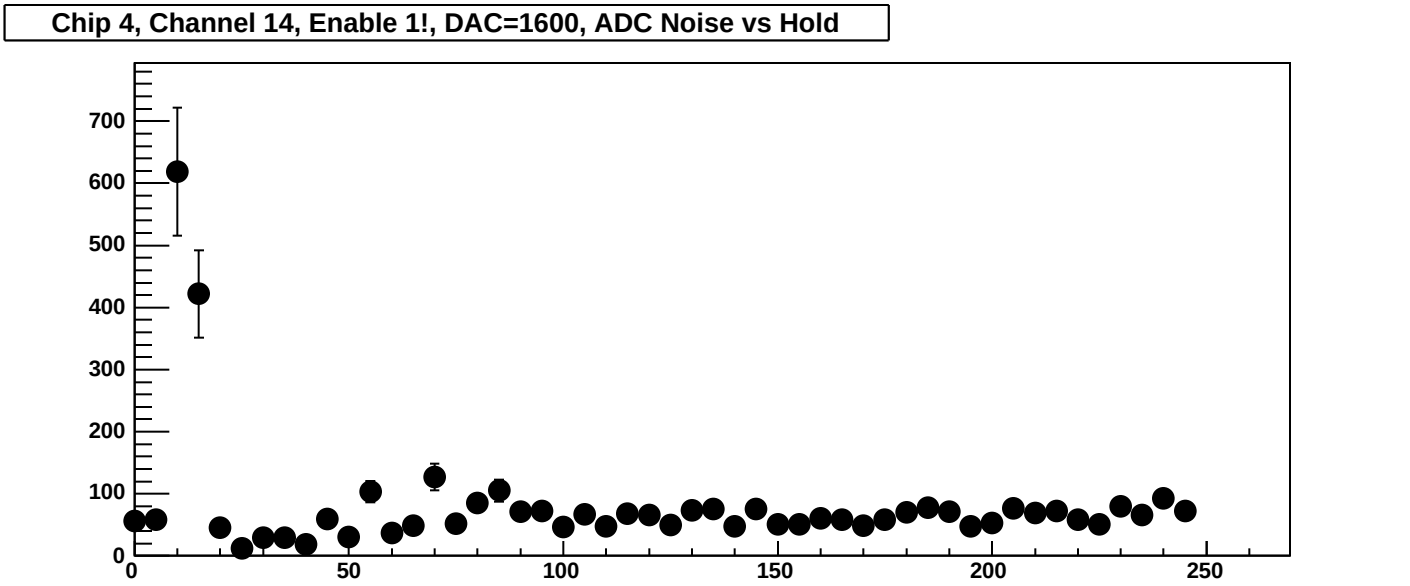

Chip 4, Channel 13, Enable 5, DAC=1600, ADC Residuals vs Hold
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

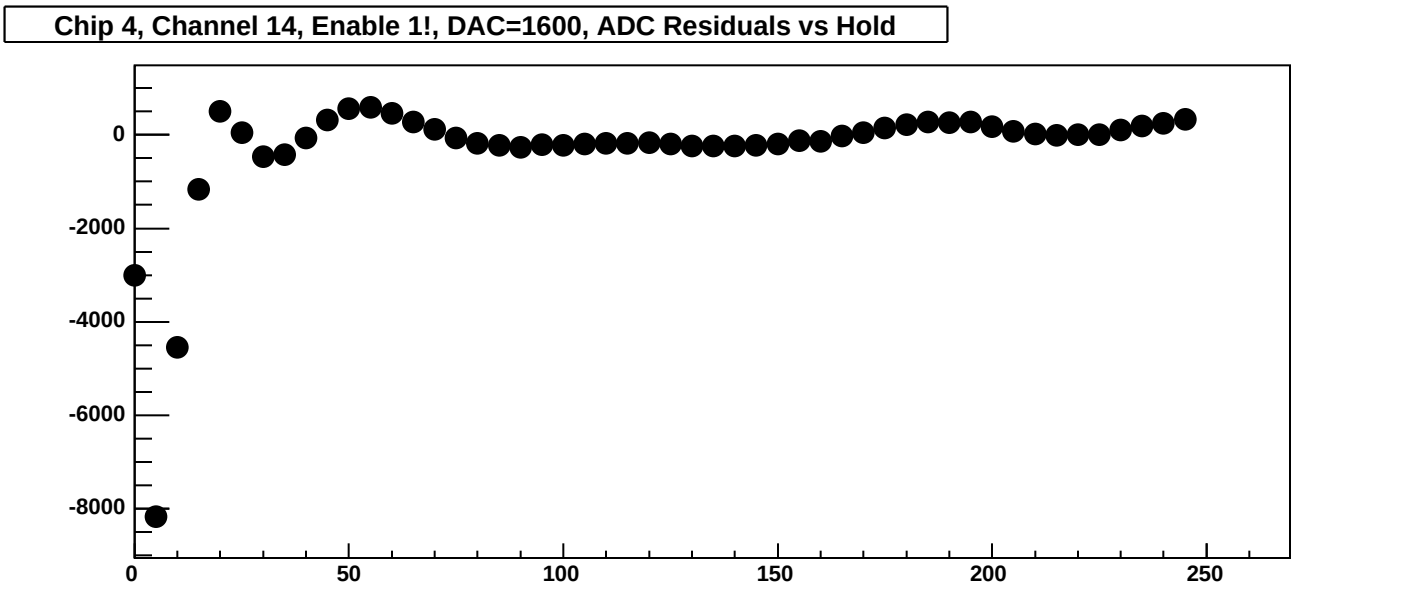

Chip 4, Channel 14, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.403e+05 / 41
p0 -1607 ± 22.35
p1 -11.54 ± 0.1212
p2 -4513 ± 23.17
p3 -1031 ± 18.25
p4 156.8 ± 0.129
20000
15000
10000
5000
0
0
50
100
150
200
250

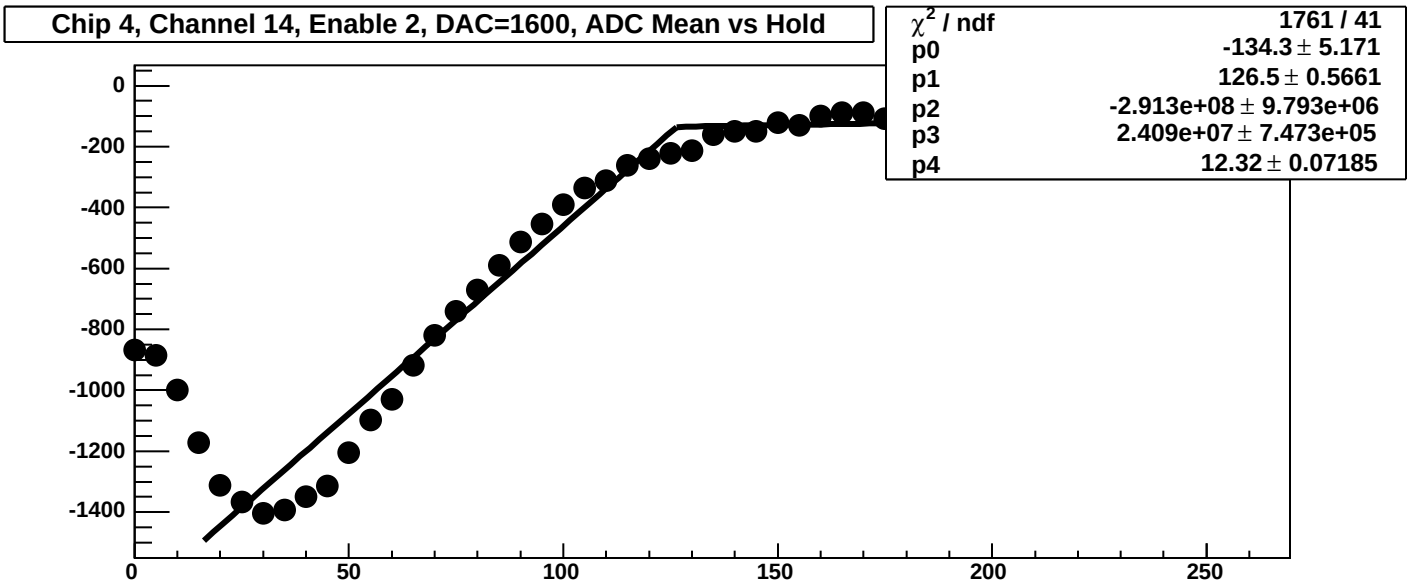

Chip 4, Channel 14, Enable 0, DAC=1600, ADC Noise vs Hold
600
500
400
300
200
100
0
0
50
100
150
200
250

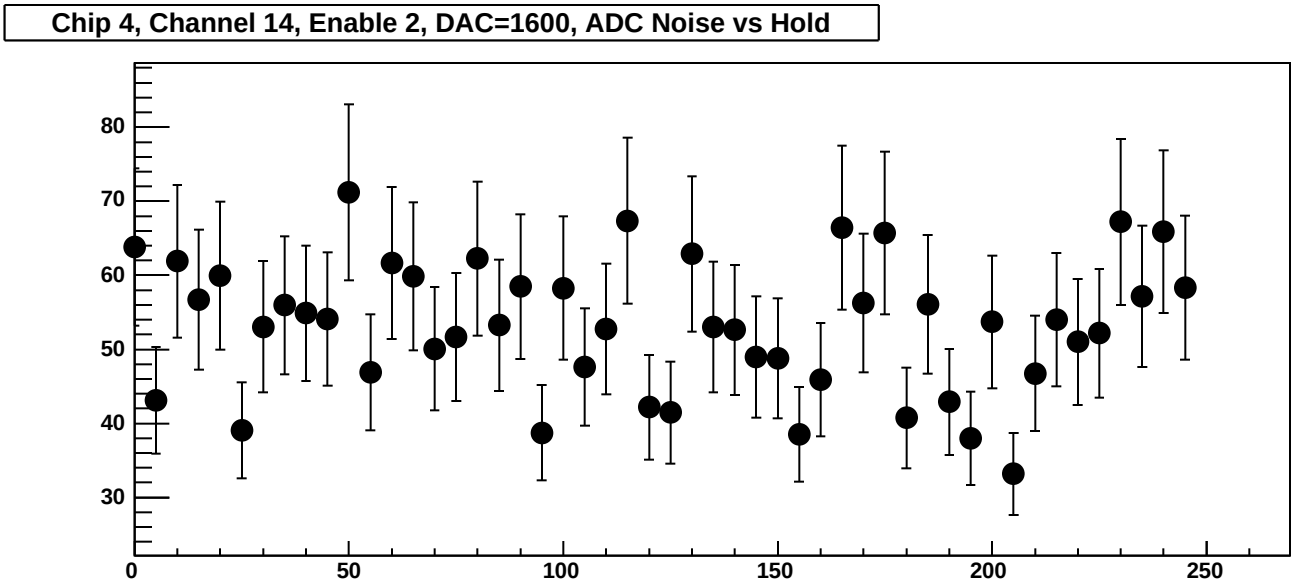

Chip 4, Channel 14, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
0
50
100
150
200
250

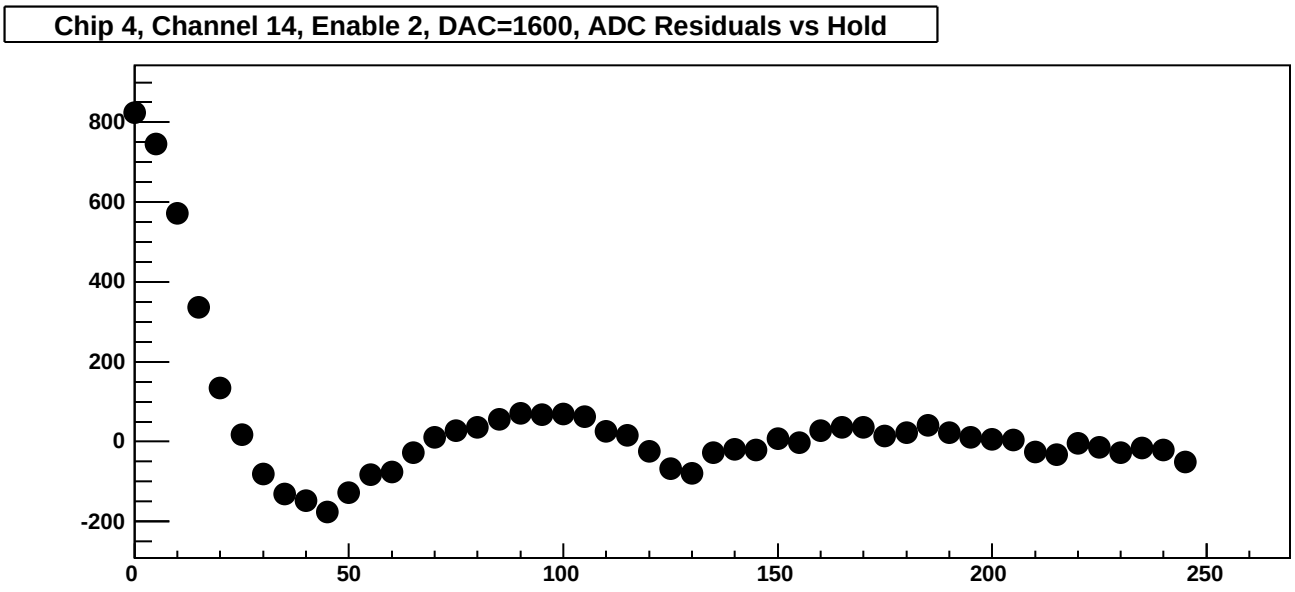

Chip 4, Channel 14, Enable 1!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.874e+04 / 42
p0 -4151 ± 4.425
p1 -2.38 ± 0.01978
p2 3.289e+04 ± 4.483
p3 31.42 ± 0.01228
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

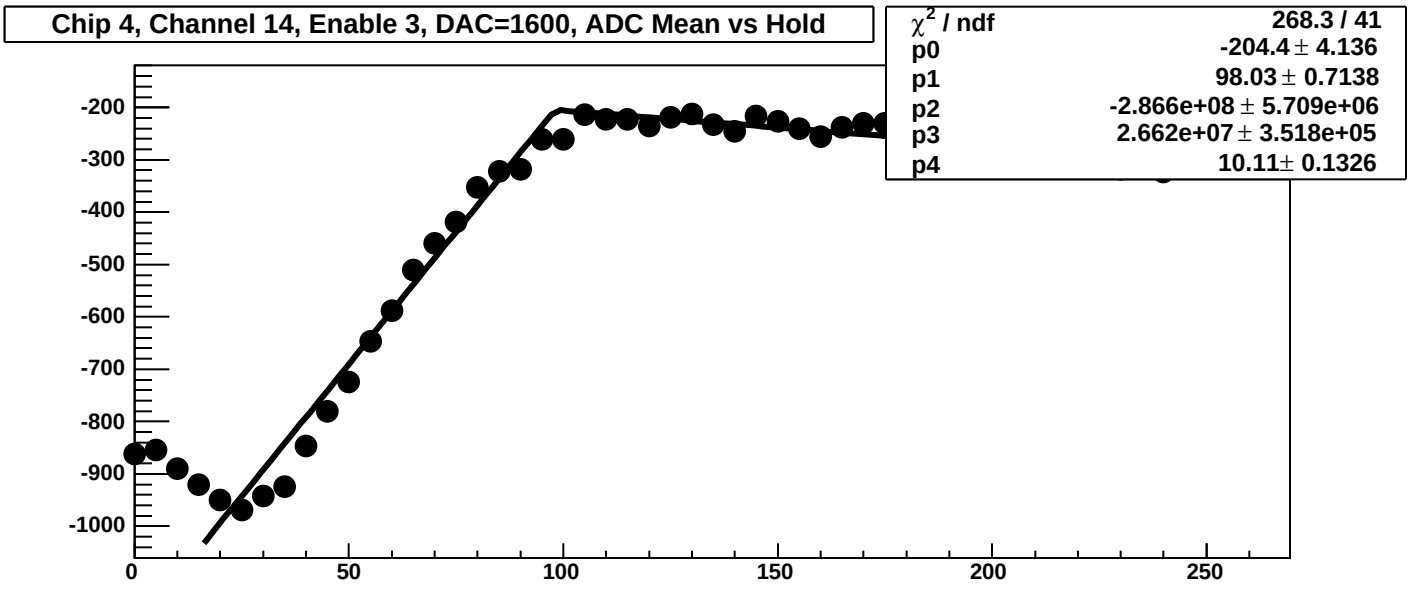

Chip 4, Channel 14, Enable 1!, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250

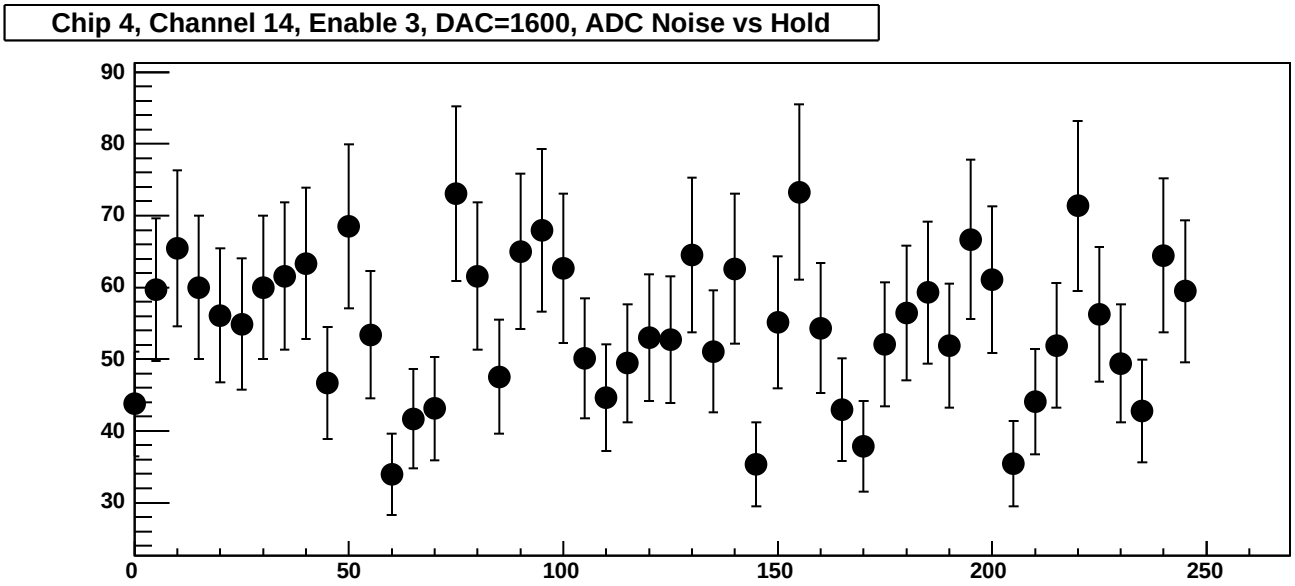

Chip 4, Channel 14, Enable 1!, DAC=1600, ADC Residuals vs Hold
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250

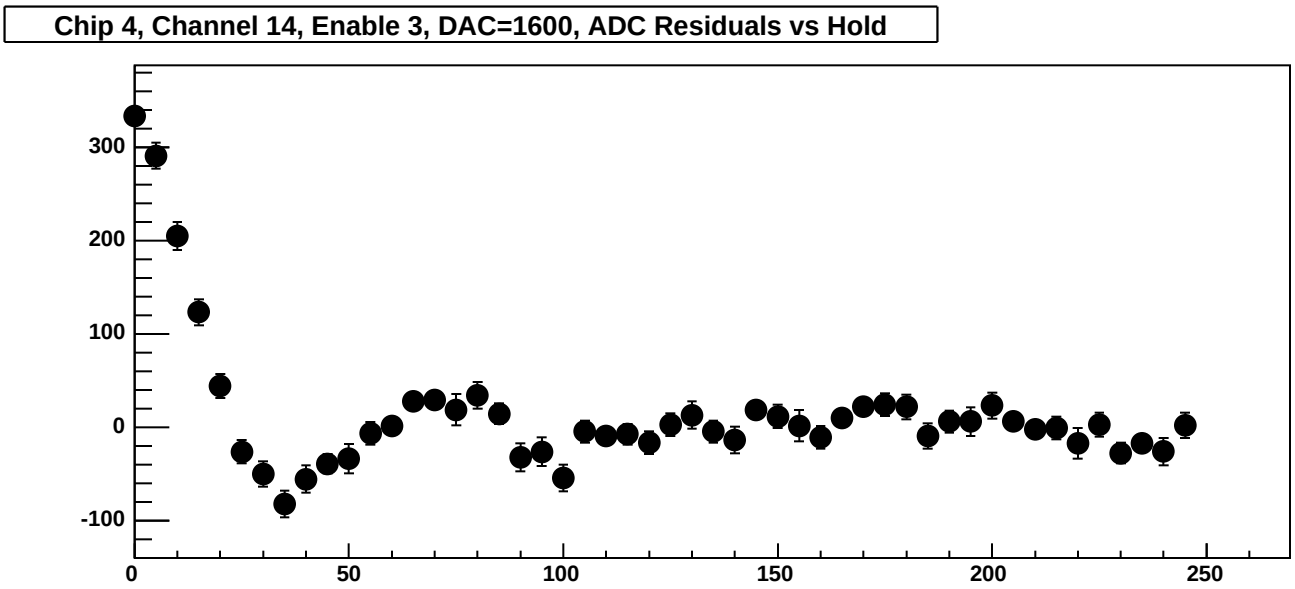

Chip 4, Channel 14, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 1761 / 41
p0 -134.3 ± 5.171
p1 126.5 ± 0.5661
p2 -2.913e+08 ± 9.793e+06
p3 2.409e+07 ± 7.473e+05
p4 12.32 ± 0.07185
0
-200
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250


Chip 4, Channel 14, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

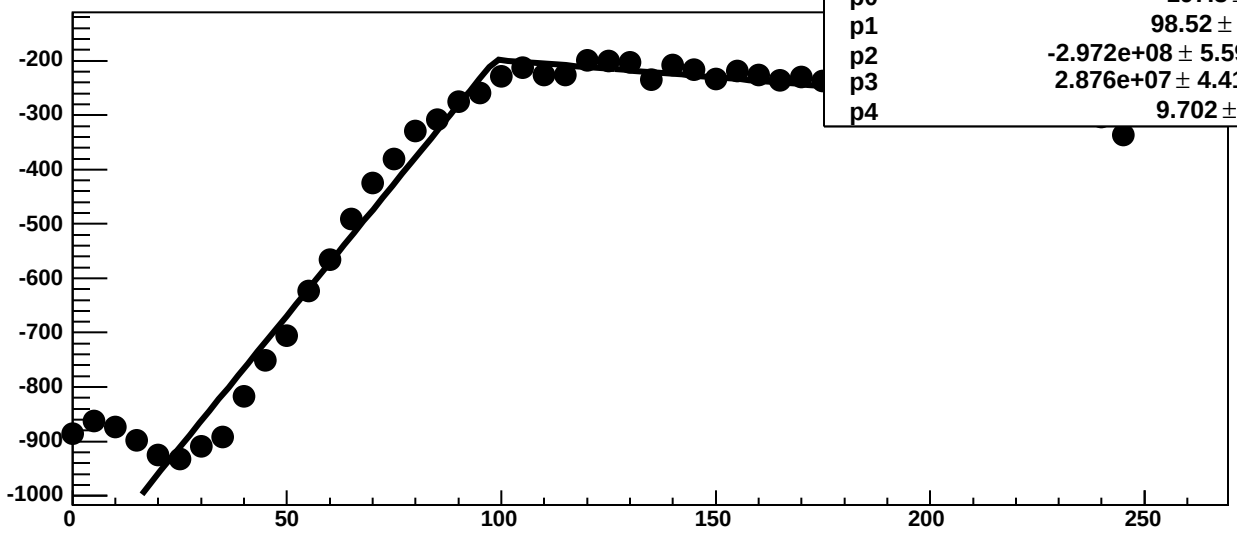

Chip 4, Channel 14, Enable 2, DAC=1600, ADC Residuals vs Hold
800
600
400
200
0
-200
0
50
100
150
200
250


Chip 4, Channel 14, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 268.3 / 41
p0 -204.4 ± 4.136
p1 98.03 ± 0.7138
p2 -2.866e+08 ± 5.709e+06
p3 2.662e+07 ± 3.518e+05
p4 10.11 ± 0.1326
-200
-300
-400
-500
-600
-700
-800
-900
-1000
0
50
100
150
200
250

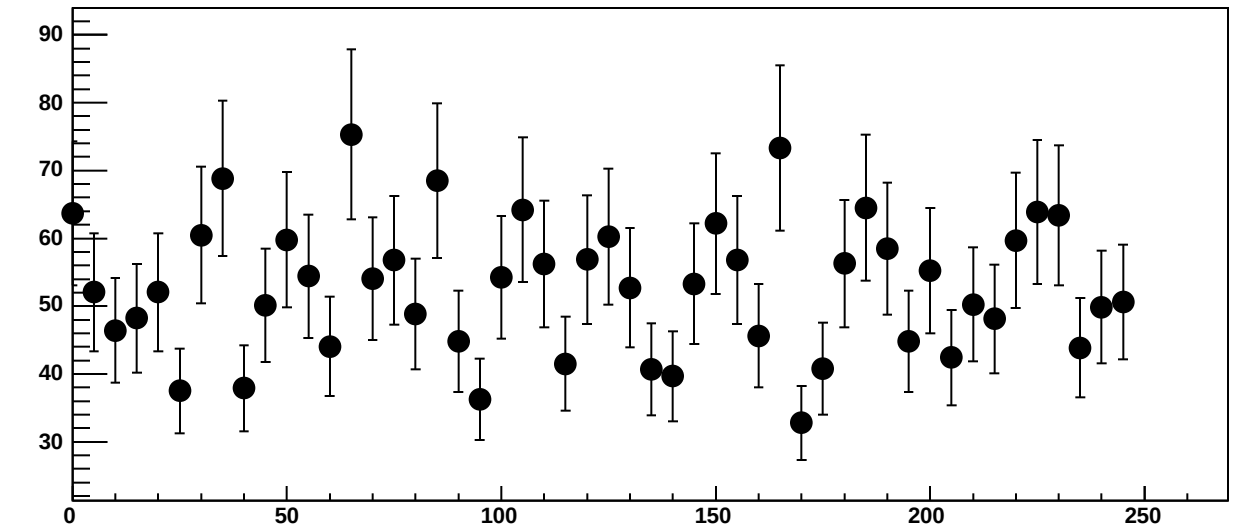

Chip 4, Channel 14, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 4, Channel 14, Enable 3, DAC=1600, ADC Residuals vs Hold
300
200
100
0
-100
0
50
100
150
200
250

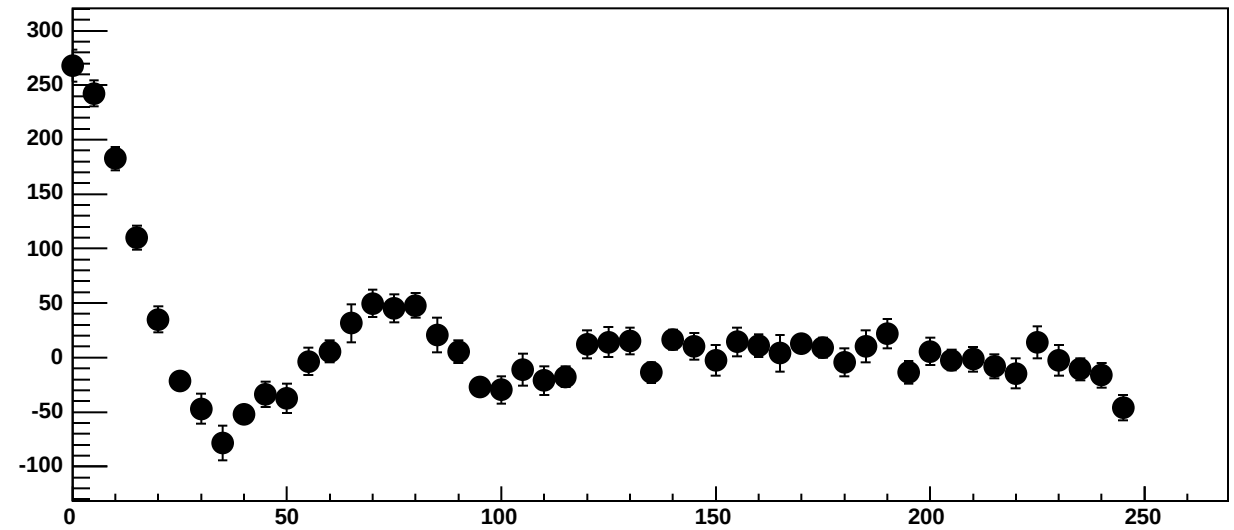
Chip 4, Channel 14, Enable 4, DAC=1600, ADC Mean vs Hold

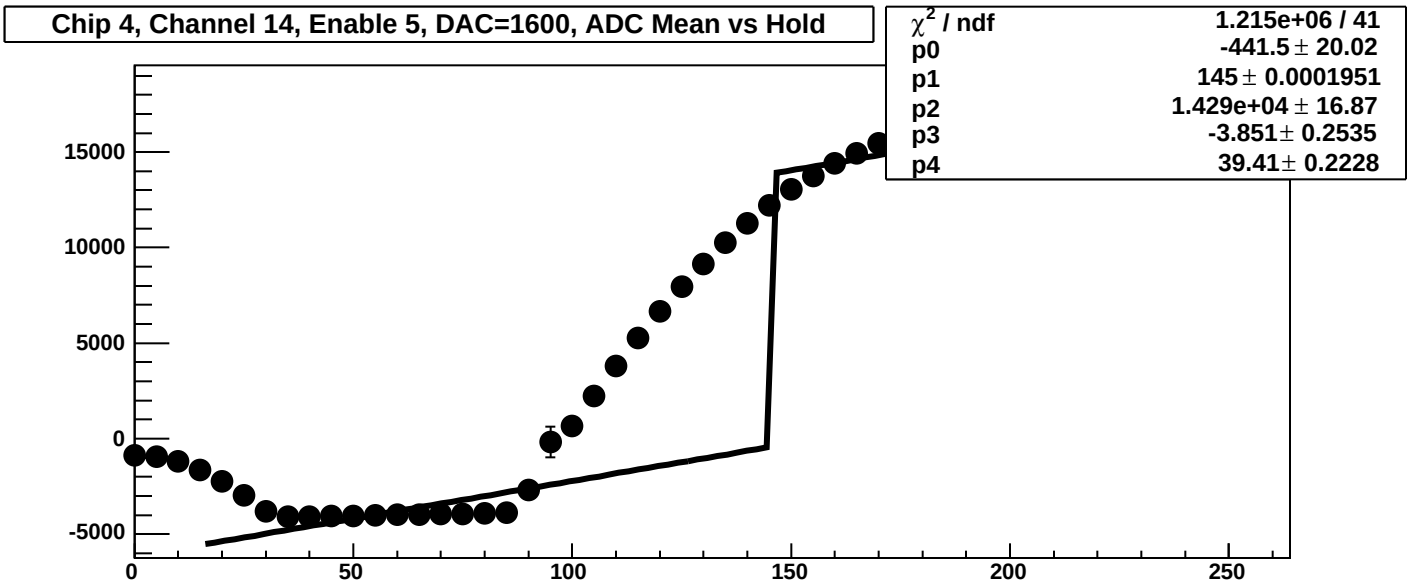


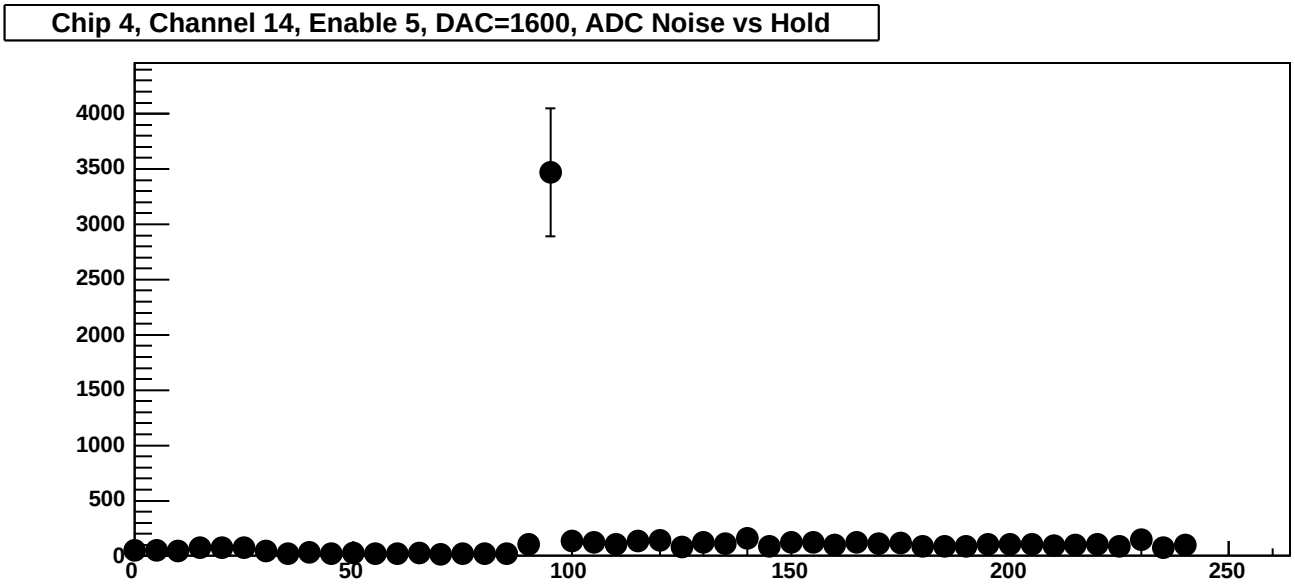
Chip 4, Channel 14, Enable 4, DAC=1600, ADC Noise vs Hold

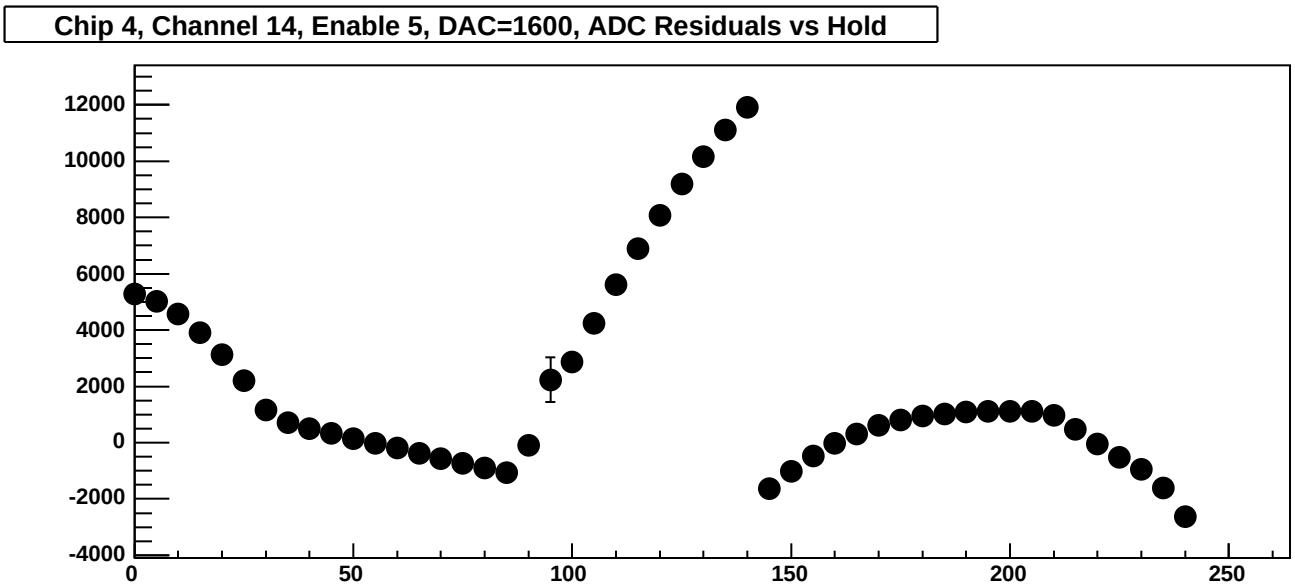


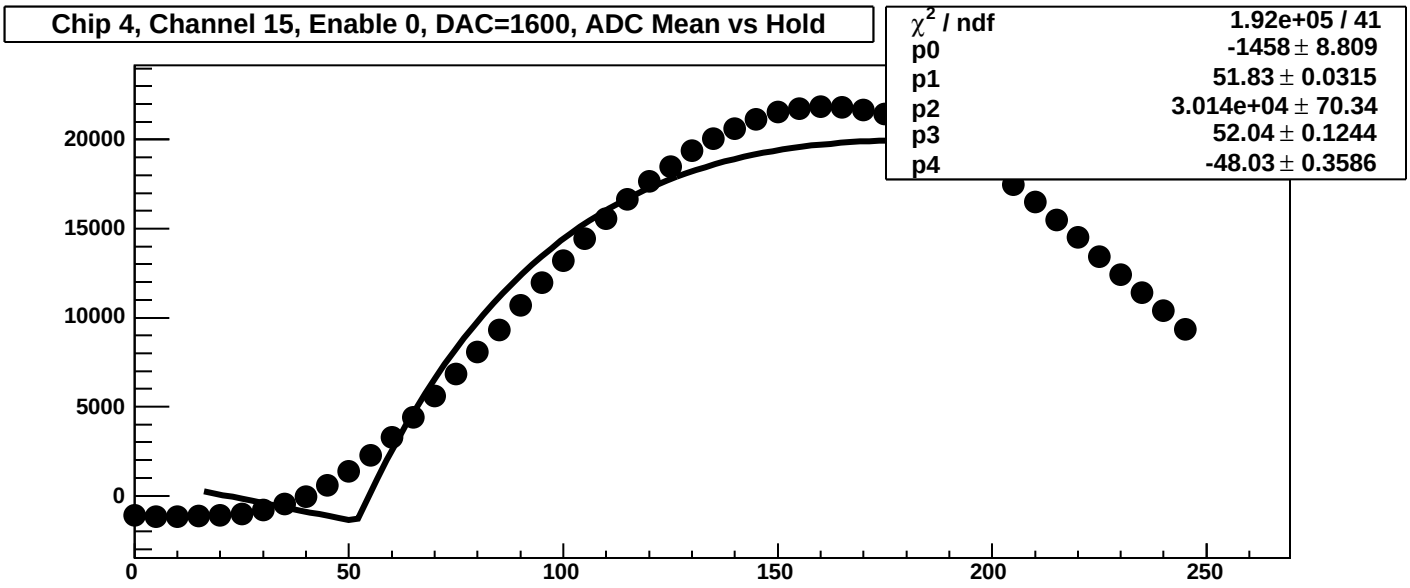
Chip 4, Channel 14, Enable 4, DAC=1600, ADC Residuals vs Hold

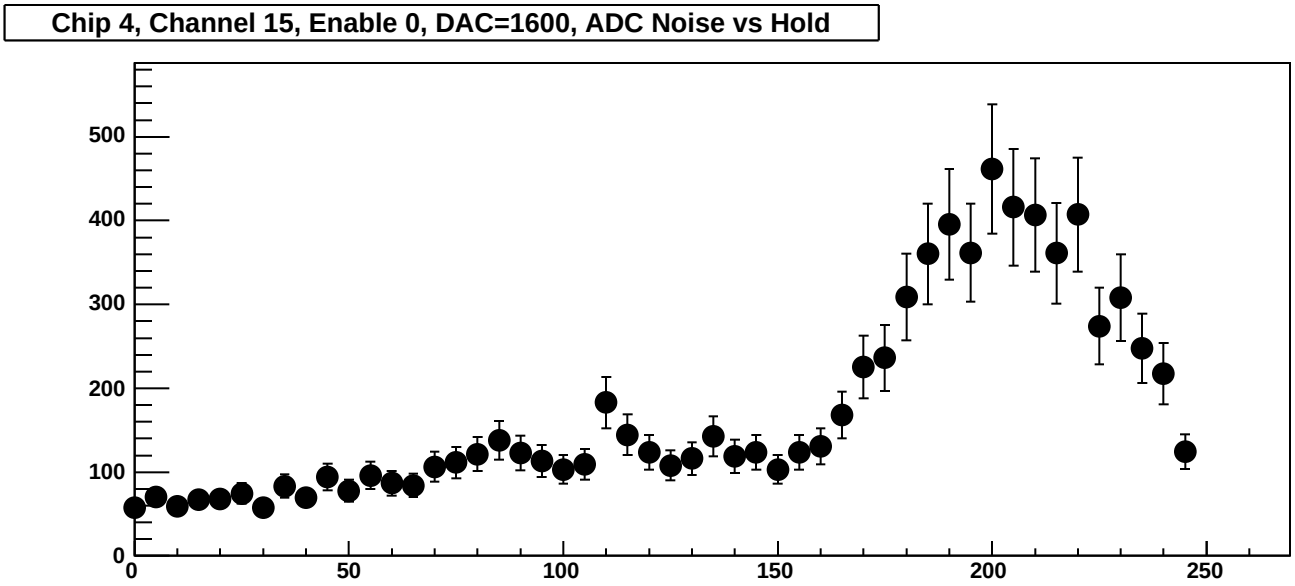


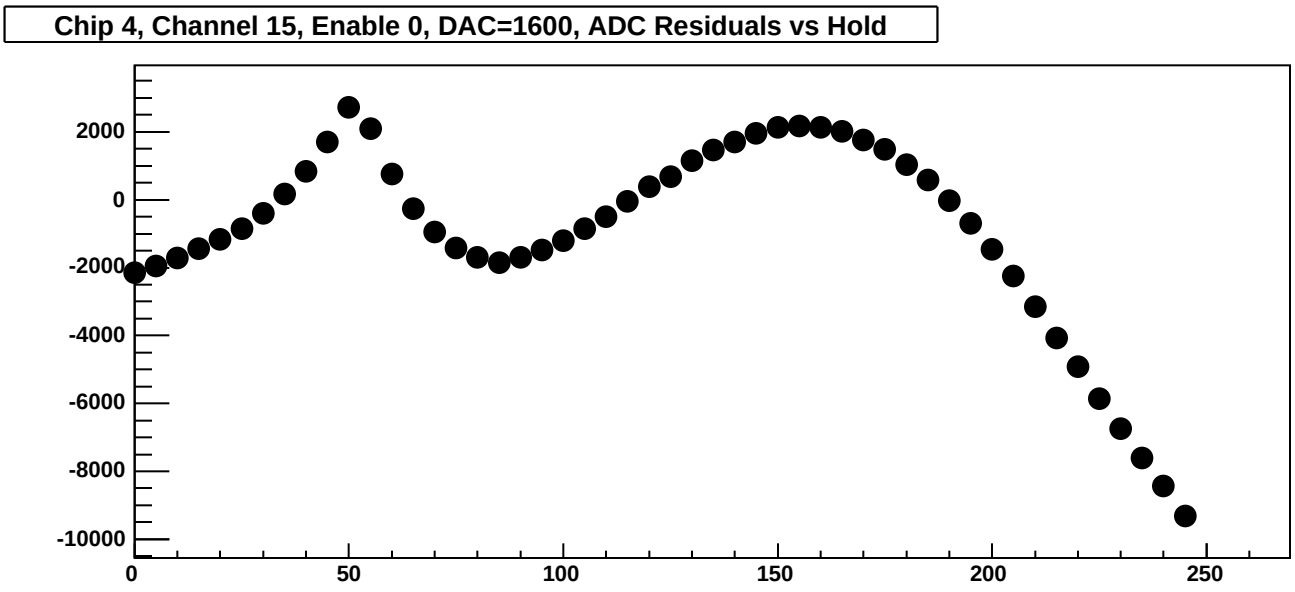

Chip 4, Channel 14, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.215e+06 / 41
p0 -441.5 ± 20.02
p1 145 ± 0.0001951
p2 1.429e+04 ± 16.87
p3 -3.851 ± 0.2535
p4 39.41 ± 0.2228
15000
10000
5000
0
-5000
0
50
100
150
200
250

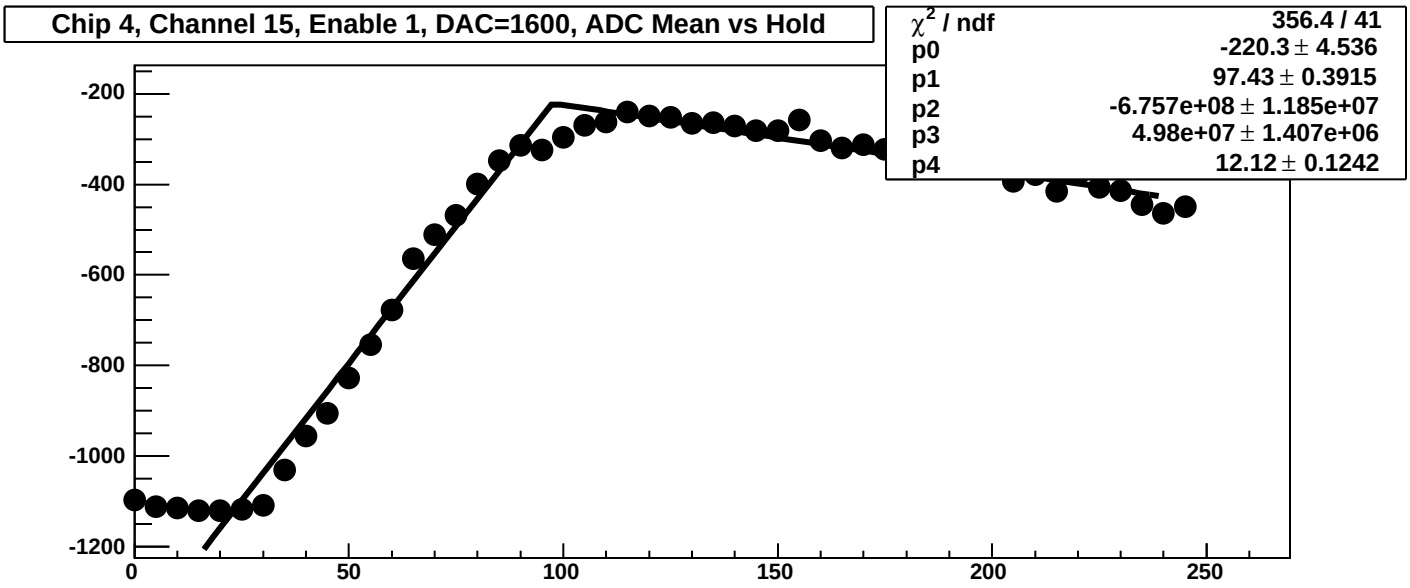

Chip 4, Channel 14, Enable 5, DAC=1600, ADC Noise vs Hold
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250

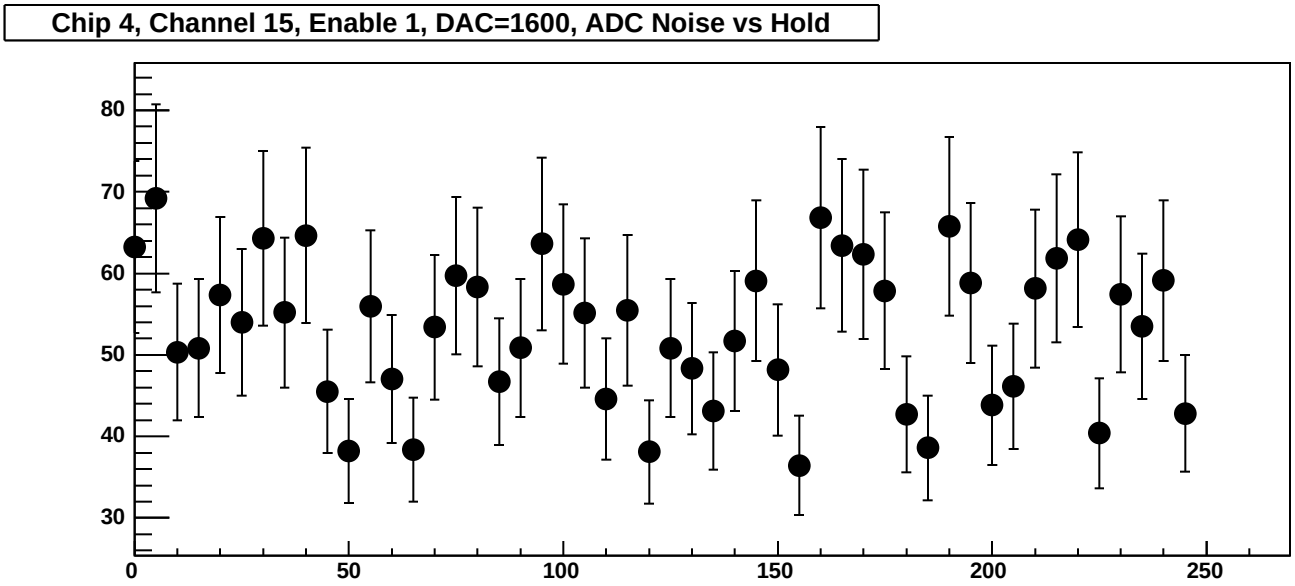

Chip 4, Channel 14, Enable 5, DAC=1600, ADC Residuals vs Hold
12000
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

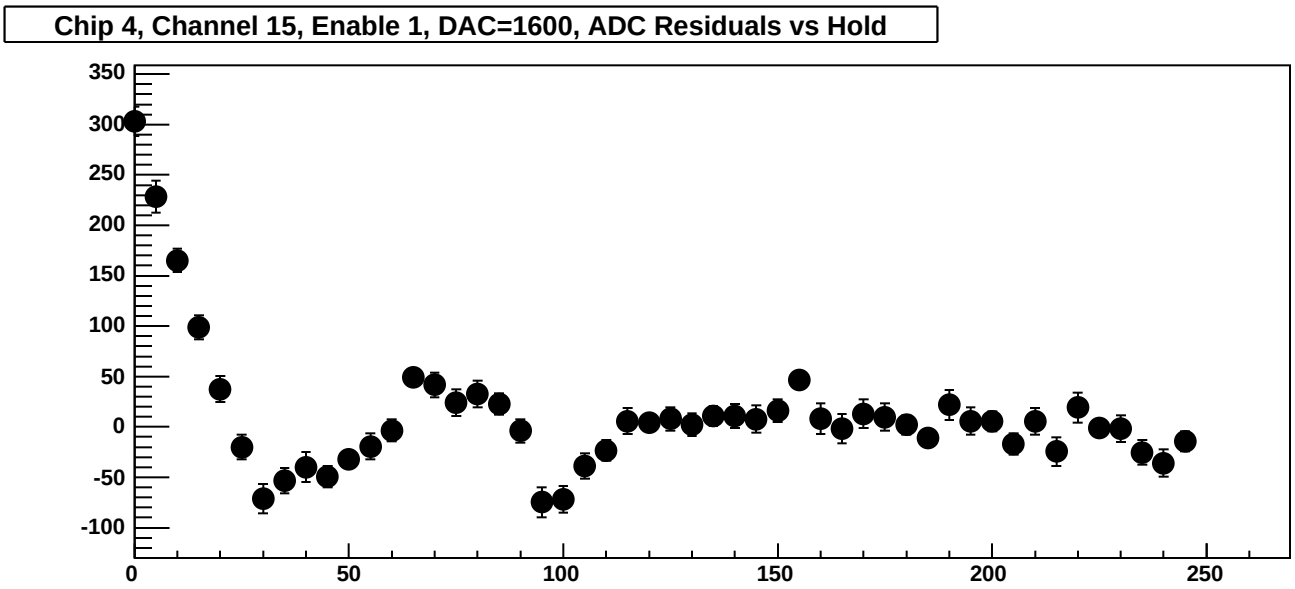

Chip 4, Channel 15, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.92e+05 / 41
p0 -1458 ± 8.809
p1 51.83 ± 0.0315
p2 3.014e+04 ± 70.34
p3 52.04 ± 0.1244
p4 -48.03 ± 0.3586
20000
15000
10000
5000
0
0
50
100
150
200
250

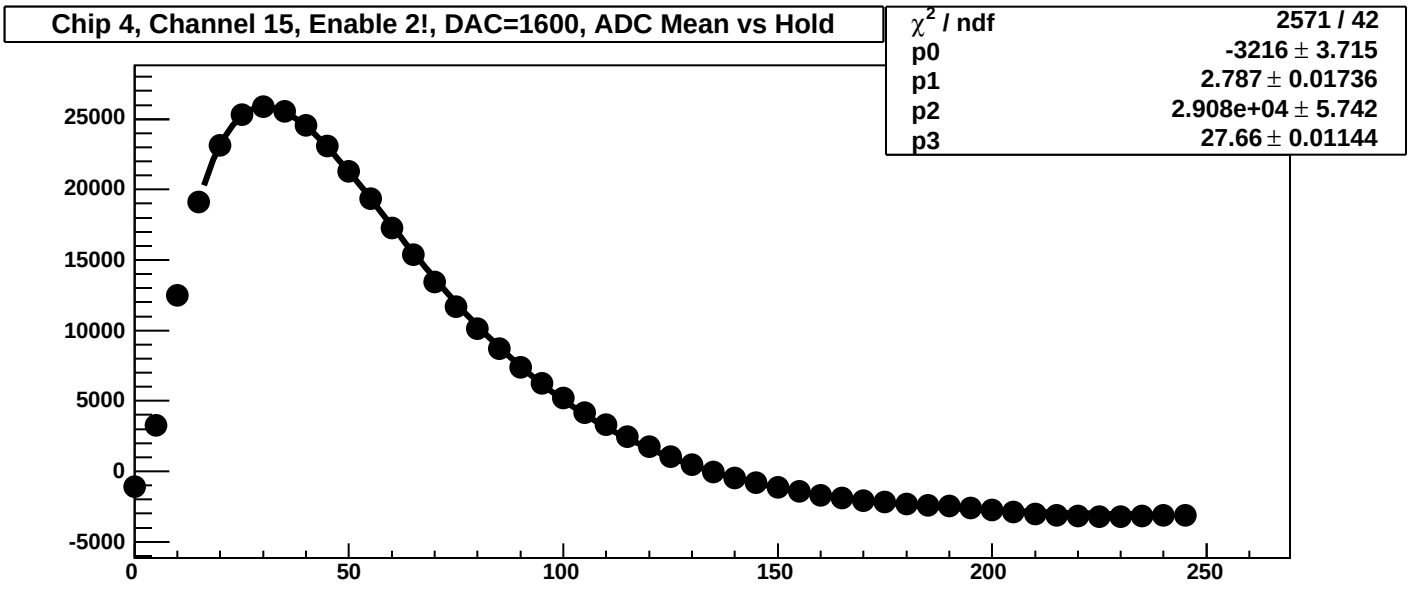

Chip 4, Channel 15, Enable 0, DAC=1600, ADC Noise vs Hold
500
400
300
200
100
0
0
50
100
150
200
250

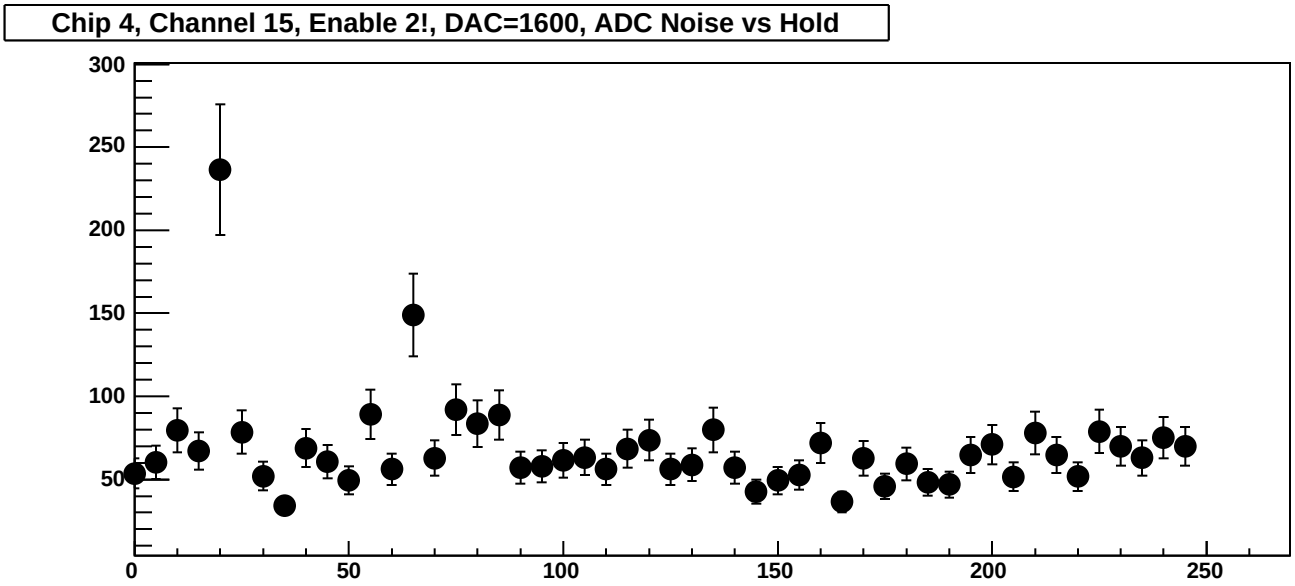

Chip 4, Channel 15, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

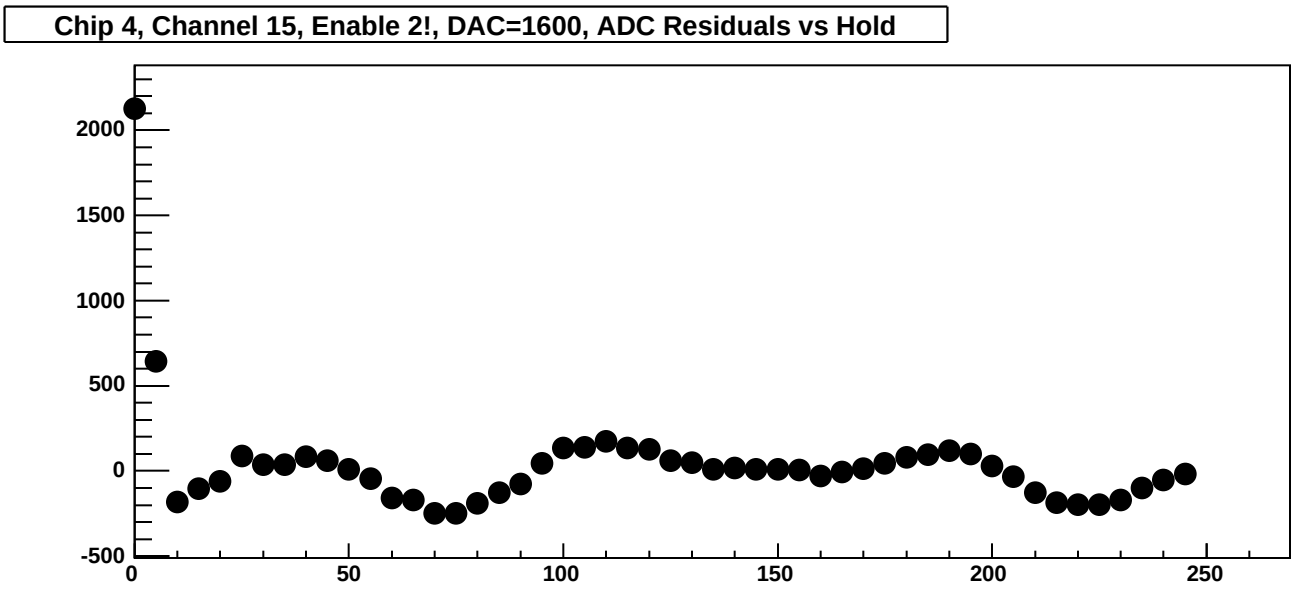

Chip 4, Channel 15, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 356.4 / 41
p0 -220.3 ± 4.536
p1 97.43 ± 0.3915
p2 -6.757e+08 ± 1.185e+07
p3 4.98e+07 ± 1.407e+06
p4 12.12 ± 0.1242
-200
-400
-600
-800
-1000
-1200
0
50
100
150
200
250


Chip 4, Channel 15, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

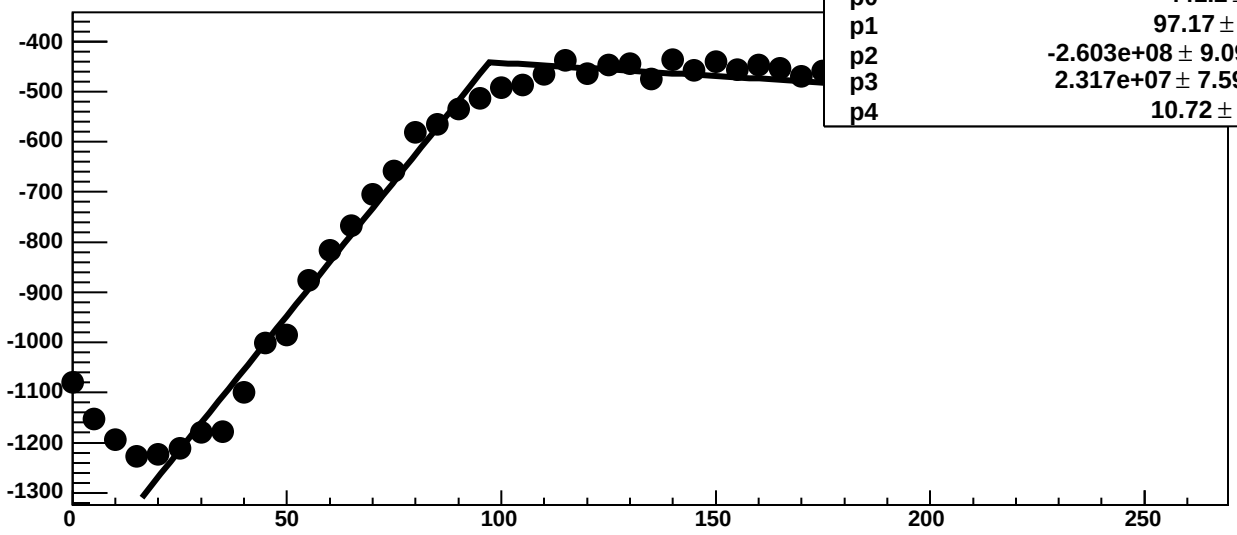

Chip 4, Channel 15, Enable 1, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250


Chip 4, Channel 15, Enable 2!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2571 / 42
p0 -3216 ± 3.715
p1 2.787 ± 0.01736
p2 2.908e+04 ± 5.742
p3 27.66 ± 0.01144
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

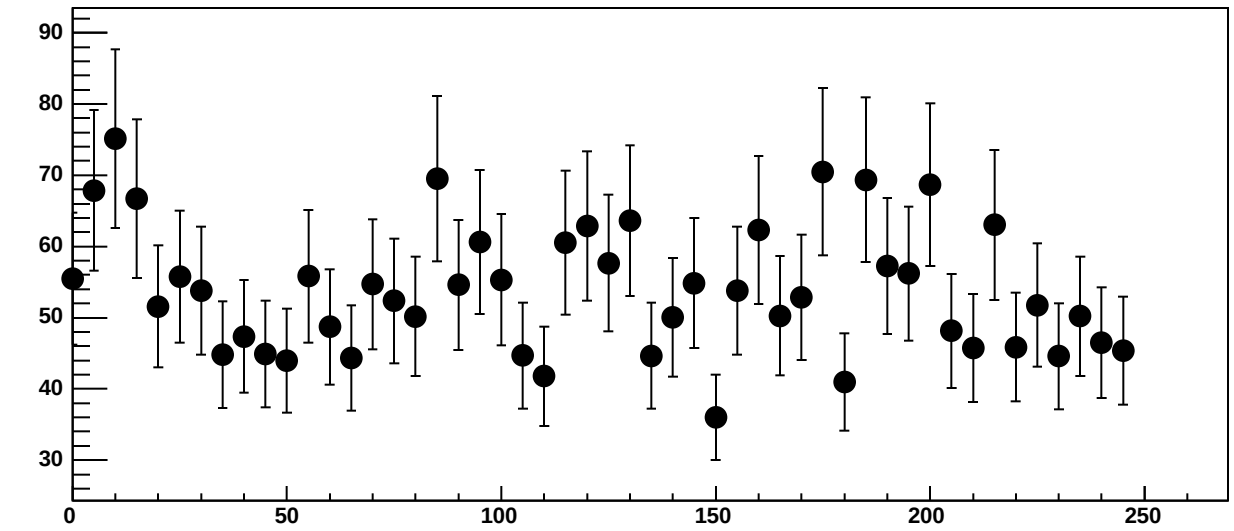

Chip 4, Channel 15, Enable 2!, DAC=1600, ADC Noise vs Hold
300
250
200
150
100
50
0
50
100
150
200
250


Chip 4, Channel 15, Enable 2!, DAC=1600, ADC Residuals vs Hold
2000
1500
1000
500
0
-500
0
50
100
150
200
250

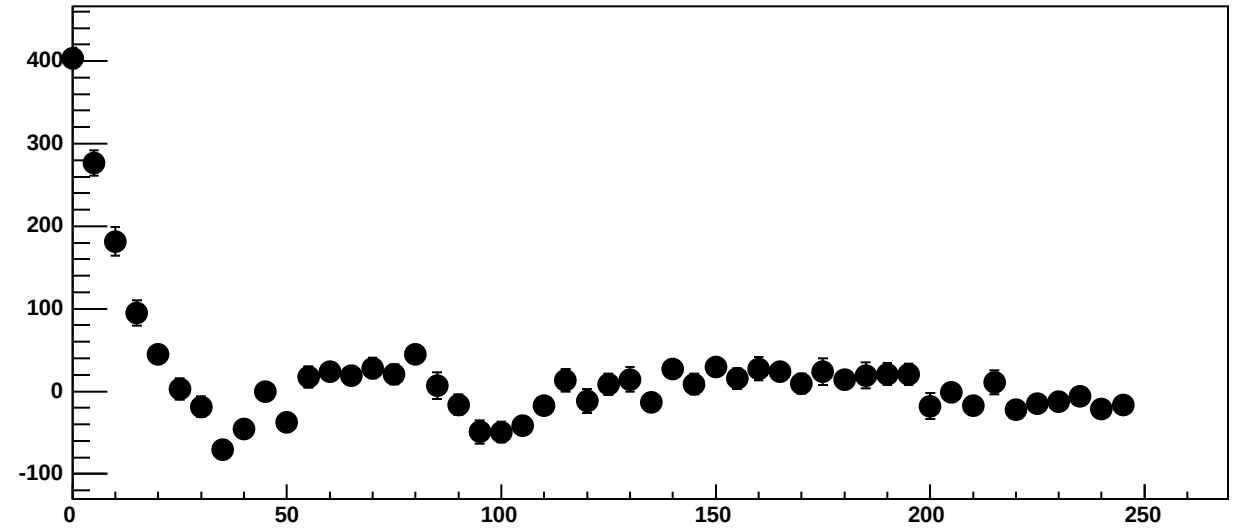
Chip 4, Channel 15, Enable 3, DAC=1600, ADC Mean vs Hold

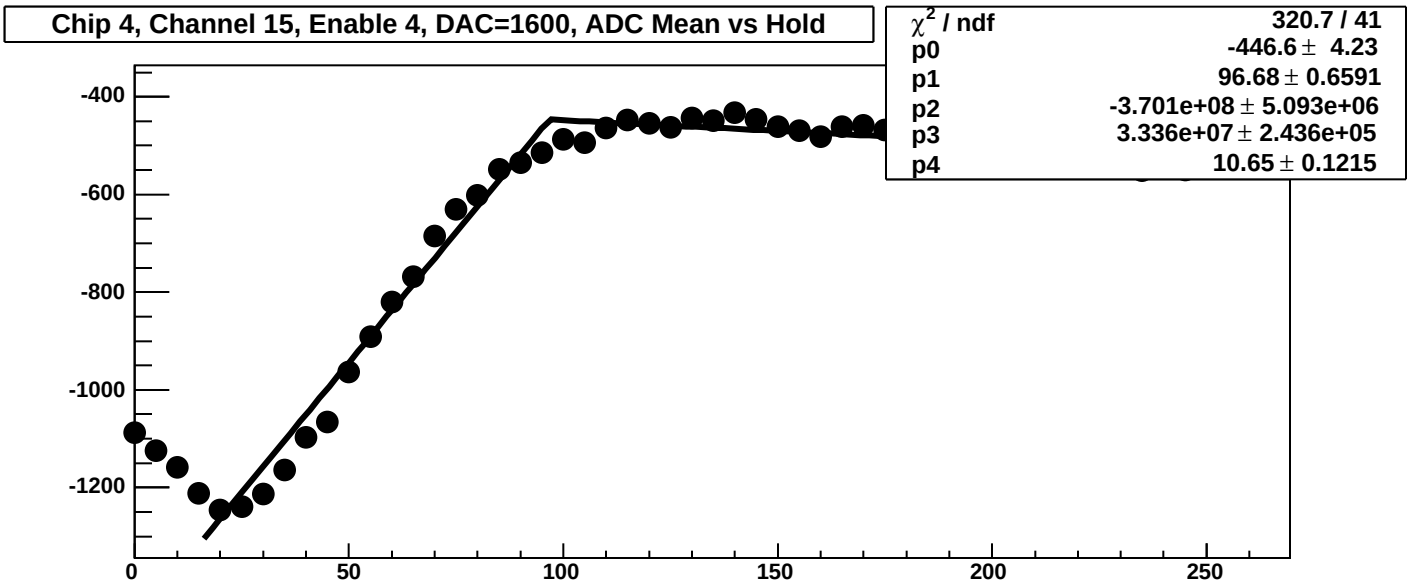


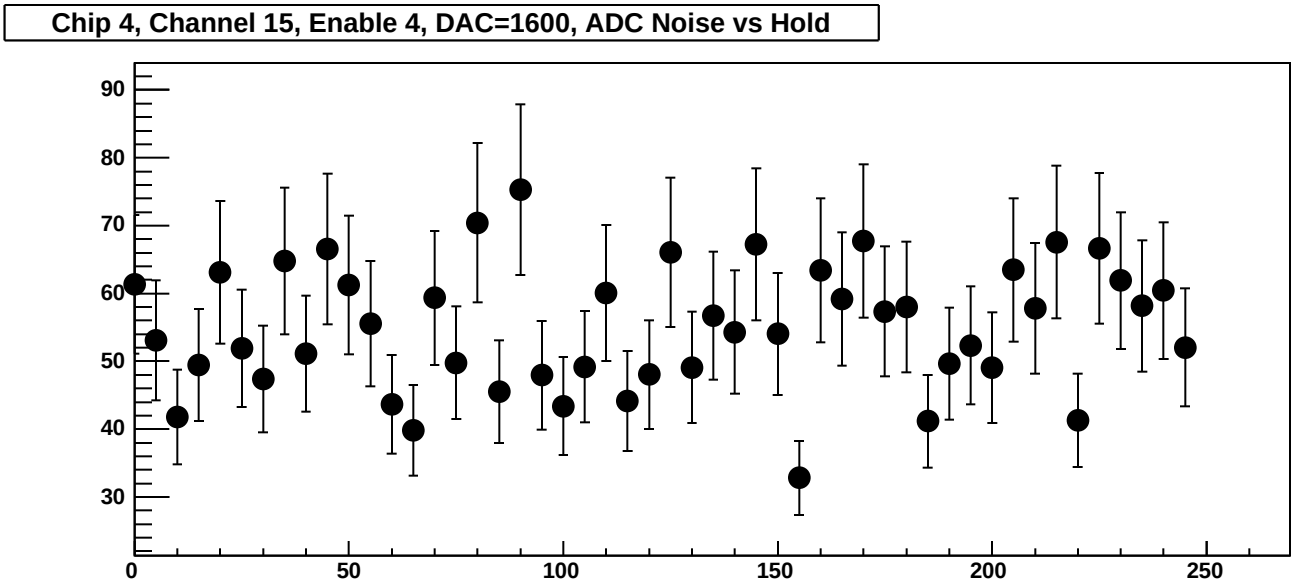
Chip 4, Channel 15, Enable 3, DAC=1600, ADC Noise vs Hold

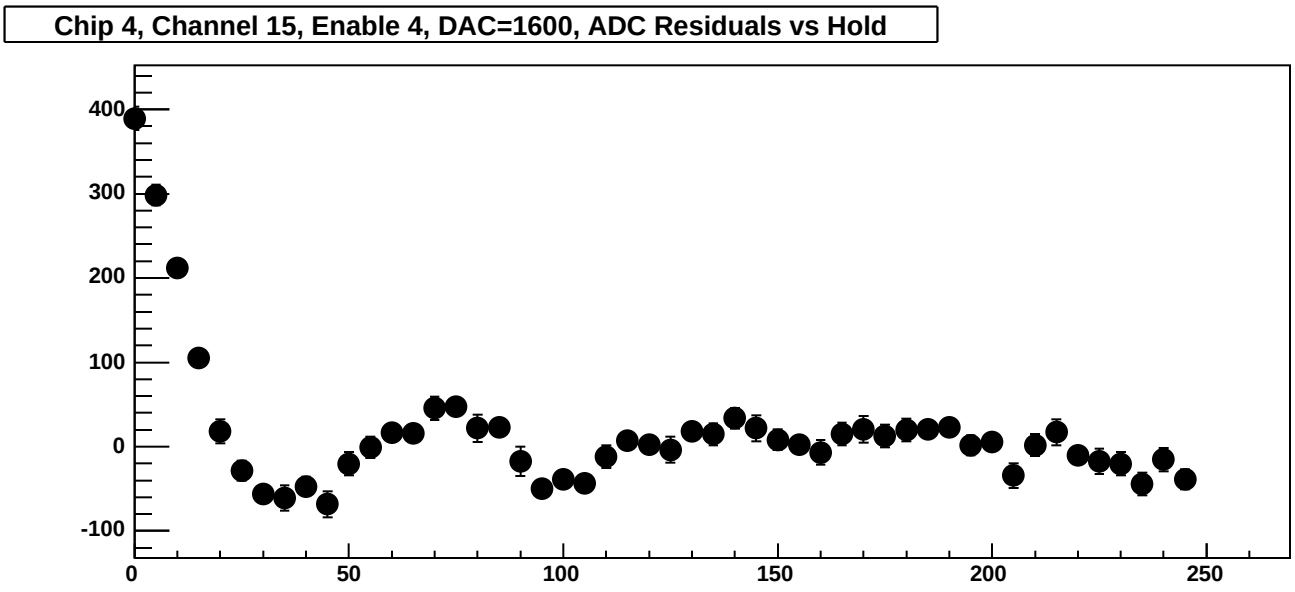


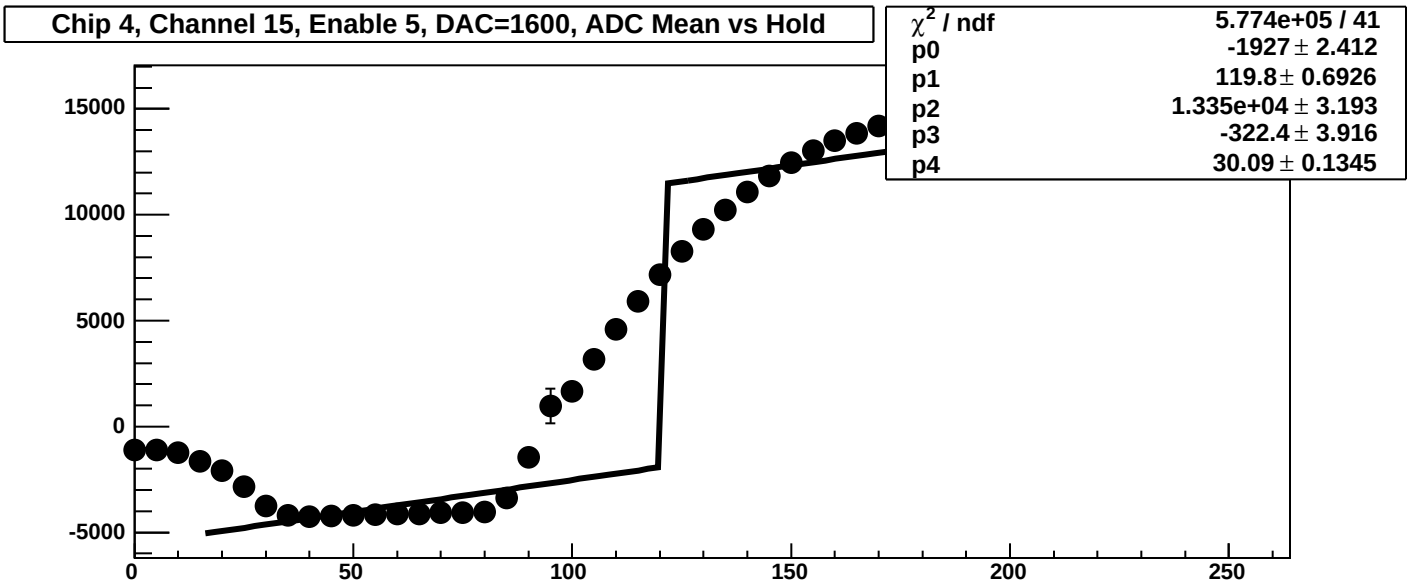
Chip 4, Channel 15, Enable 3, DAC=1600, ADC Residuals vs Hold

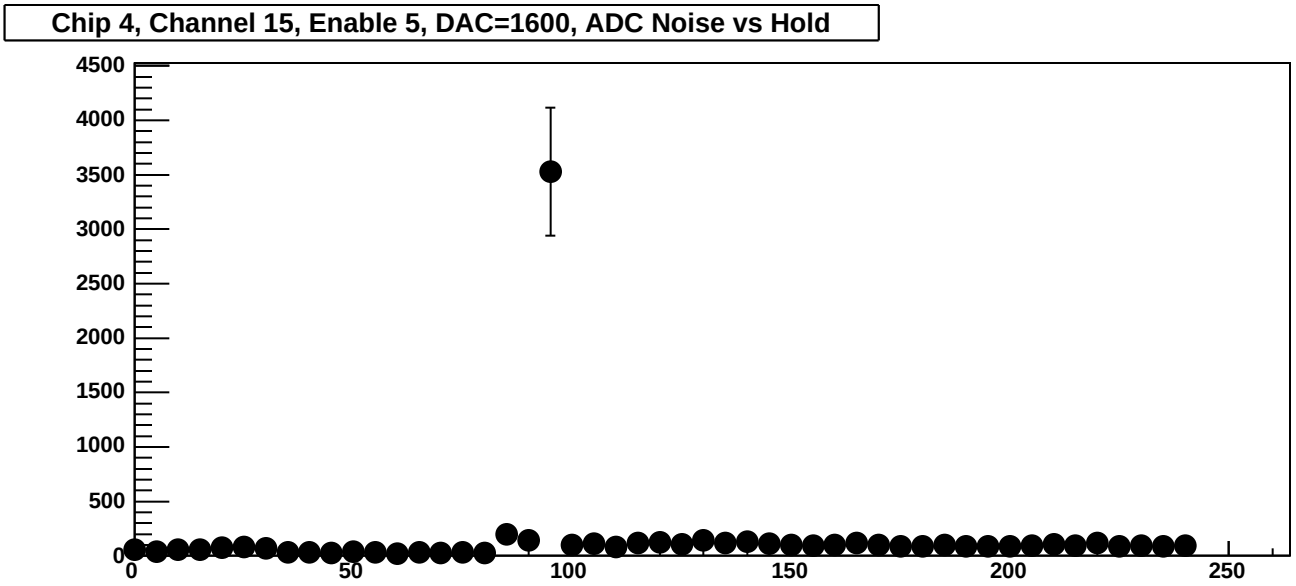


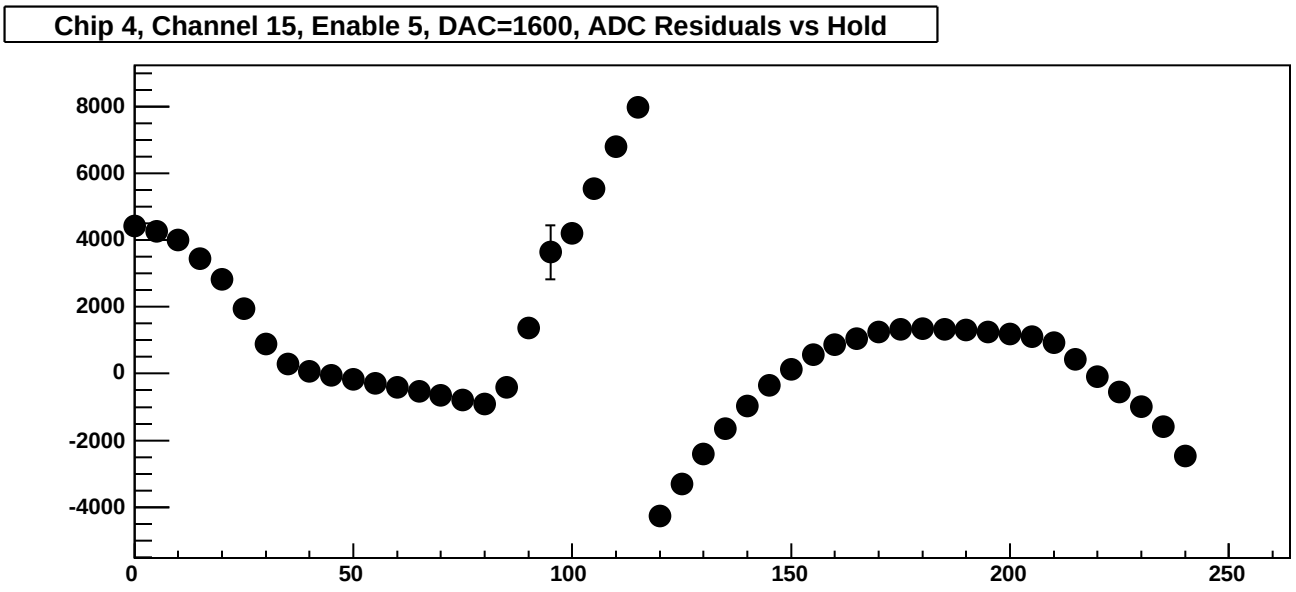

Chip 4, Channel 15, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 320.7 / 41
p0 -446.6 ± 4.23
p1 96.68 ± 0.6591
p2 -3.701e+08 ± 5.093e+06
p3 3.336e+07 ± 2.436e+05
p4 10.65 ± 0.1215
-400
-600
-800
-1000
-1200
0
50
100
150
200
250


Chip 4, Channel 15, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

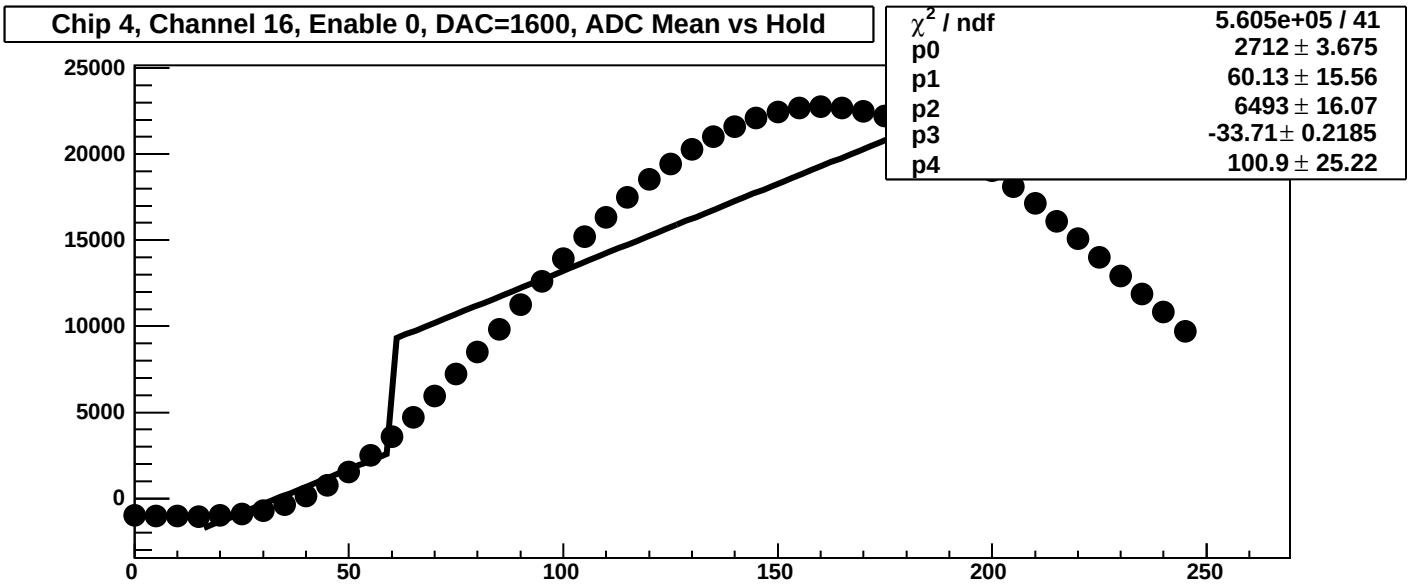

Chip 4, Channel 15, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 4, Channel 15, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.774e+05 / 41
p0 -1927 ± 2.412
p1 119.8 ± 0.6926
p2 1.335e+04 ± 3.193
p3 -322.4 ± 3.916
p4 30.09 ± 0.1345
15000
10000
5000
0
-5000
0
50
100
150
200
250

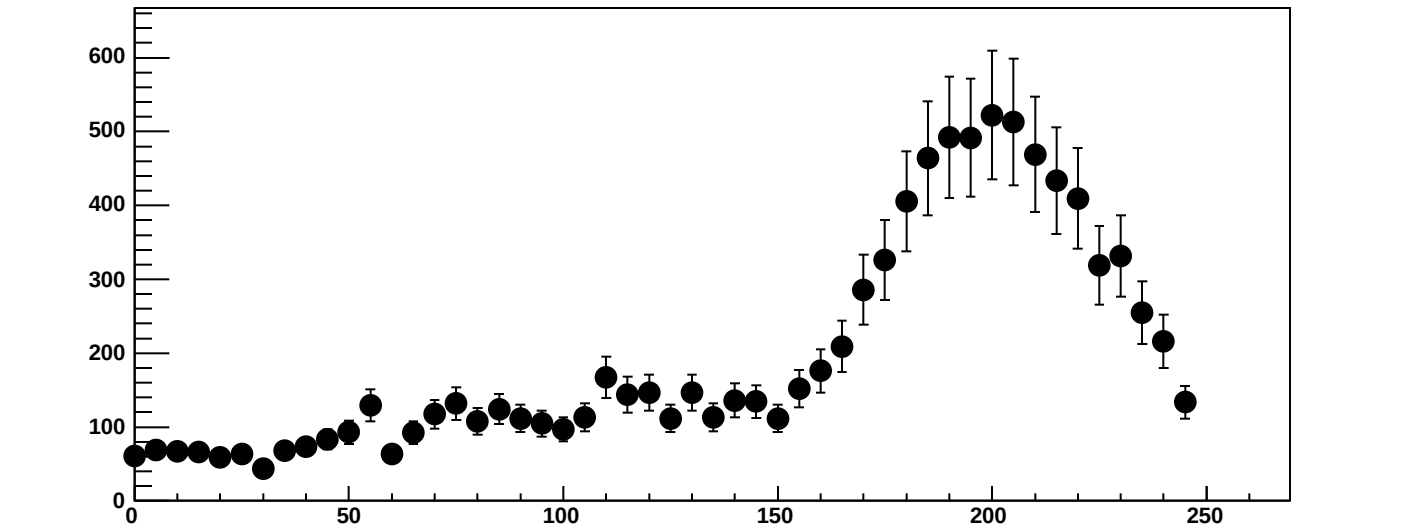

Chip 4, Channel 15, Enable 5, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 4, Channel 15, Enable 5, DAC=1600, ADC Residuals vs Hold
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

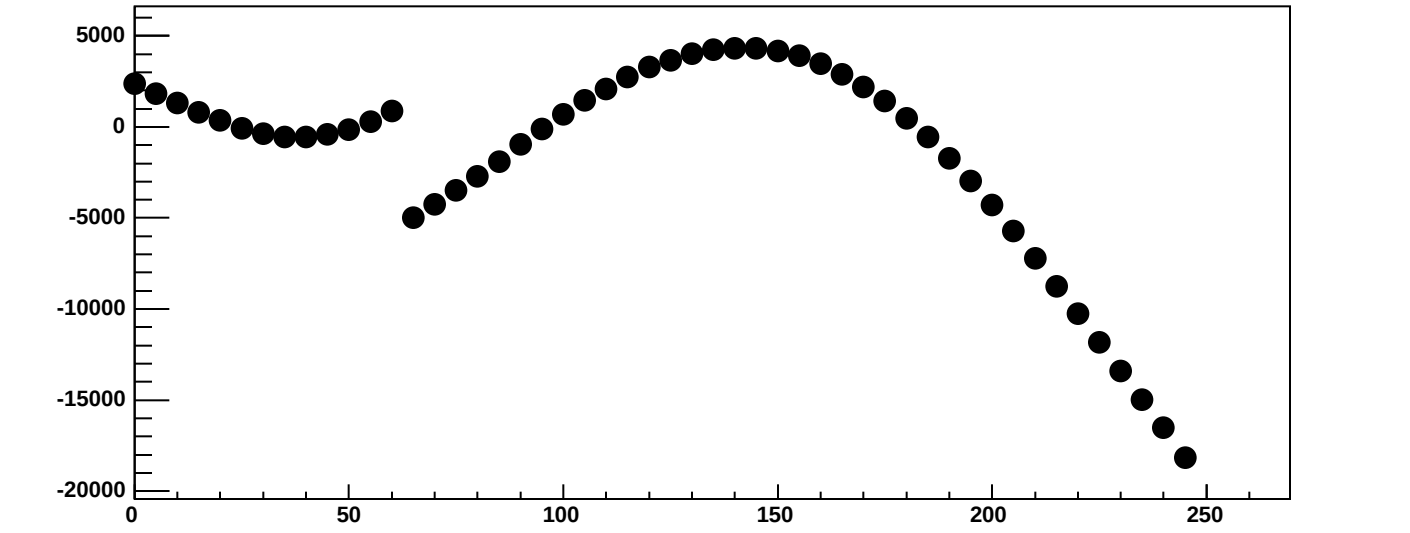
Chip 4, Channel 16, Enable 0, DAC=1600, ADC Mean vs Hold

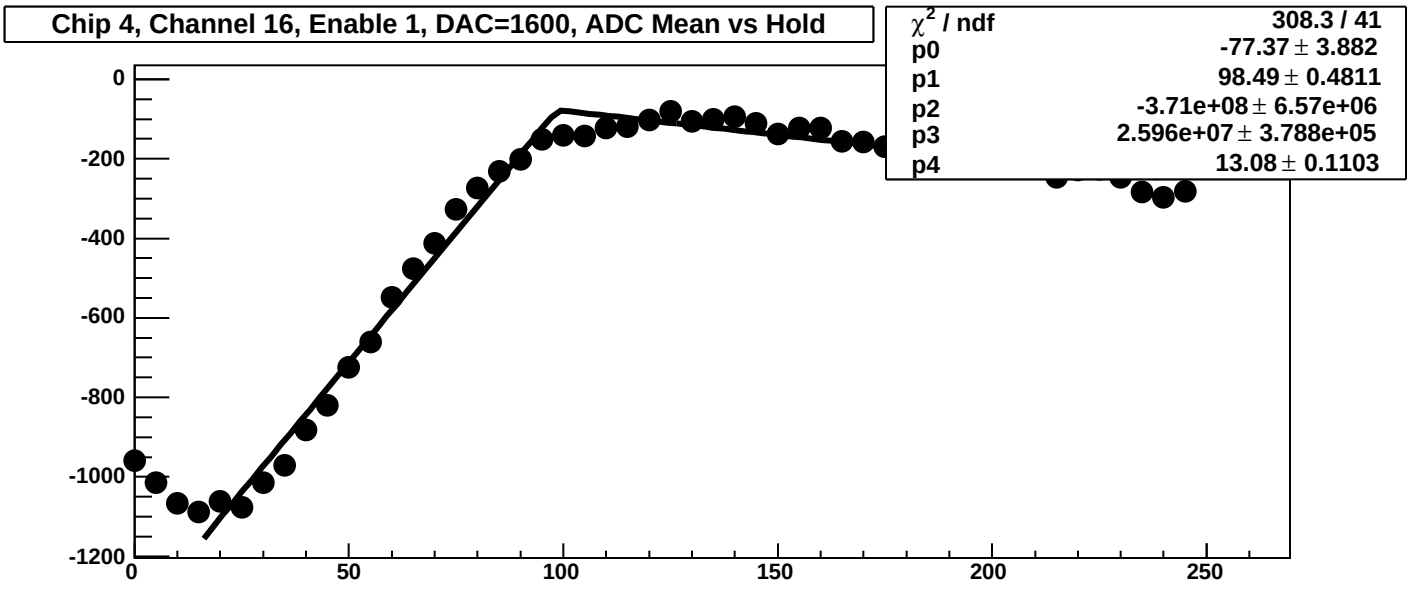


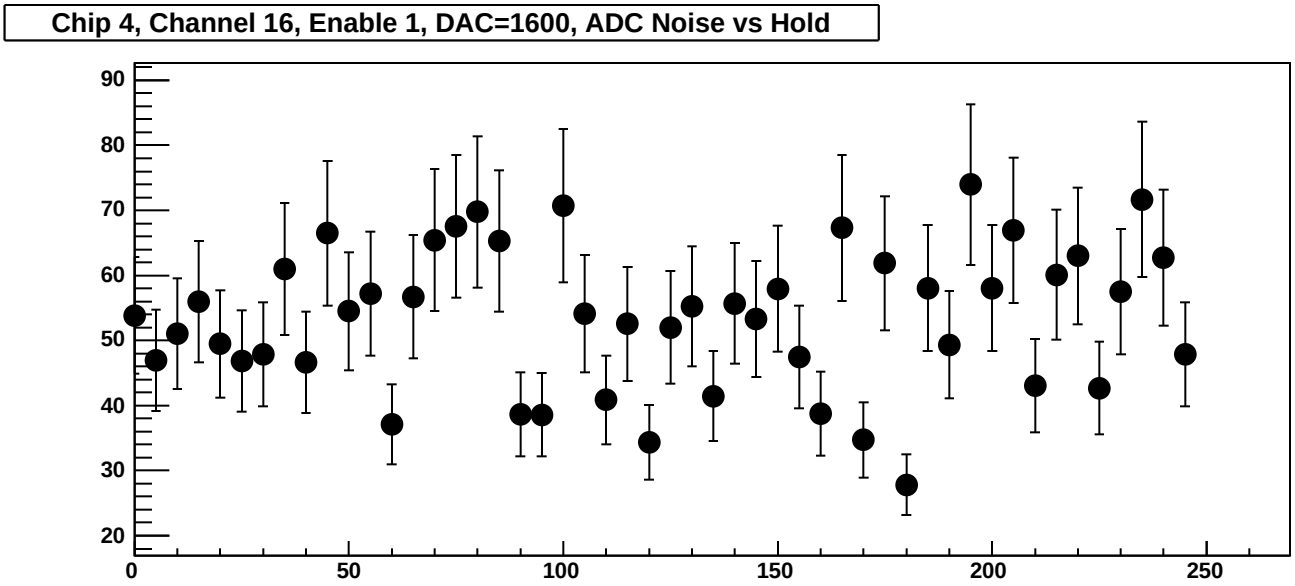
Chip 4, Channel 16, Enable 0, DAC=1600, ADC Noise vs Hold

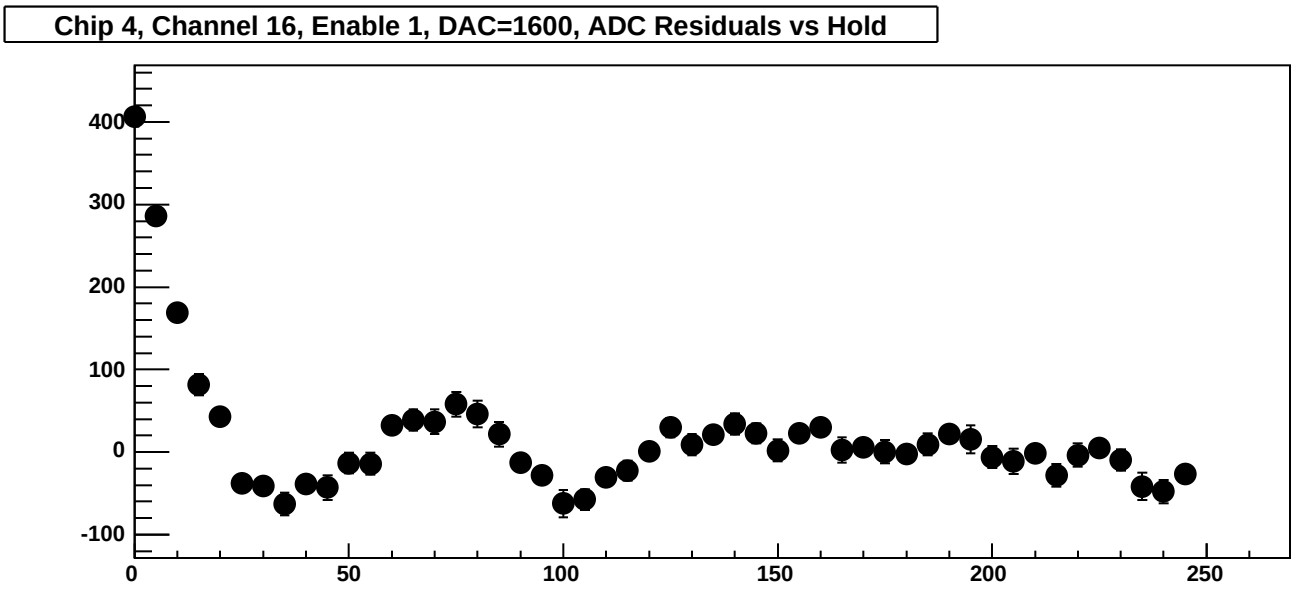


Chip 4, Channel 16, Enable 0, DAC=1600, ADC Residuals vs Hold

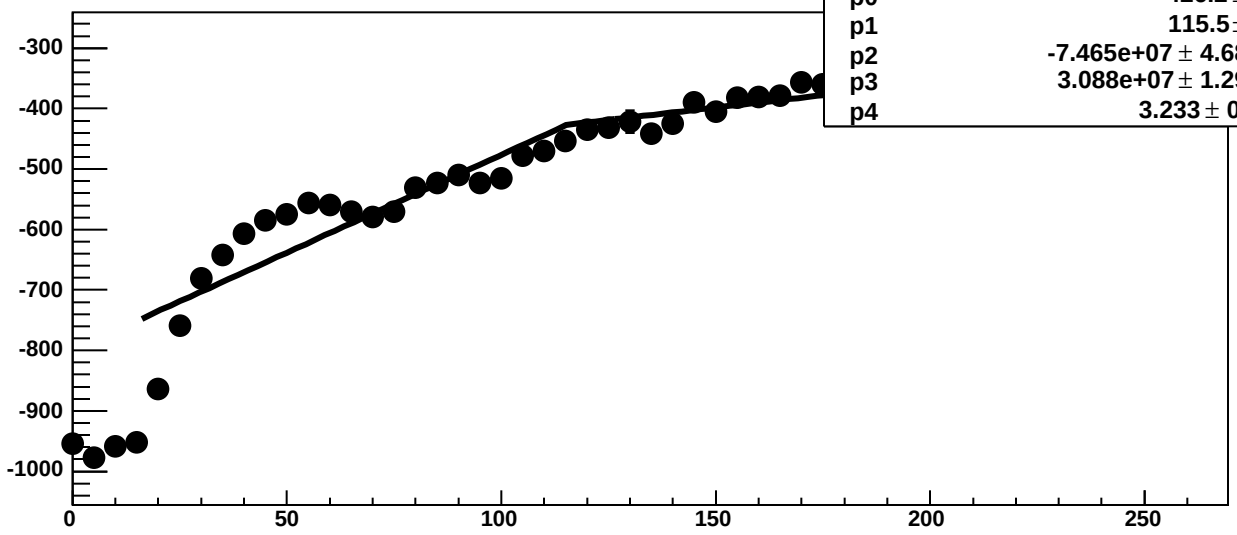



Chip 4, Channel 16, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 308.3 / 41
p0 -77.37 ± 3.882
p1 98.49 ± 0.4811
p2 -3.71e+08 ± 6.57e+06
p3 2.596e+07 ± 3.788e+05
p4 13.08 ± 0.1103
0
-200
-400
-600
-800
-1000
-1200
0
50
100
150
200
250

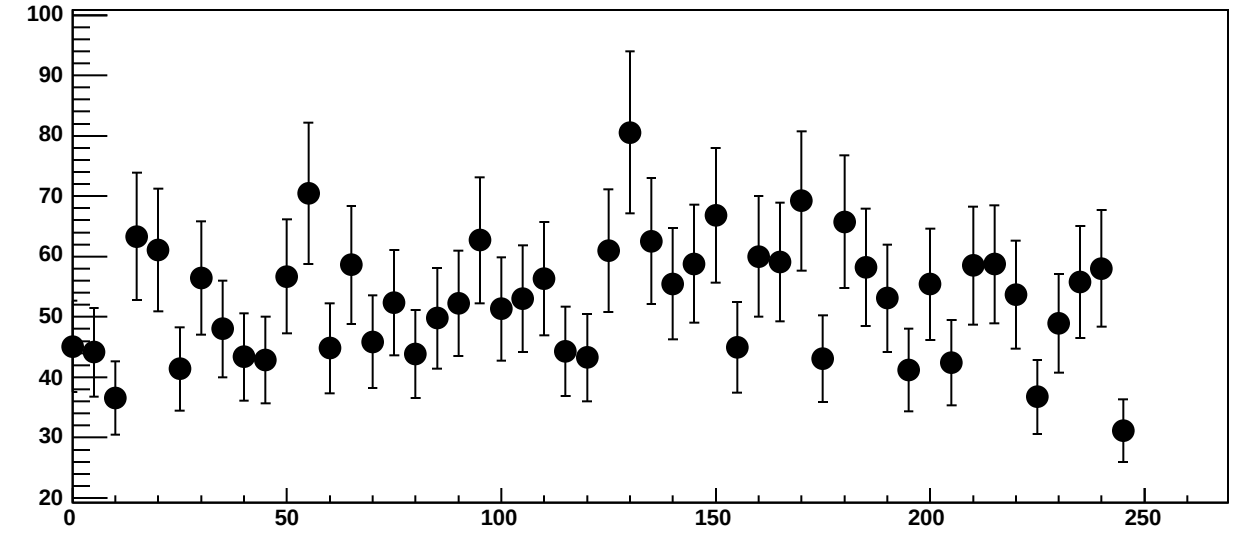

Chip 4, Channel 16, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 4, Channel 16, Enable 1, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

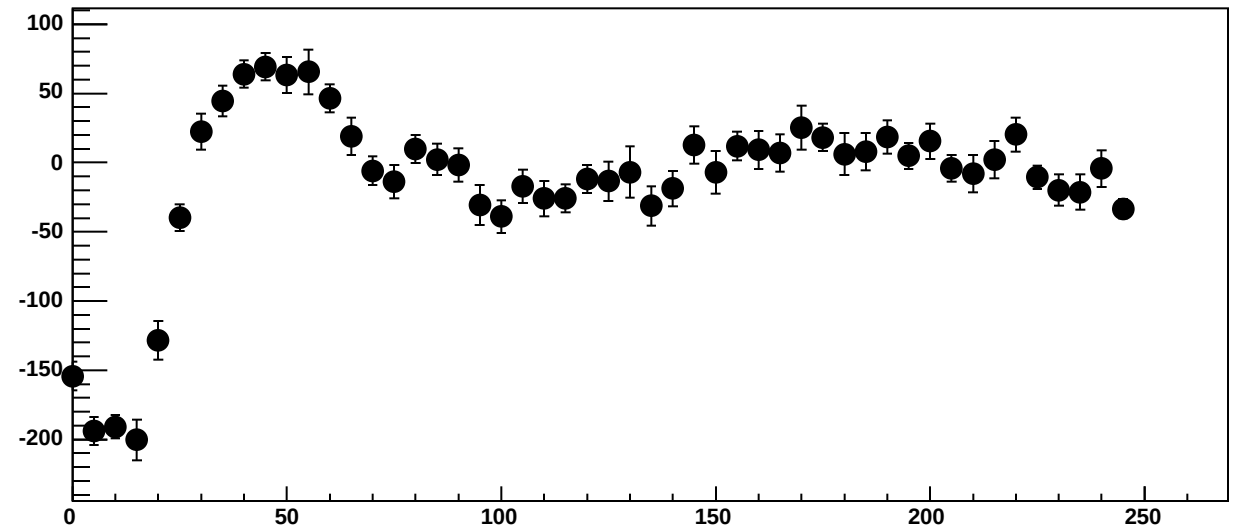
Chip 4, Channel 16, Enable 2, DAC=1600, ADC Mean vs Hold

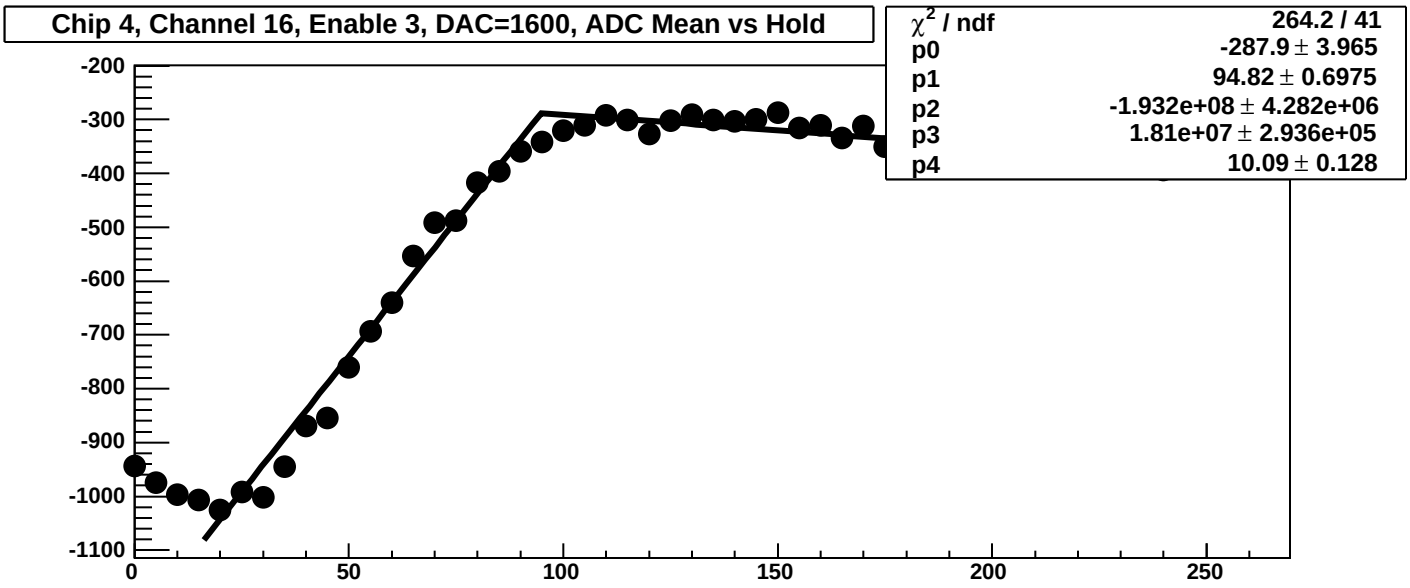


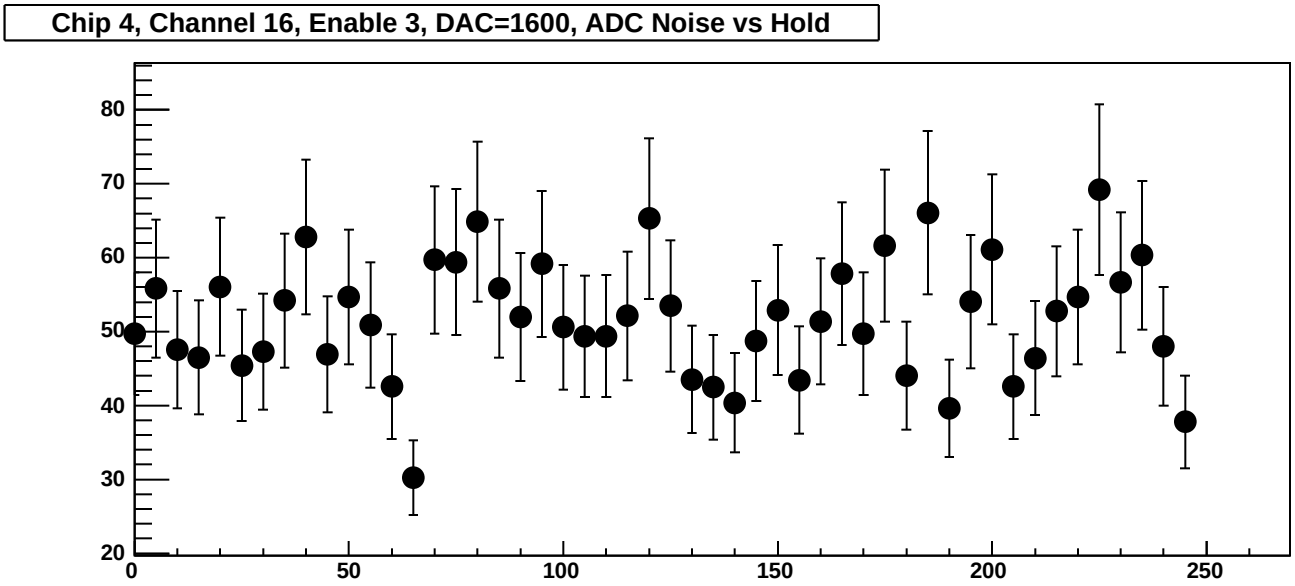
Chip 4, Channel 16, Enable 2, DAC=1600, ADC Noise vs Hold

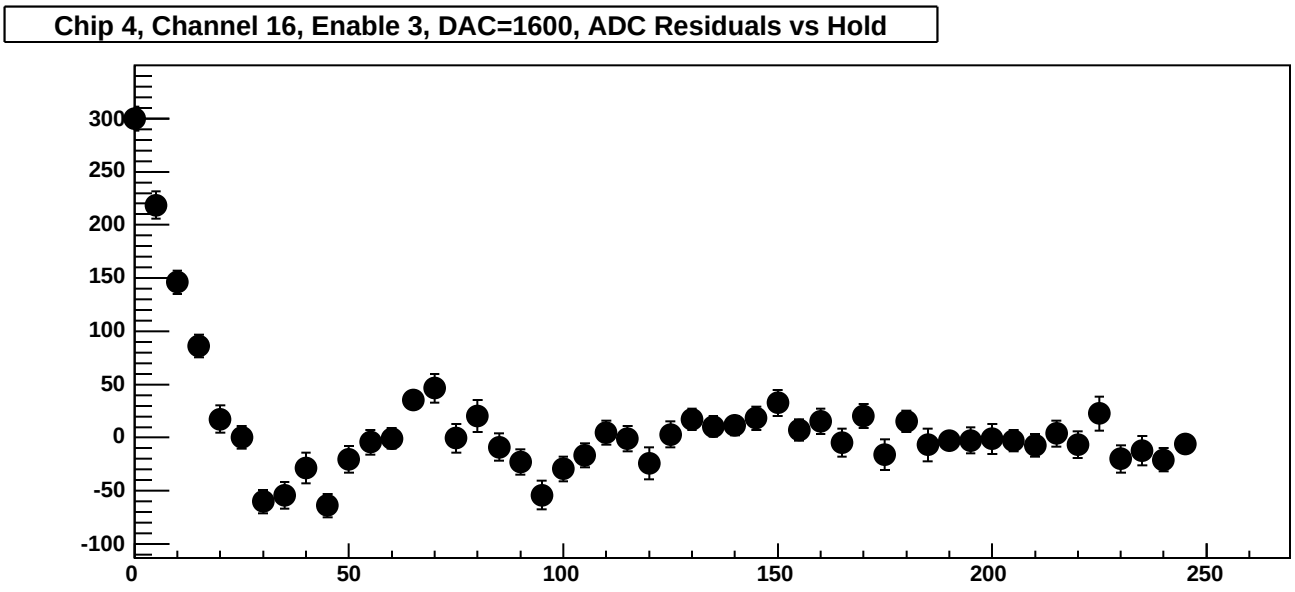


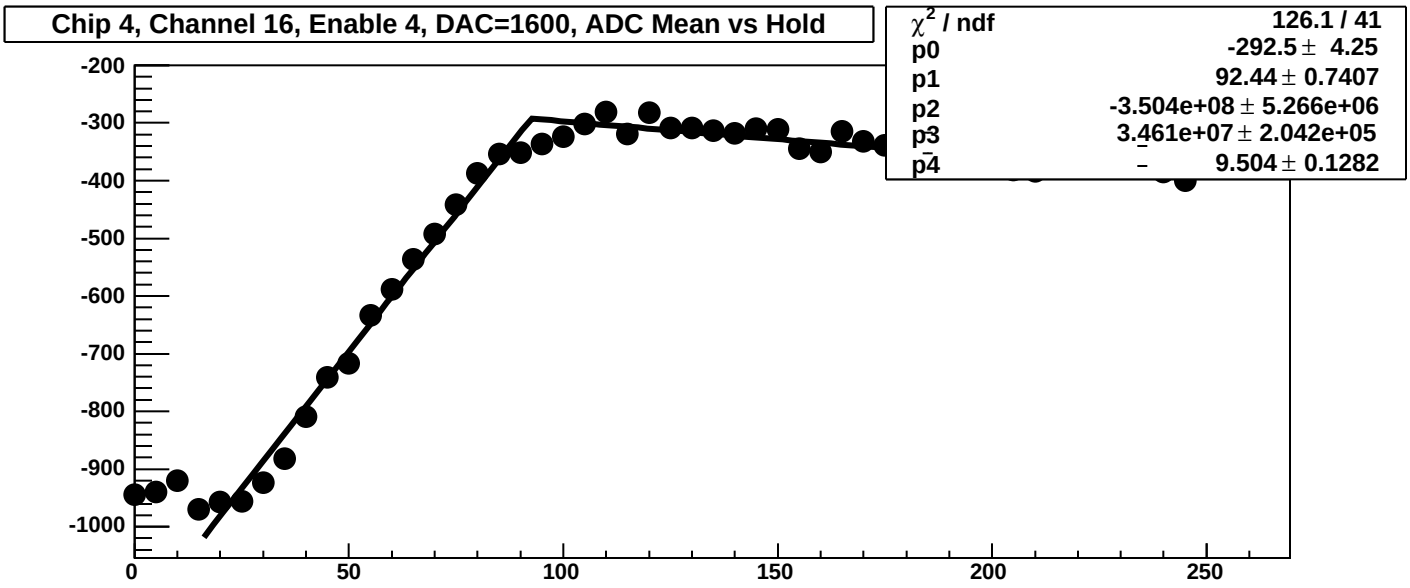
Chip 4, Channel 16, Enable 2, DAC=1600, ADC Residuals vs Hold

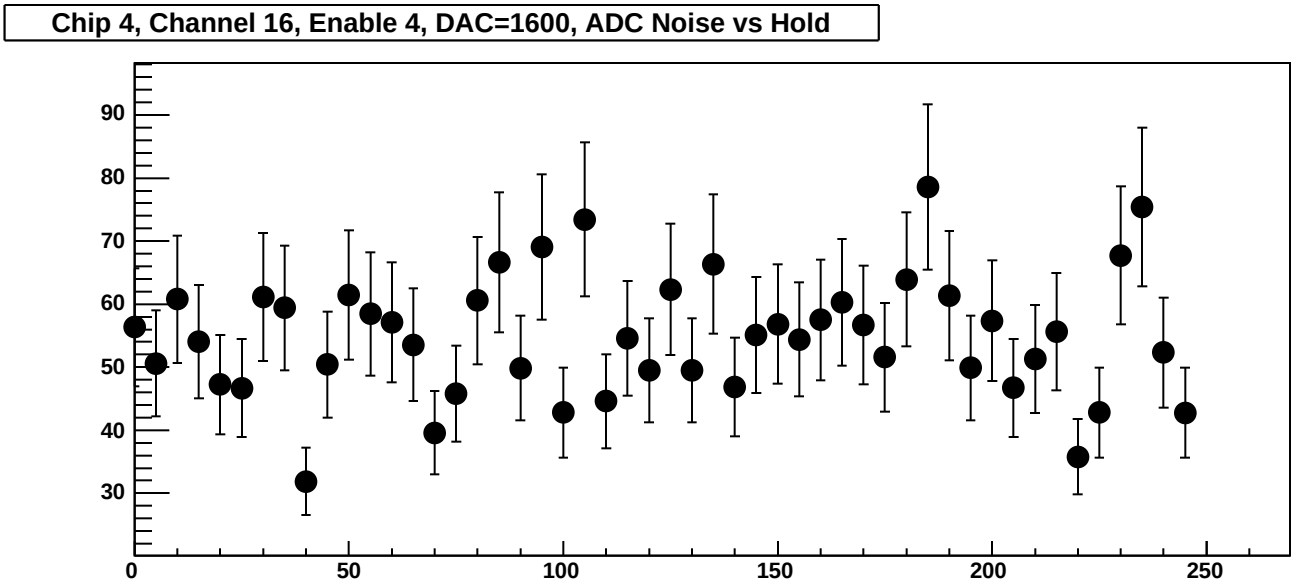


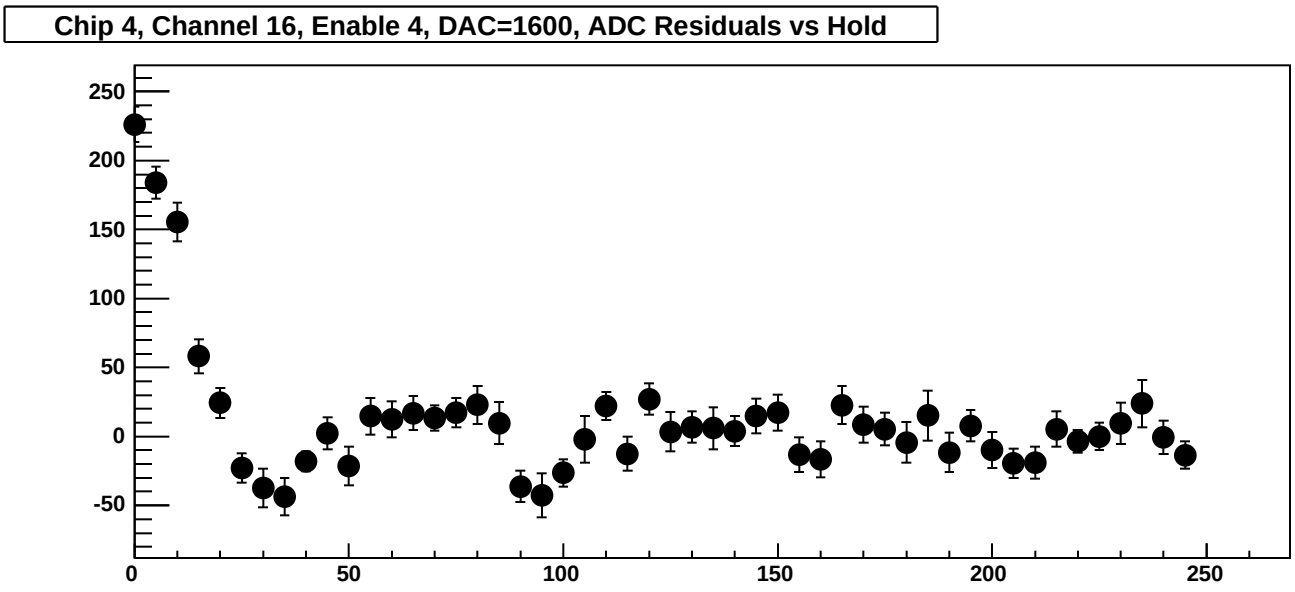

Chip 4, Channel 16, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 264.2 / 41
p0 -287.9 ± 3.965
p1 94.82 ± 0.6975
p2 -1.932e+08 ± 4.282e+06
p3 1.81e+07 ± 2.936e+05
p4 10.09 ± 0.128
-200
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
50
100
150
200
250

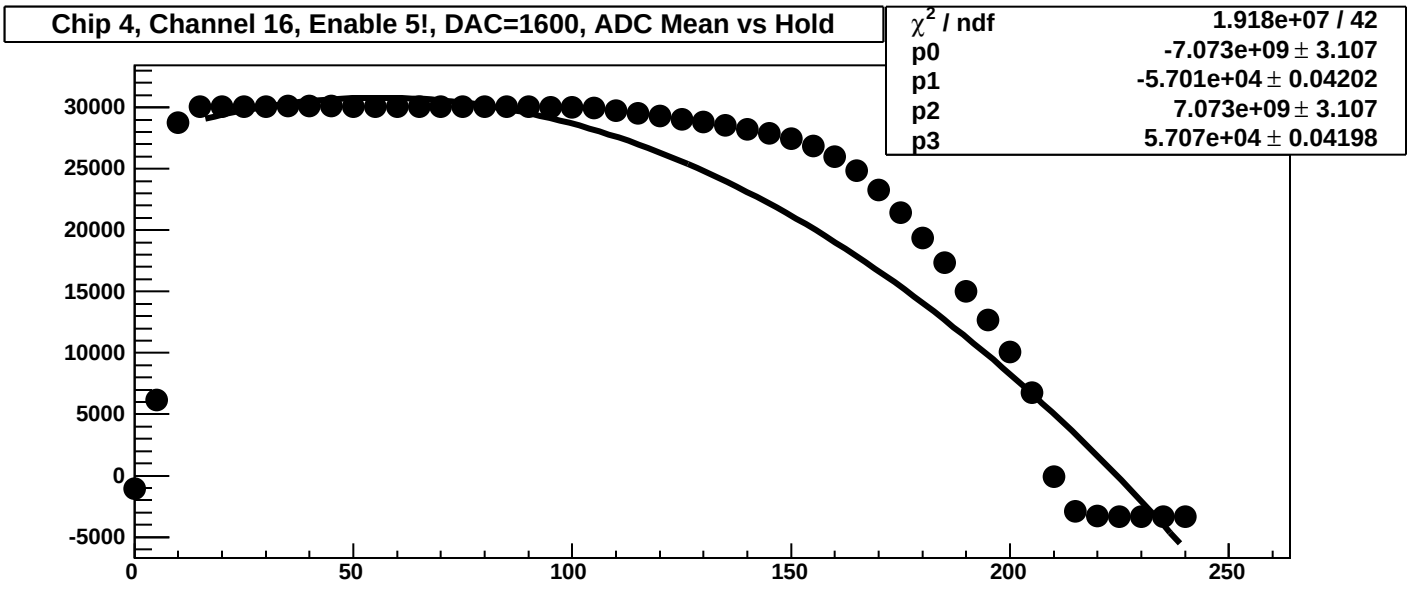

Chip 4, Channel 16, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
20
0
50
100
150
200
250

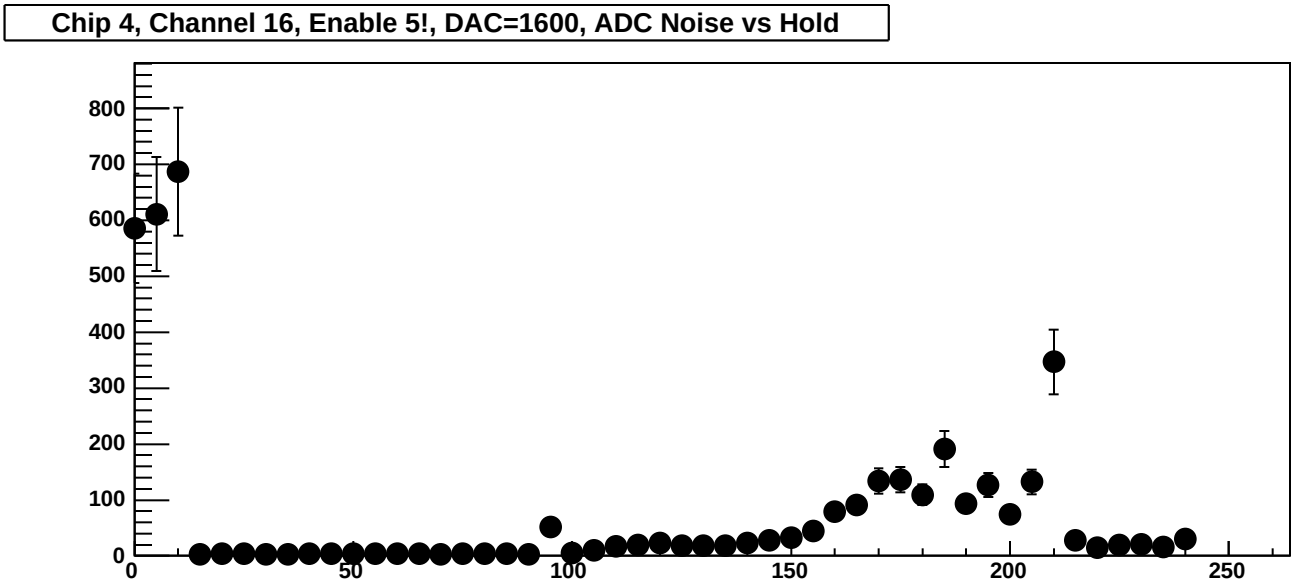

Chip 4, Channel 16, Enable 3, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

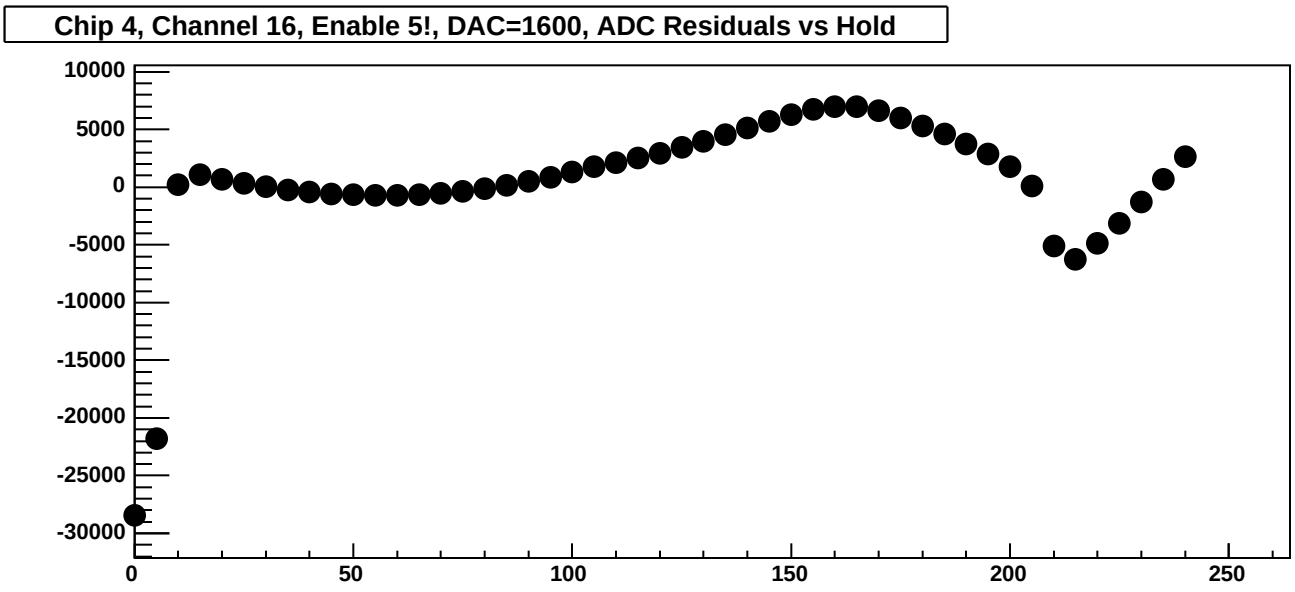

Chip 4, Channel 16, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 126.1 / 41
p0 -292.5 ± 4.25
p1 92.44 ± 0.7407
p2 -3.504e+08 ± 5.266e+06
p3 3.461e+07 ± 2.042e+05
p4 9.504 ± 0.1282
-200
-300
-400
-500
-600
-700
-800
-900
-1000
0
50
100
150
200
250


Chip 4, Channel 16, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

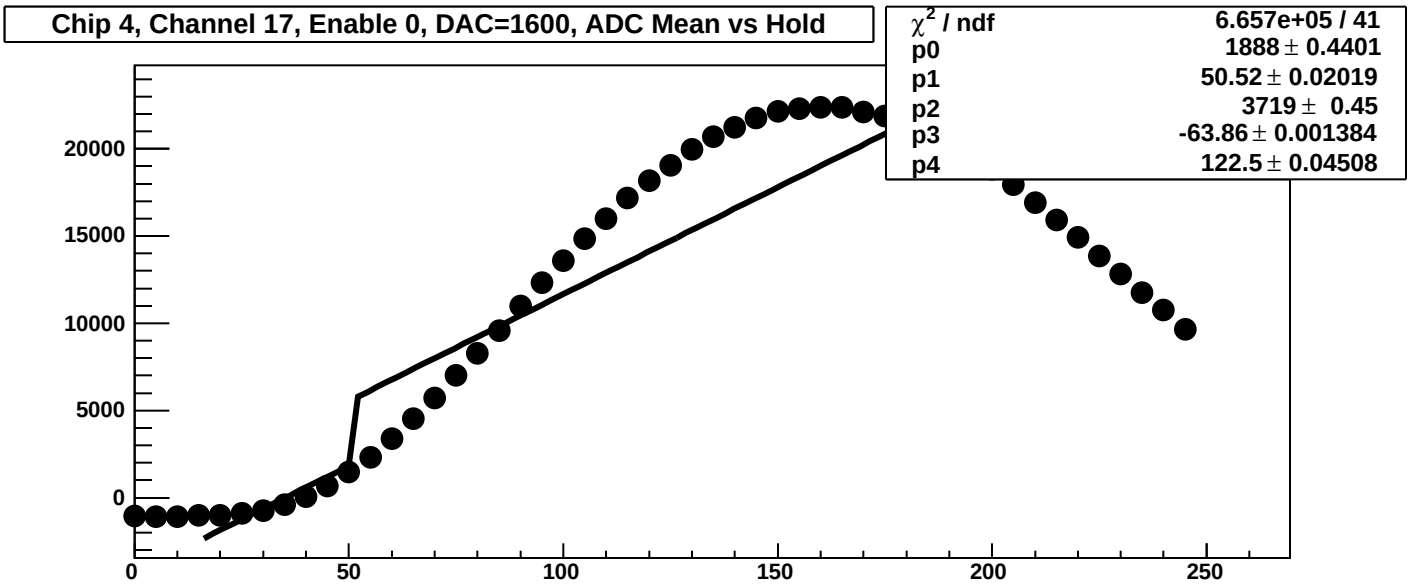

Chip 4, Channel 16, Enable 4, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
0
50
100
150
200
250


Chip 4, Channel 16, Enable 5!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.918e+07 / 42
p0 -7.073e+09 ± 3.107
p1 -5.701e+04 ± 0.04202
p2 7.073e+09 ± 3.107
p3 5.707e+04 ± 0.04198
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

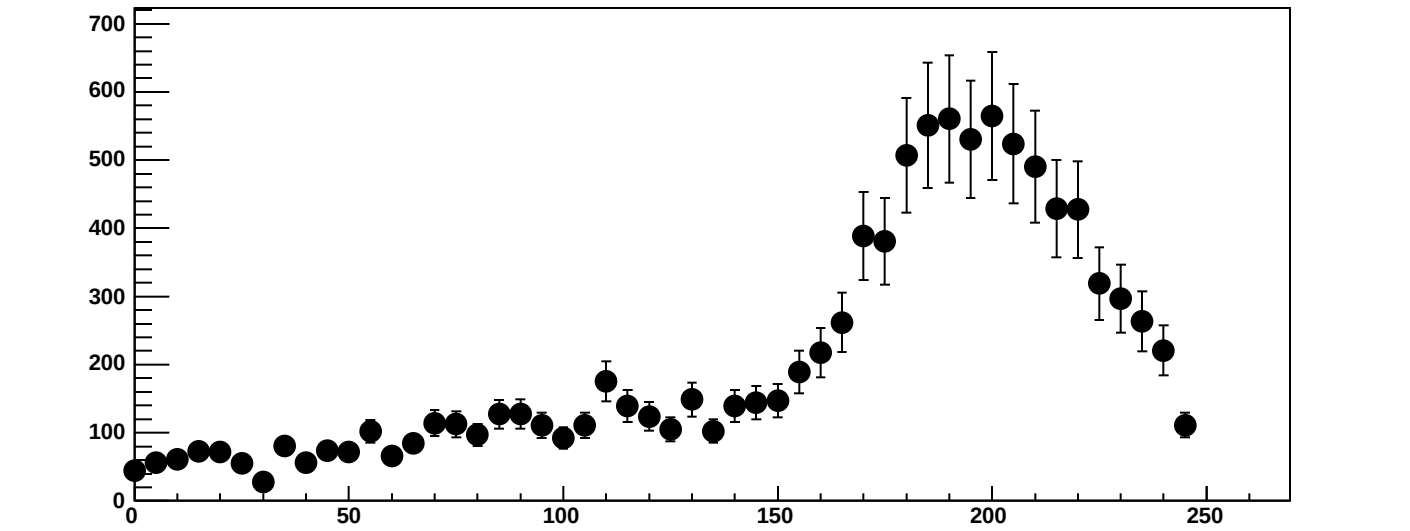

Chip 4, Channel 16, Enable 5!, DAC=1600, ADC Noise vs Hold
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 4, Channel 16, Enable 5!, DAC=1600, ADC Residuals vs Hold
10000
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
0
50
100
150
200
250

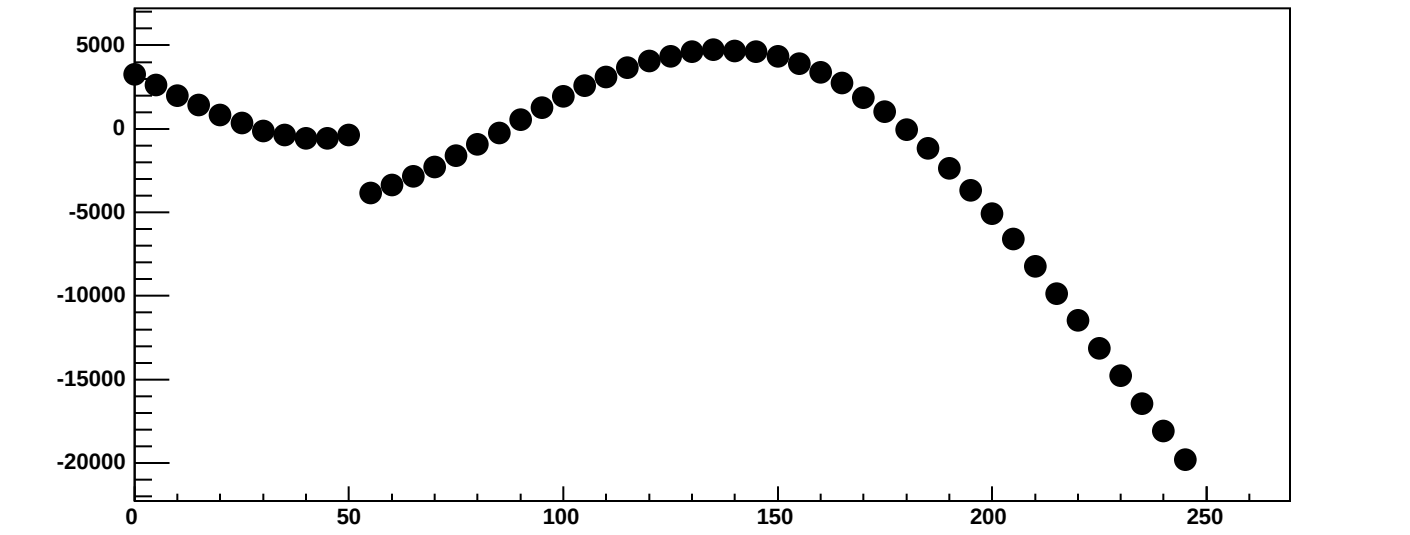
Chip 4, Channel 17, Enable 0, DAC=1600, ADC Mean vs Hold

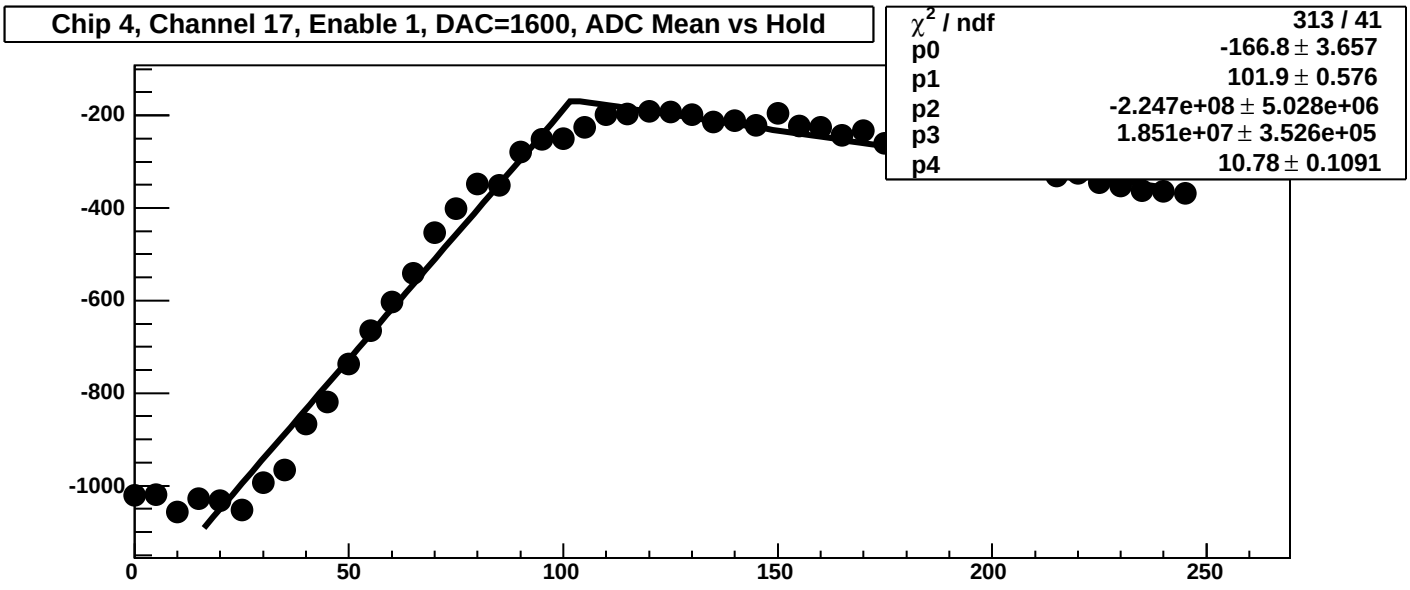


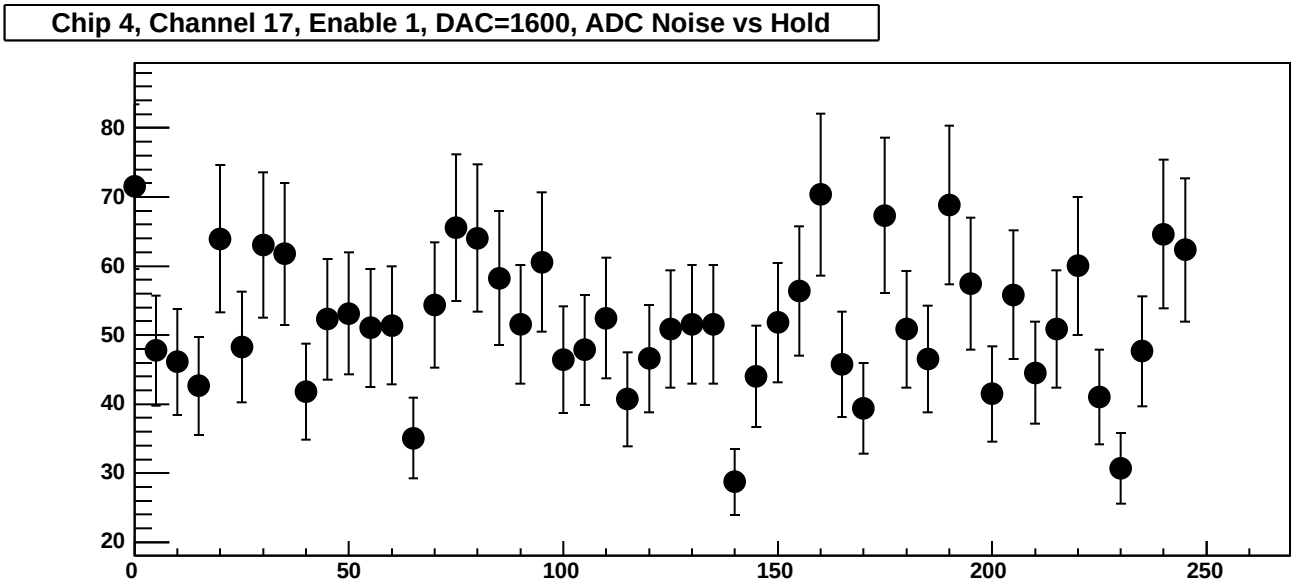
Chip 4, Channel 17, Enable 0, DAC=1600, ADC Noise vs Hold

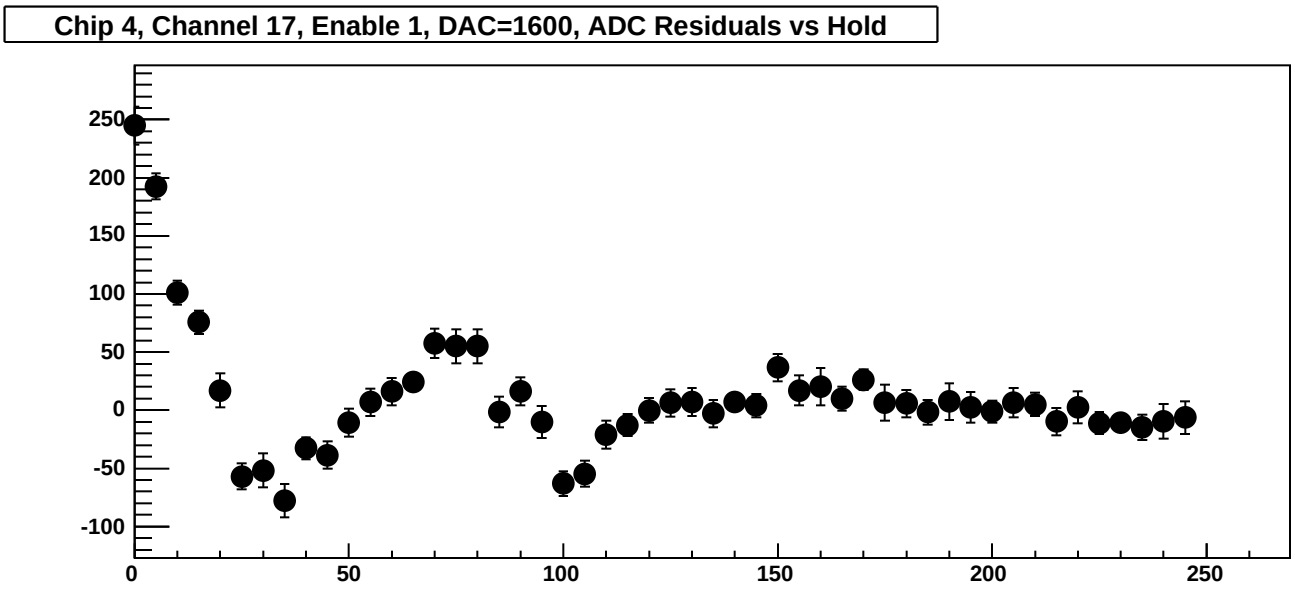


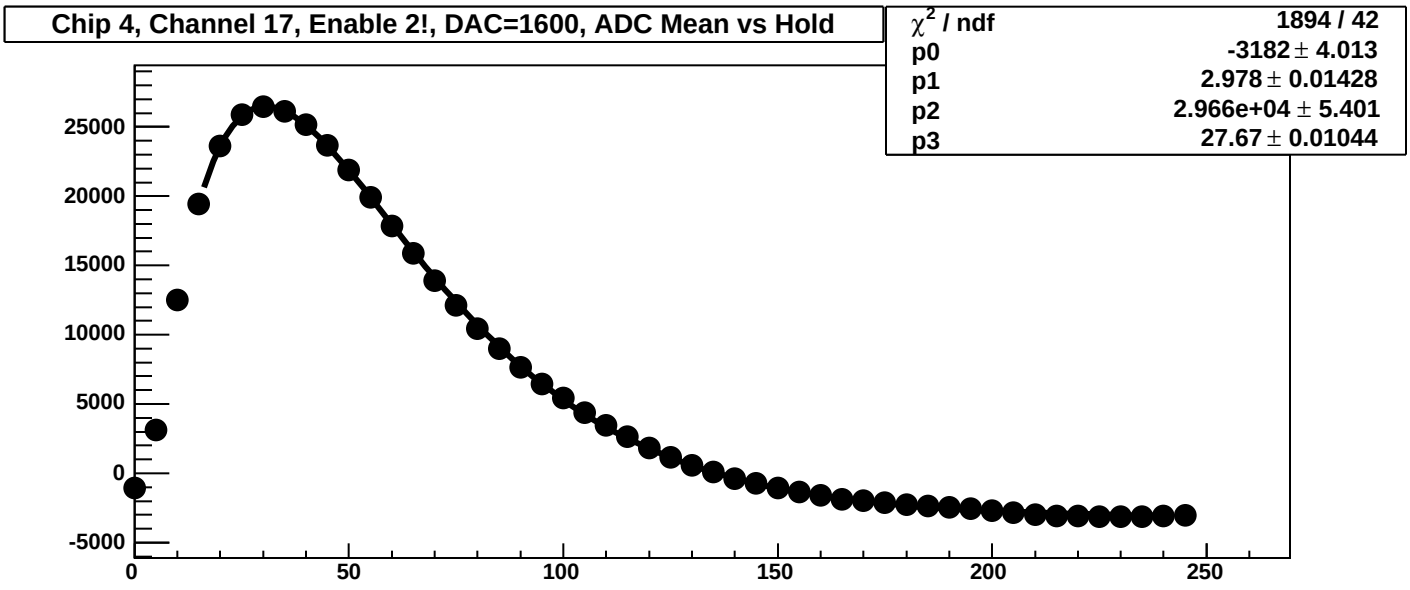
Chip 4, Channel 17, Enable 0, DAC=1600, ADC Residuals vs Hold

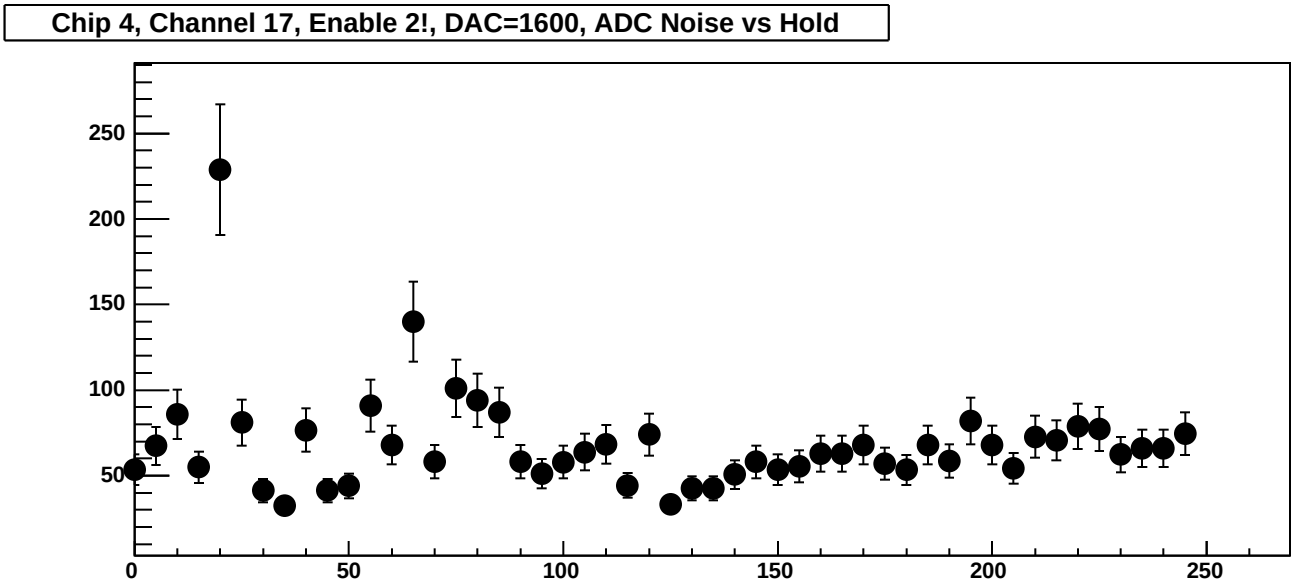


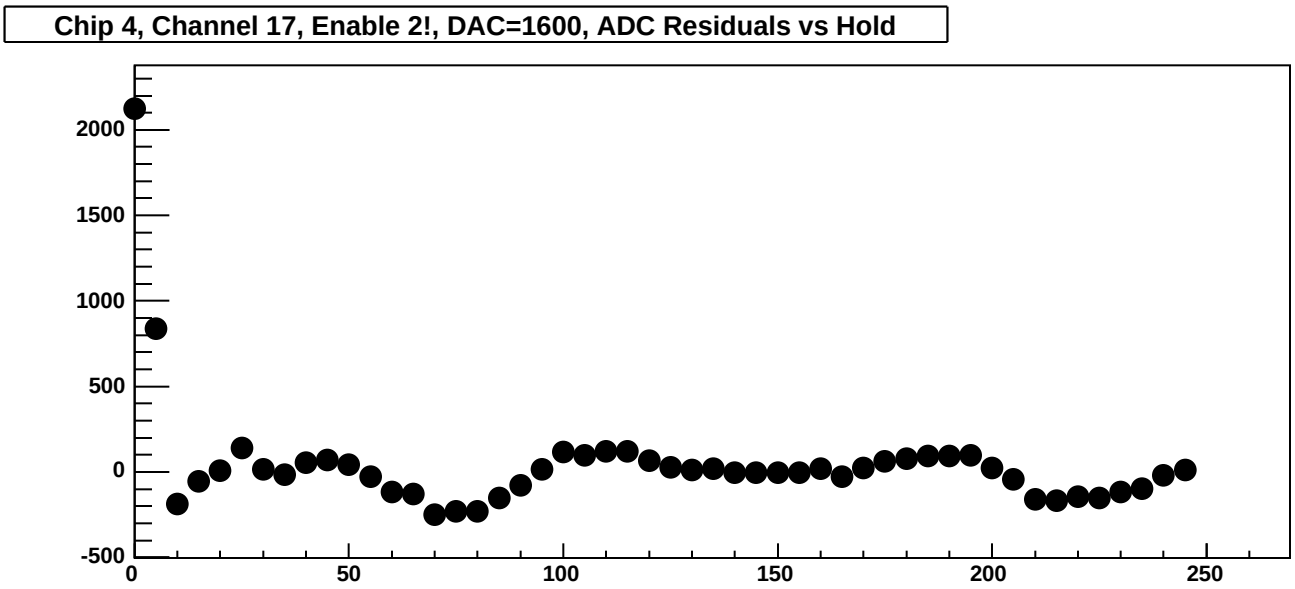

Chip 4, Channel 17, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 313 / 41
p0 -166.8 ± 3.657
p1 101.9 ± 0.576
p2 -2.247e+08 ± 5.028e+06
p3 1.851e+07 ± 3.526e+05
p4 10.78 ± 0.1091
-200
-400
-600
-800
-1000
0
50
100
150
200
250

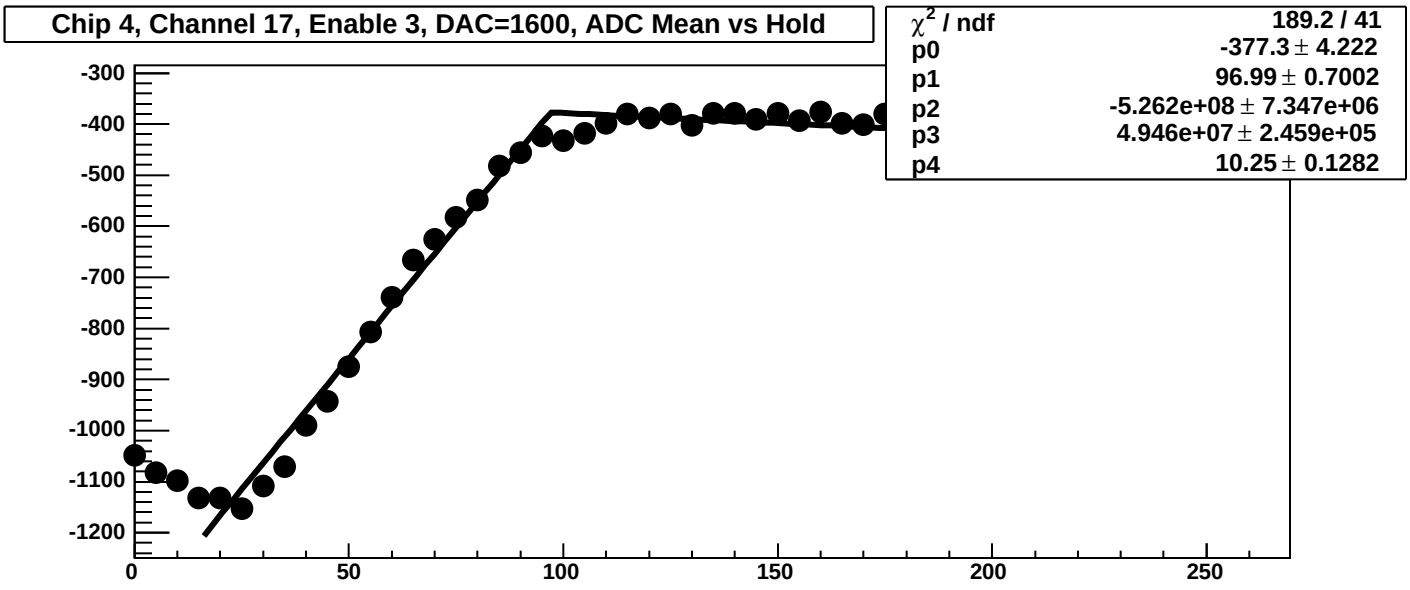

Chip 4, Channel 17, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
20
0
50
100
150
200
250

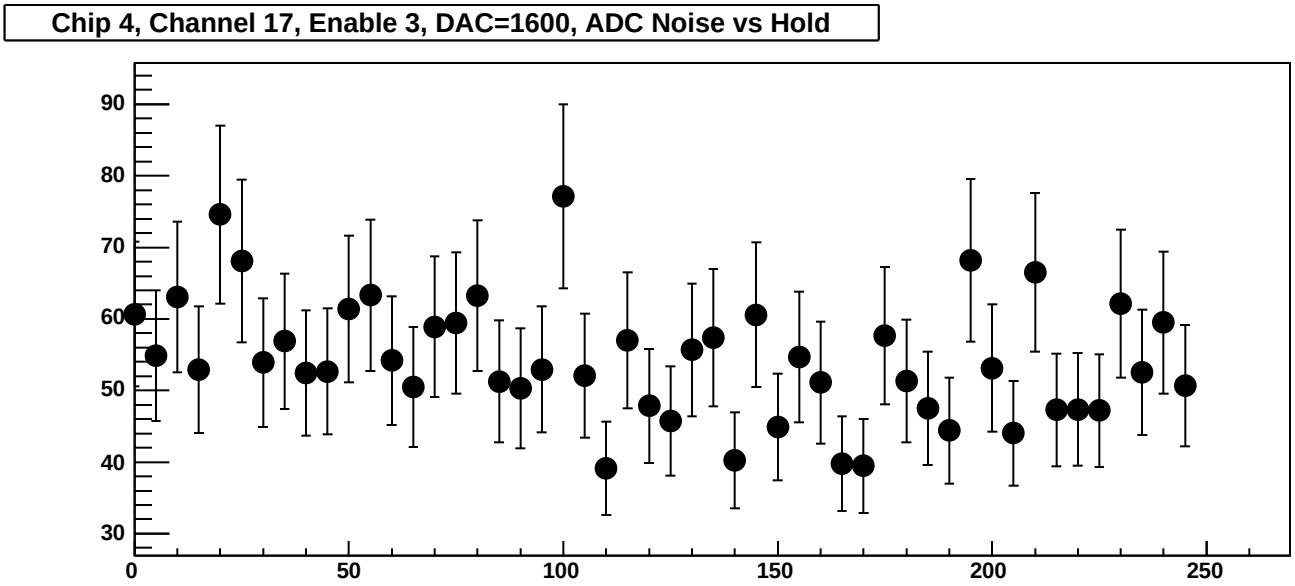

Chip 4, Channel 17, Enable 1, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

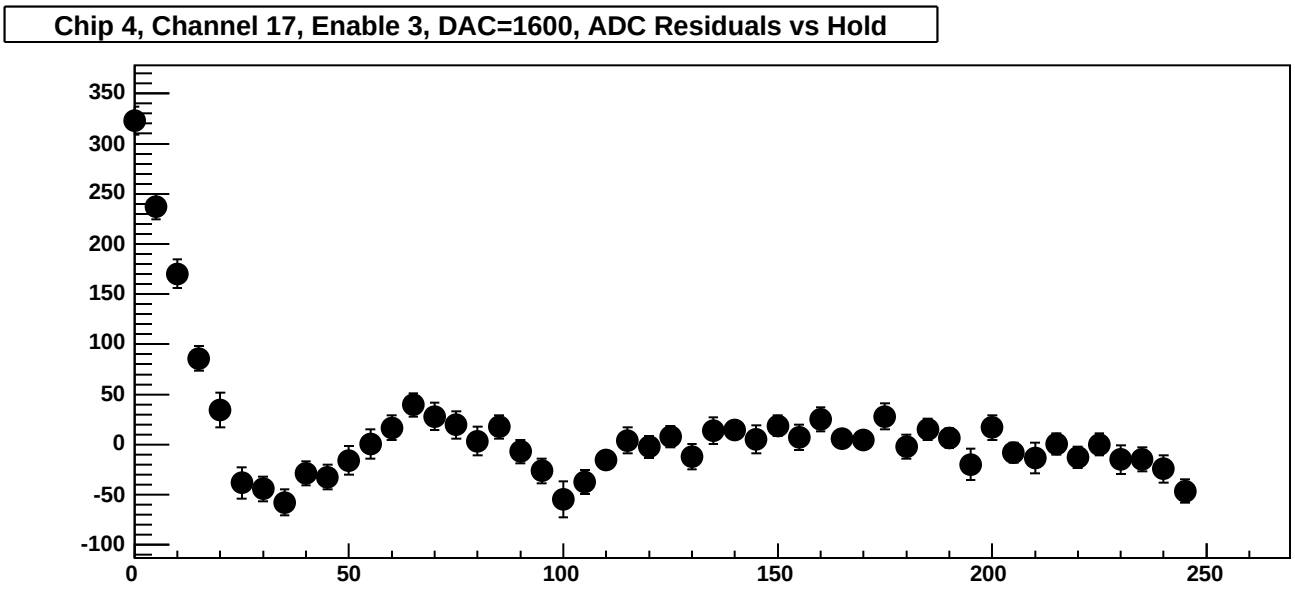

Chip 4, Channel 17, Enable 2!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1894 / 42
p0 -3182 ± 4.013
p1 2.978 ± 0.01428
p2 2.966e+04 ± 5.401
p3 27.67 ± 0.01044
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 4, Channel 17, Enable 2!, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
50
100
150
200
250

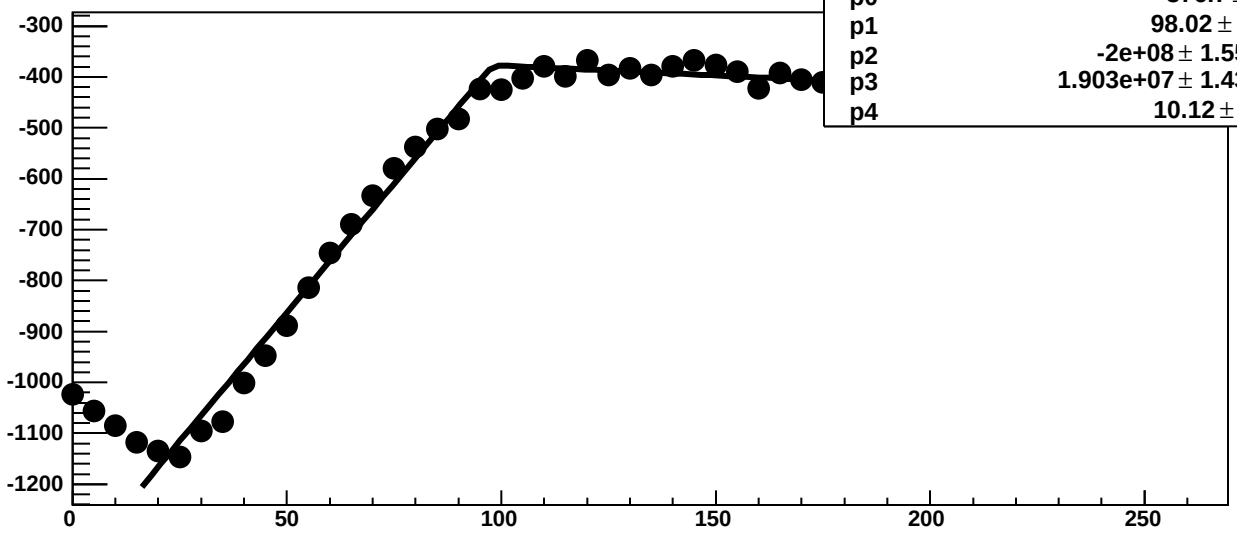

Chip 4, Channel 17, Enable 2!, DAC=1600, ADC Residuals vs Hold
2000
1500
1000
500
0
-500
0
50
100
150
200
250


Chip 4, Channel 17, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 189.2 / 41
p0 -377.3 ± 4.222
p1 96.99 ± 0.7002
p2 -5.262e+08 ± 7.347e+06
p3 4.946e+07 ± 2.459e+05
p4 10.25 ± 0.1282
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
50
100
150
200
250

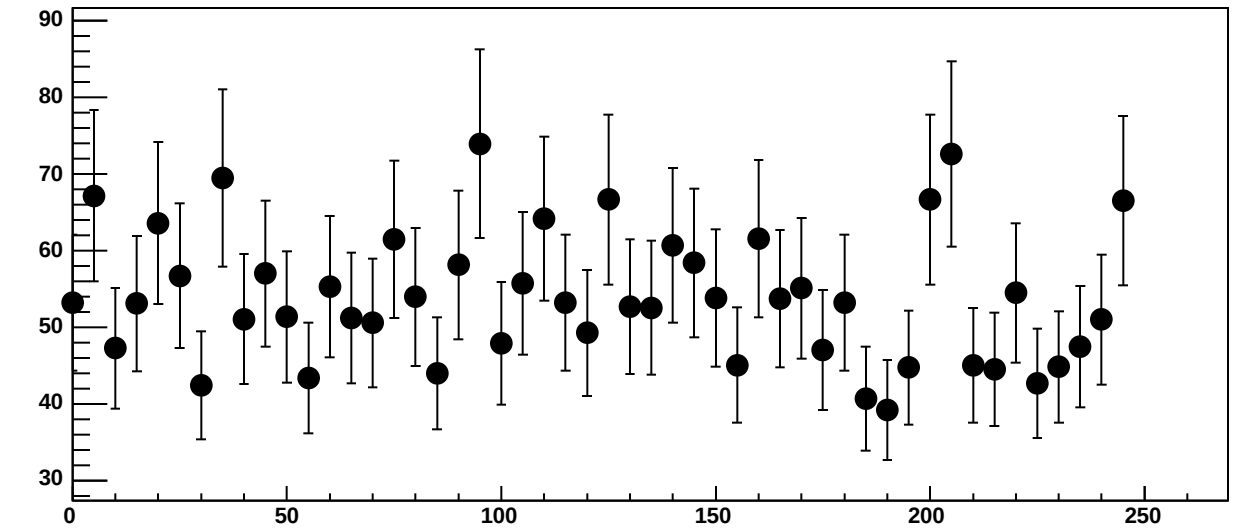

Chip 4, Channel 17, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 4, Channel 17, Enable 3, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

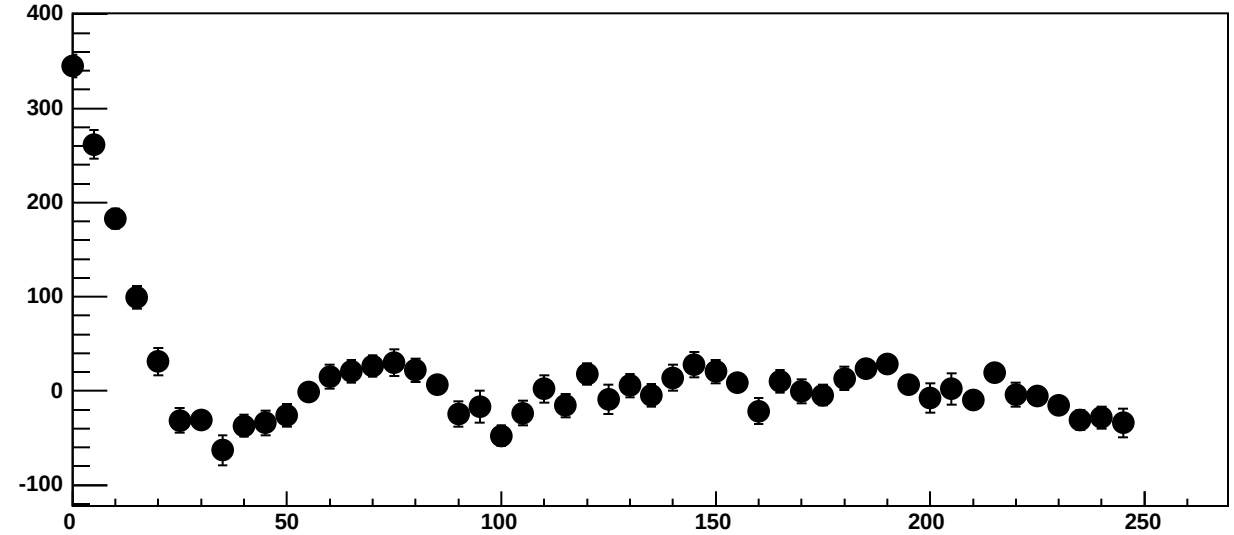
Chip 4, Channel 17, Enable 4, DAC=1600, ADC Mean vs Hold

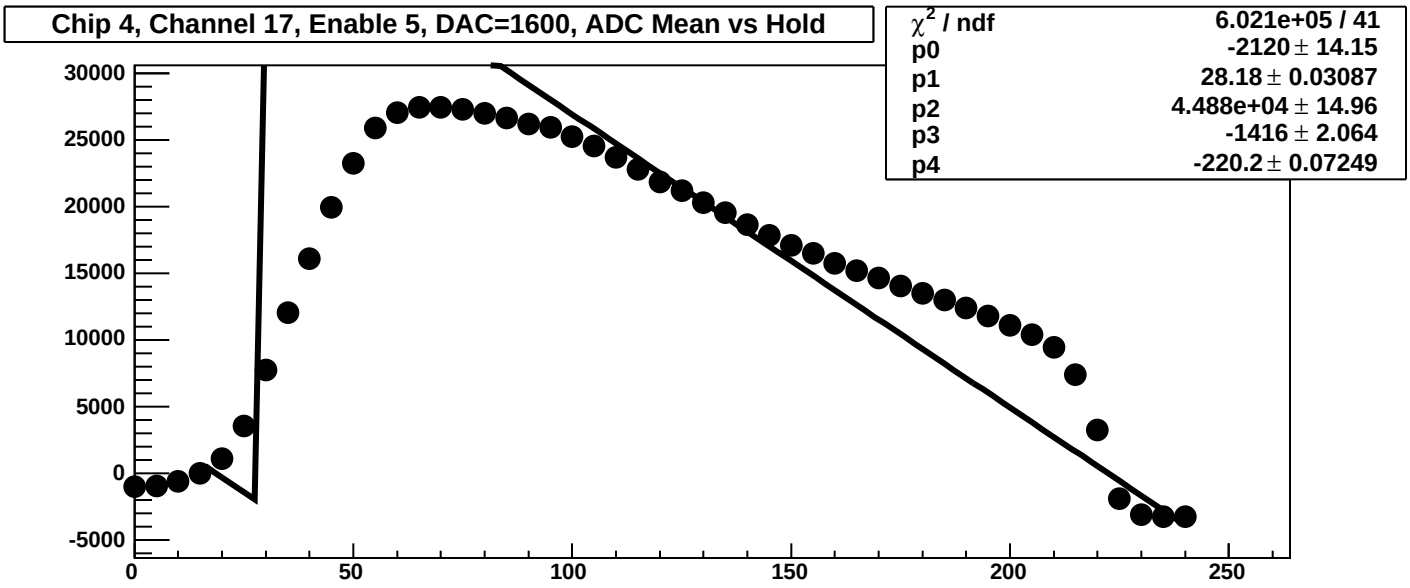


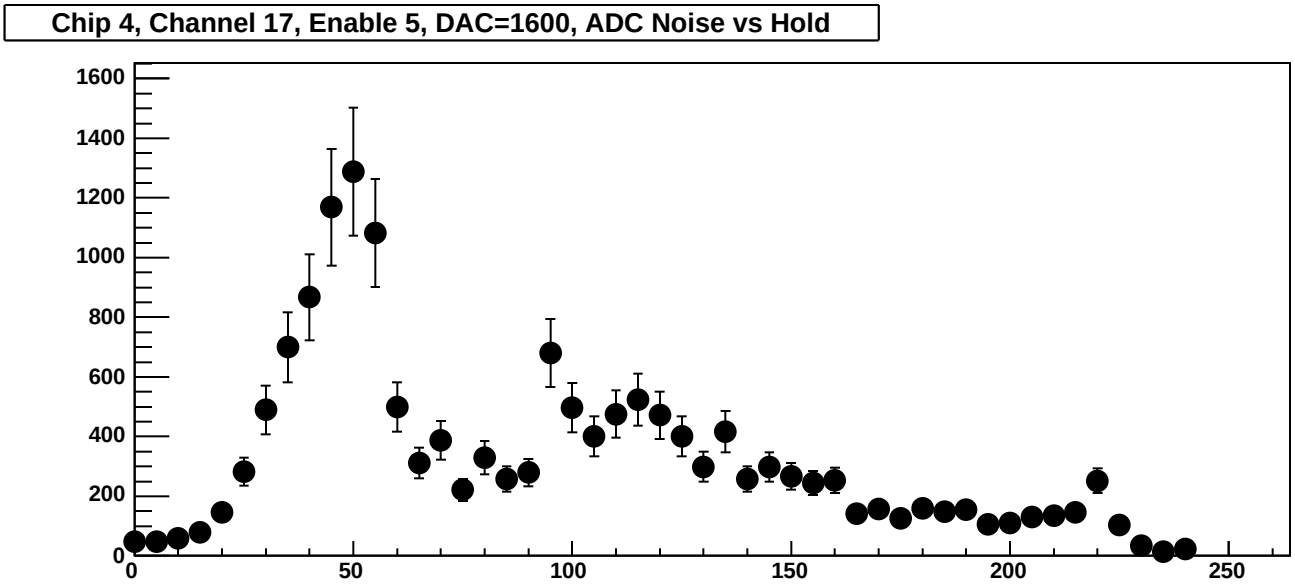
Chip 4, Channel 17, Enable 4, DAC=1600, ADC Noise vs Hold

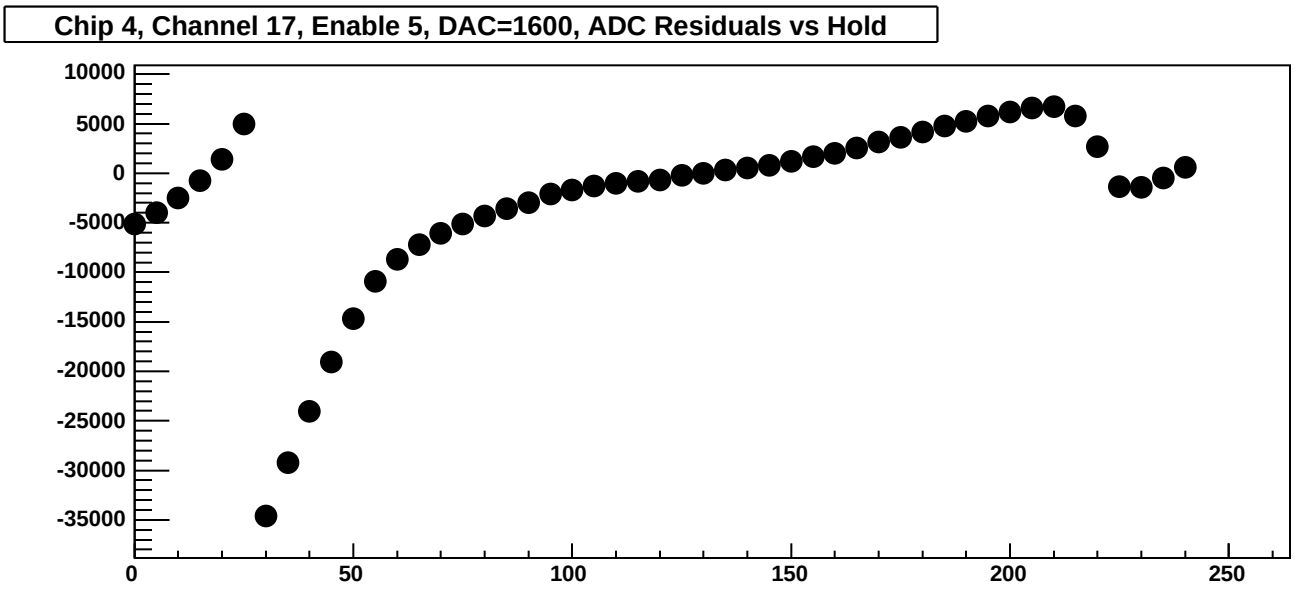


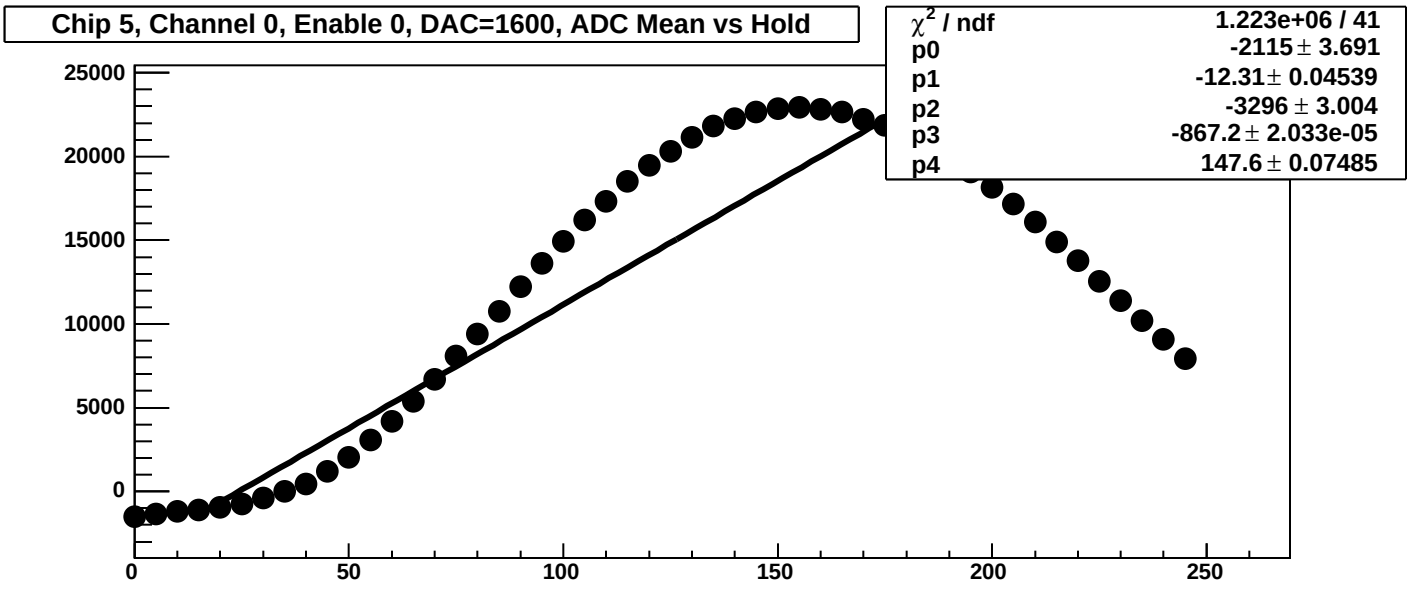
Chip 4, Channel 17, Enable 4, DAC=1600, ADC Residuals vs Hold

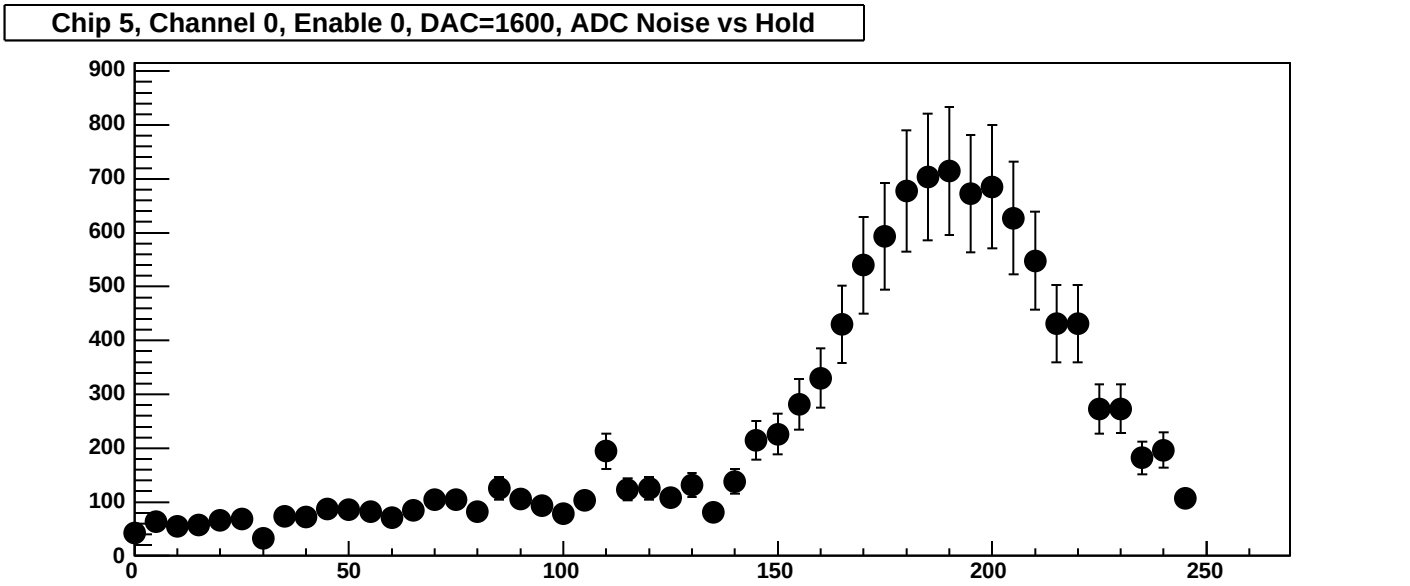


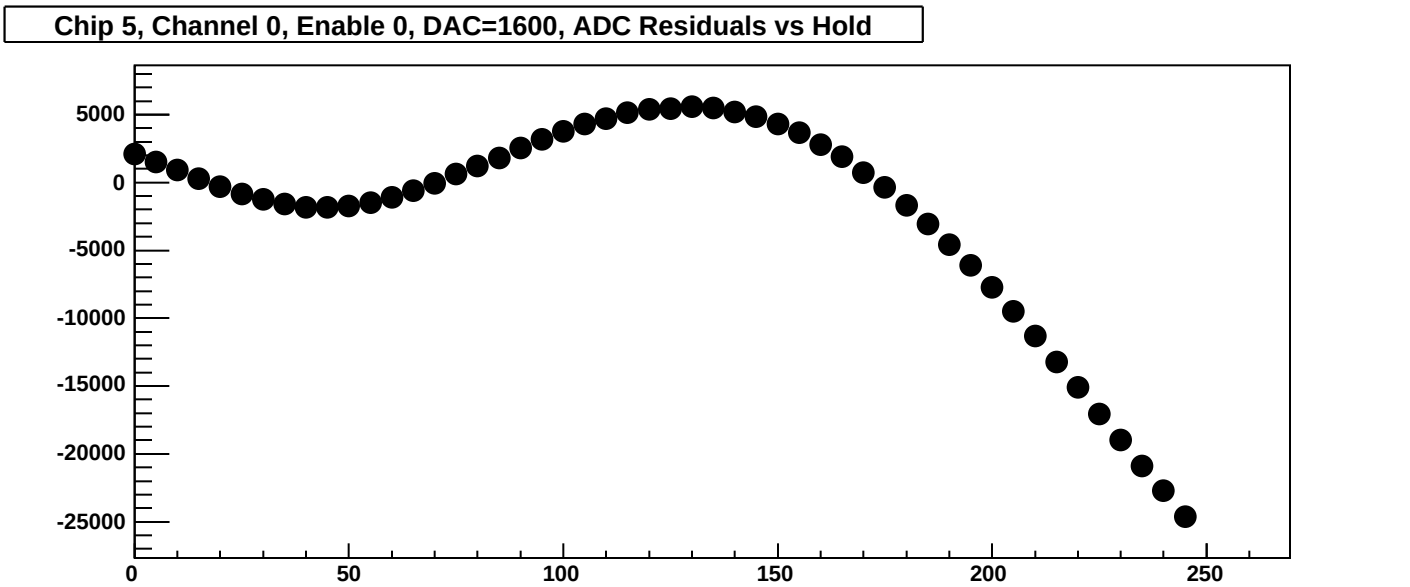

Chip 4, Channel 17, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 6.021e+05 / 41
p0 -2120 ± 14.15
p1 28.18 ± 0.03087
p2 4.488e+04 ± 14.96
p3 -1416 ± 2.064
p4 -220.2 ± 0.07249
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 4, Channel 17, Enable 5, DAC=1600, ADC Noise vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

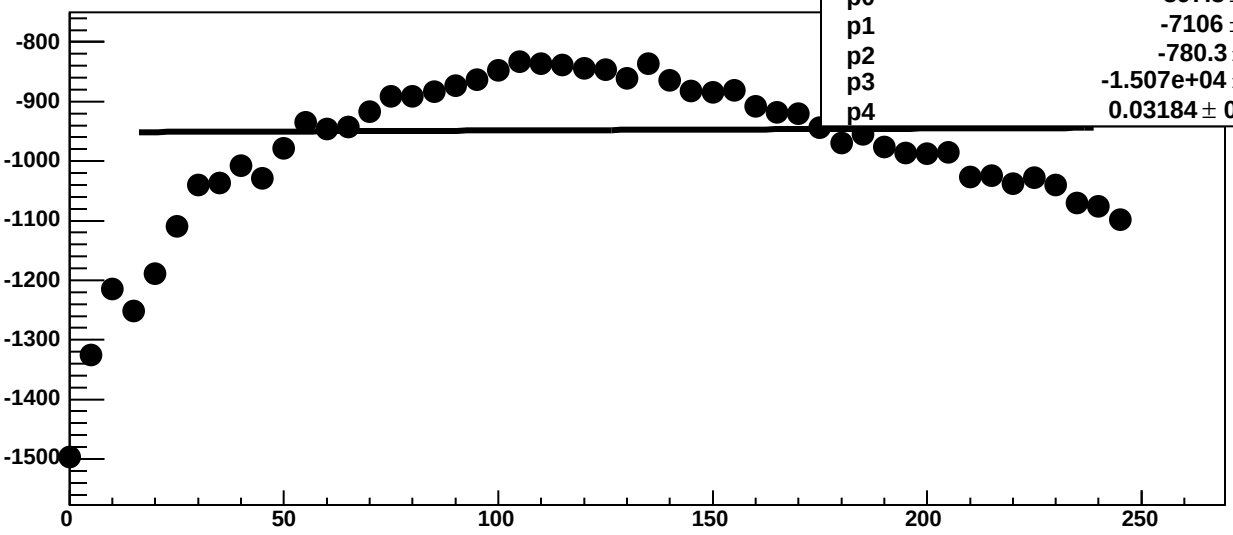

Chip 4, Channel 17, Enable 5, DAC=1600, ADC Residuals vs Hold
10000
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
-35000
0
50
100
150
200
250


Chip 5, Channel 0, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.223e+06 / 41
p0 -2115 ± 3.691
p1 -12.31 ± 0.04539
p2 -3296 ± 3.004
p3 -867.2 ± 2.033e-05
p4 147.6 ± 0.07485
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

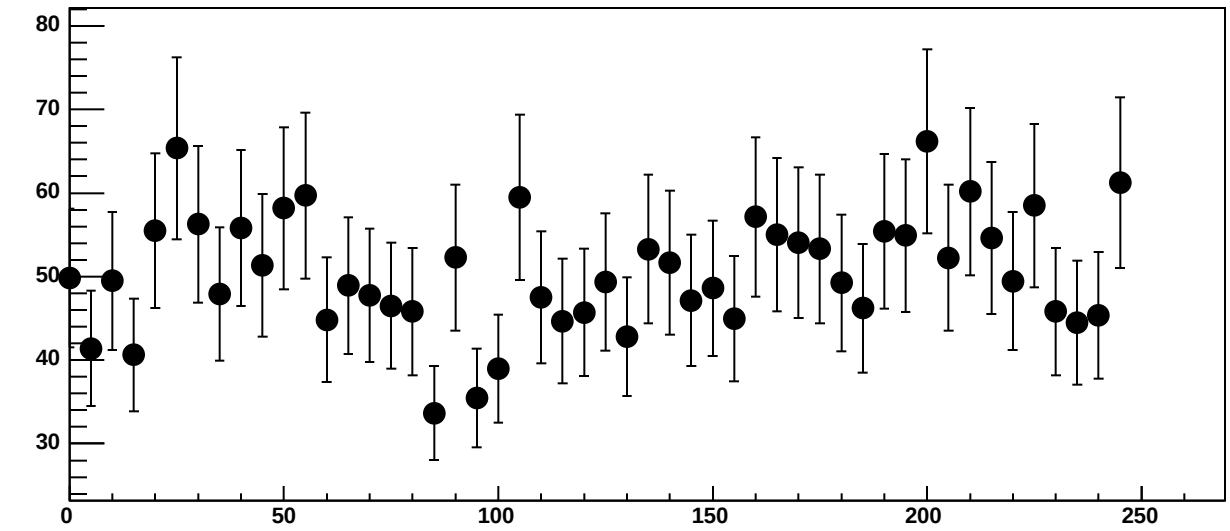

Chip 5, Channel 0, Enable 0, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 5, Channel 0, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
0
50
100
150
200
250

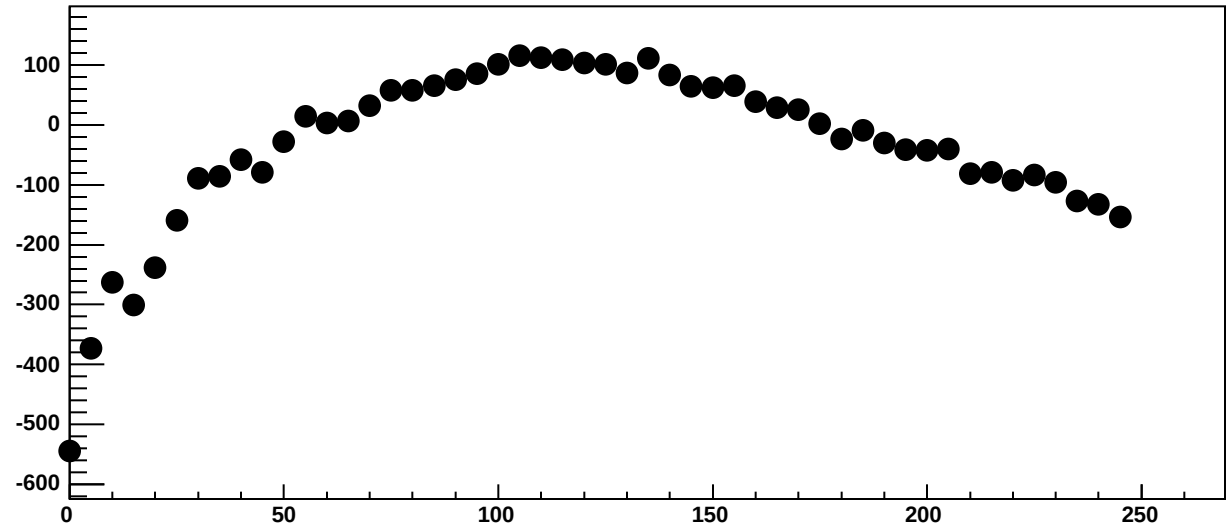
Chip 5, Channel 0, Enable 1, DAC=1600, ADC Mean vs Hold



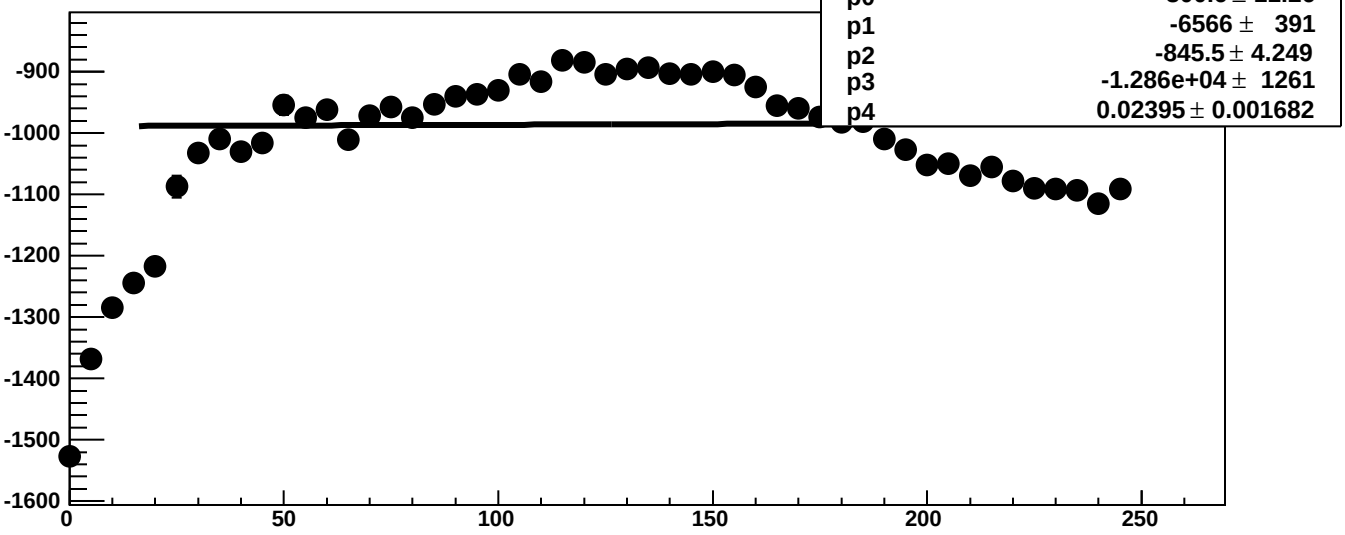
Chip 5, Channel 0, Enable 1, DAC=1600, ADC Noise vs Hold



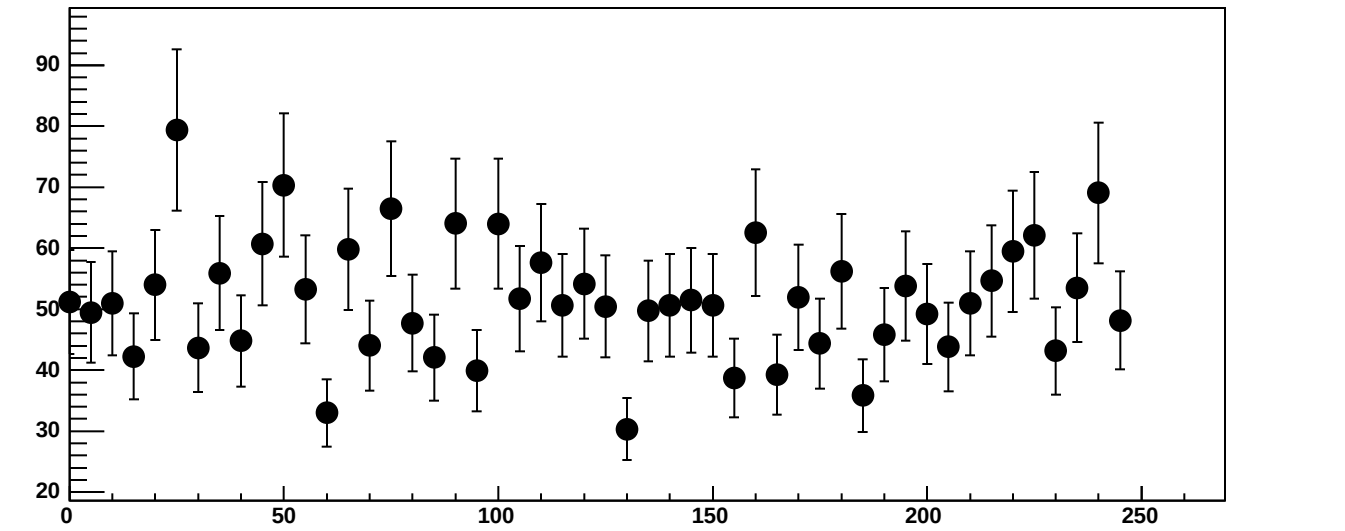
Chip 5, Channel 0, Enable 1, DAC=1600, ADC Residuals vs Hold



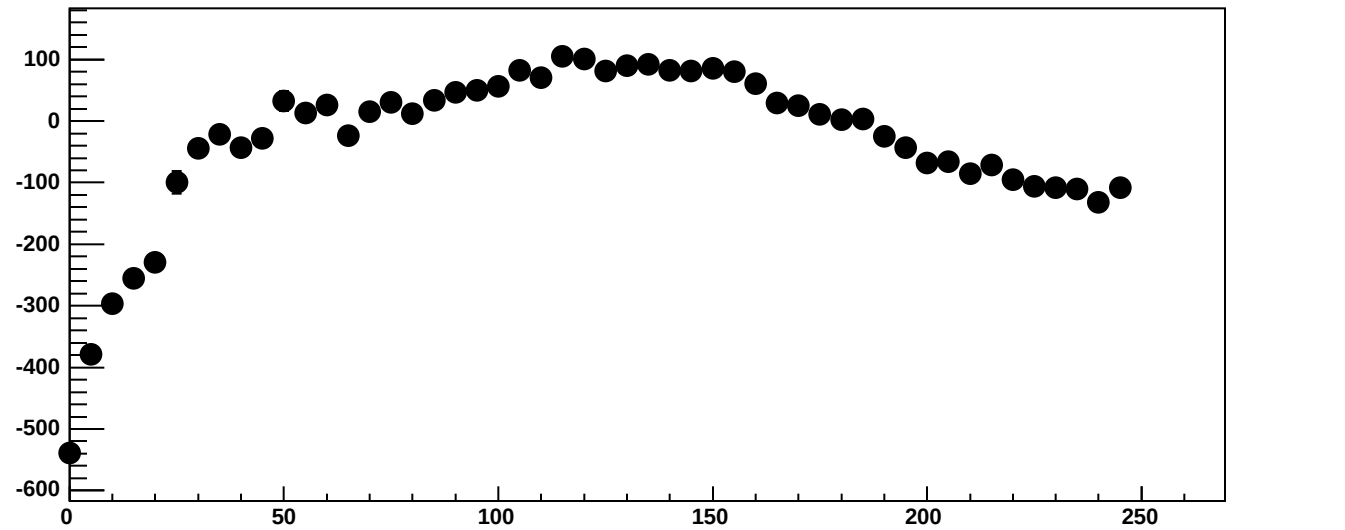
Chip 5, Channel 0, Enable 2, DAC=1600, ADC Mean vs Hold



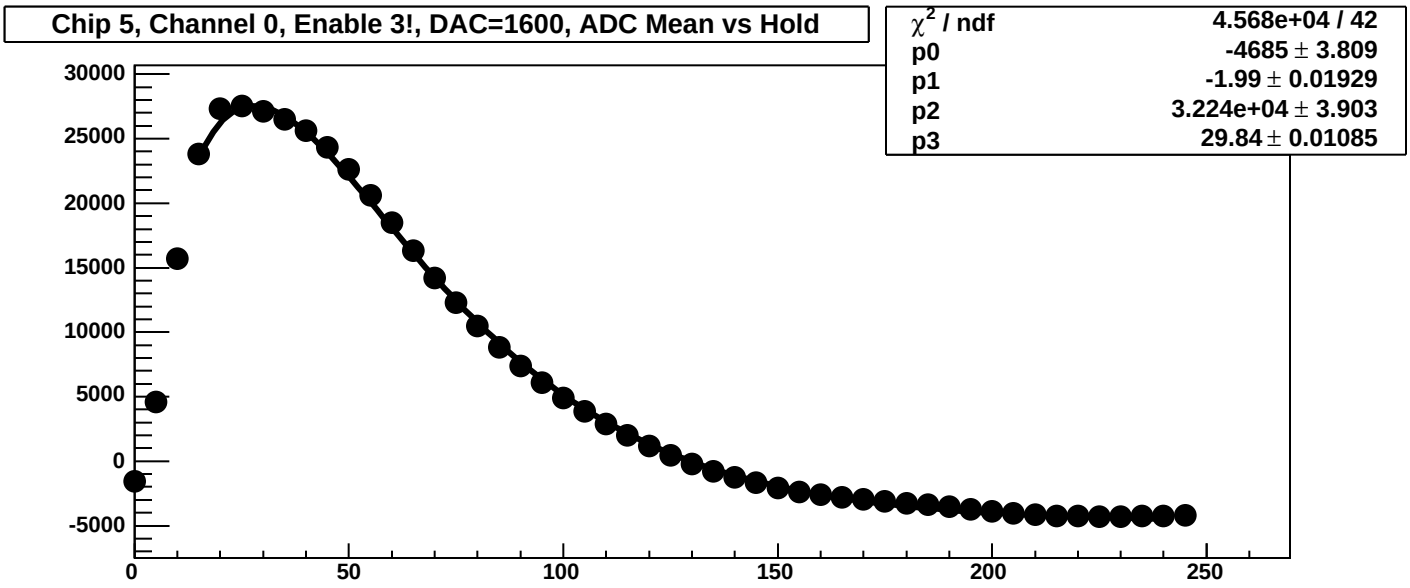
Chip 5, Channel 0, Enable 2, DAC=1600, ADC Noise vs Hold



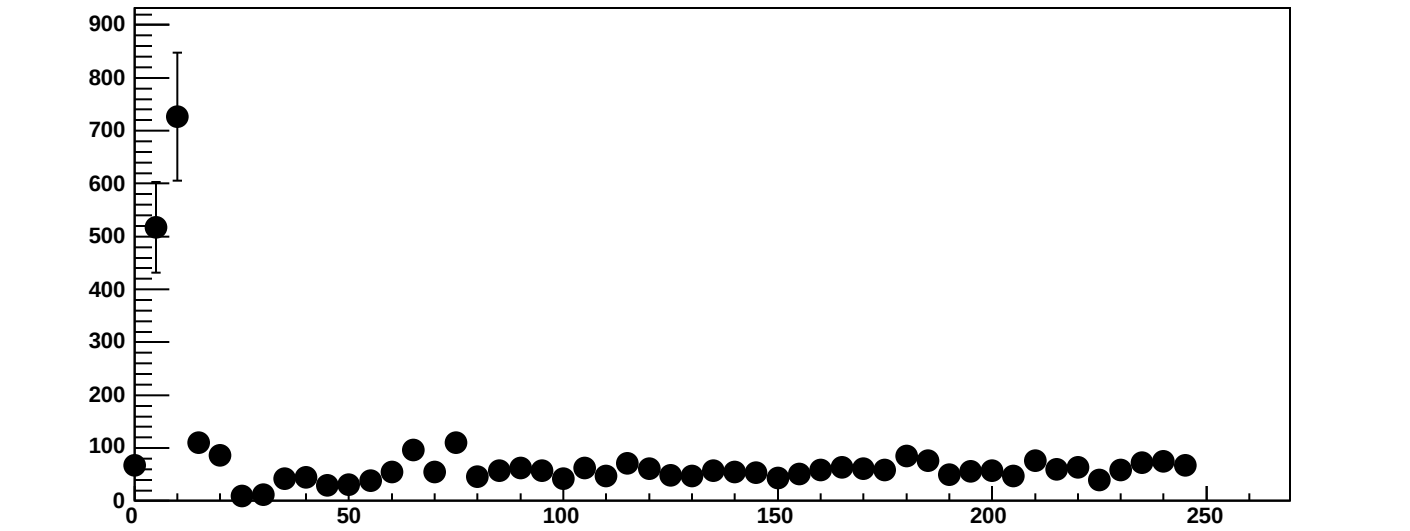
Chip 5, Channel 0, Enable 2, DAC=1600, ADC Residuals vs Hold



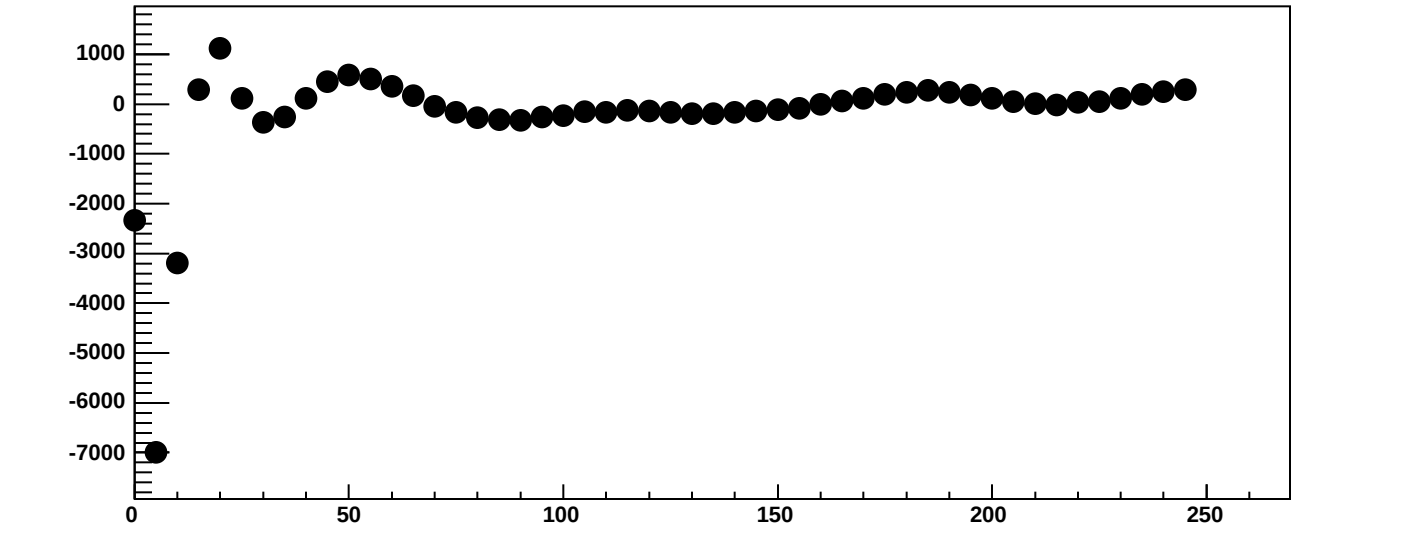
Chip 5, Channel 0, Enable 3!, DAC=1600, ADC Mean vs Hold



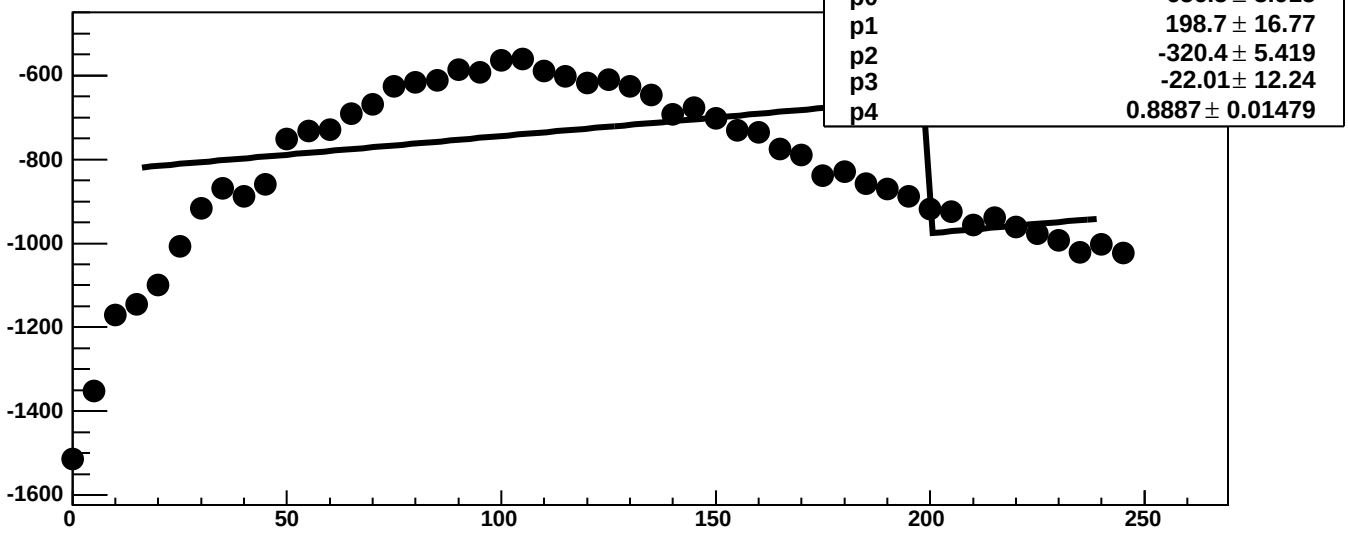
Chip 5, Channel 0, Enable 3!, DAC=1600, ADC Noise vs Hold



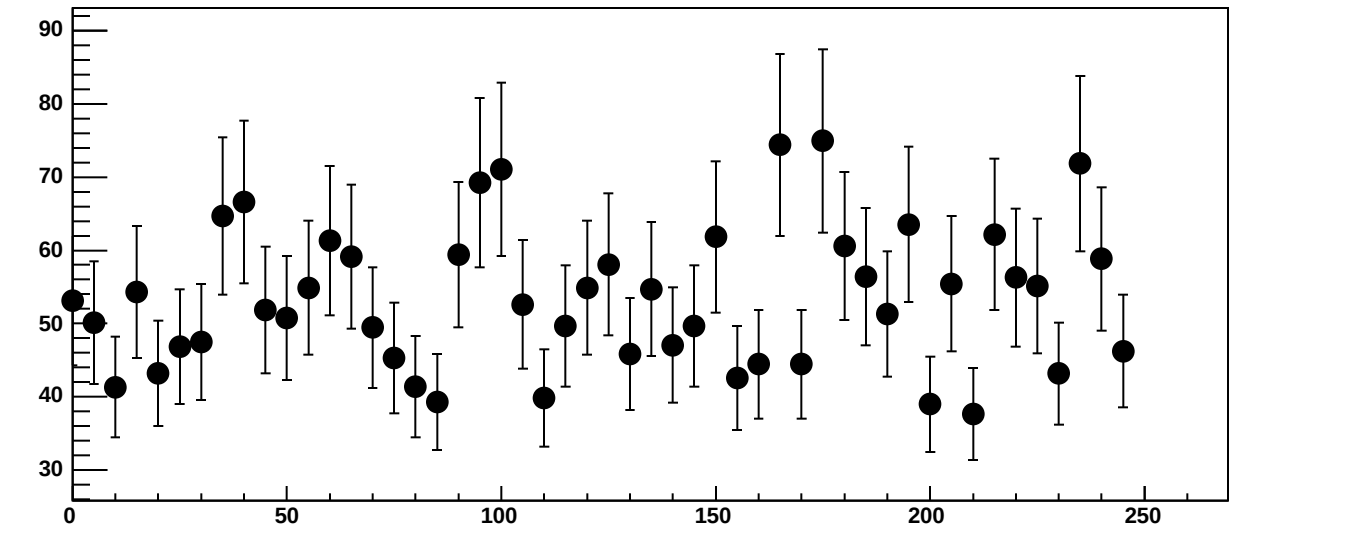
Chip 5, Channel 0, Enable 3!, DAC=1600, ADC Residuals vs Hold



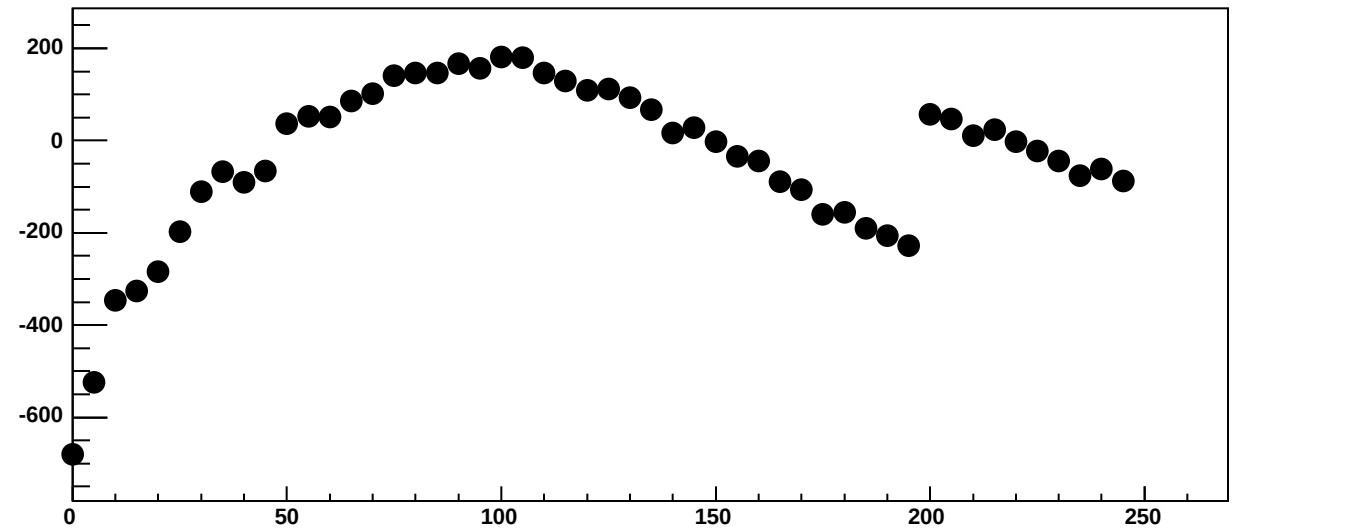
Chip 5, Channel 0, Enable 4, DAC=1600, ADC Mean vs Hold



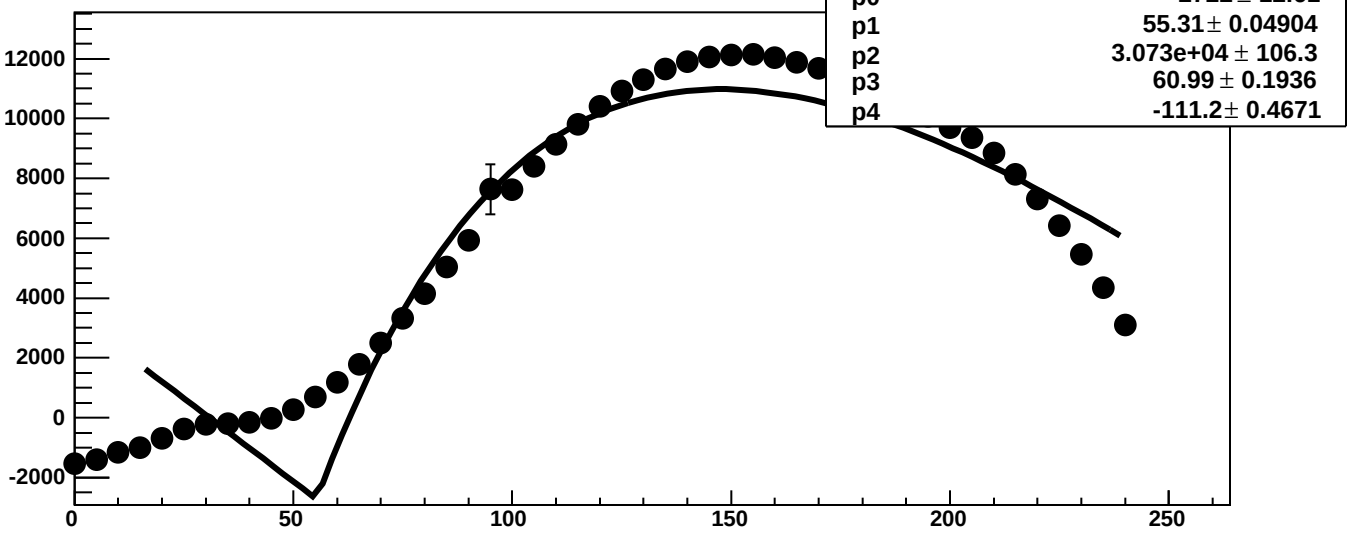
Chip 5, Channel 0, Enable 4, DAC=1600, ADC Noise vs Hold



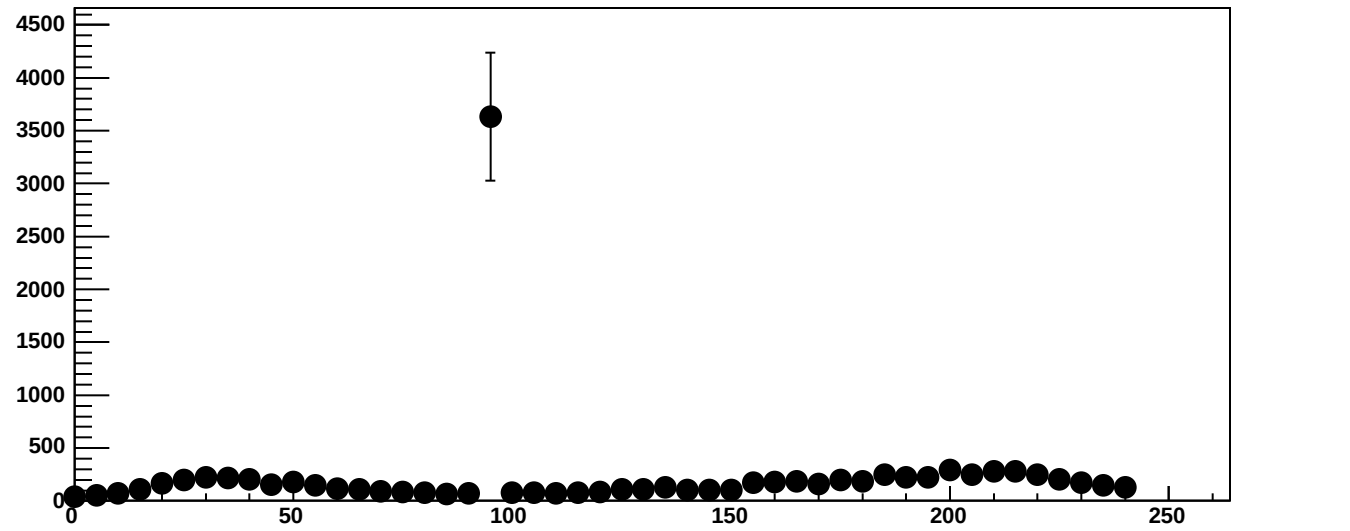
Chip 5, Channel 0, Enable 4, DAC=1600, ADC Residuals vs Hold



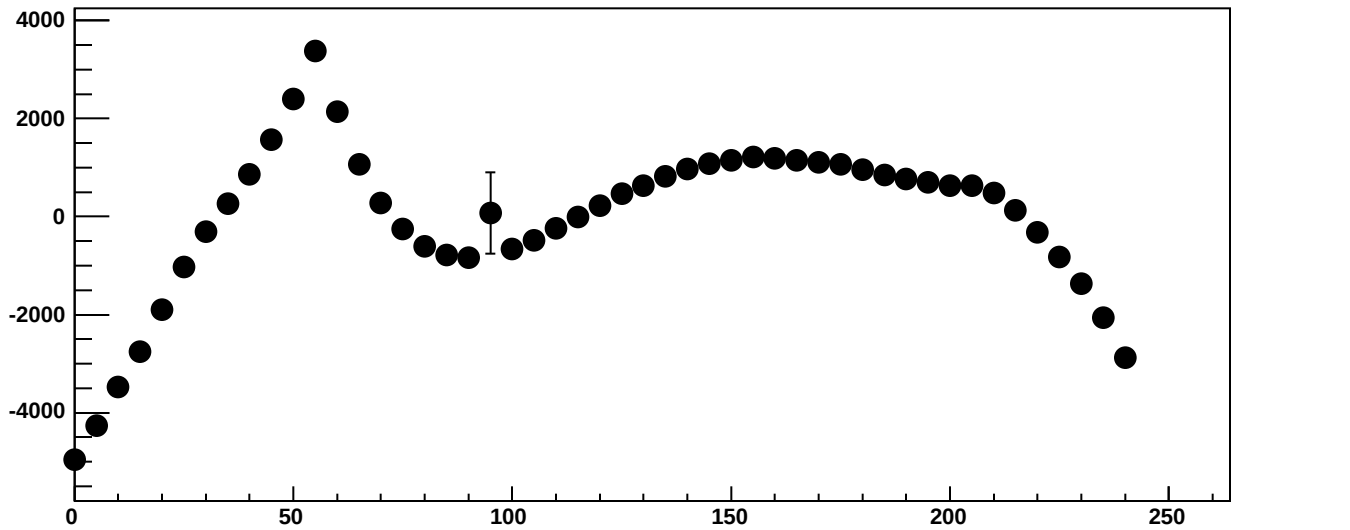
Chip 5, Channel 0, Enable 5, DAC=1600, ADC Mean vs Hold

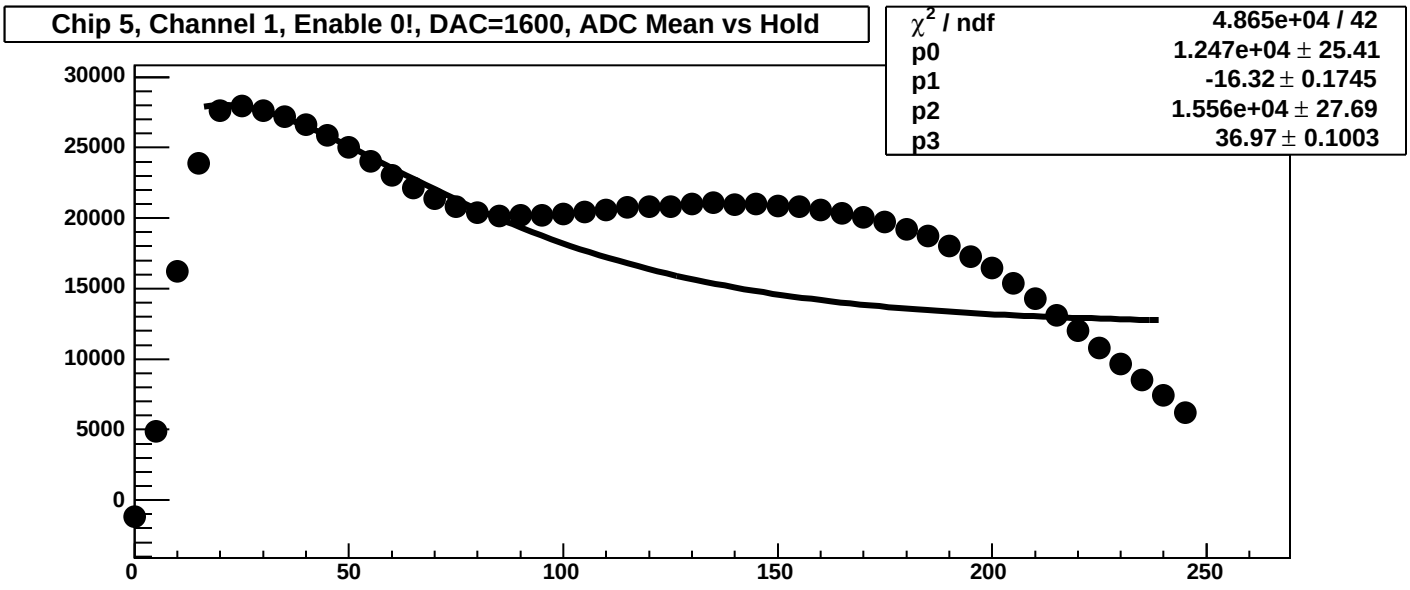


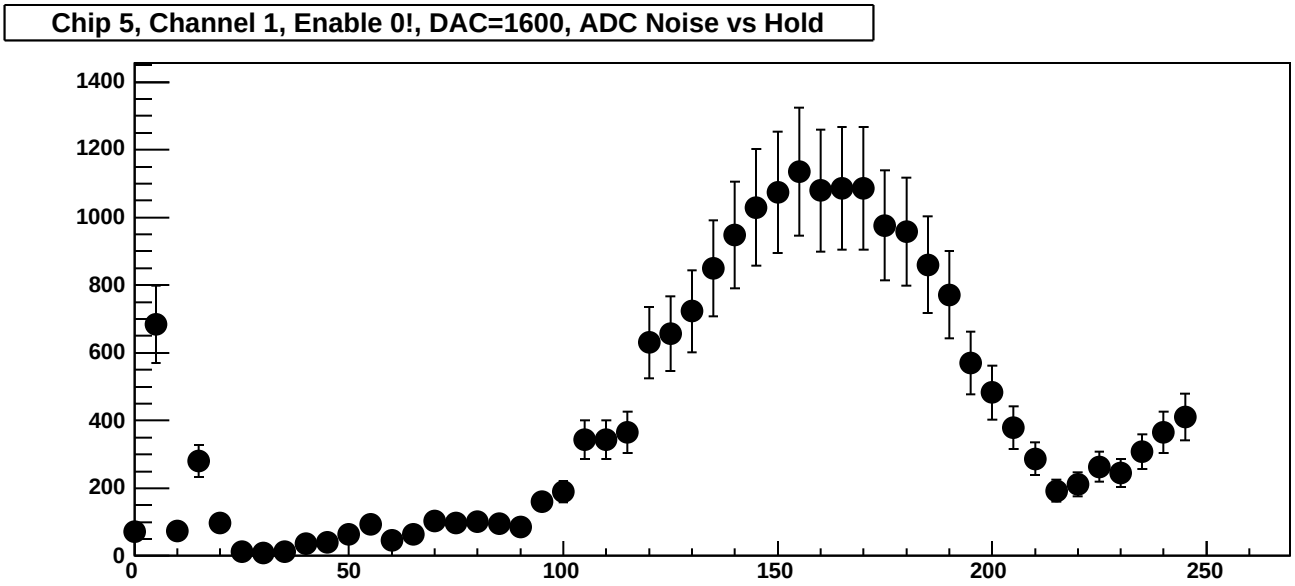
Chip 5, Channel 0, Enable 5, DAC=1600, ADC Noise vs Hold

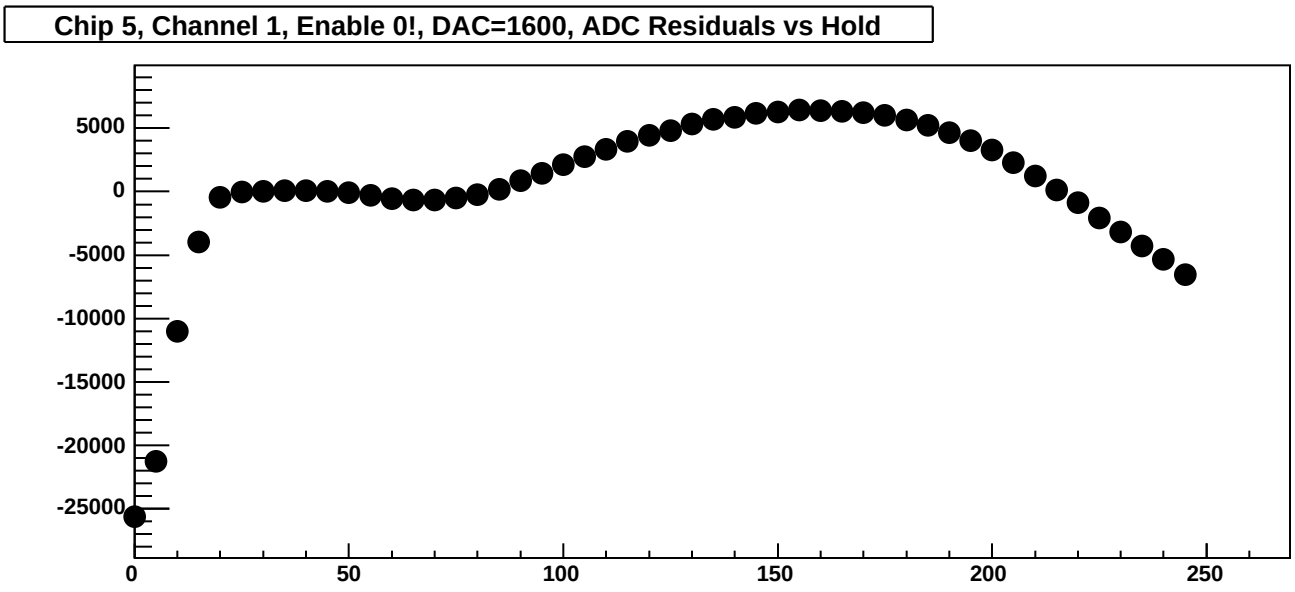


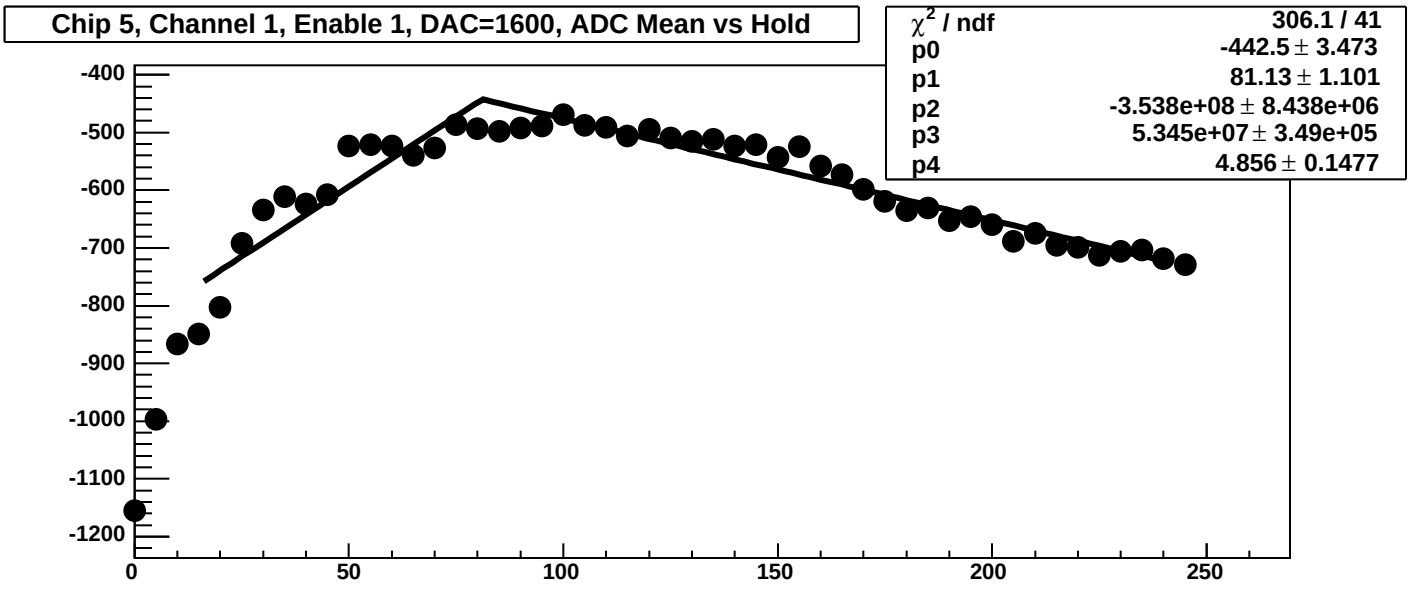
Chip 5, Channel 0, Enable 5, DAC=1600, ADC Residuals vs Hold

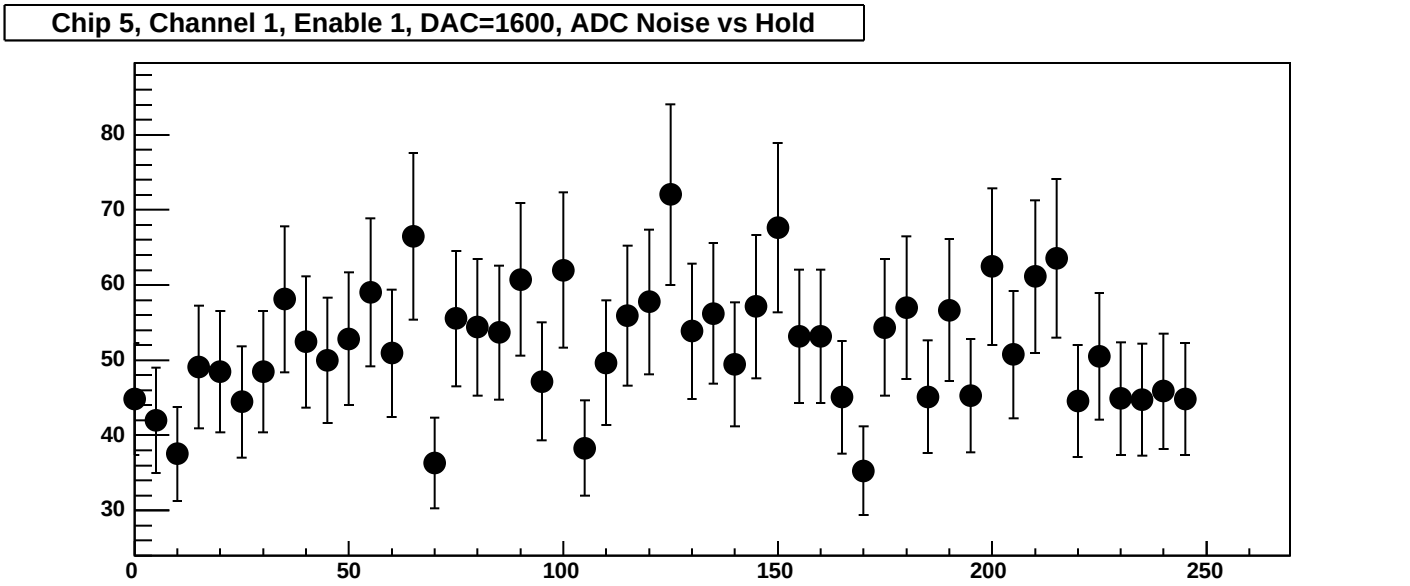


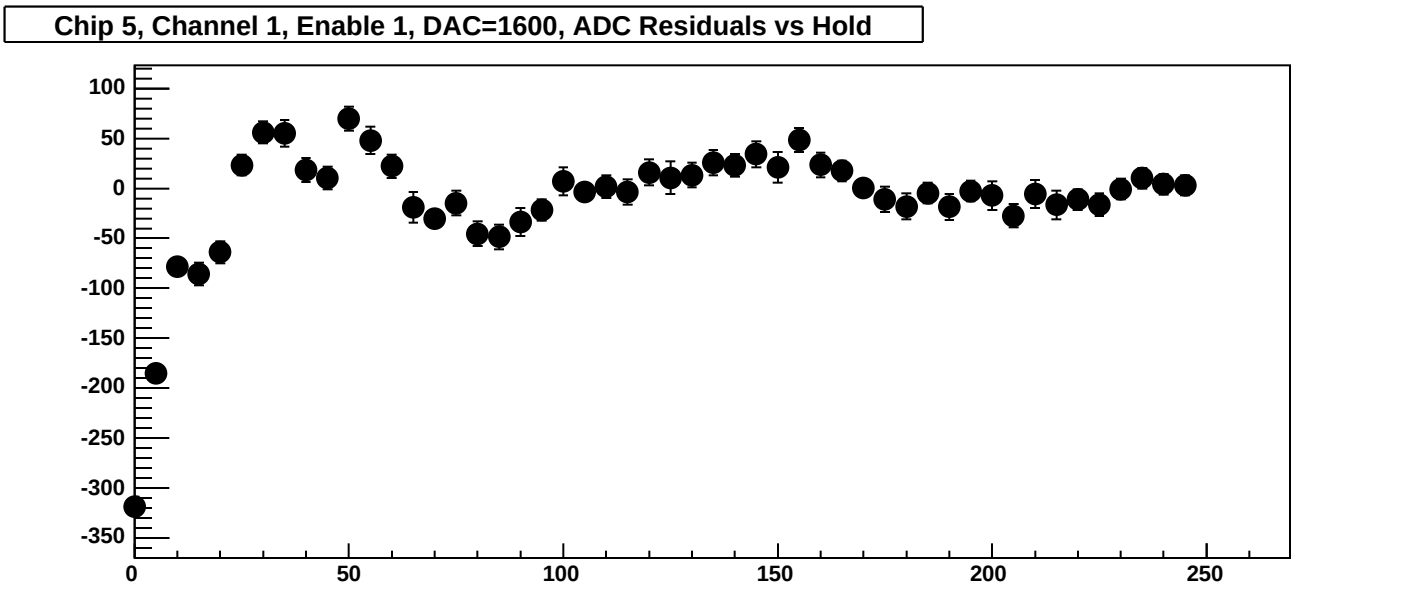

Chip 5, Channel 1, Enable 0!, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.865e+04 / 42
p0 1.247e+04 ± 25.41
p1 -16.32 ± 0.1745
p2 1.556e+04 ± 27.69
p3 36.97 ± 0.1003
30000
25000
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 5, Channel 1, Enable 0!, DAC=1600, ADC Noise vs Hold
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

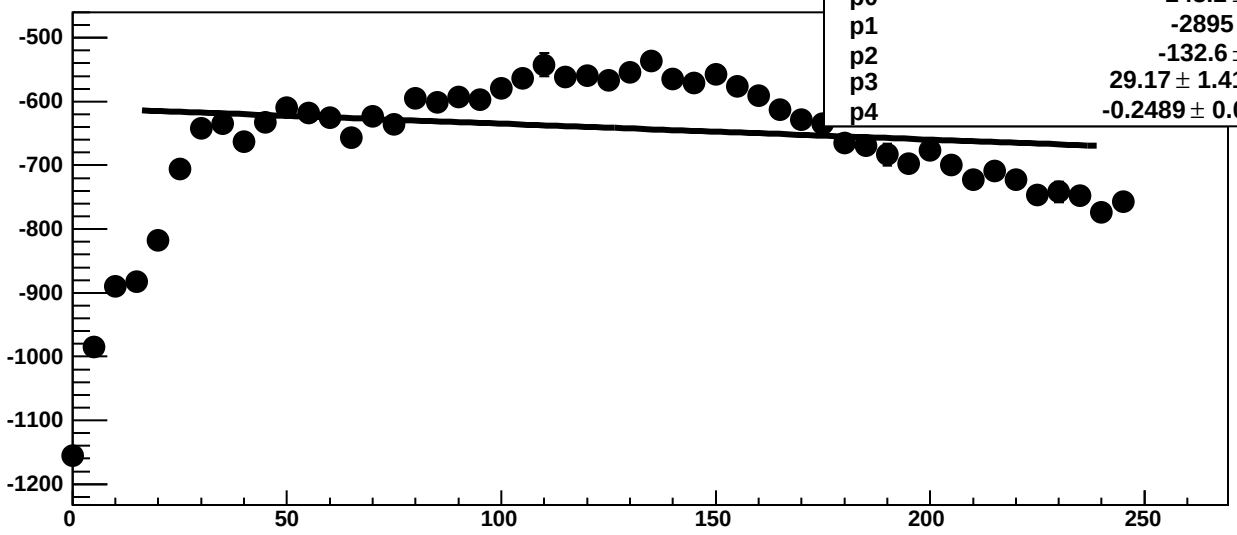

Chip 5, Channel 1, Enable 0!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
0
50
100
150
200
250


Chip 5, Channel 1, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 306.1 / 41
p0 -442.5 ± 3.473
p1 81.13 ± 1.101
p2 -3.538e+08 ± 8.438e+06
p3 5.345e+07 ± 3.49e+05
p4 4.856 ± 0.1477
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
50
100
150
200
250

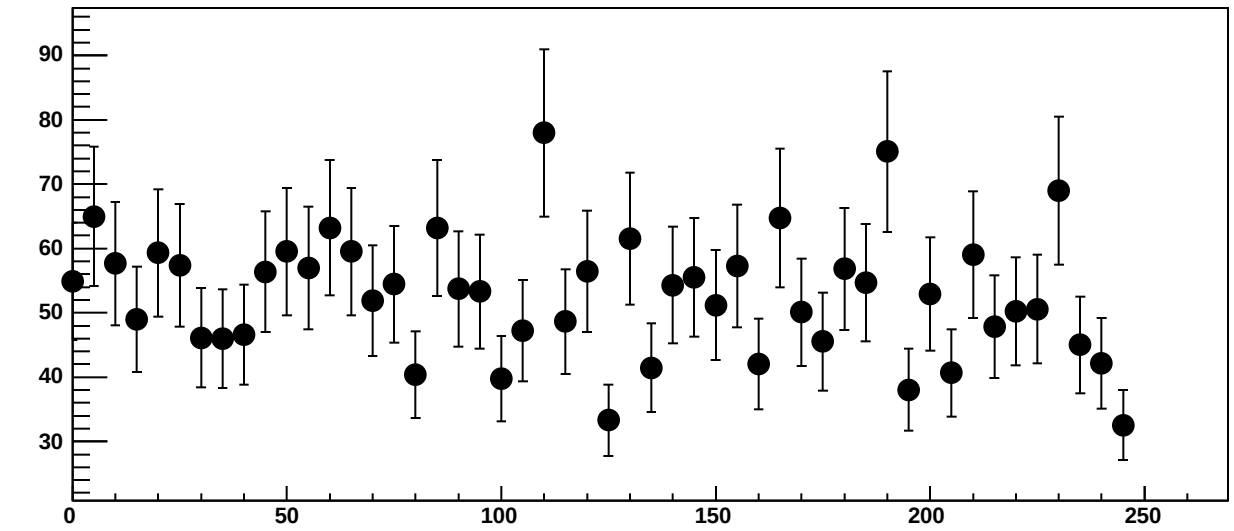

Chip 5, Channel 1, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 5, Channel 1, Enable 1, DAC=1600, ADC Residuals vs Hold
100
50
0
-50
-100
-150
-200
-250
-300
-350
0
50
100
150
200
250

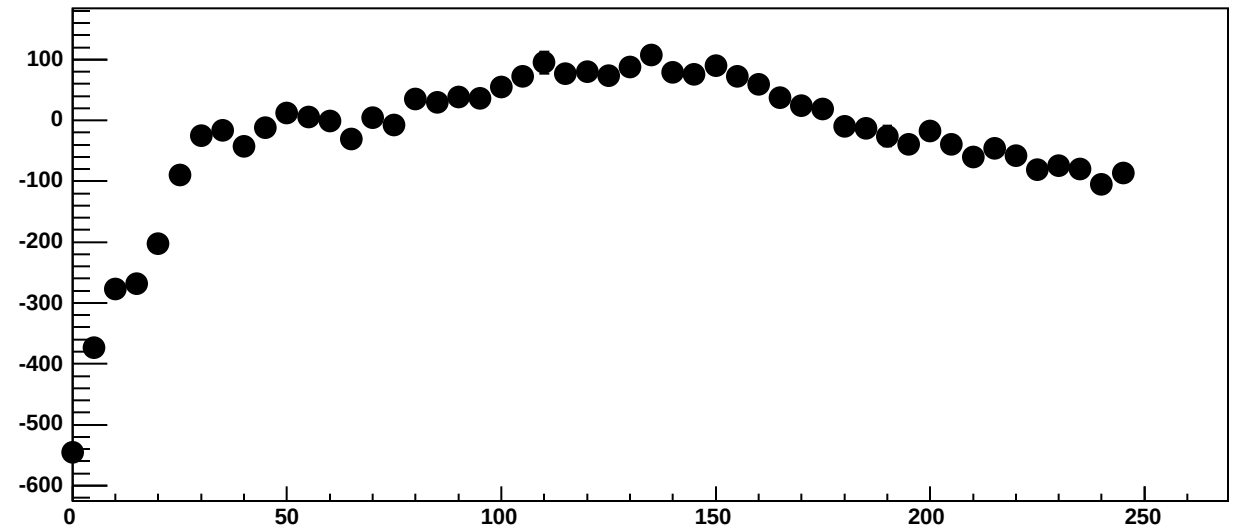
Chip 5, Channel 1, Enable 2, DAC=1600, ADC Mean vs Hold



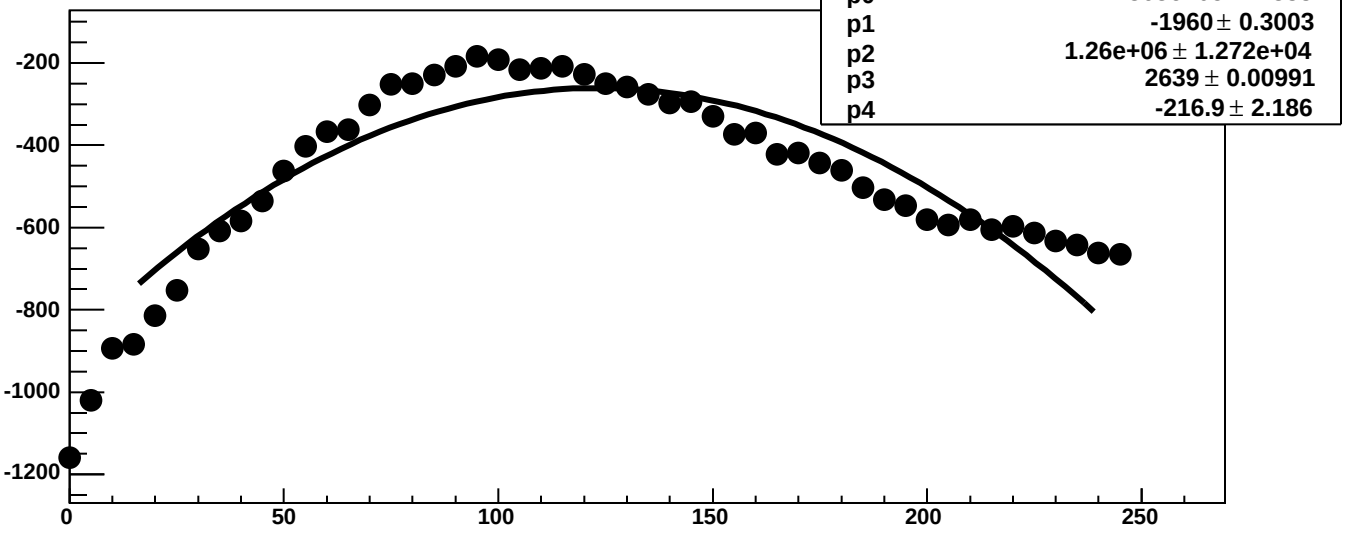
Chip 5, Channel 1, Enable 2, DAC=1600, ADC Noise vs Hold



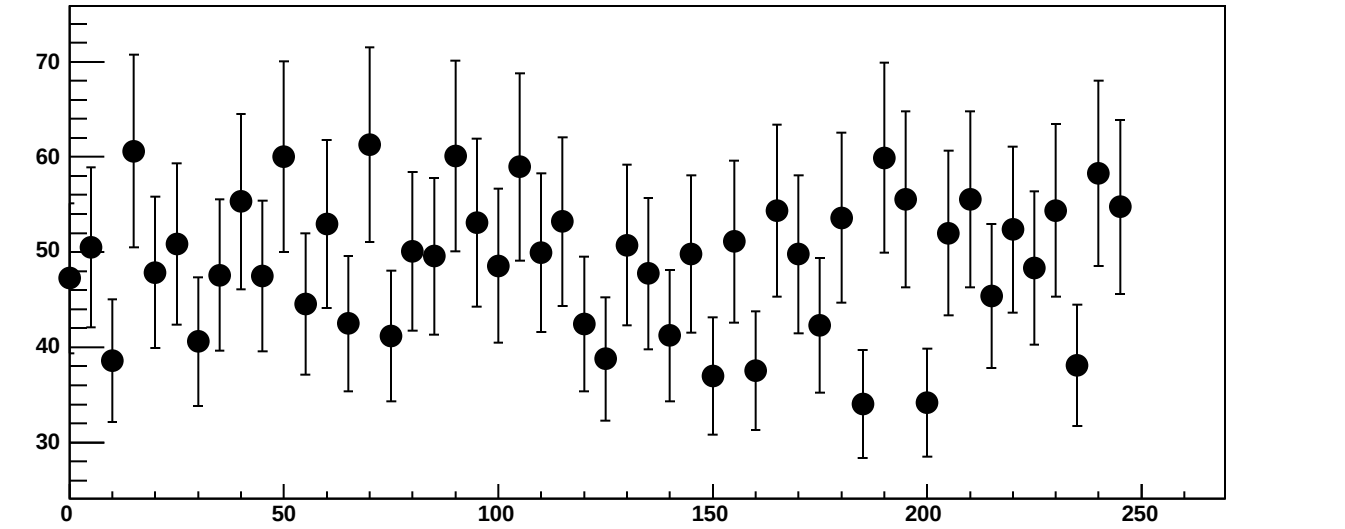
Chip 5, Channel 1, Enable 2, DAC=1600, ADC Residuals vs Hold



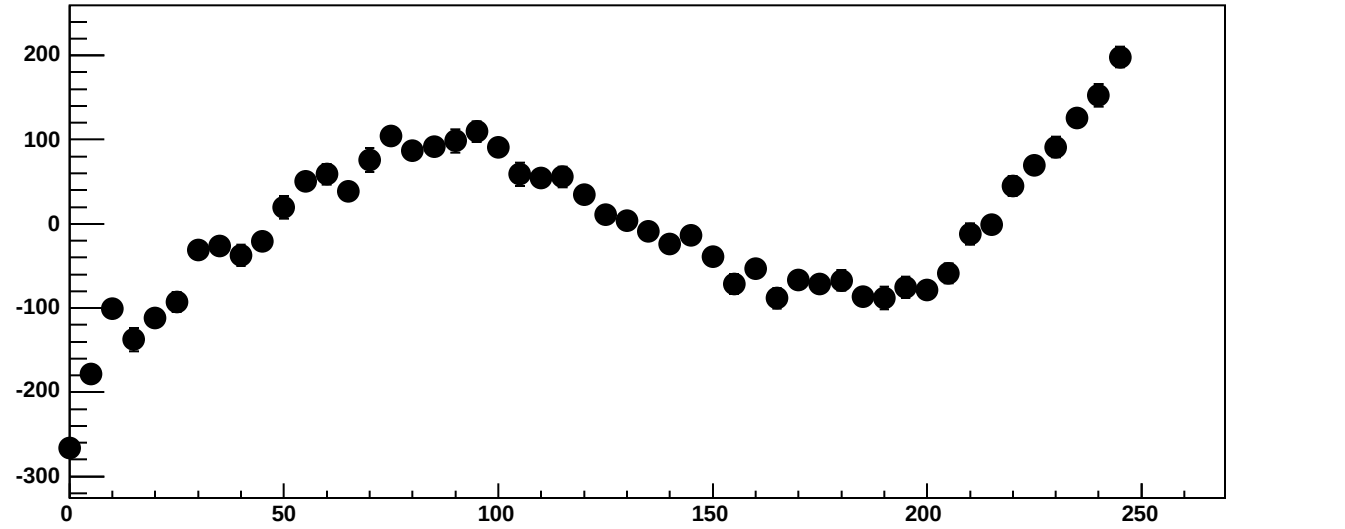
Chip 5, Channel 1, Enable 3, DAC=1600, ADC Mean vs Hold



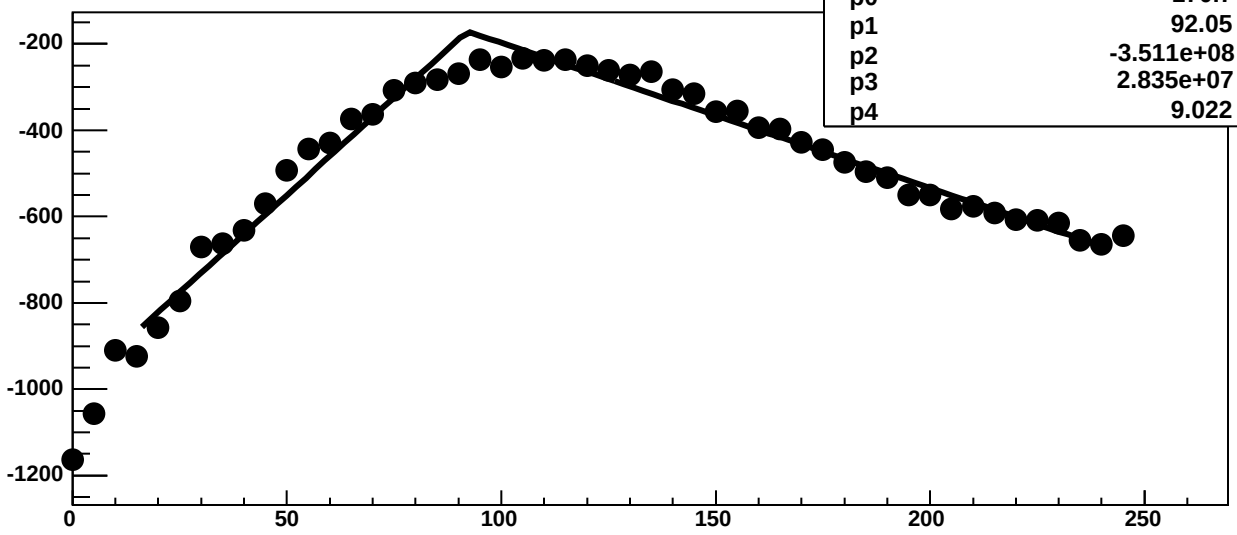
Chip 5, Channel 1, Enable 3, DAC=1600, ADC Noise vs Hold



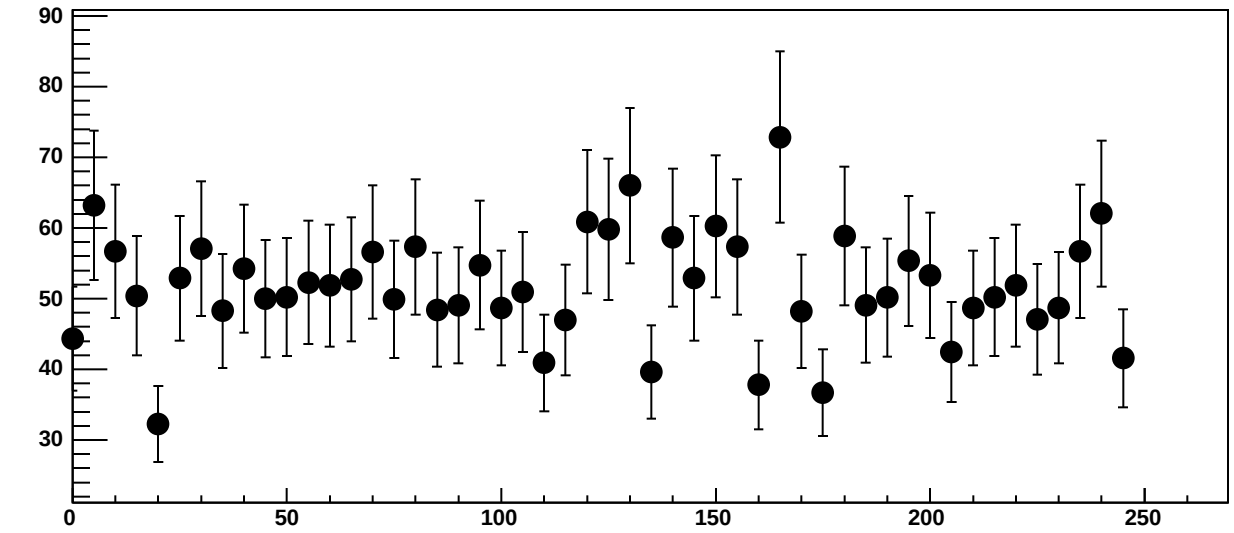
Chip 5, Channel 1, Enable 3, DAC=1600, ADC Residuals vs Hold



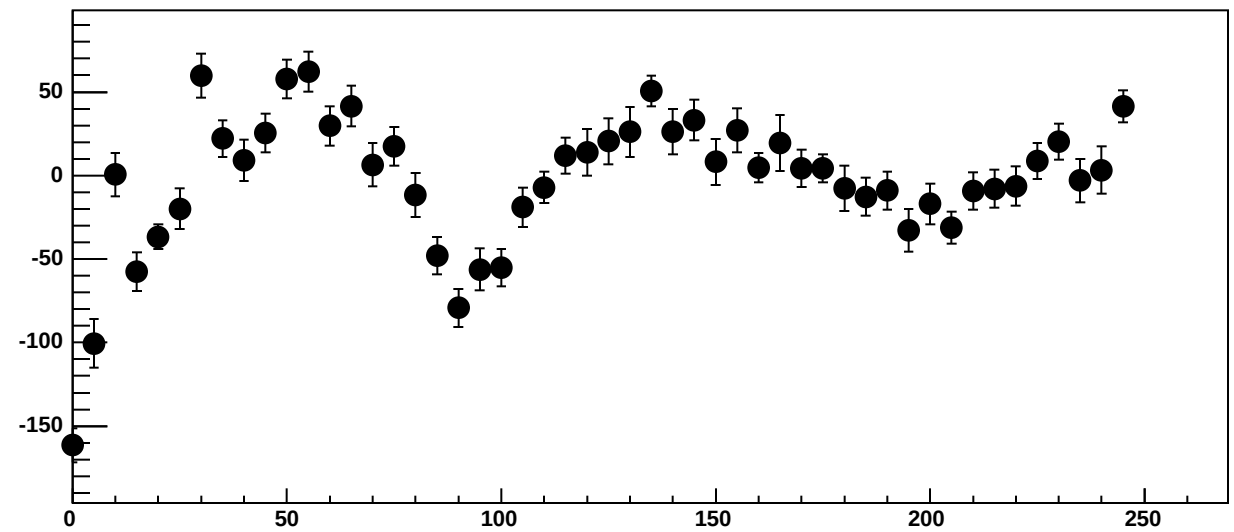
Chip 5, Channel 1, Enable 4, DAC=1600, ADC Mean vs Hold



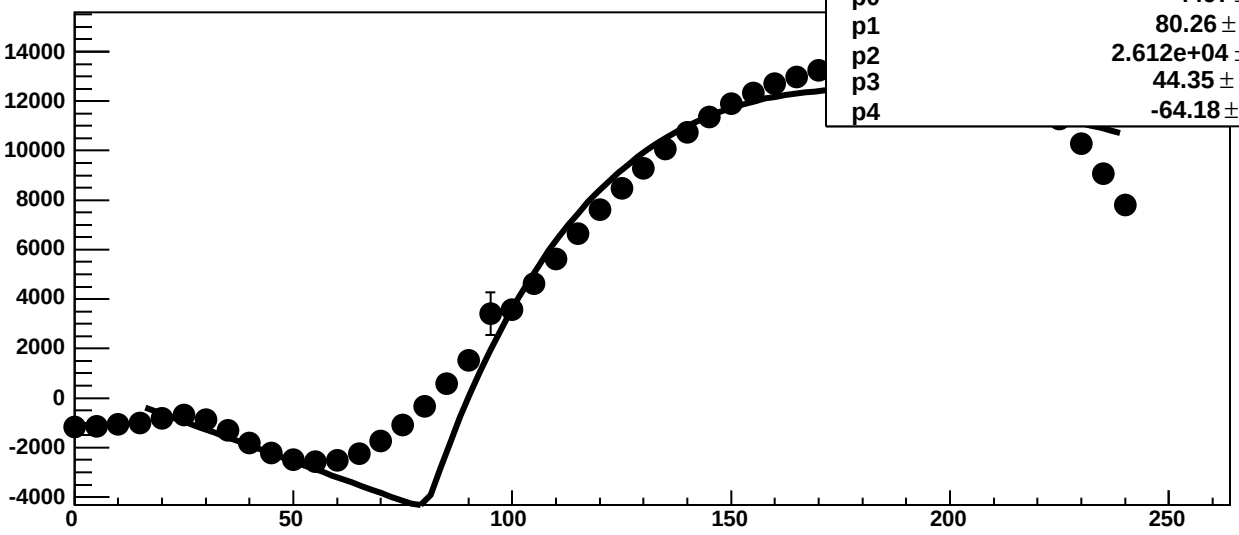
Chip 5, Channel 1, Enable 4, DAC=1600, ADC Noise vs Hold



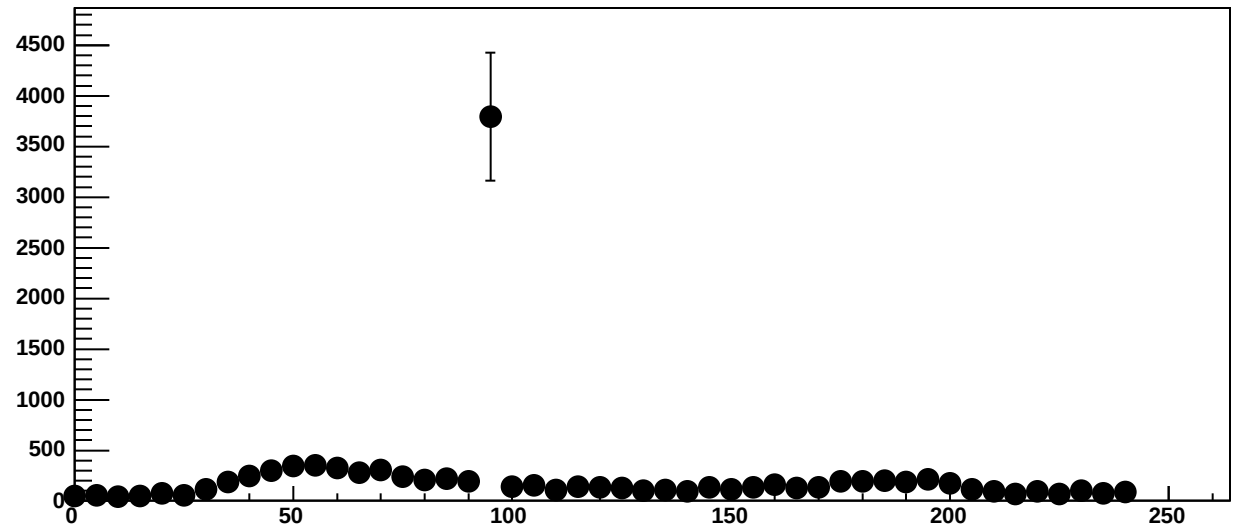
Chip 5, Channel 1, Enable 4, DAC=1600, ADC Residuals vs Hold



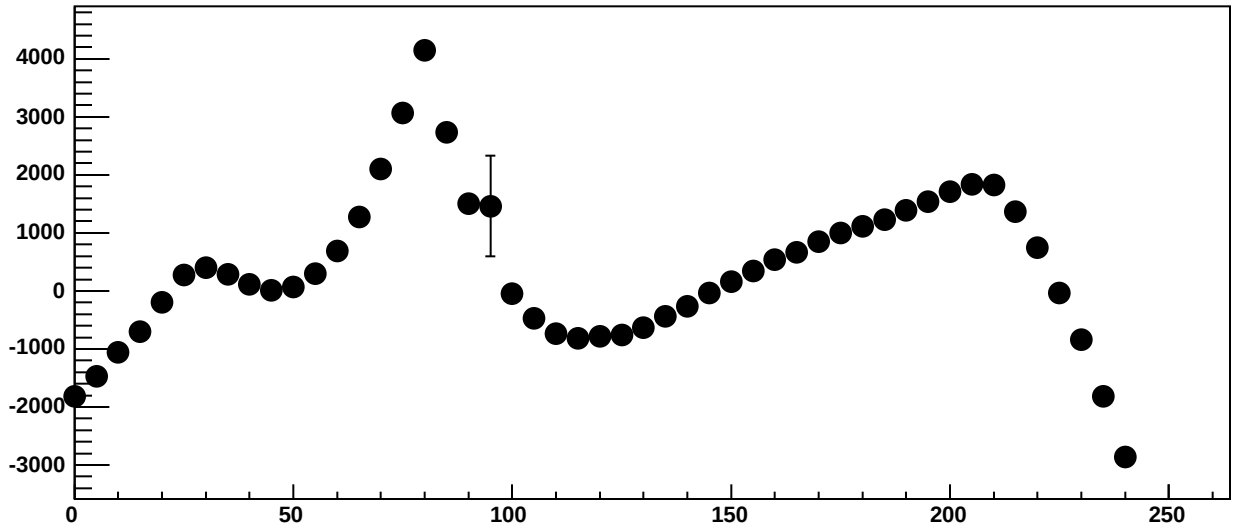
Chip 5, Channel 1, Enable 5, DAC=1600, ADC Mean vs Hold

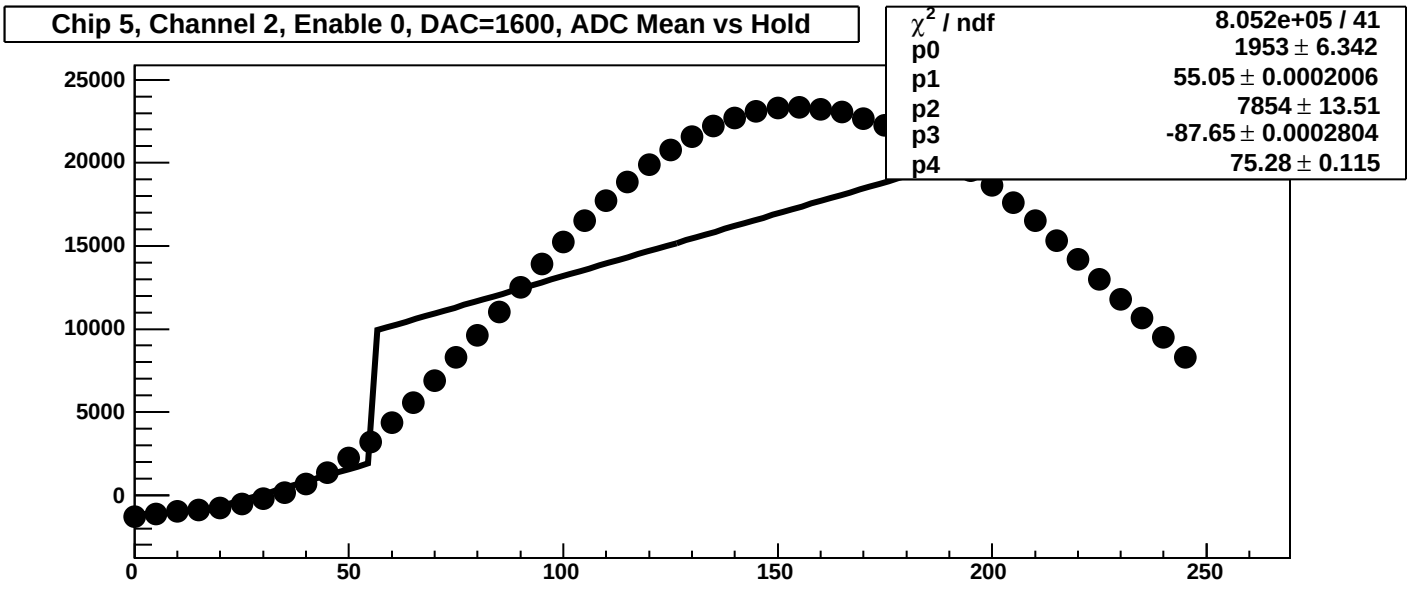


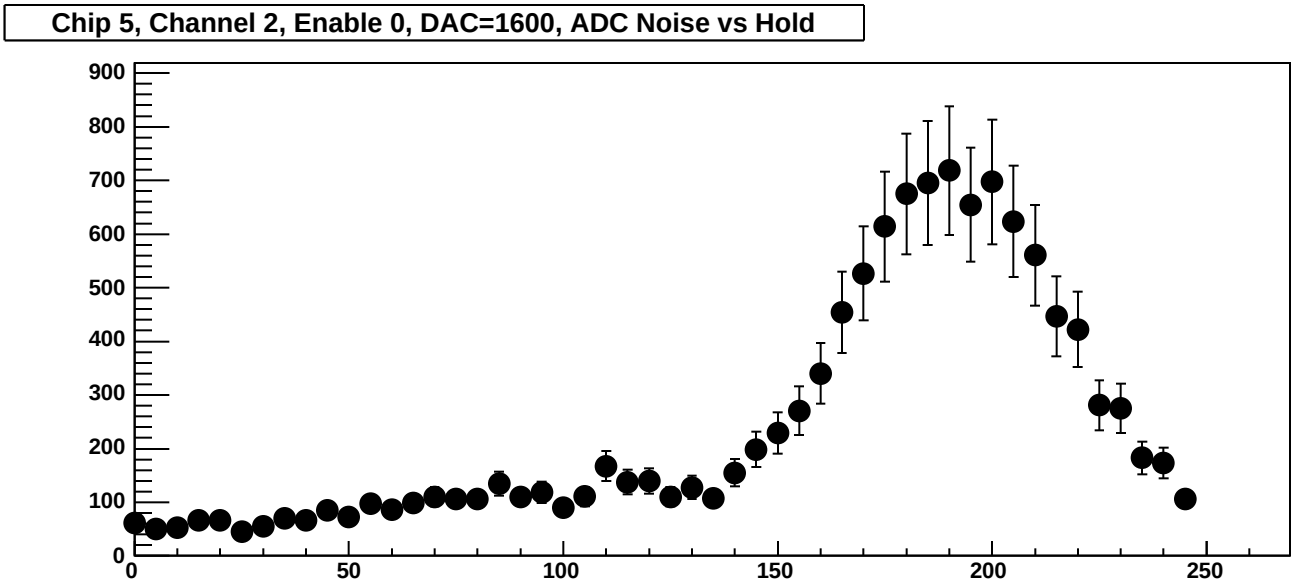
Chip 5, Channel 1, Enable 5, DAC=1600, ADC Noise vs Hold

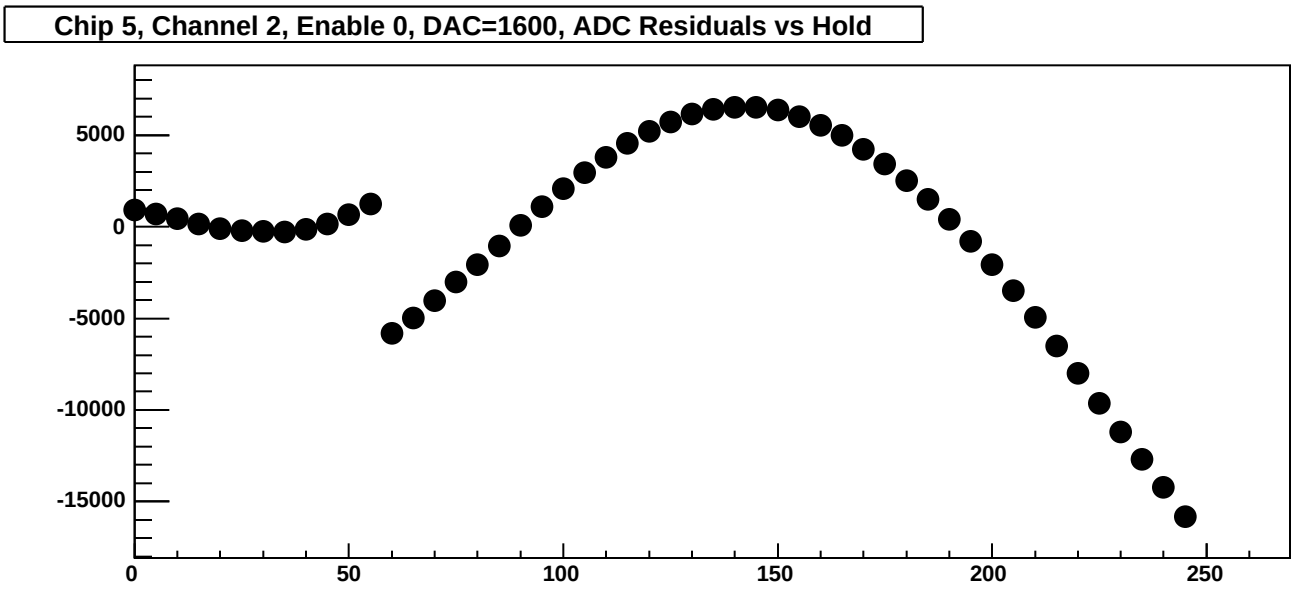


Chip 5, Channel 1, Enable 5, DAC=1600, ADC Residuals vs Hold

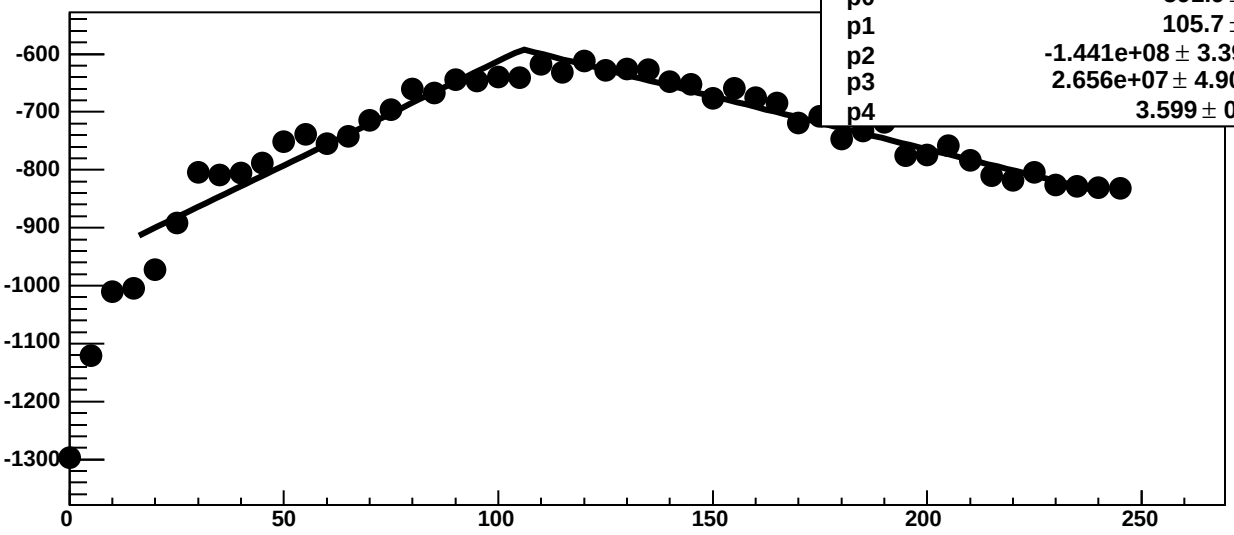



Chip 5, Channel 2, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 8.052e+05 / 41
p0 1953 ± 6.342
p1 55.05 ± 0.0002006
p2 7854 ± 13.51
p3 -87.65 ± 0.0002804
p4 75.28 ± 0.115
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

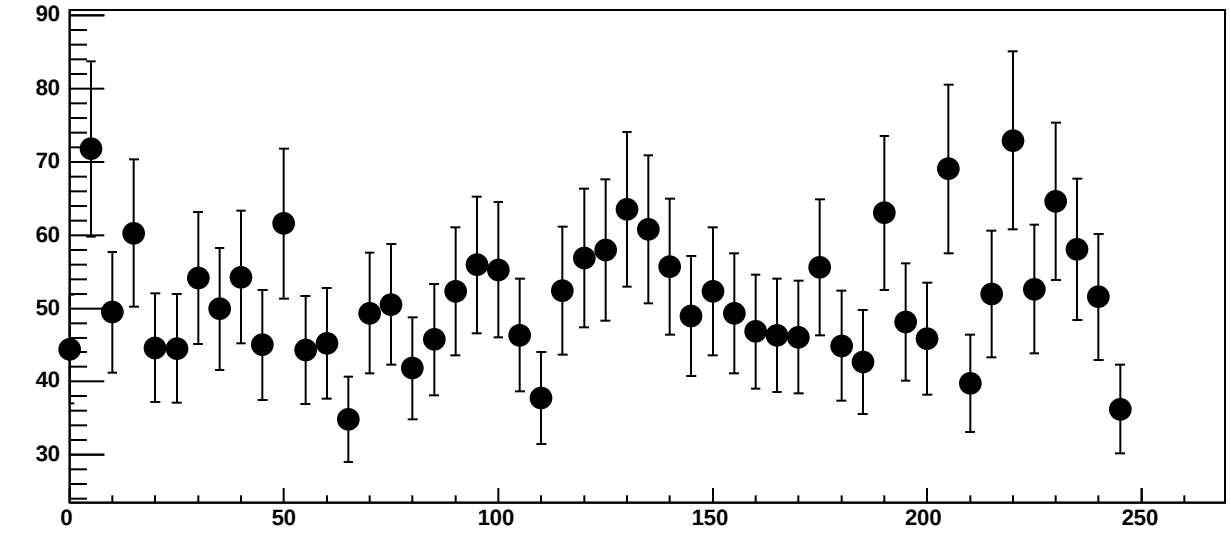

Chip 5, Channel 2, Enable 0, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 5, Channel 2, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
0
50
100
150
200
250

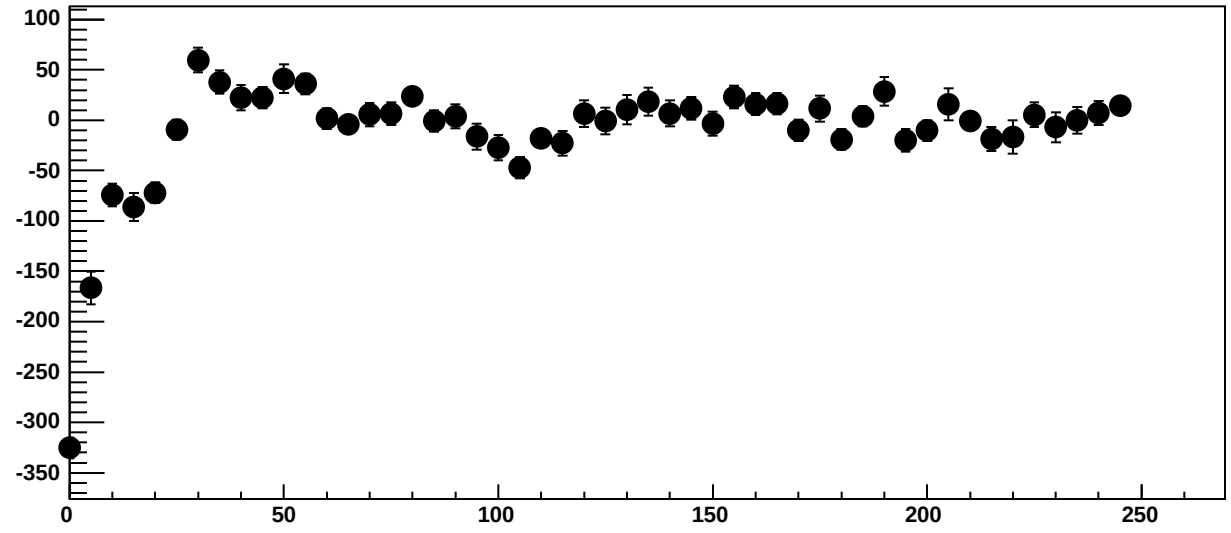
Chip 5, Channel 2, Enable 1, DAC=1600, ADC Mean vs Hold

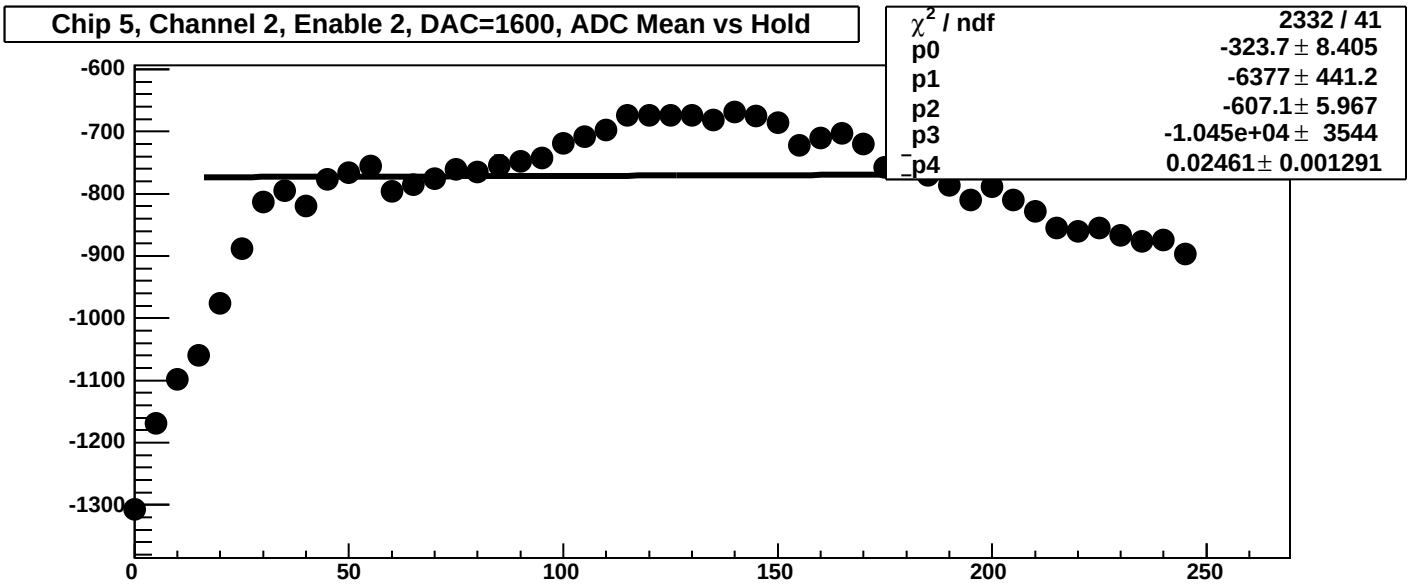


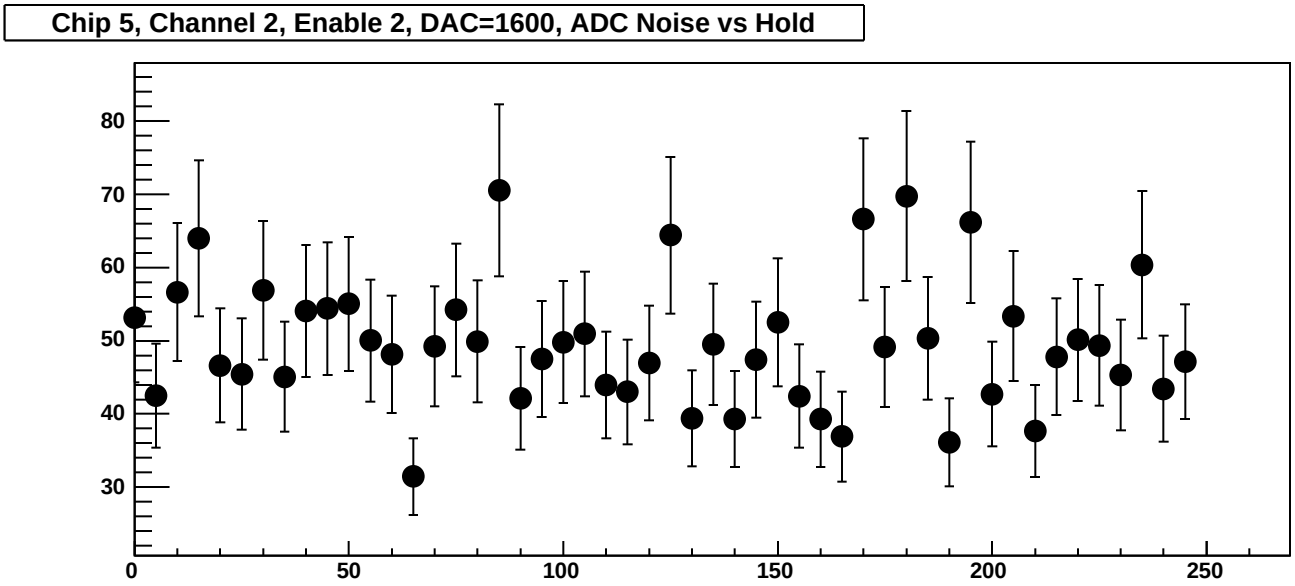
Chip 5, Channel 2, Enable 1, DAC=1600, ADC Noise vs Hold

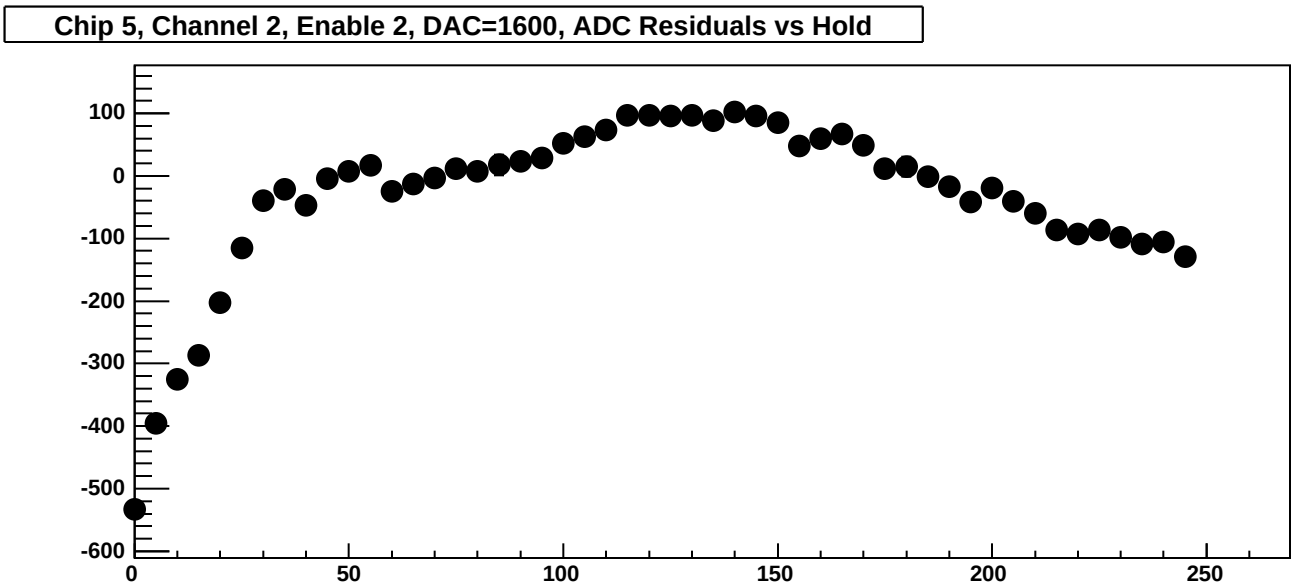


Chip 5, Channel 2, Enable 1, DAC=1600, ADC Residuals vs Hold



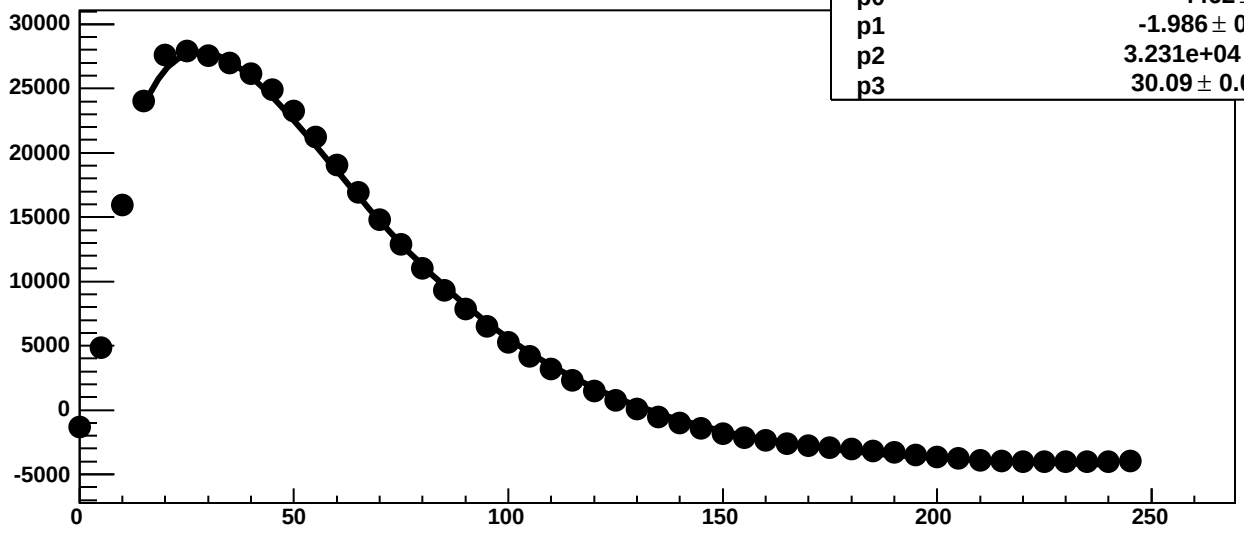

Chip 5, Channel 2, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 2332 / 41
p0 -323.7 ± 8.405
p1 -6377 ± 441.2
p2 -607.1 ± 5.967
p3 -1.045e+04 ± 3544
p4 0.02461 ± 0.001291
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
50
100
150
200
250


Chip 5, Channel 2, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

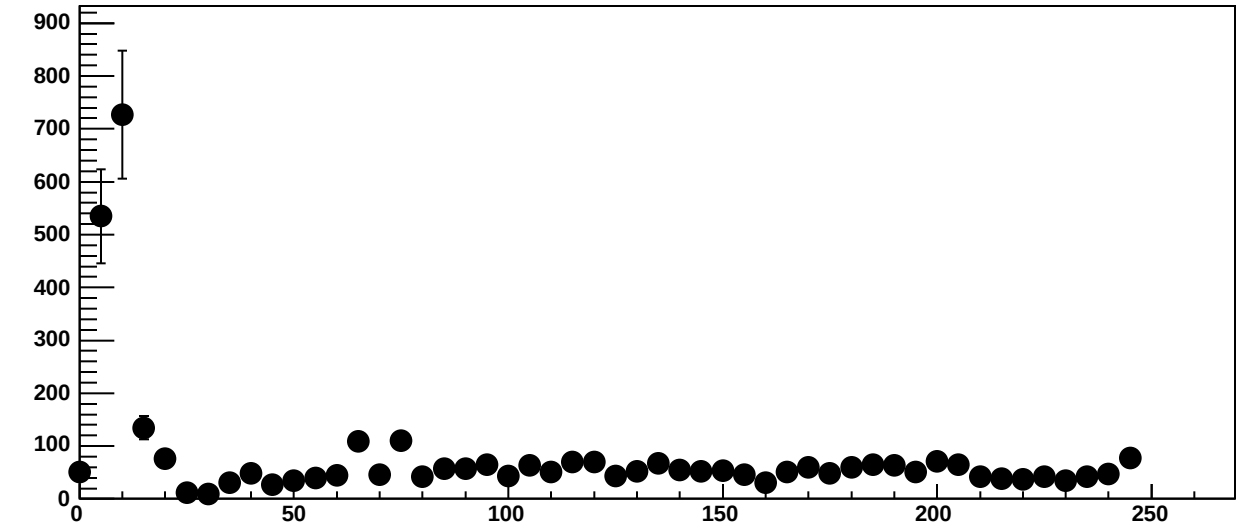

Chip 5, Channel 2, Enable 2, DAC=1600, ADC Residuals vs Hold
100
0
-100
-200
-300
-400
-500
-600
0
50
100
150
200
250

Chip 5, Channel 2, Enable 3!, DAC=1600, ADC Mean vs Hold

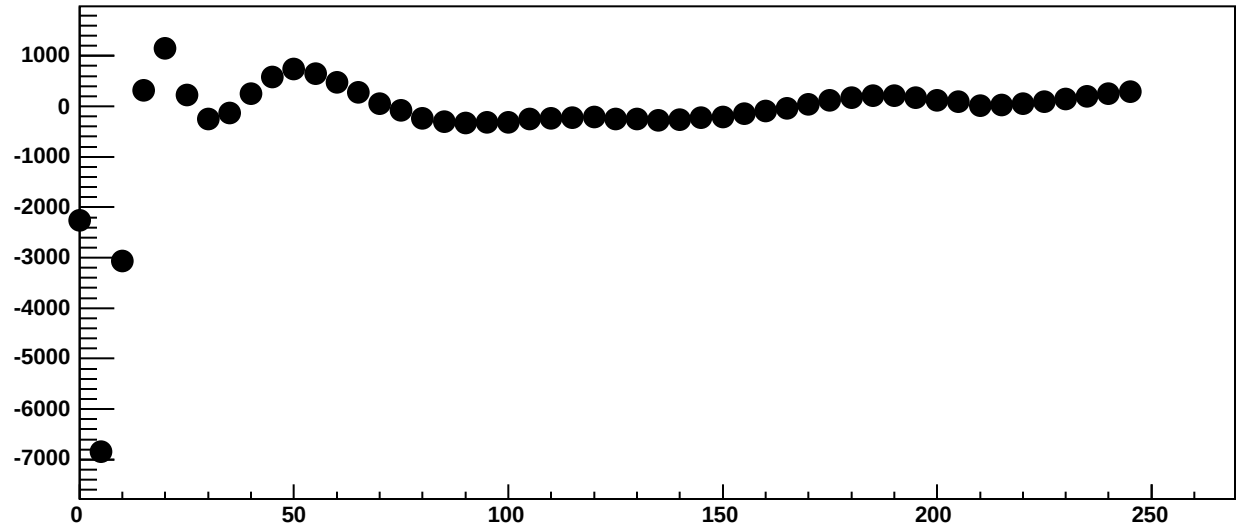
χ^2 / ndf	6.199e+04 / 42
p0	-4462 ± 3.089
p1	-1.986 ± 0.01843
p2	3.231e+04 ± 3.35
p3	30.09 ± 0.009909



Chip 5, Channel 2, Enable 3!, DAC=1600, ADC Noise vs Hold

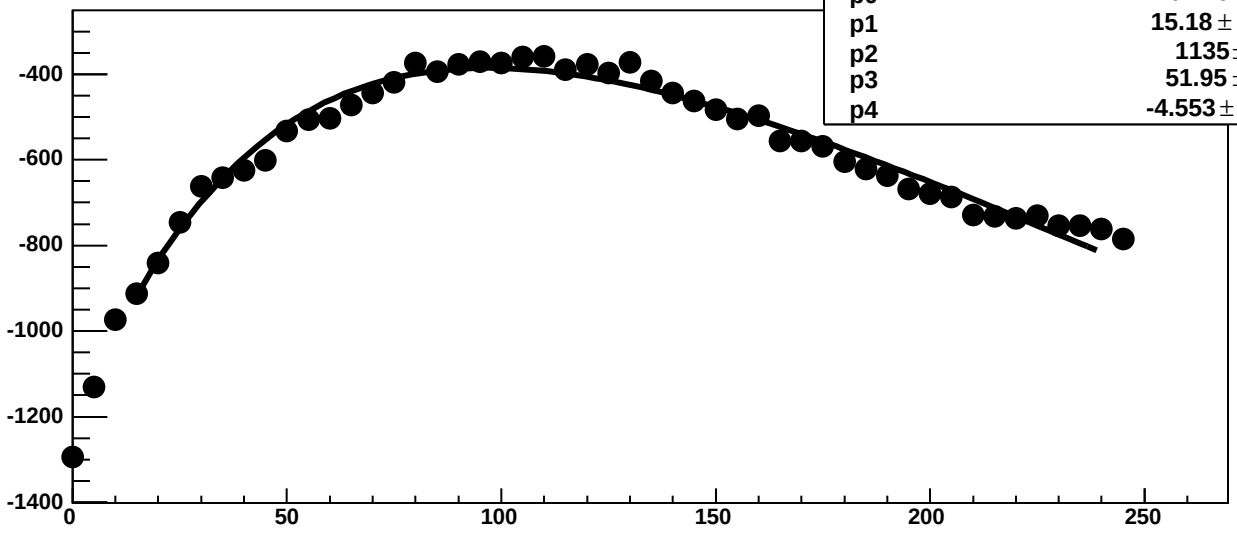


Chip 5, Channel 2, Enable 3!, DAC=1600, ADC Residuals vs Hold

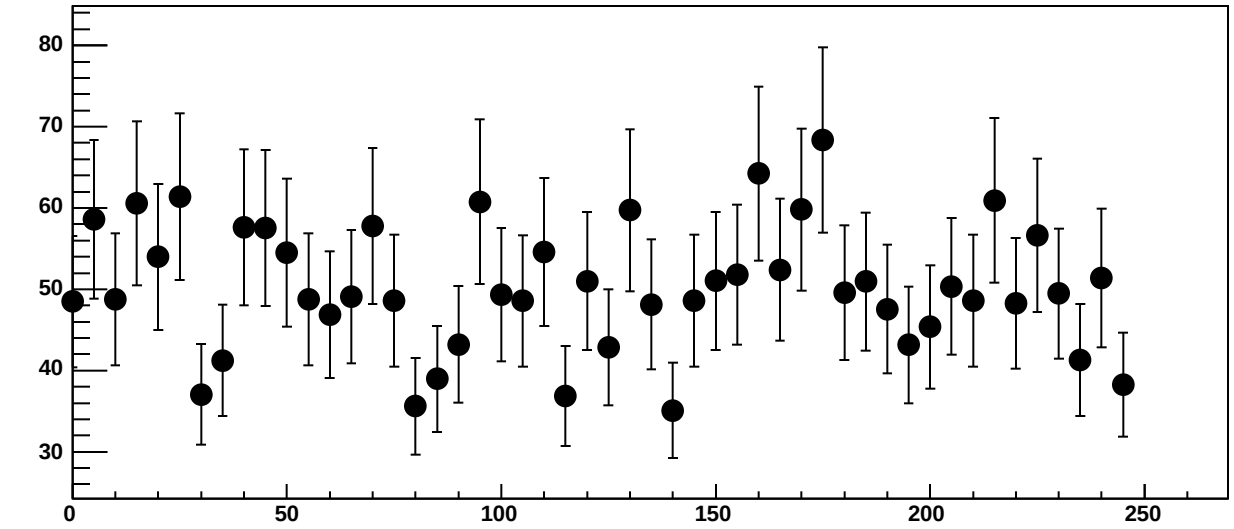


Chip 5, Channel 2, Enable 4, DAC=1600, ADC Mean vs Hold

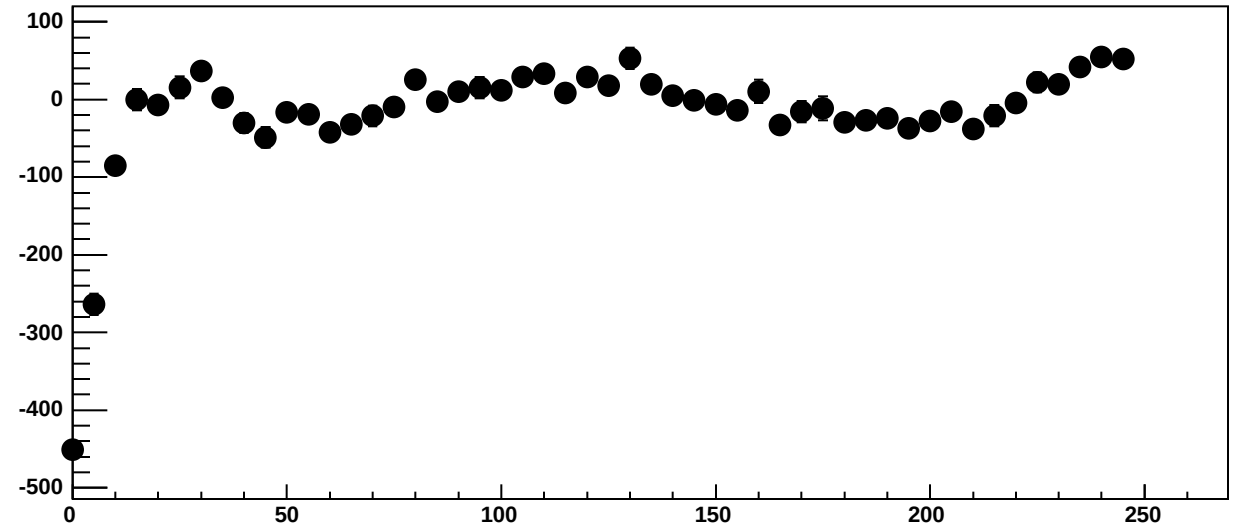
χ^2 / ndf	237.7 / 41
p0	-912.9 ± 11.18
p1	15.18 ± 0.7785
p2	1135 ± 34.24
p3	51.95 ± 2.069
p4	-4.553 ± 0.1422



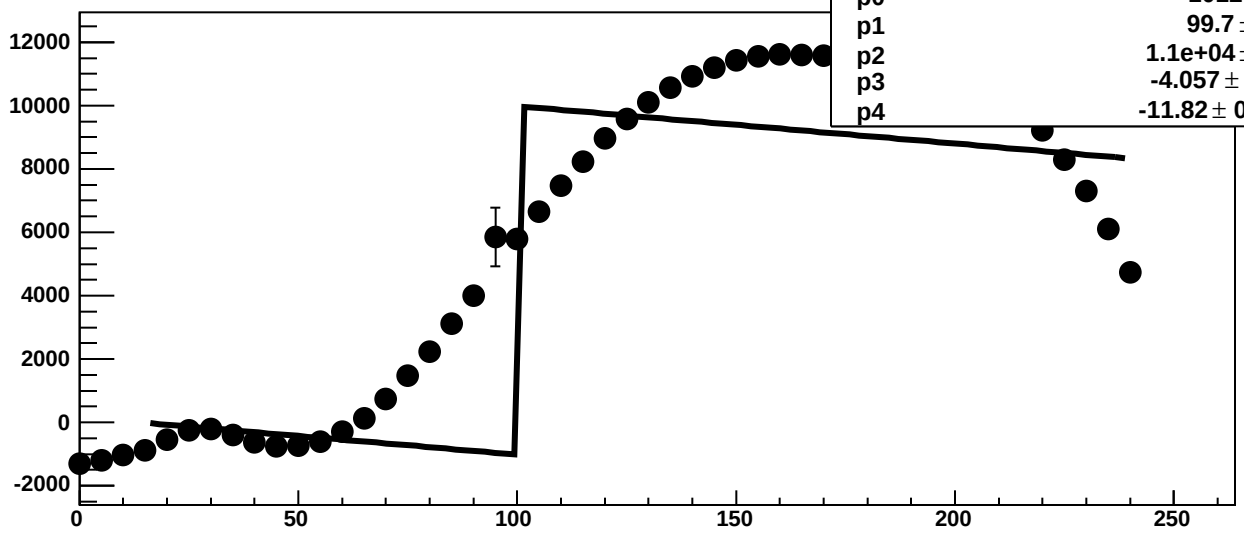
Chip 5, Channel 2, Enable 4, DAC=1600, ADC Noise vs Hold



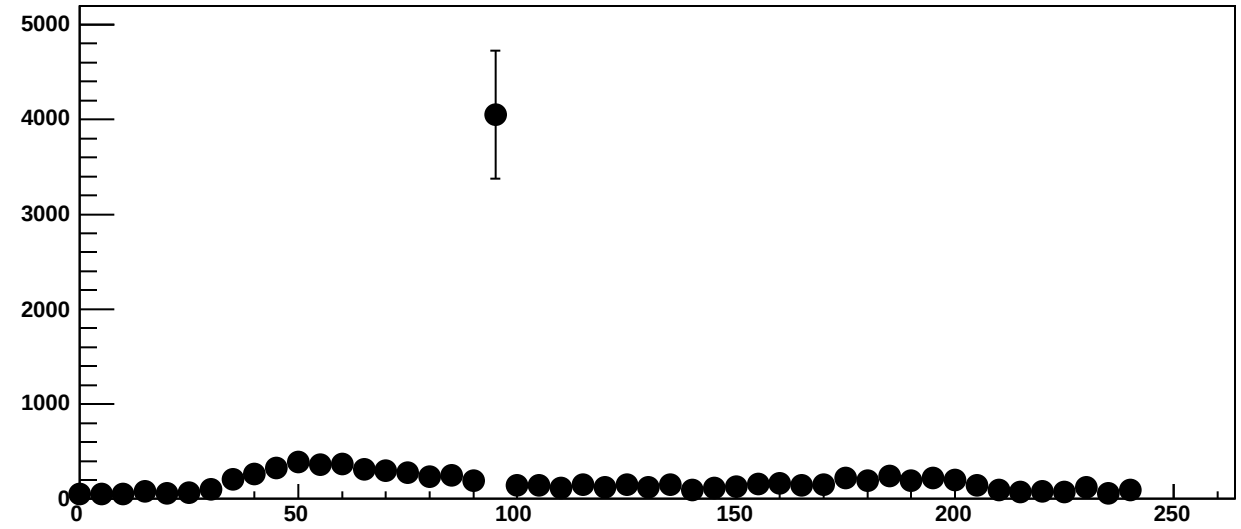
Chip 5, Channel 2, Enable 4, DAC=1600, ADC Residuals vs Hold



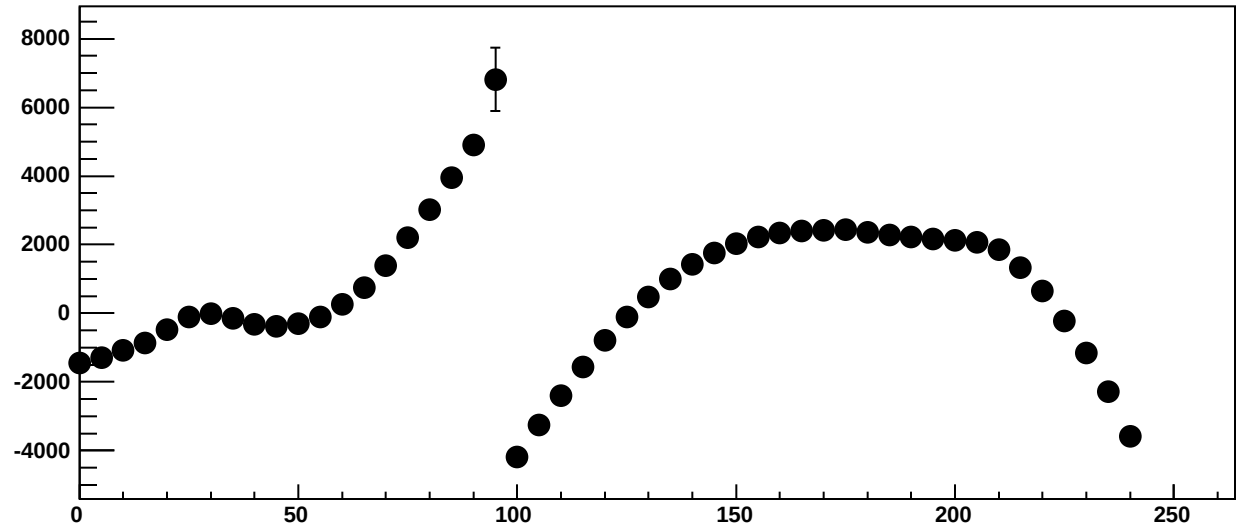
Chip 5, Channel 2, Enable 5, DAC=1600, ADC Mean vs Hold



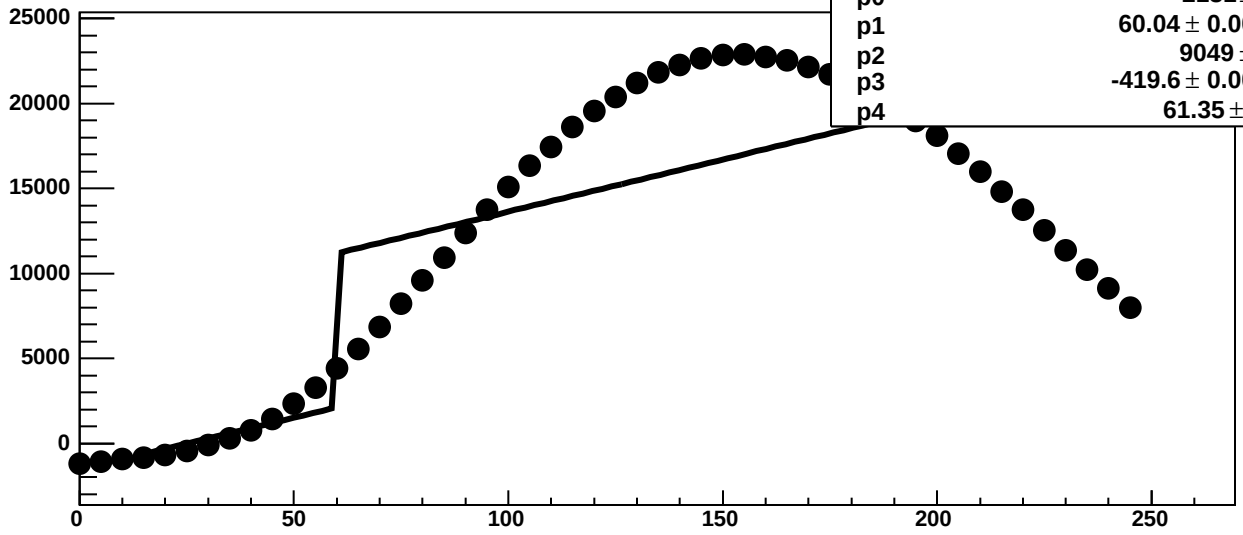
Chip 5, Channel 2, Enable 5, DAC=1600, ADC Noise vs Hold



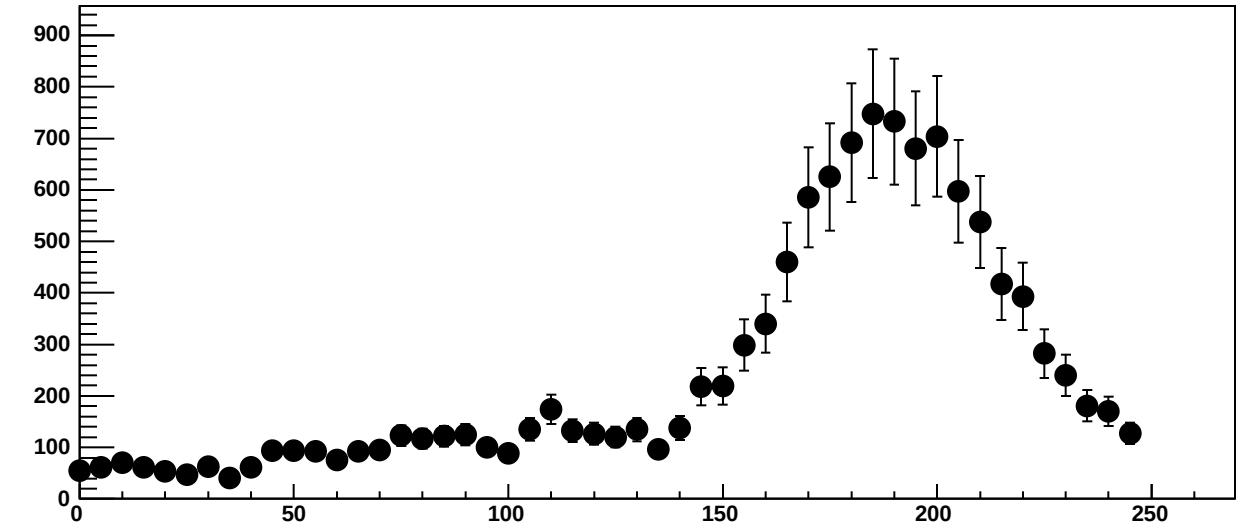
Chip 5, Channel 2, Enable 5, DAC=1600, ADC Residuals vs Hold



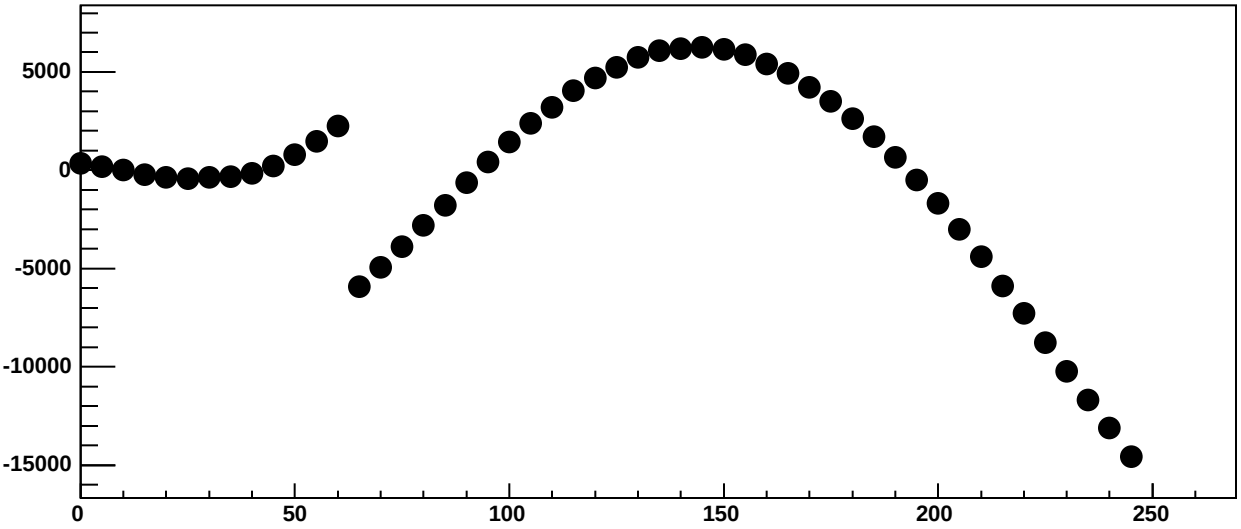
Chip 5, Channel 3, Enable 0, DAC=1600, ADC Mean vs Hold



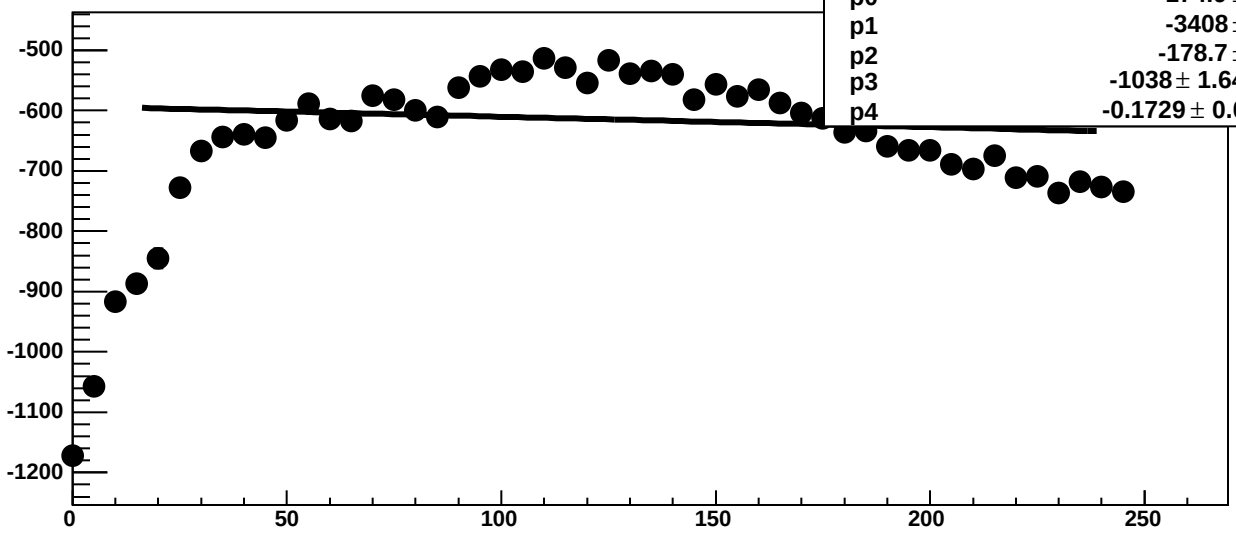
Chip 5, Channel 3, Enable 0, DAC=1600, ADC Noise vs Hold



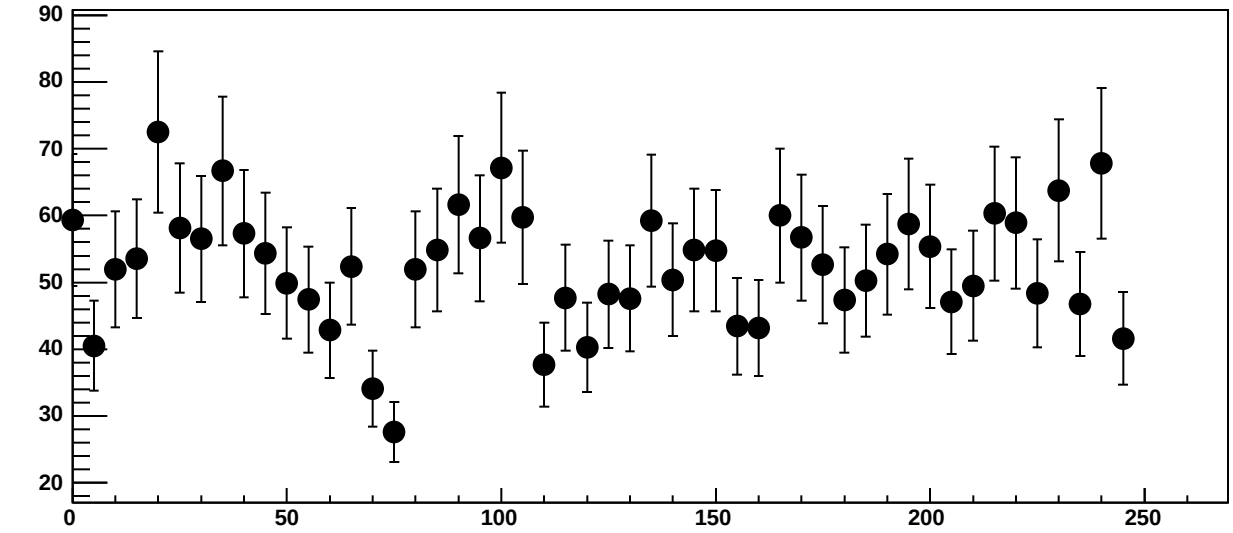
Chip 5, Channel 3, Enable 0, DAC=1600, ADC Residuals vs Hold



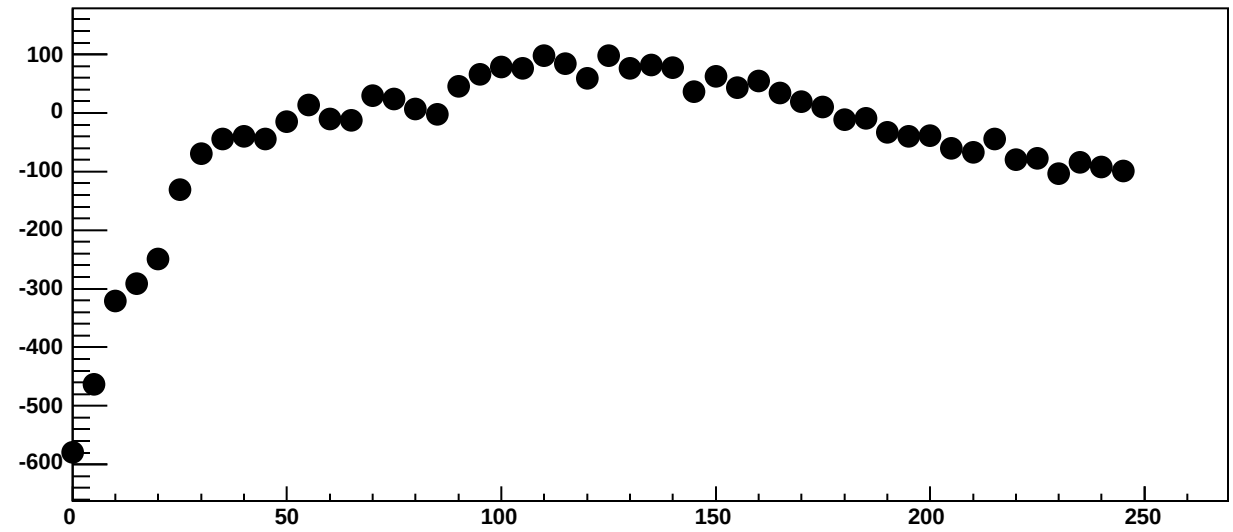
Chip 5, Channel 3, Enable 1, DAC=1600, ADC Mean vs Hold

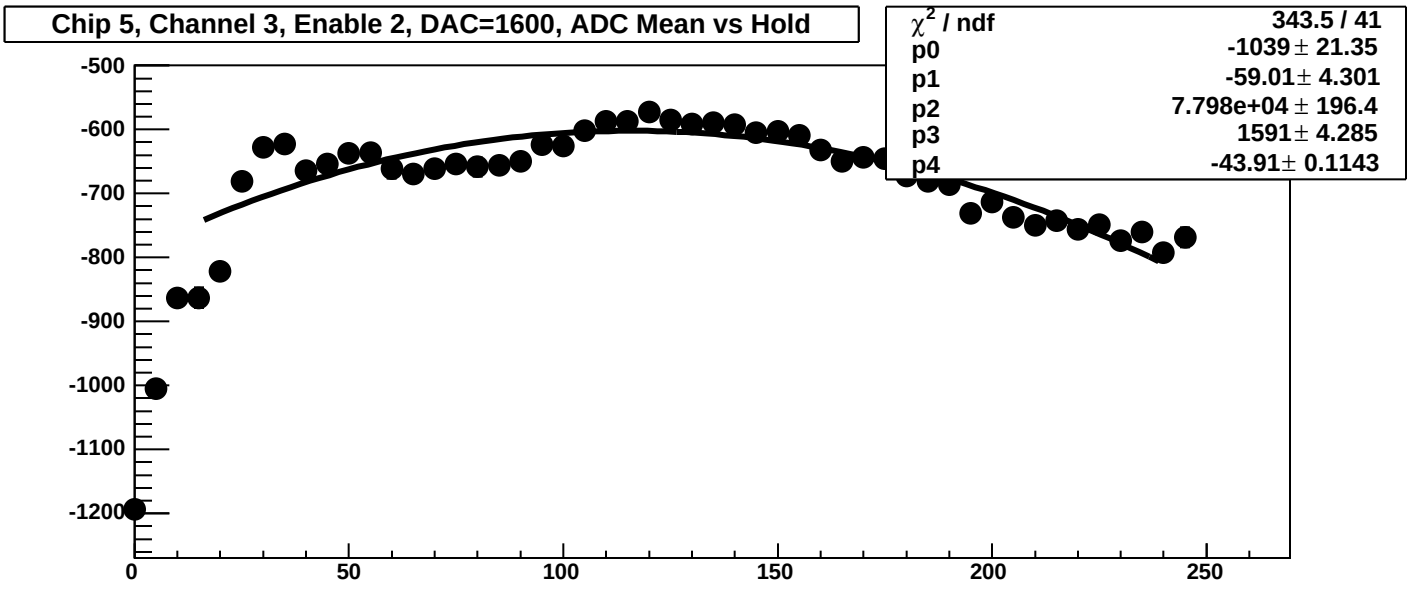


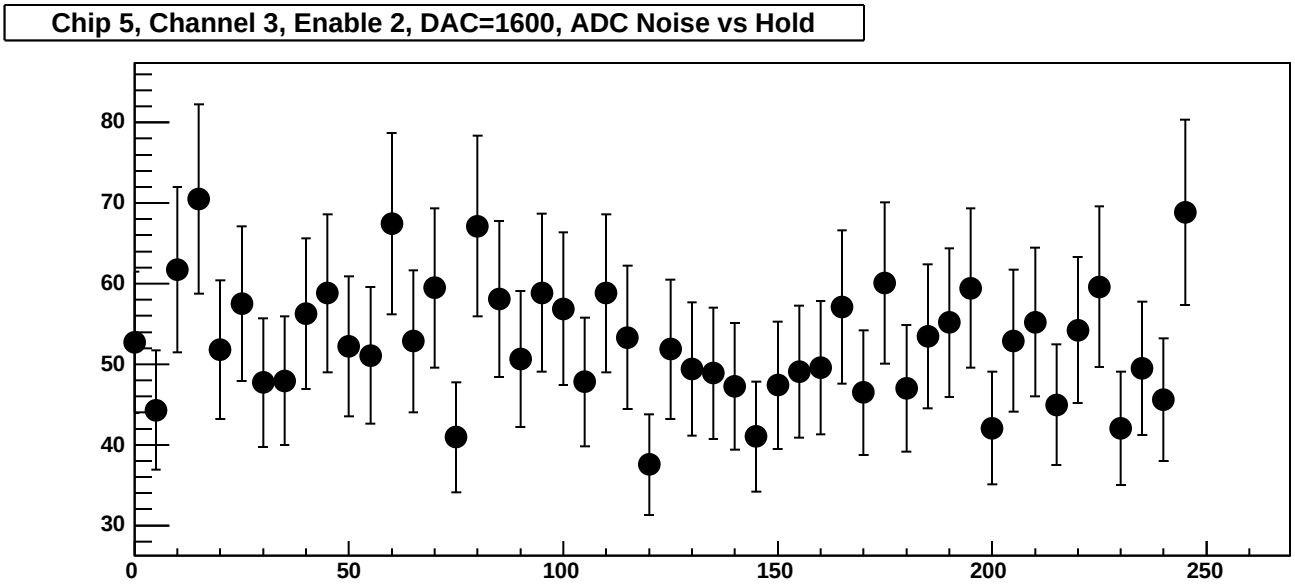
Chip 5, Channel 3, Enable 1, DAC=1600, ADC Noise vs Hold

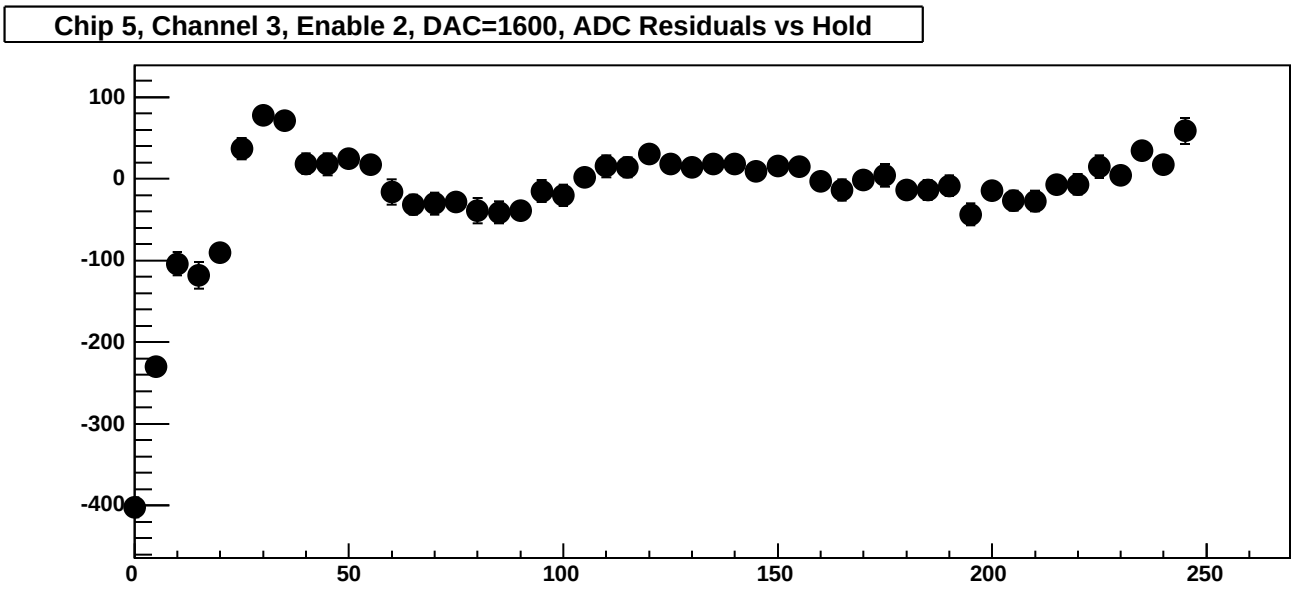


Chip 5, Channel 3, Enable 1, DAC=1600, ADC Residuals vs Hold

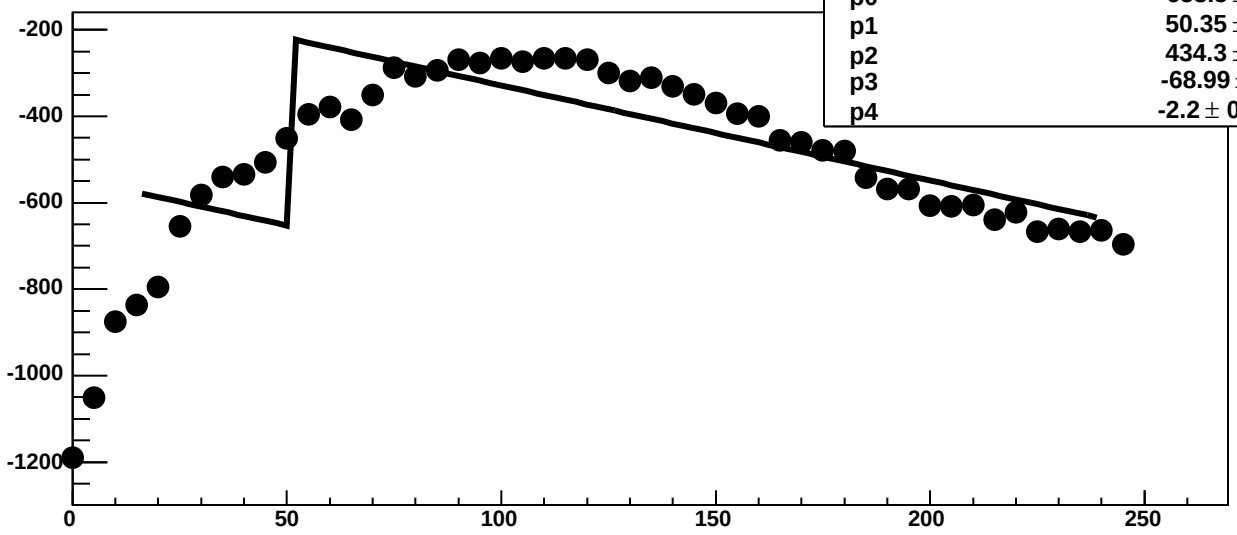



Chip 5, Channel 3, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 343.5 / 41
p0 -1039 ± 21.35
p1 -59.01 ± 4.301
p2 7.798e+04 ± 196.4
p3 1591 ± 4.285
p4 -43.91 ± 0.1143
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
50
100
150
200
250

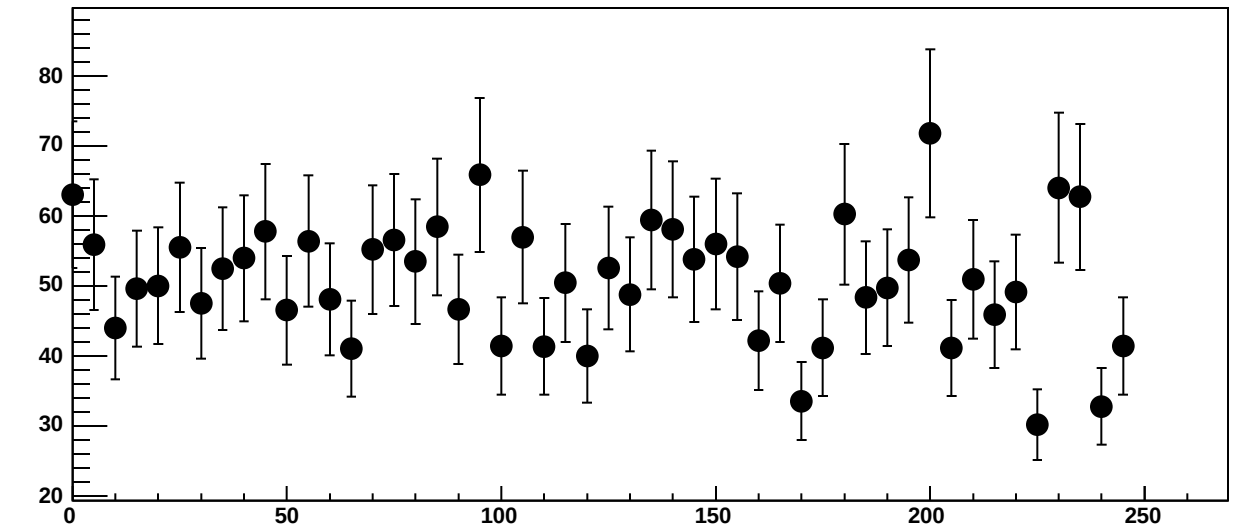

Chip 5, Channel 3, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 5, Channel 3, Enable 2, DAC=1600, ADC Residuals vs Hold
100
0
-100
-200
-300
-400
0
50
100
150
200
250

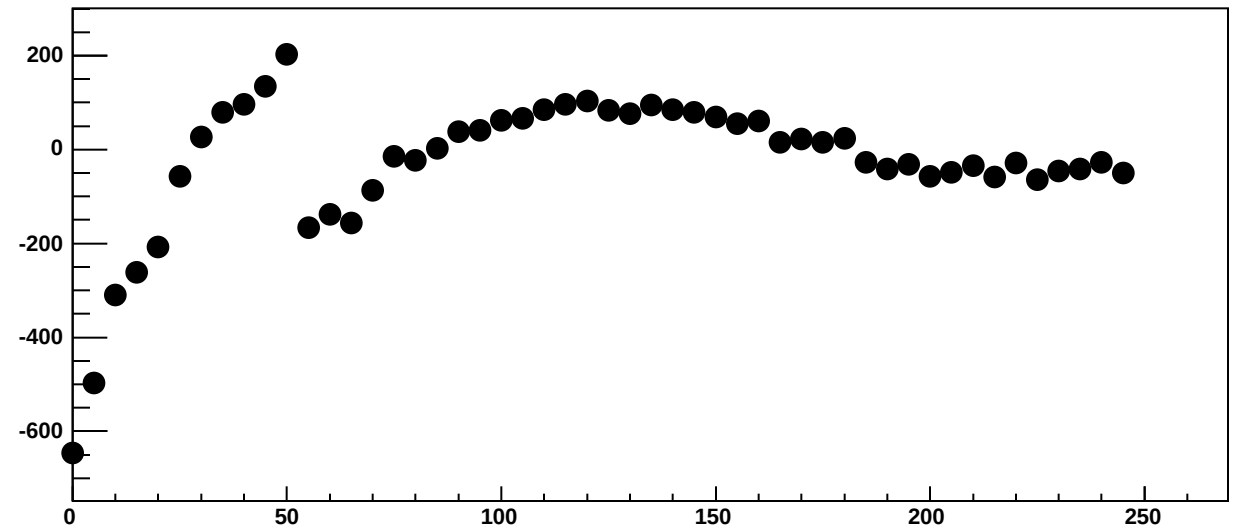
Chip 5, Channel 3, Enable 3, DAC=1600, ADC Mean vs Hold



Chip 5, Channel 3, Enable 3, DAC=1600, ADC Noise vs Hold

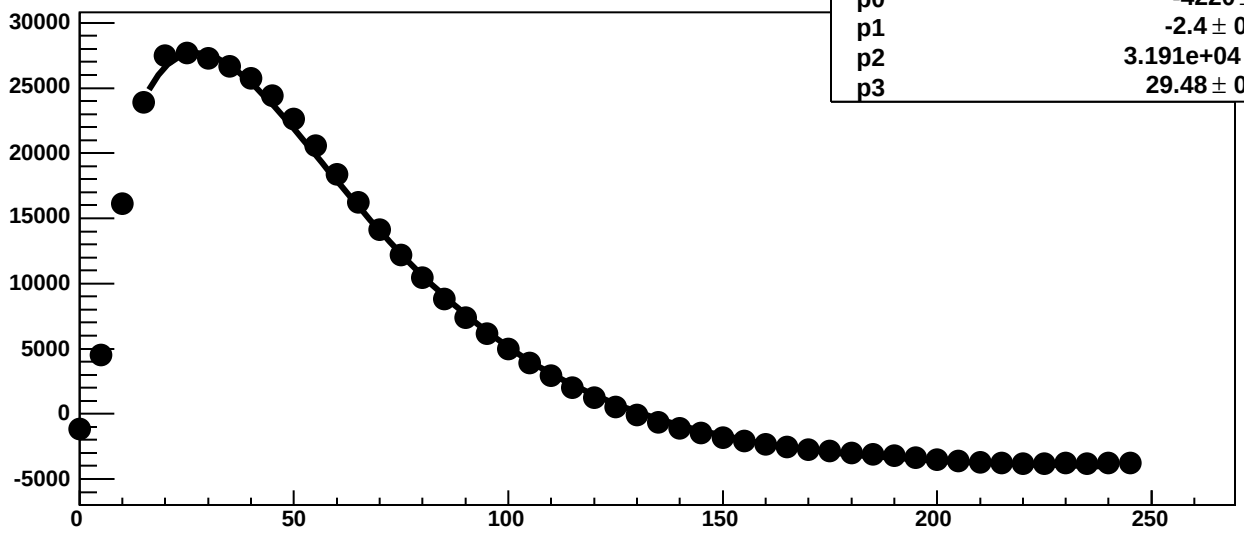


Chip 5, Channel 3, Enable 3, DAC=1600, ADC Residuals vs Hold

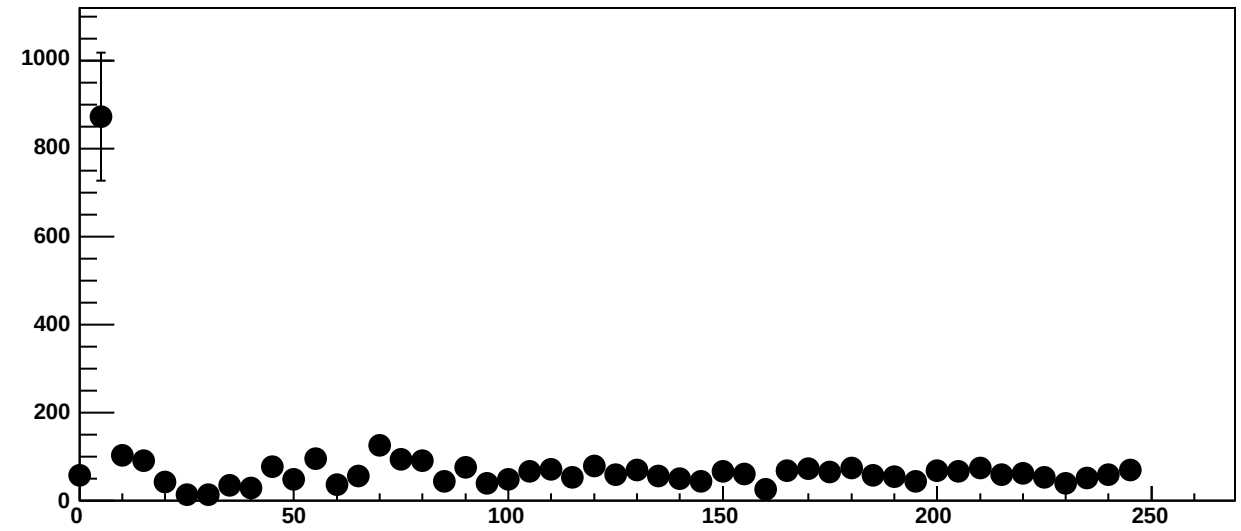


Chip 5, Channel 3, Enable 4!, DAC=1600, ADC Mean vs Hold

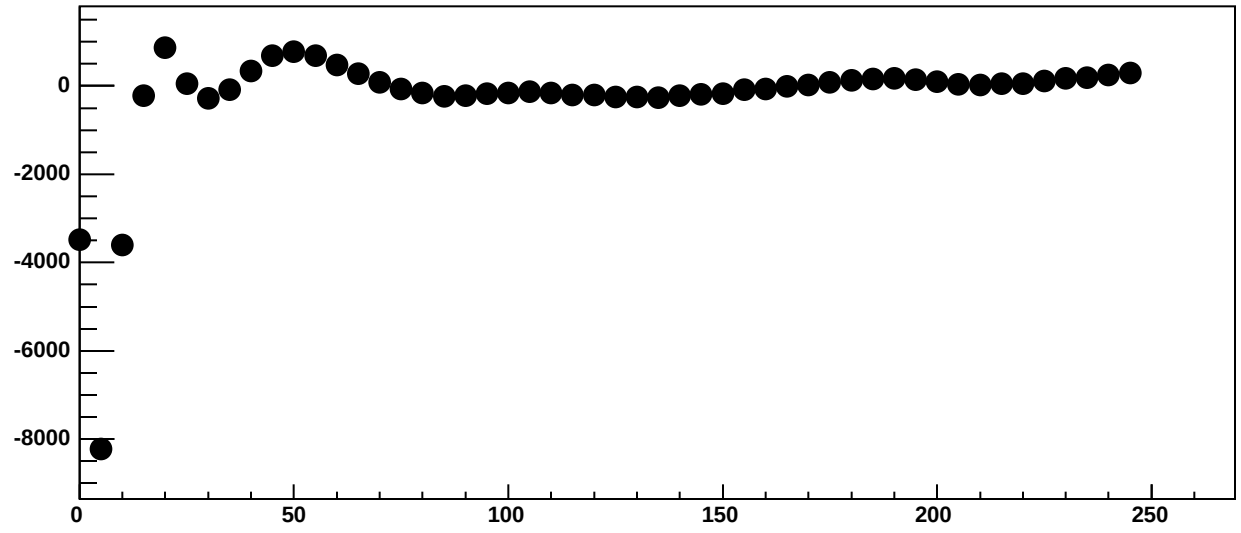
χ^2 / ndf	3.513e+04 / 42
p0	-4220 ± 3.558
p1	-2.4 ± 0.01982
p2	3.191e+04 ± 3.94
p3	29.48 ± 0.01066



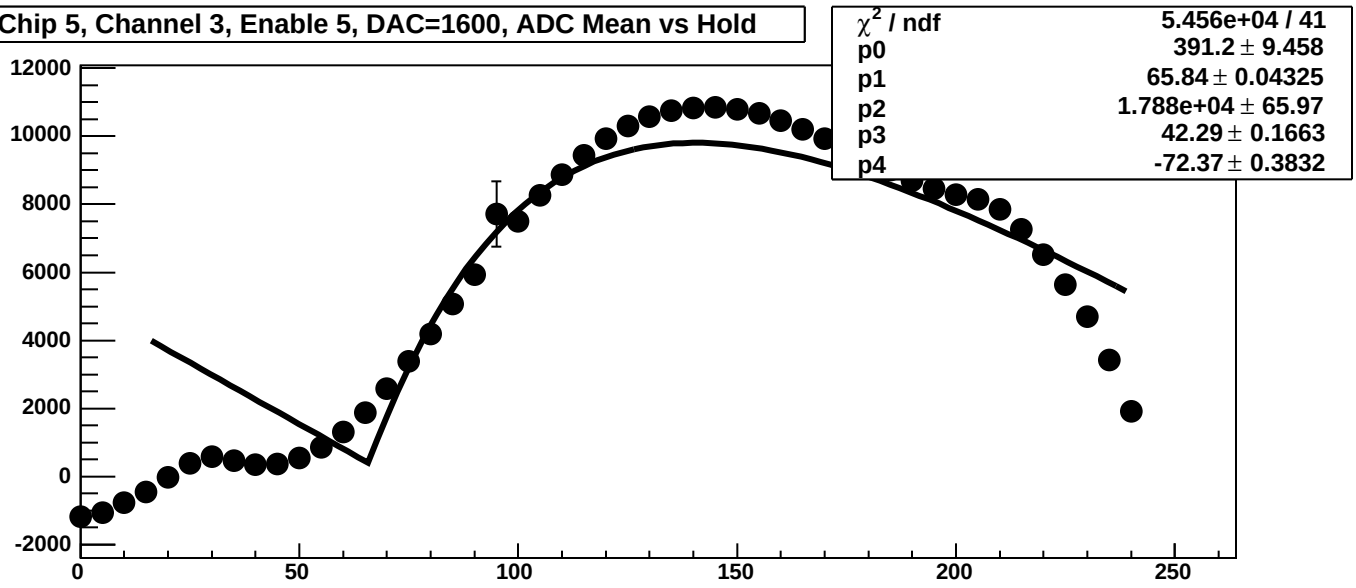
Chip 5, Channel 3, Enable 4!, DAC=1600, ADC Noise vs Hold



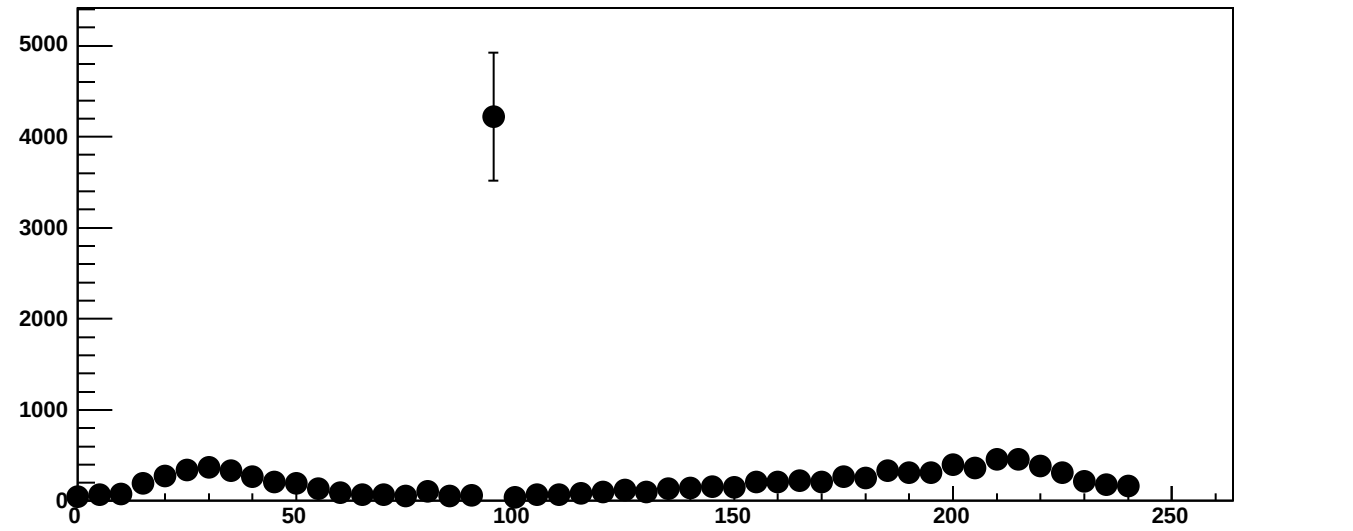
Chip 5, Channel 3, Enable 4!, DAC=1600, ADC Residuals vs Hold



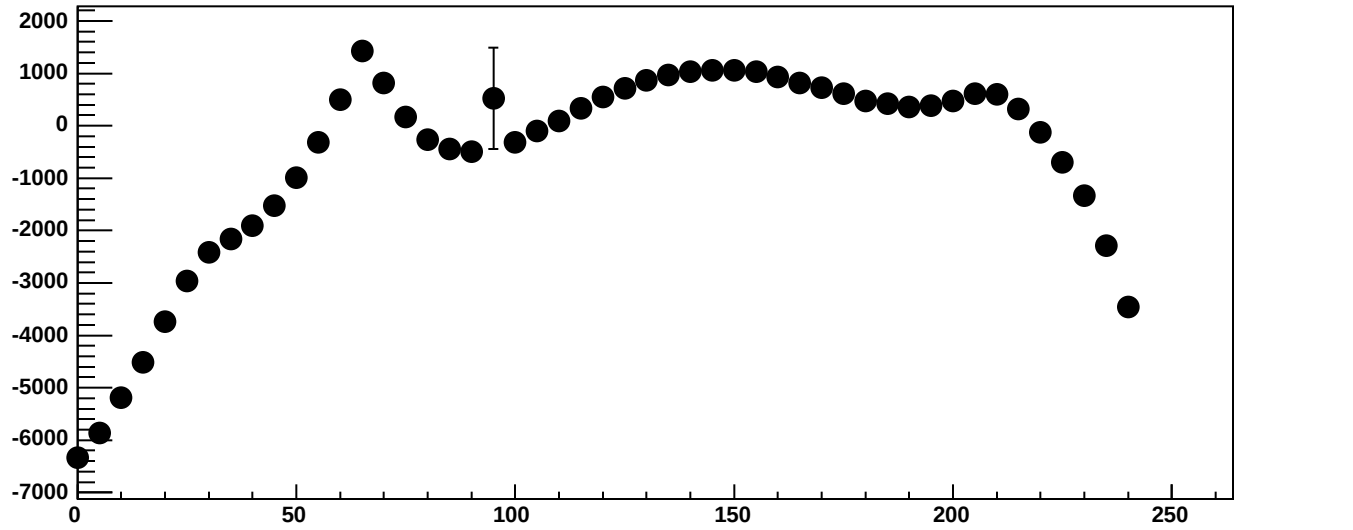
Chip 5, Channel 3, Enable 5, DAC=1600, ADC Mean vs Hold

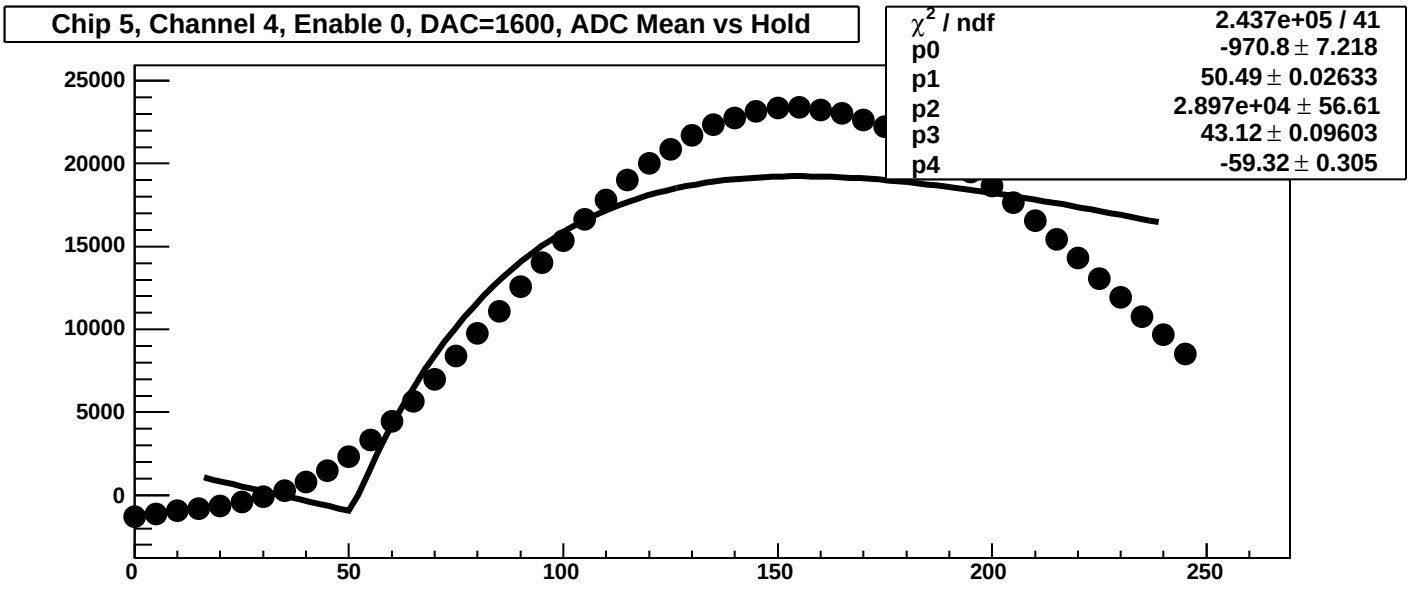


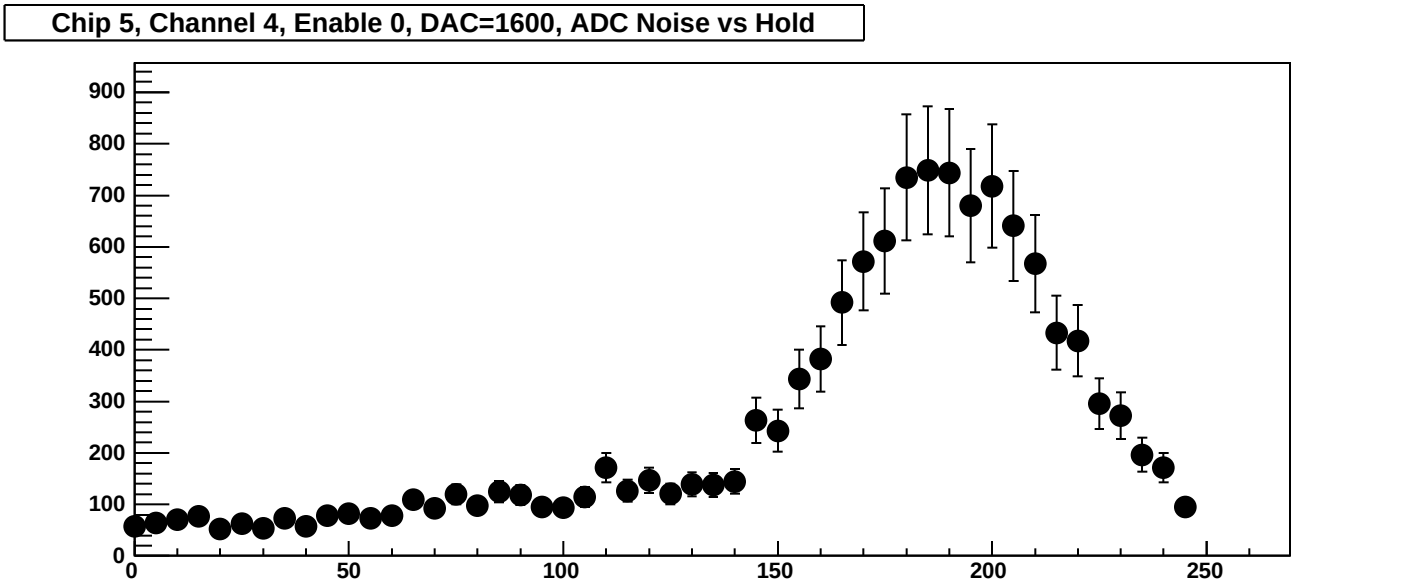
Chip 5, Channel 3, Enable 5, DAC=1600, ADC Noise vs Hold

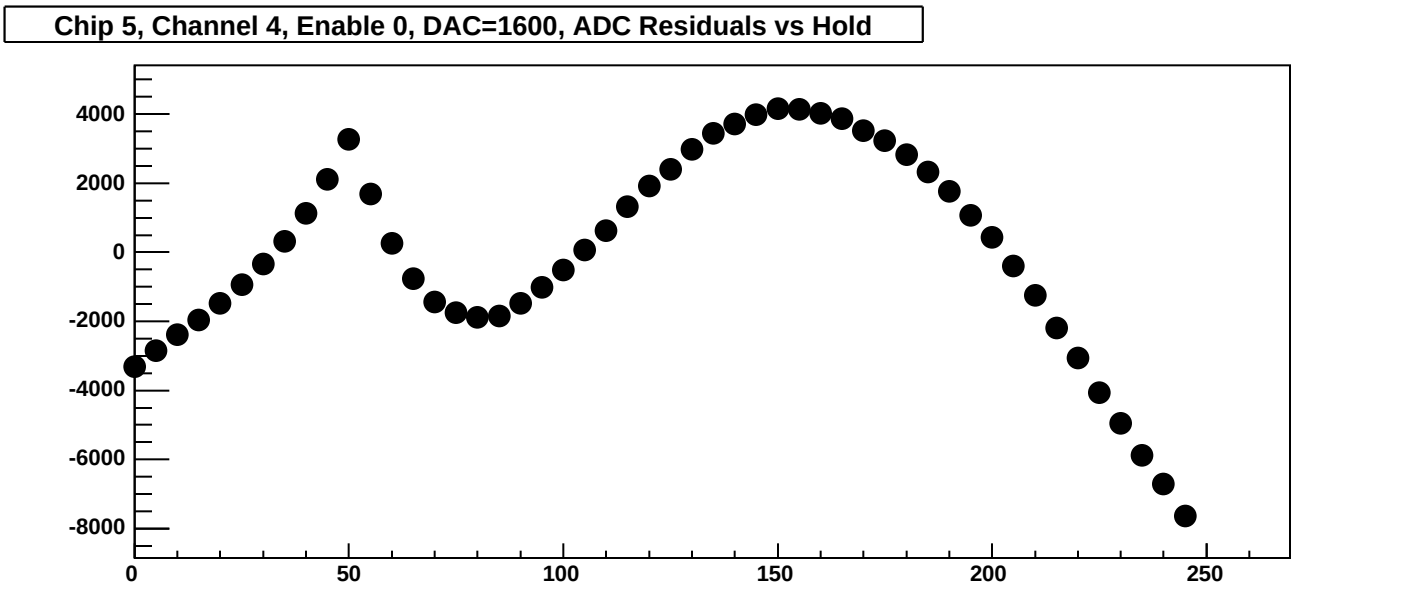


Chip 5, Channel 3, Enable 5, DAC=1600, ADC Residuals vs Hold

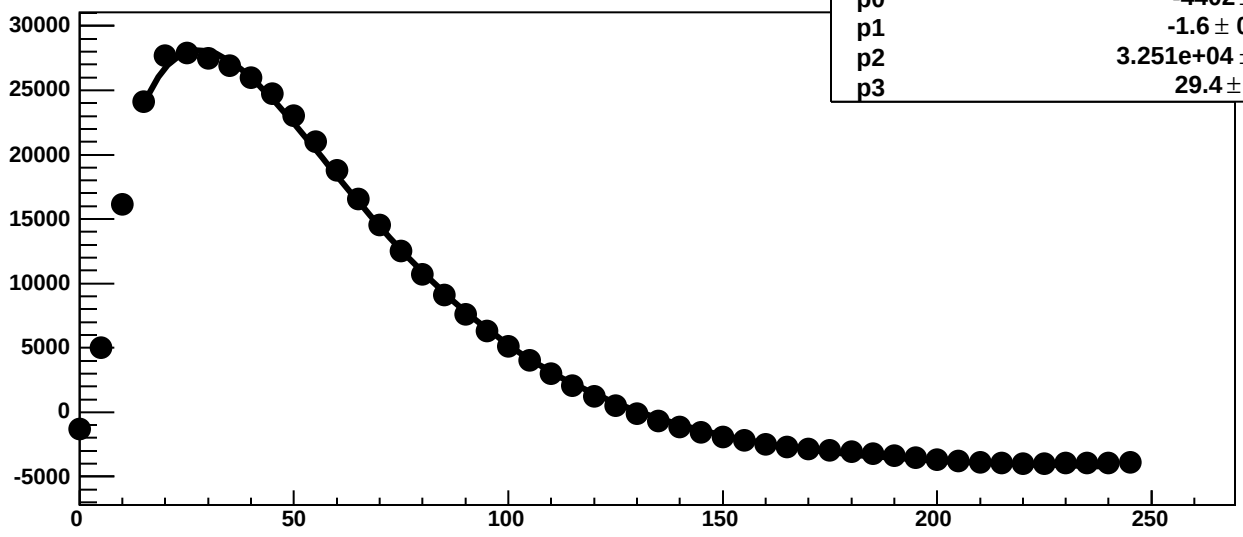



Chip 5, Channel 4, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.437e+05 / 41
p0 -970.8 ± 7.218
p1 50.49 ± 0.02633
p2 2.897e+04 ± 56.61
p3 43.12 ± 0.09603
p4 -59.32 ± 0.305
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

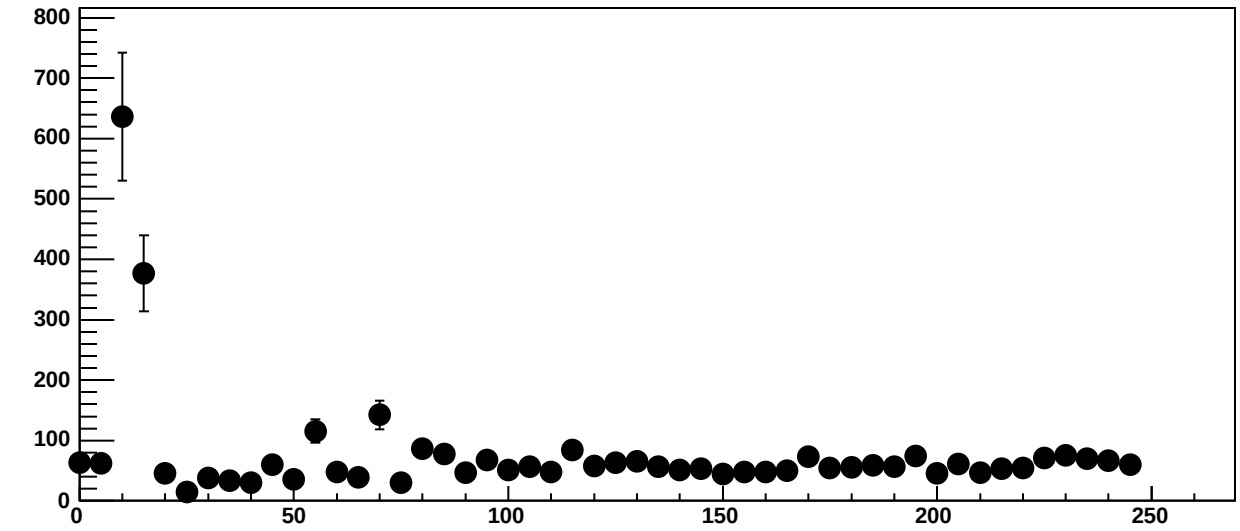

Chip 5, Channel 4, Enable 0, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 5, Channel 4, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250

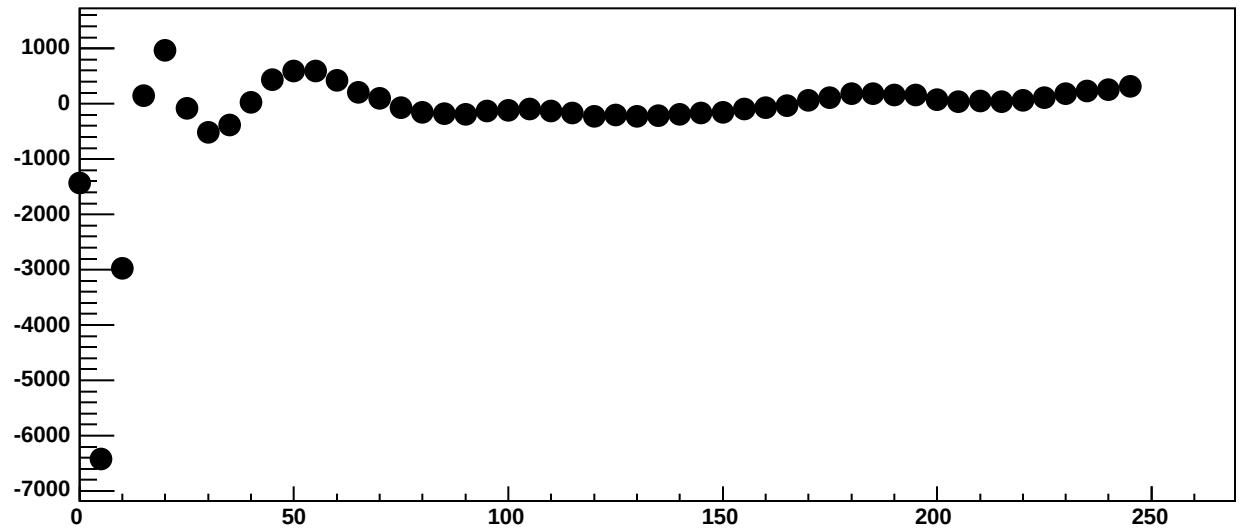
Chip 5, Channel 4, Enable 1!, DAC=1600, ADC Mean vs Hold



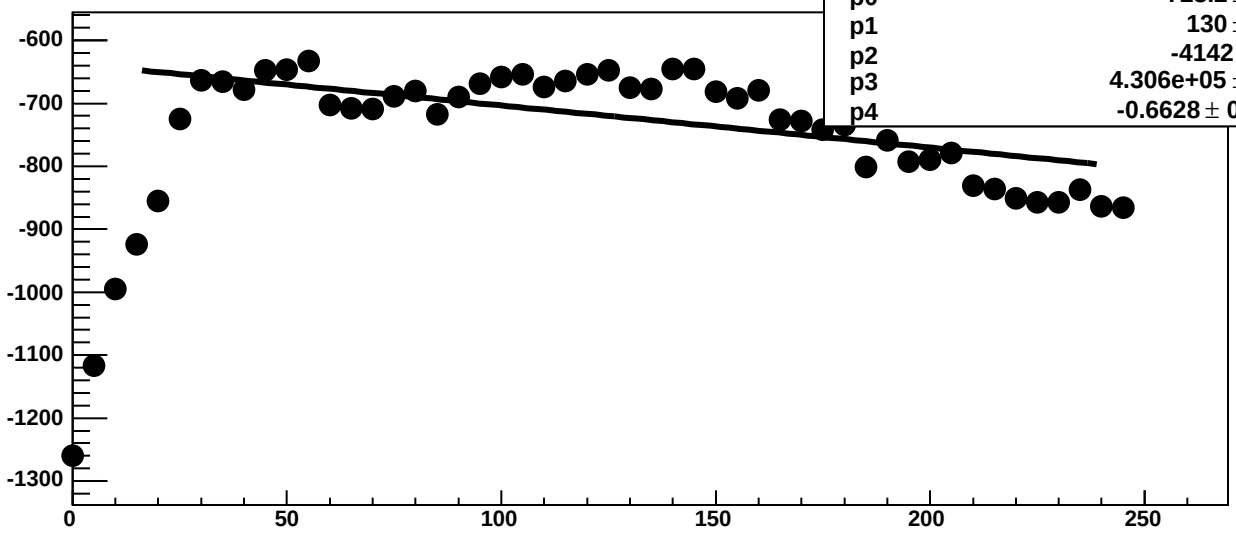
Chip 5, Channel 4, Enable 1!, DAC=1600, ADC Noise vs Hold



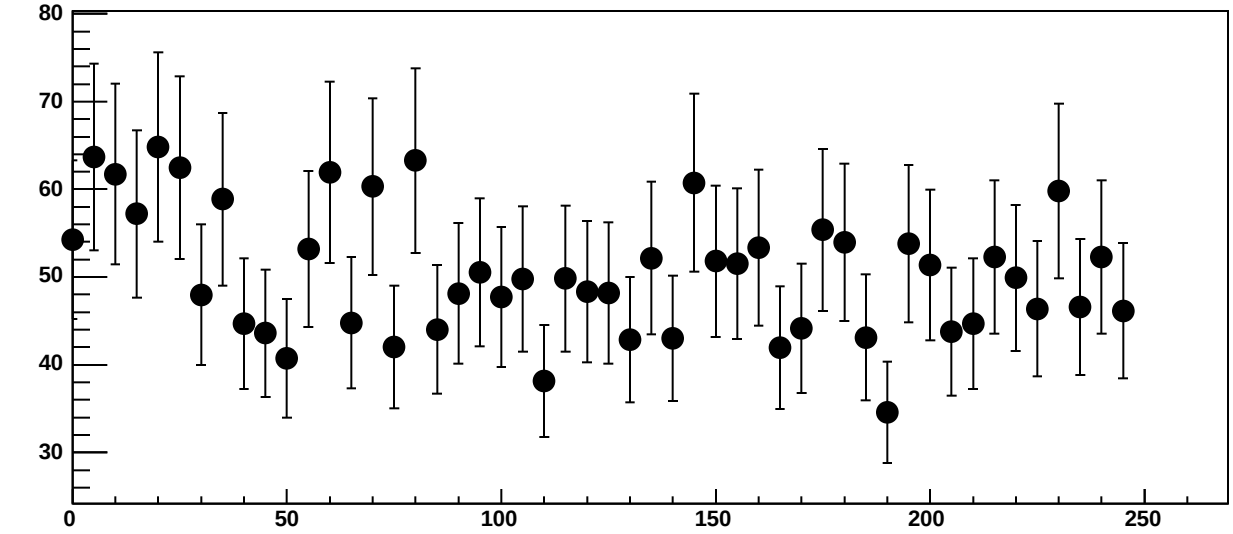
Chip 5, Channel 4, Enable 1!, DAC=1600, ADC Residuals vs Hold



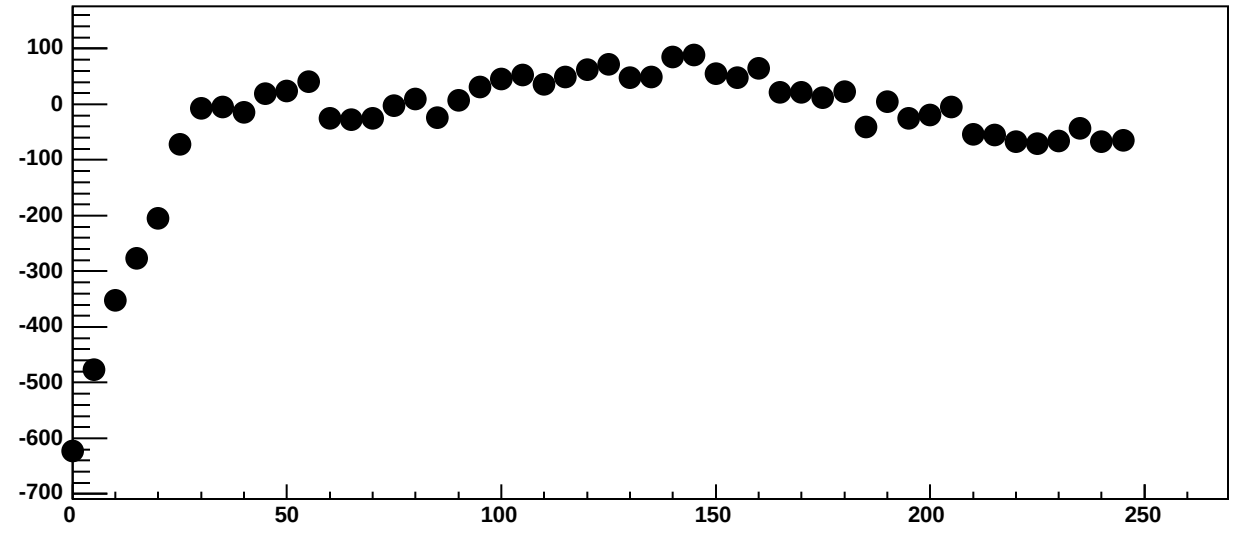
Chip 5, Channel 4, Enable 2, DAC=1600, ADC Mean vs Hold



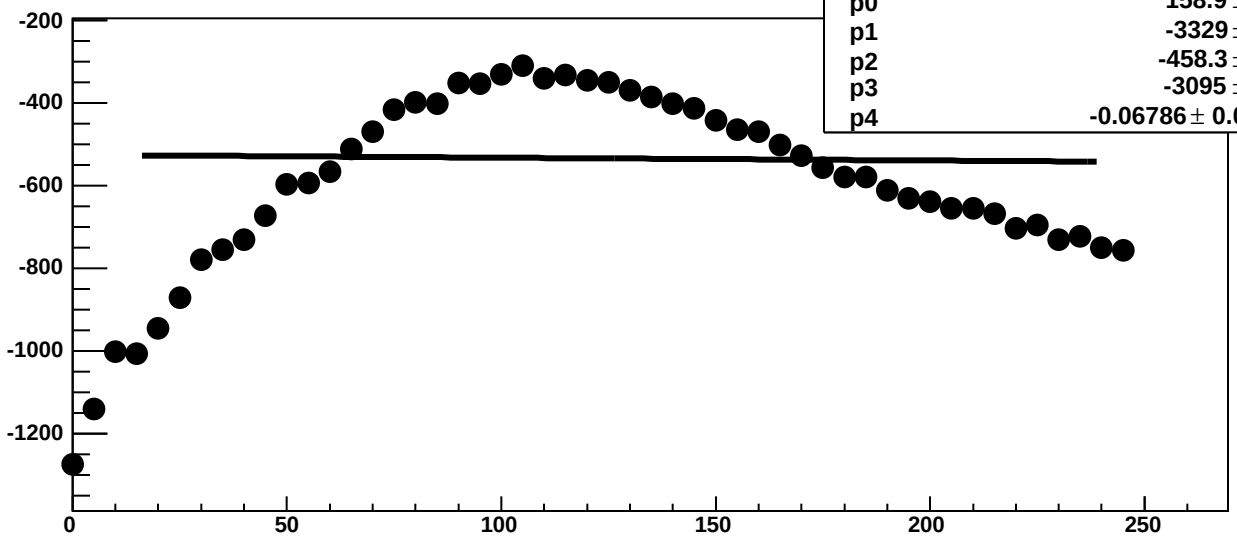
Chip 5, Channel 4, Enable 2, DAC=1600, ADC Noise vs Hold



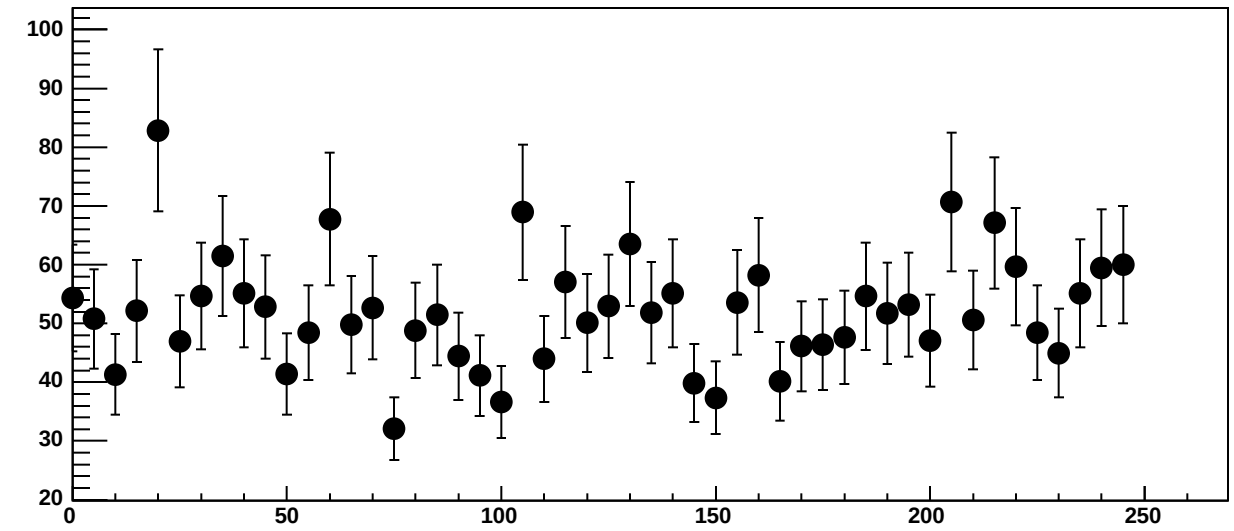
Chip 5, Channel 4, Enable 2, DAC=1600, ADC Residuals vs Hold



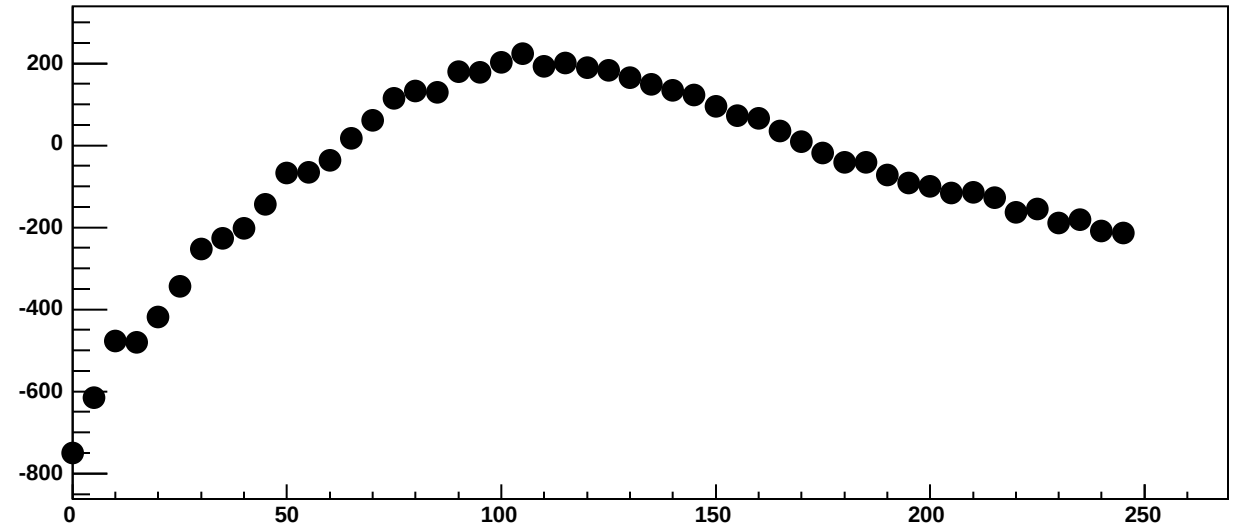
Chip 5, Channel 4, Enable 3, DAC=1600, ADC Mean vs Hold



Chip 5, Channel 4, Enable 3, DAC=1600, ADC Noise vs Hold

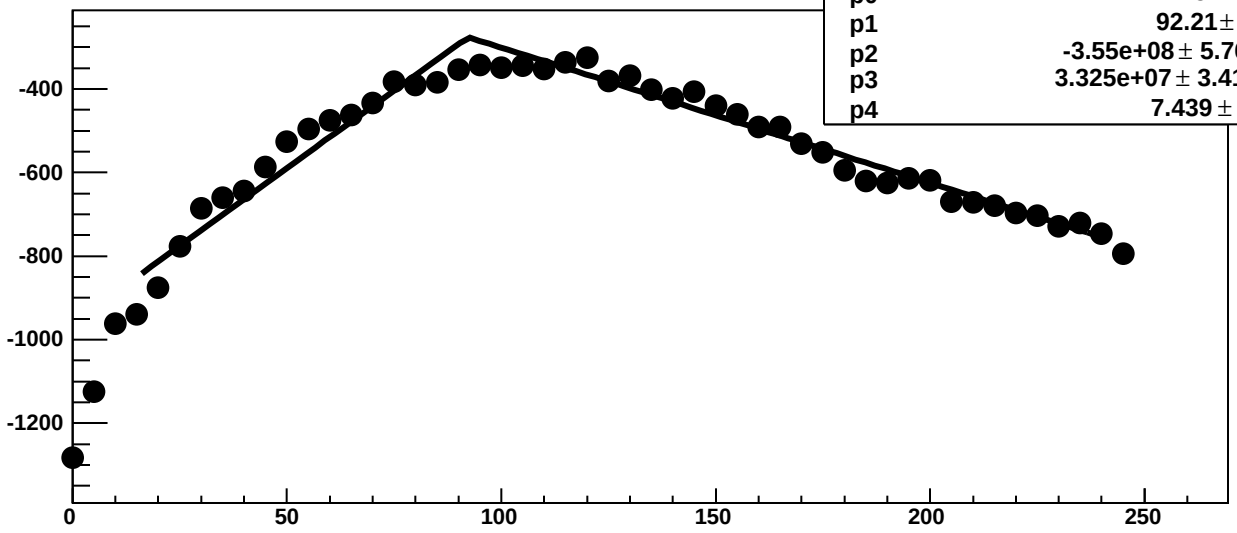


Chip 5, Channel 4, Enable 3, DAC=1600, ADC Residuals vs Hold

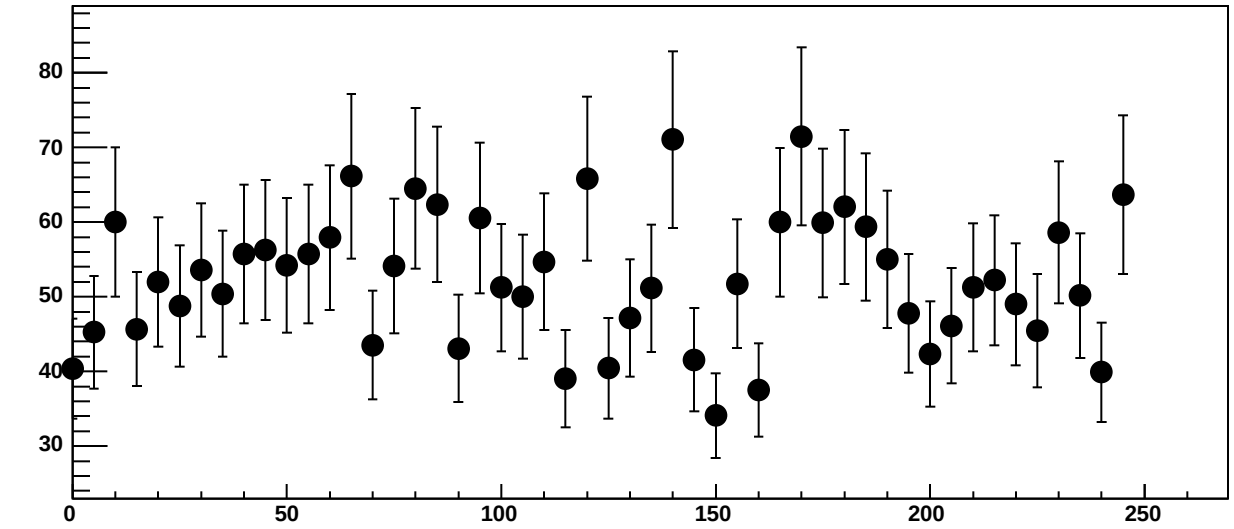


Chip 5, Channel 4, Enable 4, DAC=1600, ADC Mean vs Hold

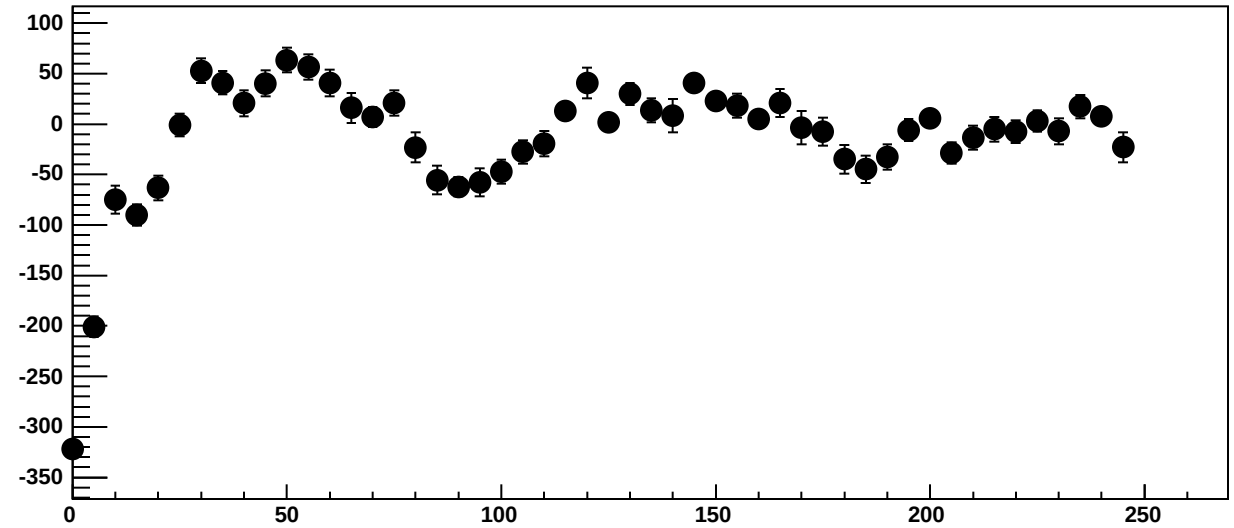
χ^2 / ndf	390.3 / 41
p0	-275.7 ± 3.396
p1	92.21 ± 0.6773
p2	-3.55e+08 ± 5.761e+06
p3	3.325e+07 ± 3.419e+05
p4	7.439 ± 0.1254

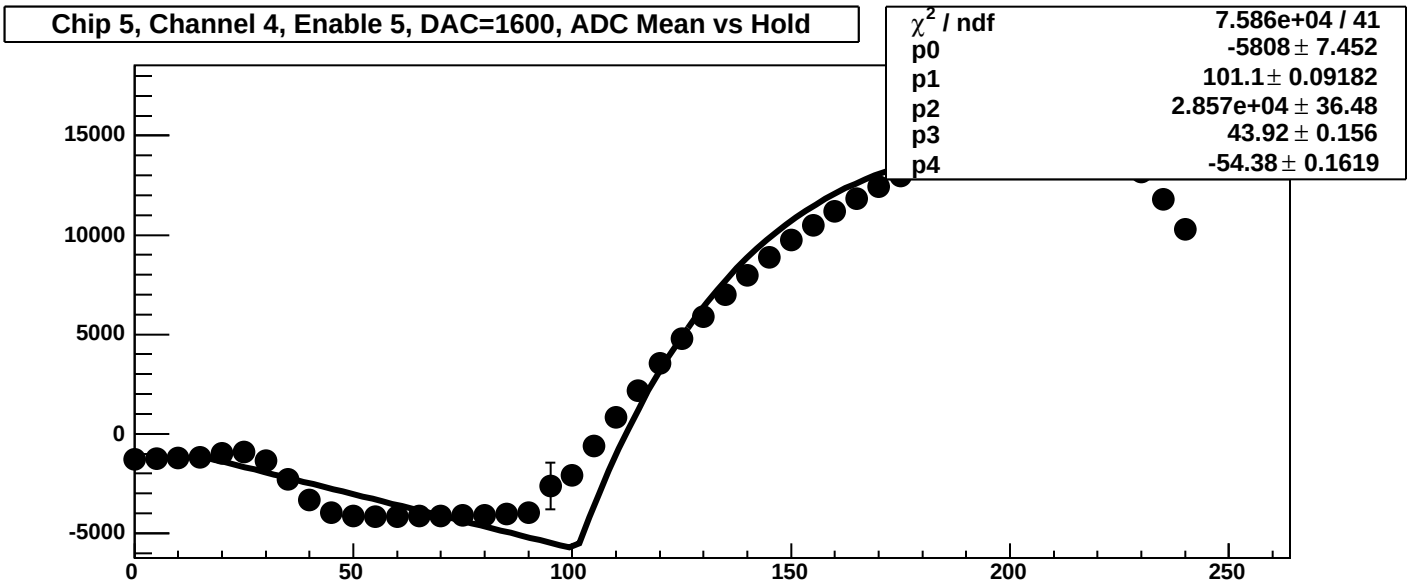


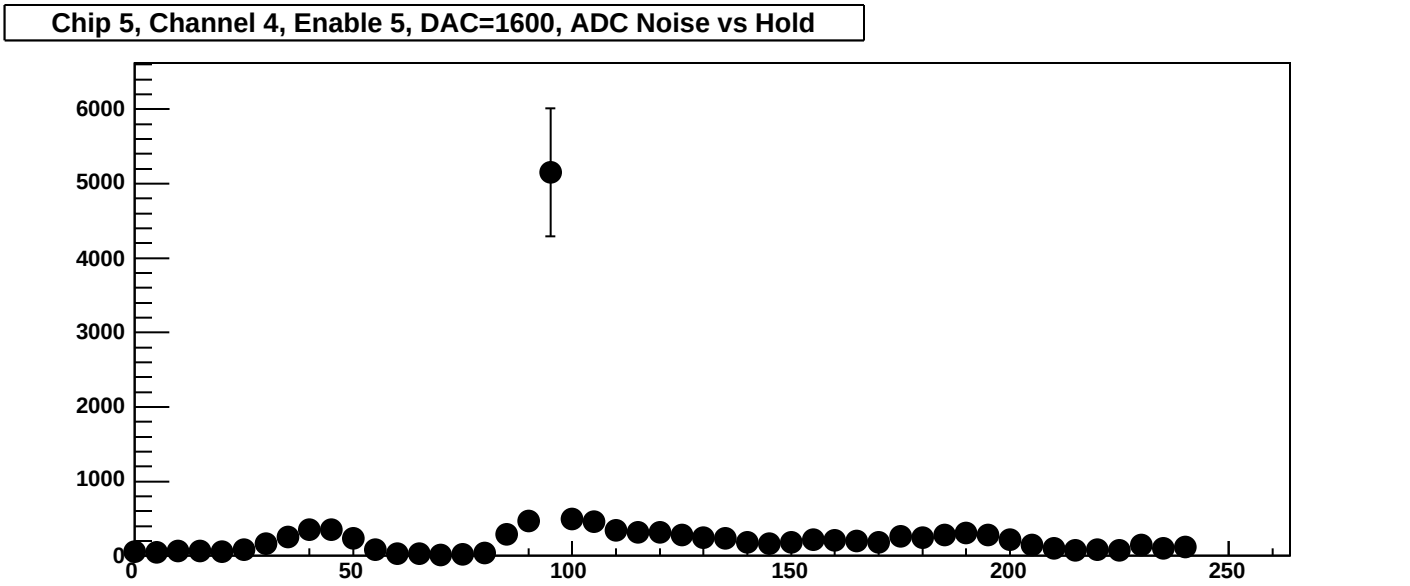
Chip 5, Channel 4, Enable 4, DAC=1600, ADC Noise vs Hold

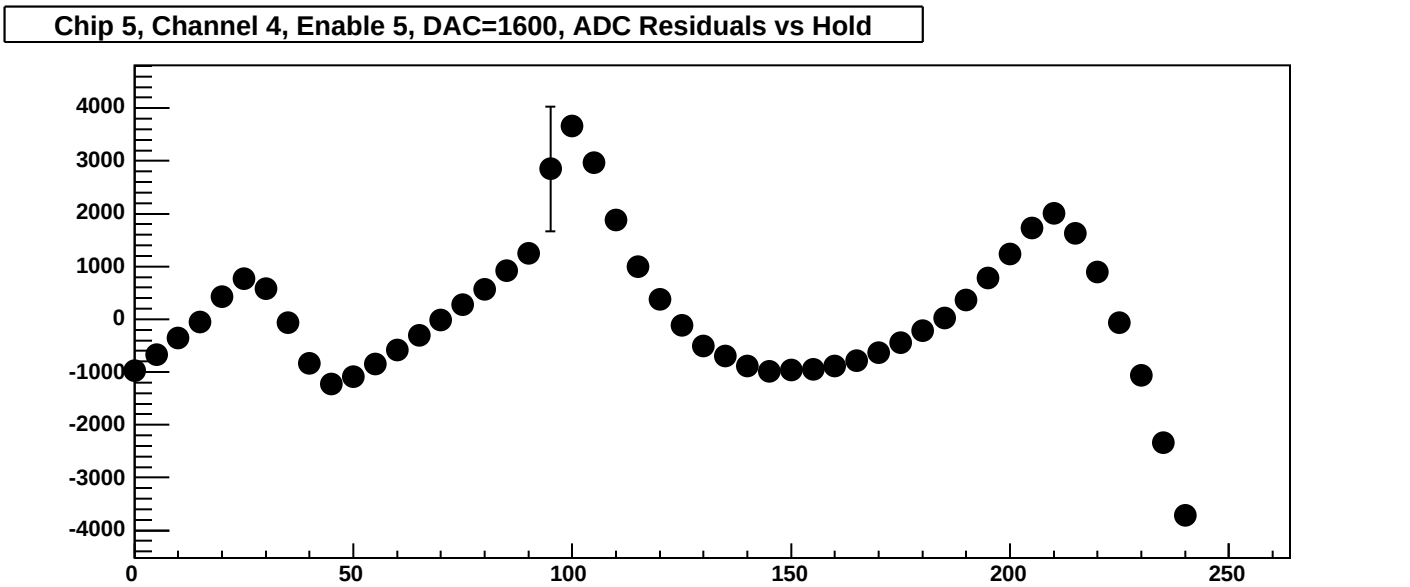


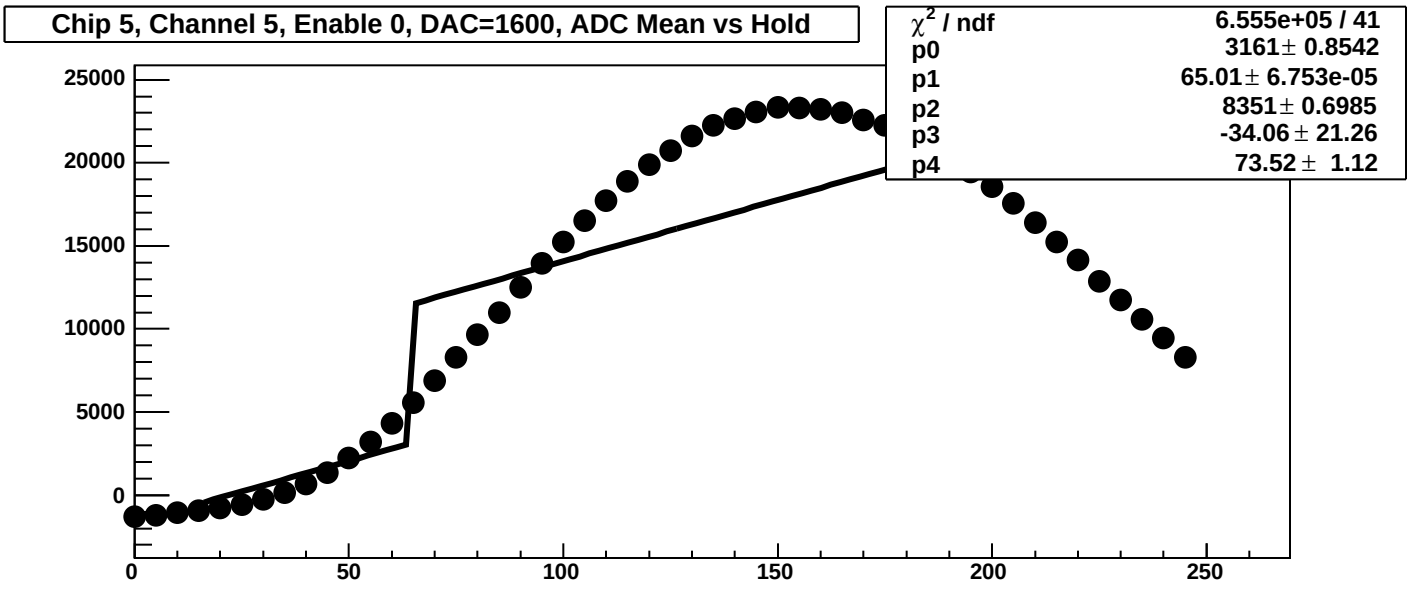
Chip 5, Channel 4, Enable 4, DAC=1600, ADC Residuals vs Hold

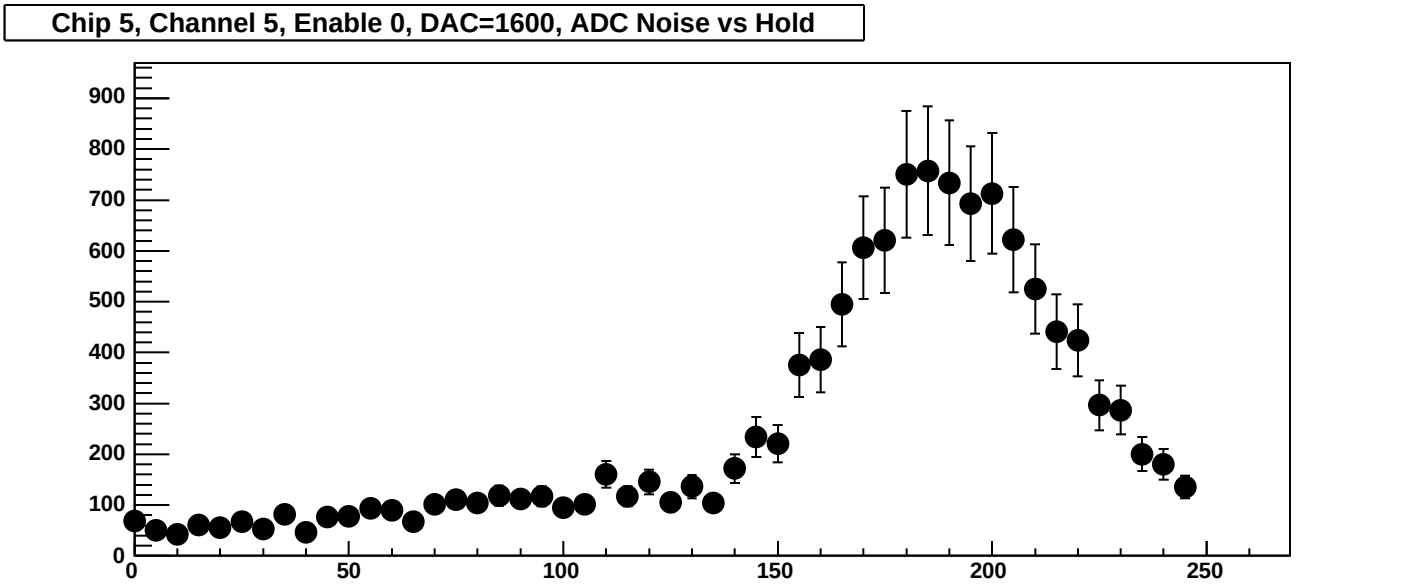


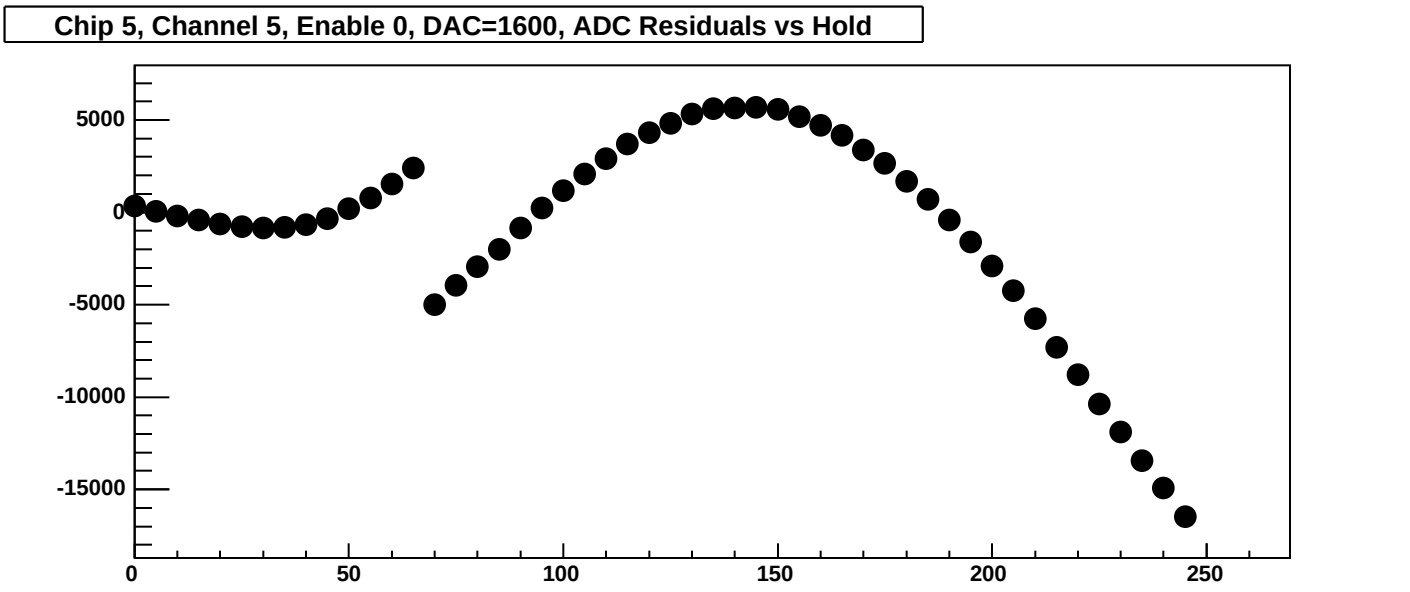

Chip 5, Channel 4, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.586e+04 / 41
p0 -5808 ± 7.452
p1 101.1 ± 0.09182
p2 2.857e+04 ± 36.48
p3 43.92 ± 0.156
p4 -54.38 ± 0.1619
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 5, Channel 4, Enable 5, DAC=1600, ADC Noise vs Hold
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

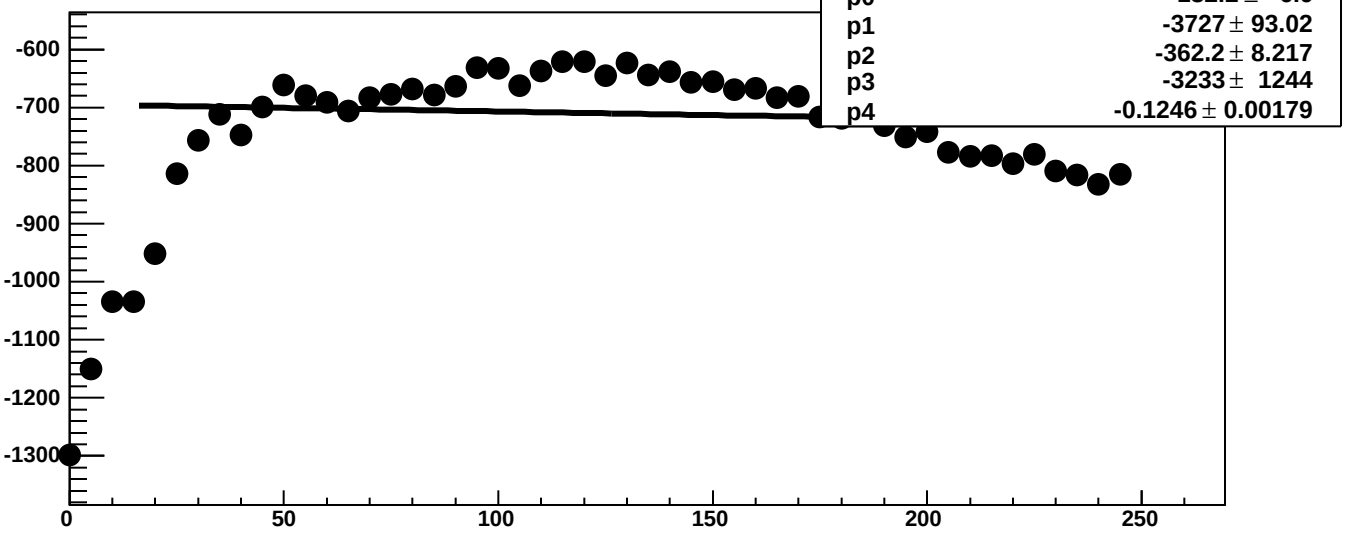

Chip 5, Channel 4, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
3000
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250


Chip 5, Channel 5, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 6.555e+05 / 41
p0 3161 ± 0.8542
p1 65.01 ± 6.753e-05
p2 8351 ± 0.6985
p3 -34.06 ± 21.26
p4 73.52 ± 1.12
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

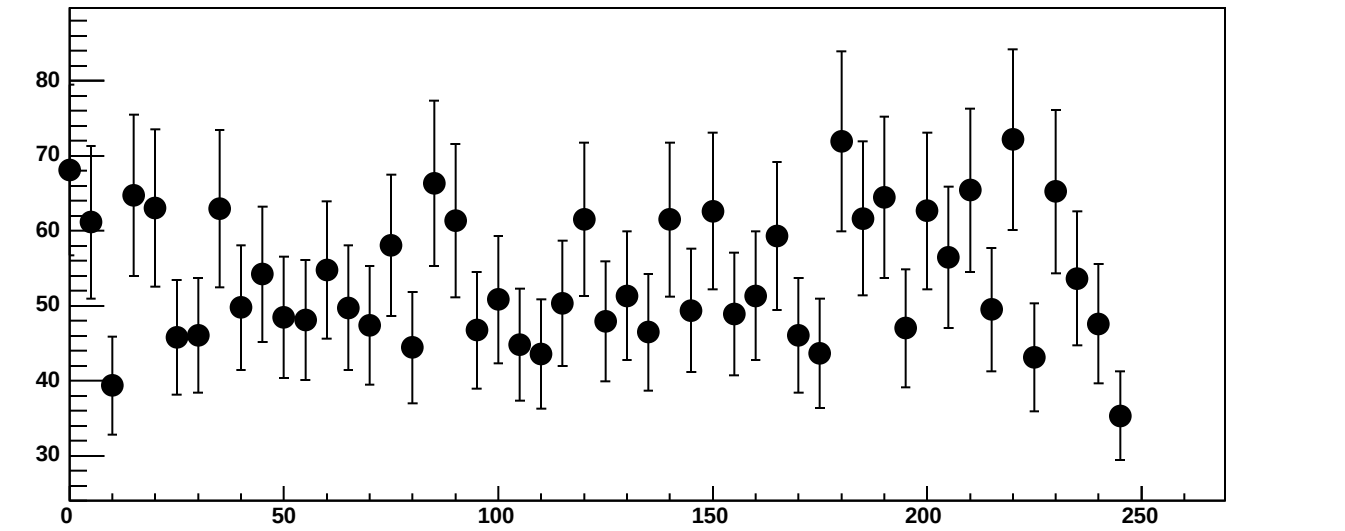

Chip 5, Channel 5, Enable 0, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 5, Channel 5, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
0
50
100
150
200
250

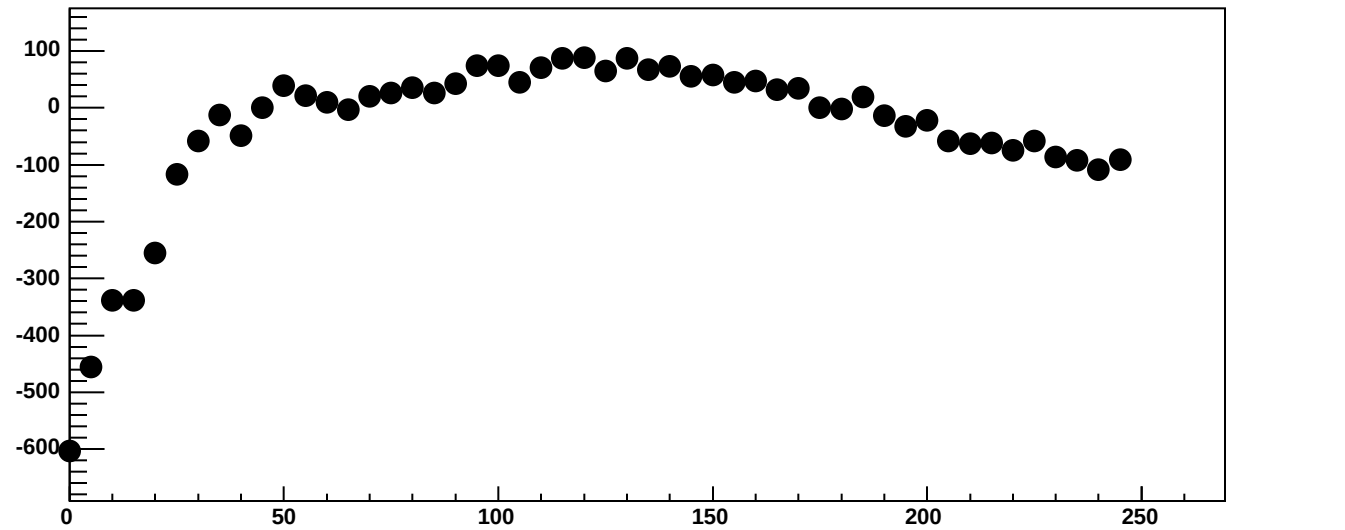
Chip 5, Channel 5, Enable 1, DAC=1600, ADC Mean vs Hold

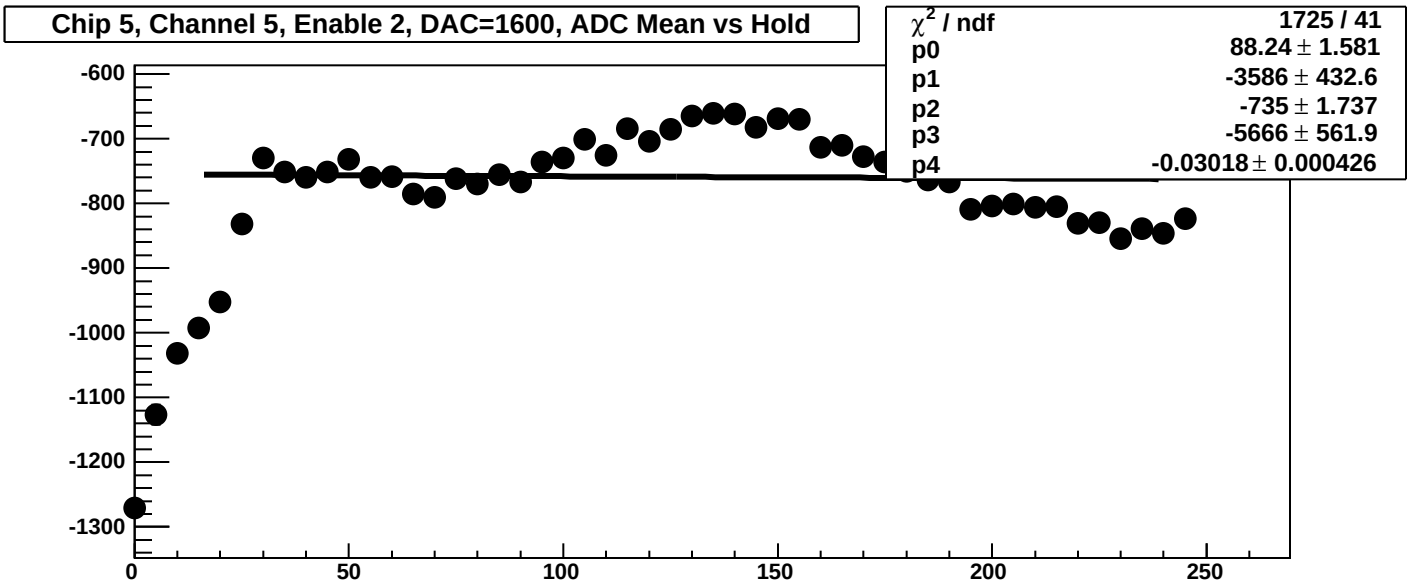


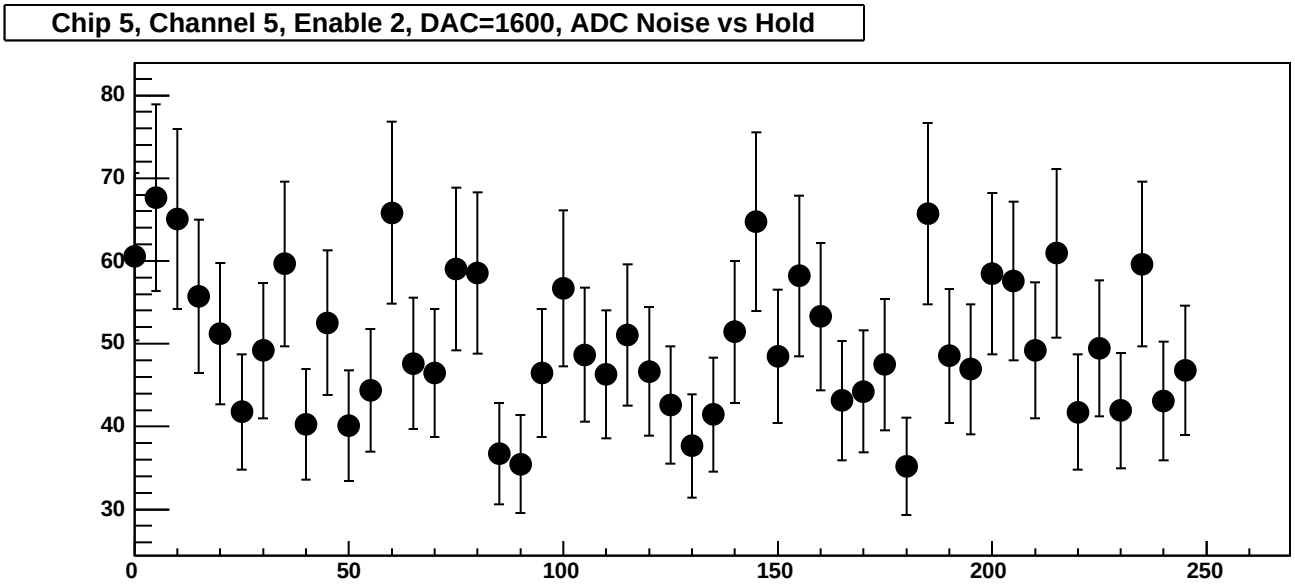
Chip 5, Channel 5, Enable 1, DAC=1600, ADC Noise vs Hold

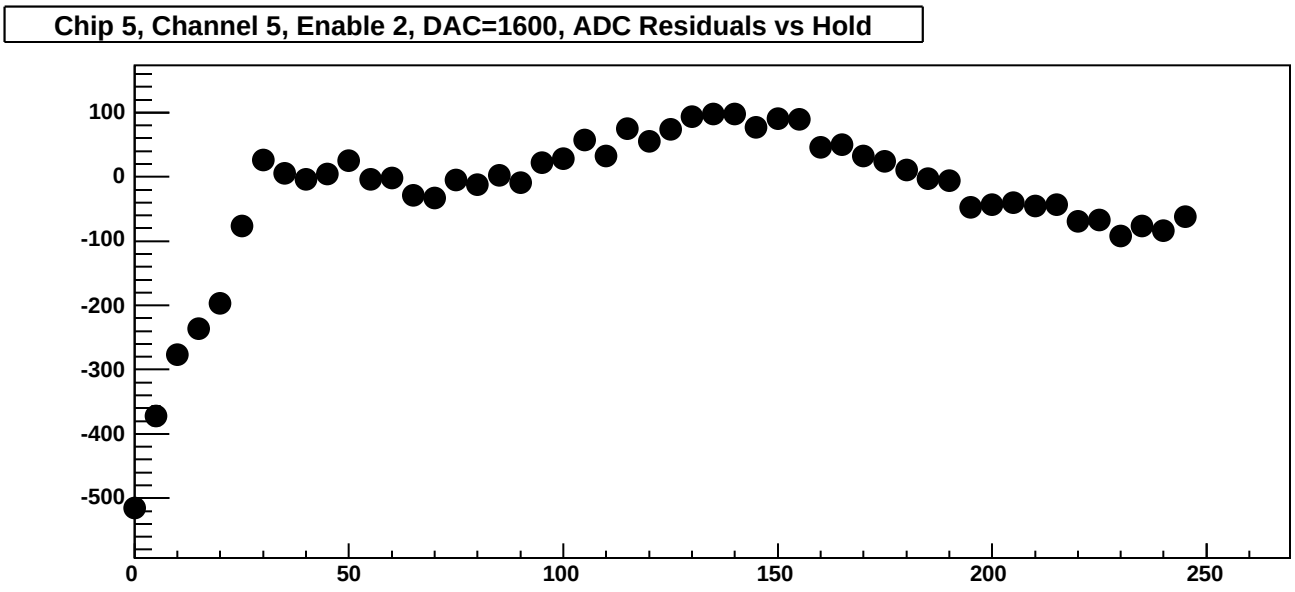


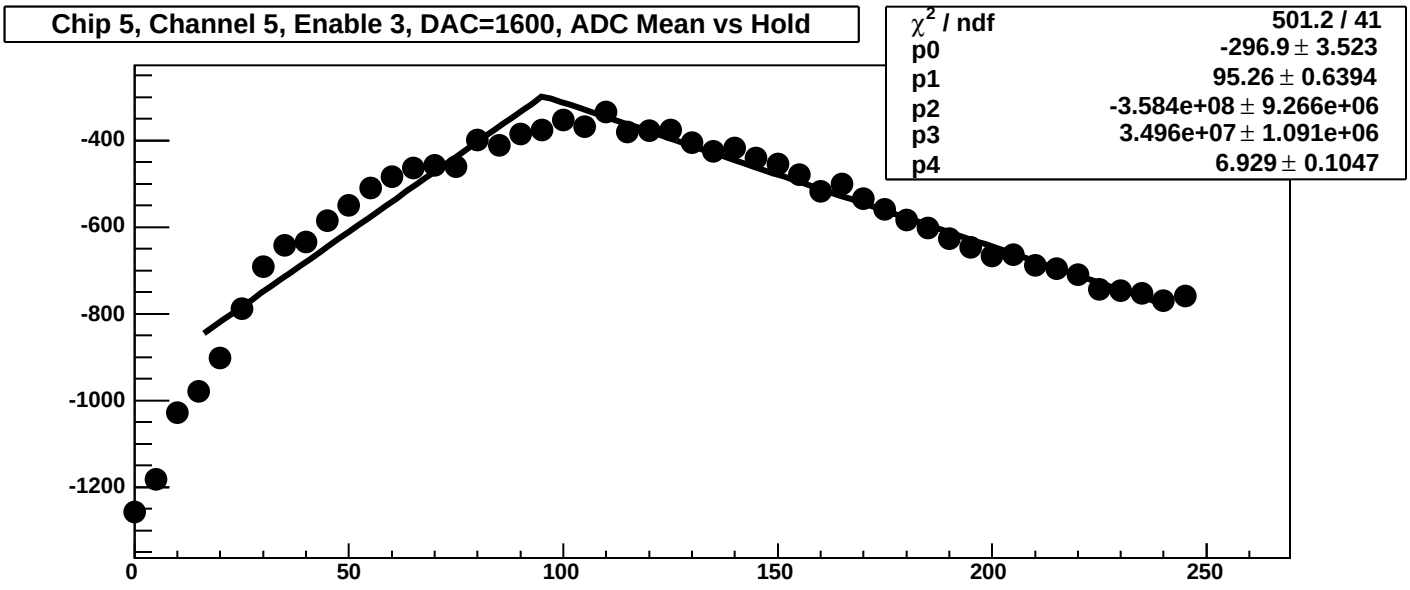
Chip 5, Channel 5, Enable 1, DAC=1600, ADC Residuals vs Hold

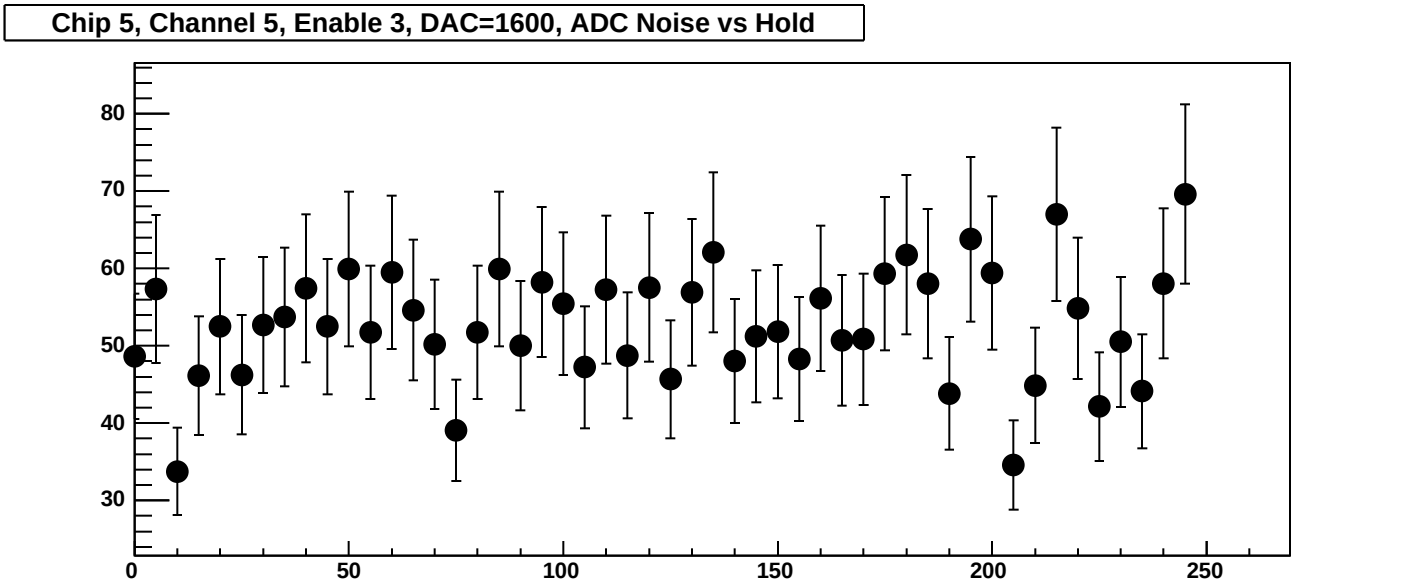


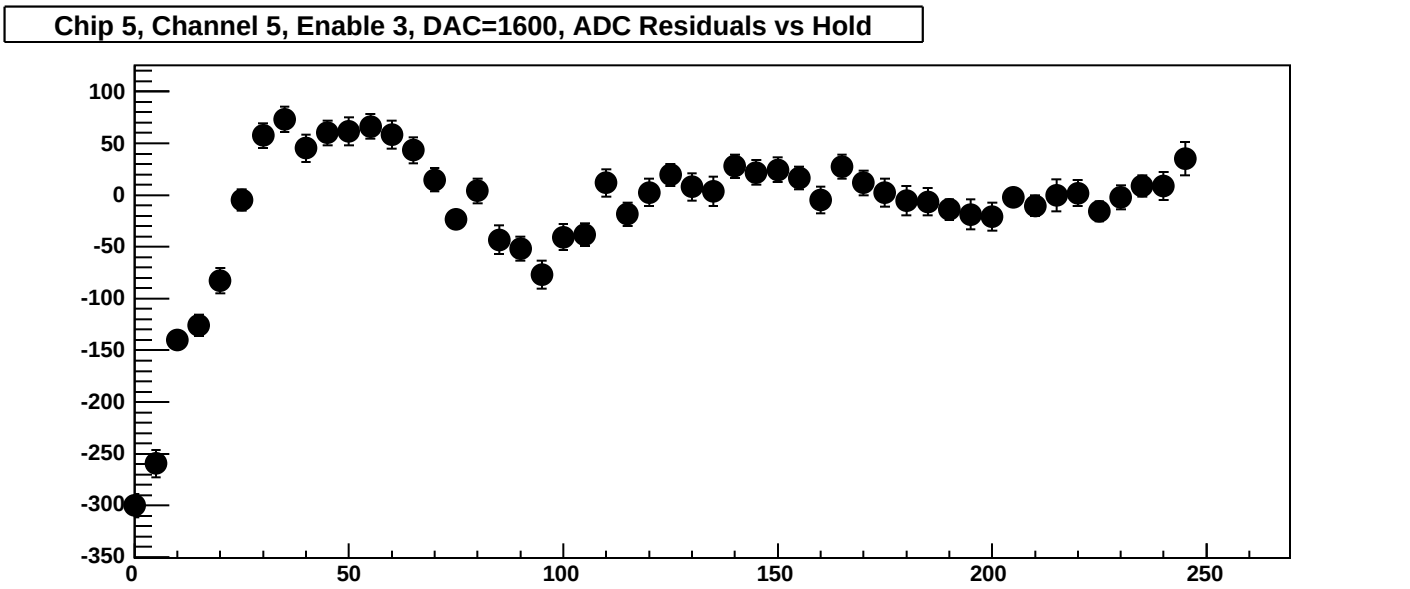

Chip 5, Channel 5, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 1725 / 41
p0 88.24 ± 1.581
p1 -3586 ± 432.6
p2 -735 ± 1.737
p3 -5666 ± 561.9
p4 -0.03018 ± 0.000426
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
50
100
150
200
250

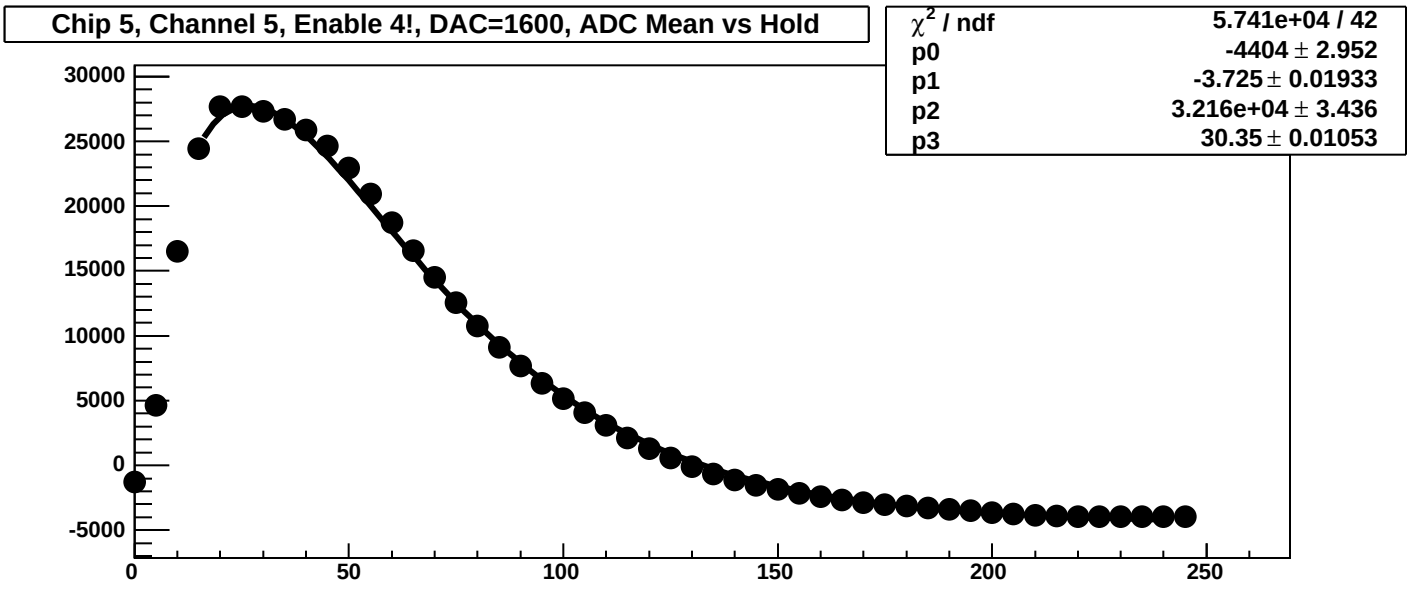

Chip 5, Channel 5, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

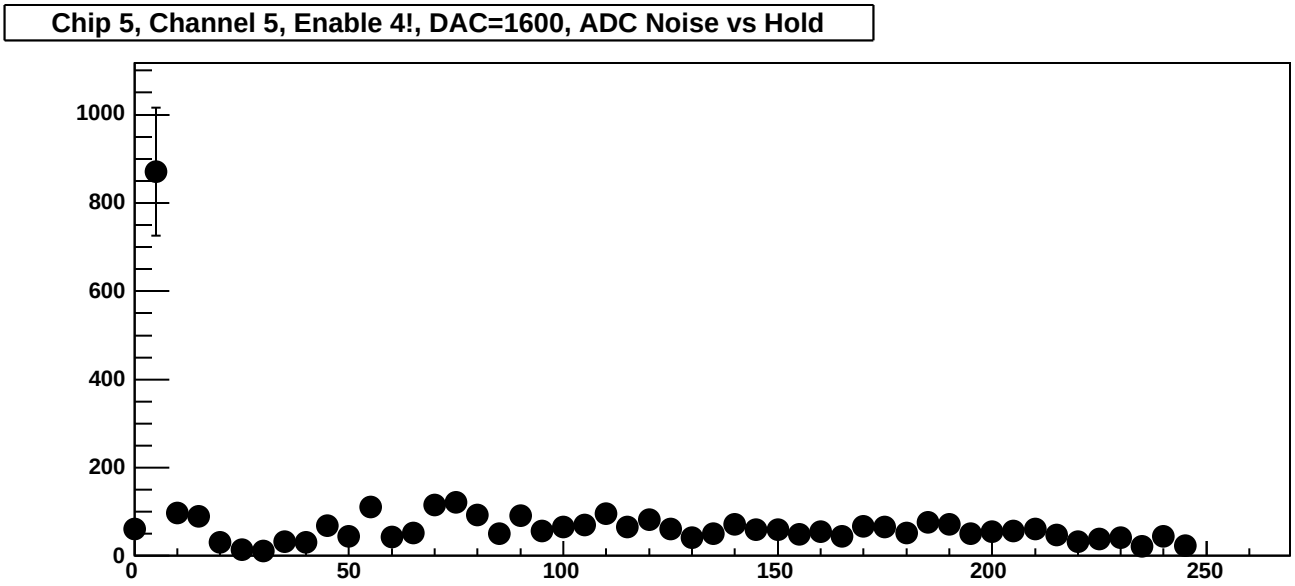

Chip 5, Channel 5, Enable 2, DAC=1600, ADC Residuals vs Hold
100
0
-100
-200
-300
-400
-500
0
50
100
150
200
250

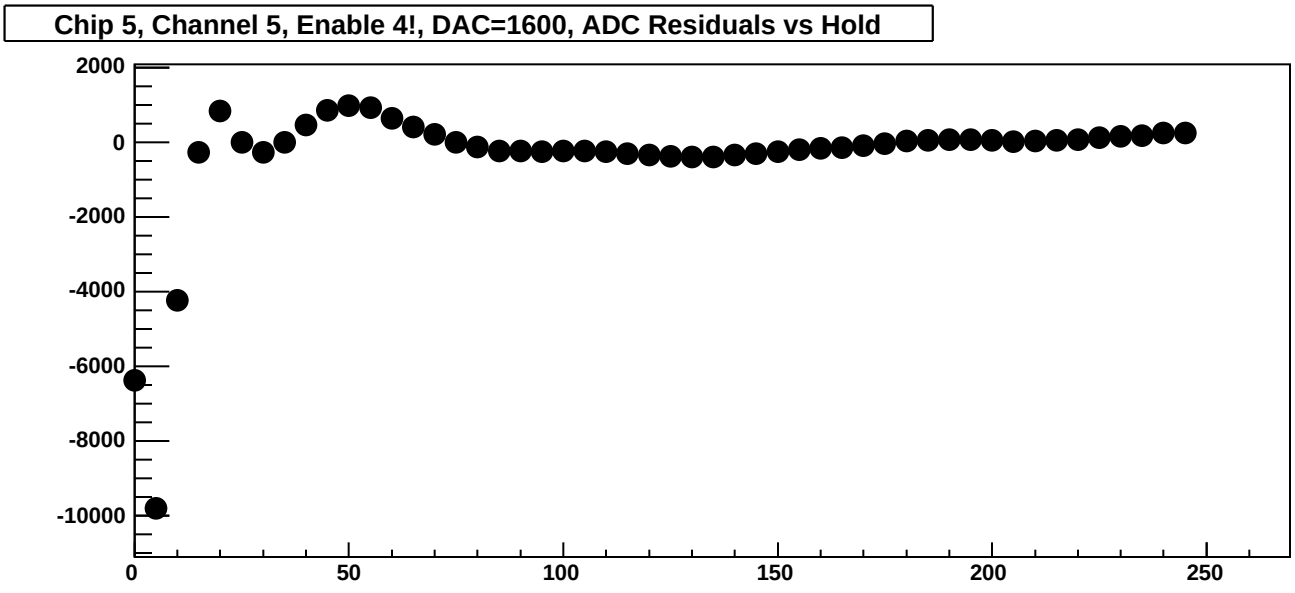

Chip 5, Channel 5, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 501.2 / 41
p0 -296.9 ± 3.523
p1 95.26 ± 0.6394
p2 -3.584e+08 ± 9.266e+06
p3 3.496e+07 ± 1.091e+06
p4 6.929 ± 0.1047
-400
-600
-800
-1000
-1200
0
50
100
150
200
250


Chip 5, Channel 5, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

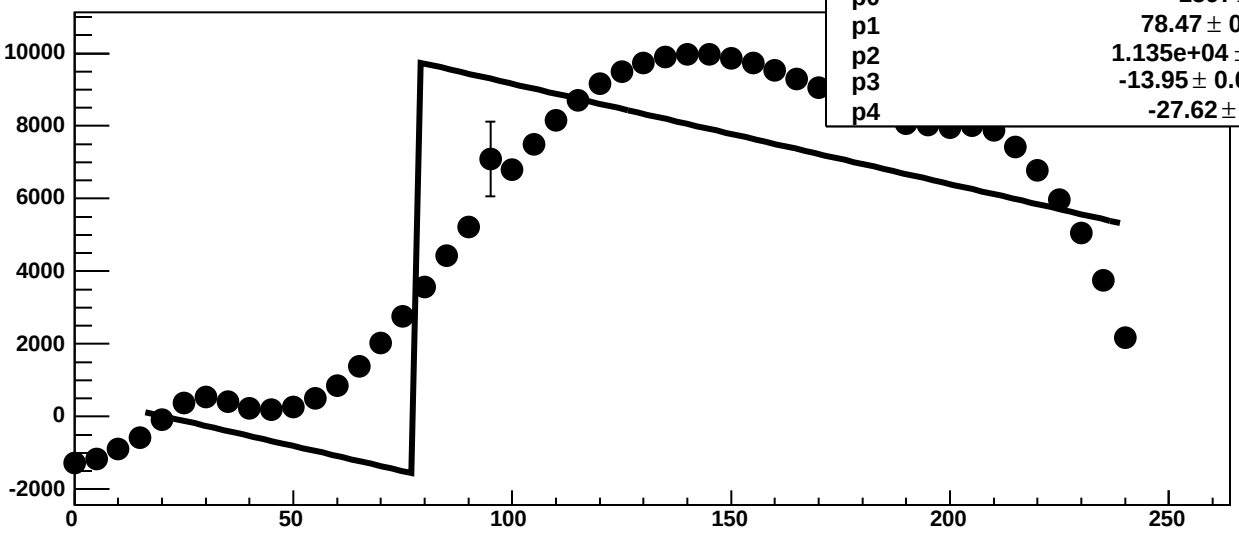

Chip 5, Channel 5, Enable 3, DAC=1600, ADC Residuals vs Hold
100
50
0
-50
-100
-150
-200
-250
-300
-350
0
50
100
150
200
250


Chip 5, Channel 5, Enable 4!, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.741e+04 / 42
p0 -4404 ± 2.952
p1 -3.725 ± 0.01933
p2 3.216e+04 ± 3.436
p3 30.35 ± 0.01053
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

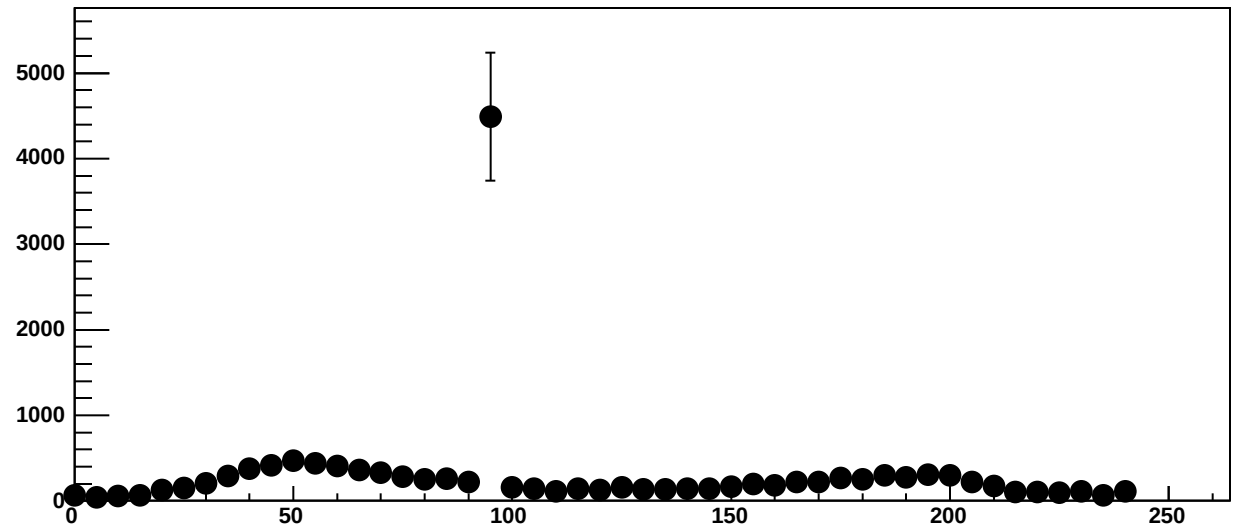

Chip 5, Channel 5, Enable 4!, DAC=1600, ADC Noise vs Hold
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 5, Channel 5, Enable 4!, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

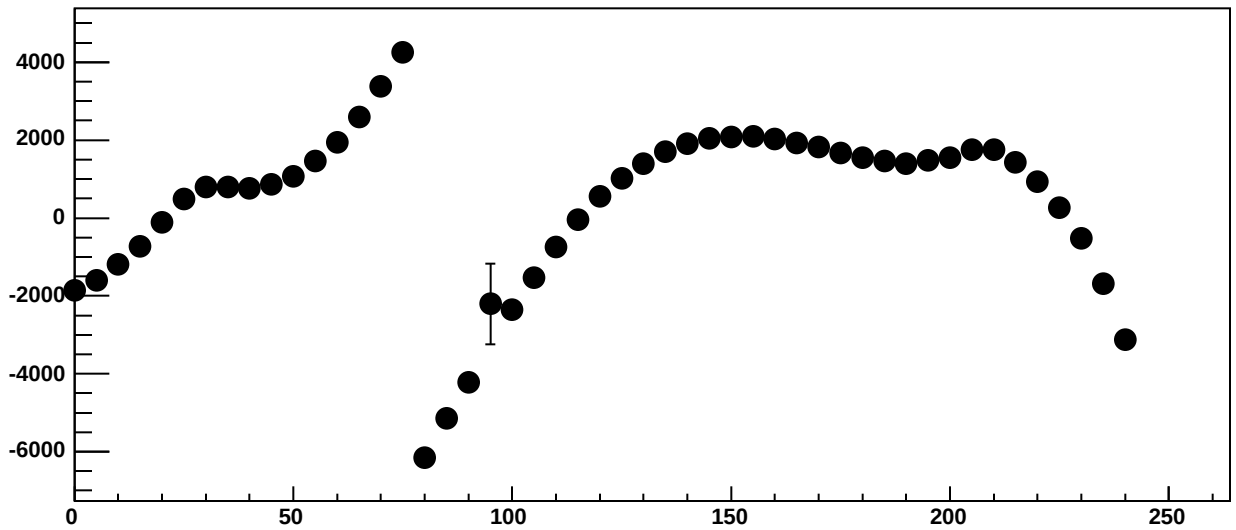
Chip 5, Channel 5, Enable 5, DAC=1600, ADC Mean vs Hold

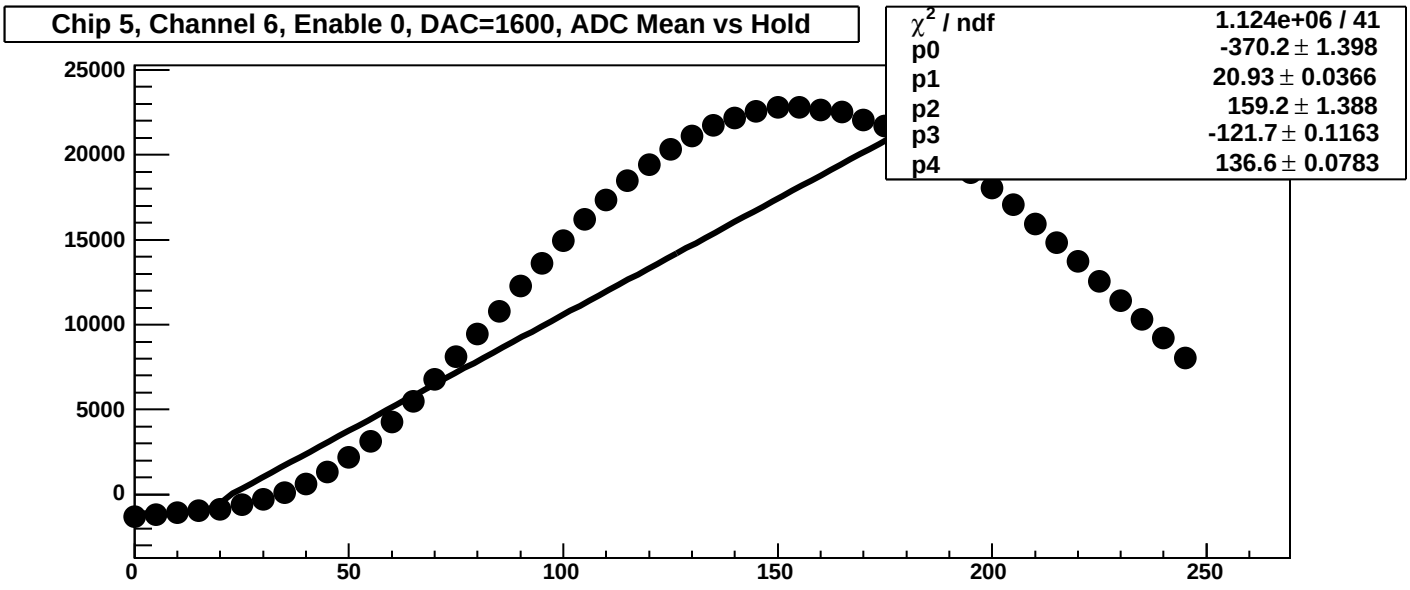


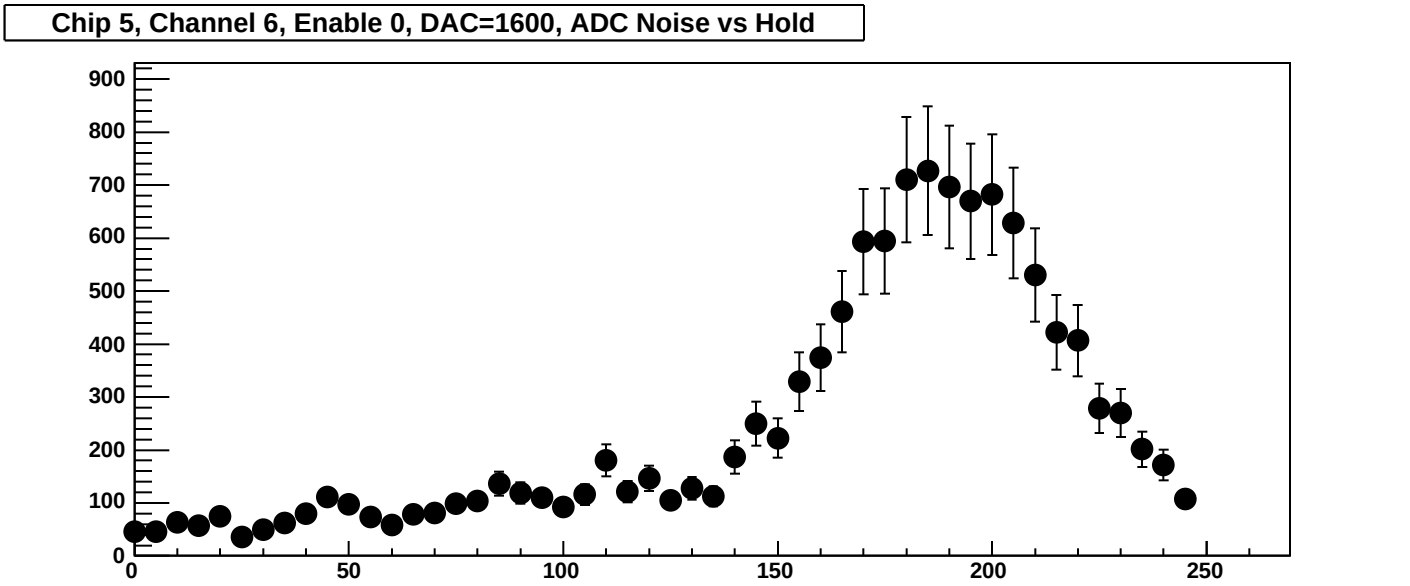
Chip 5, Channel 5, Enable 5, DAC=1600, ADC Noise vs Hold

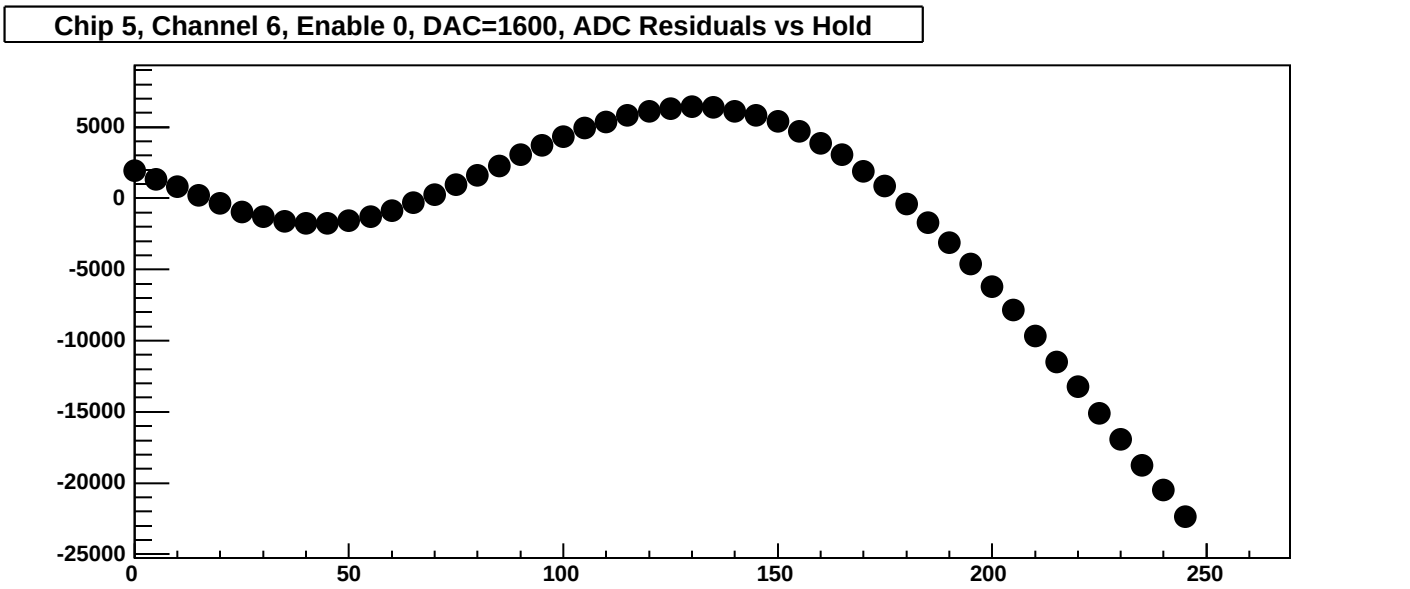


Chip 5, Channel 5, Enable 5, DAC=1600, ADC Residuals vs Hold

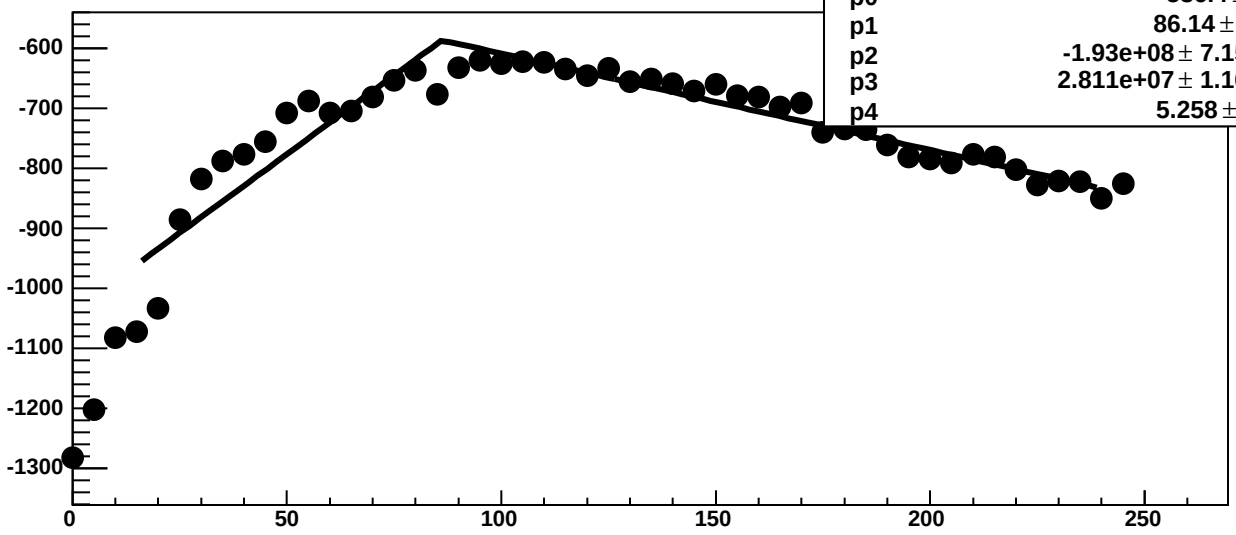



Chip 5, Channel 6, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.124e+06 / 41
p0 -370.2 ± 1.398
p1 20.93 ± 0.0366
p2 159.2 ± 1.388
p3 -121.7 ± 0.1163
p4 136.6 ± 0.0783
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

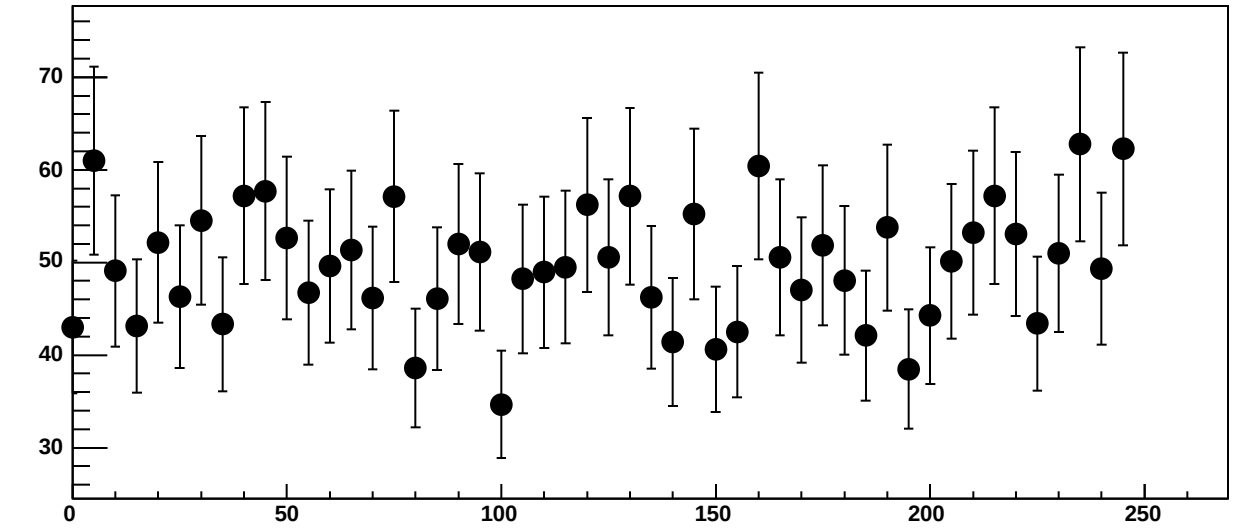

Chip 5, Channel 6, Enable 0, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 5, Channel 6, Enable 0, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
0
50
100
150
200
250

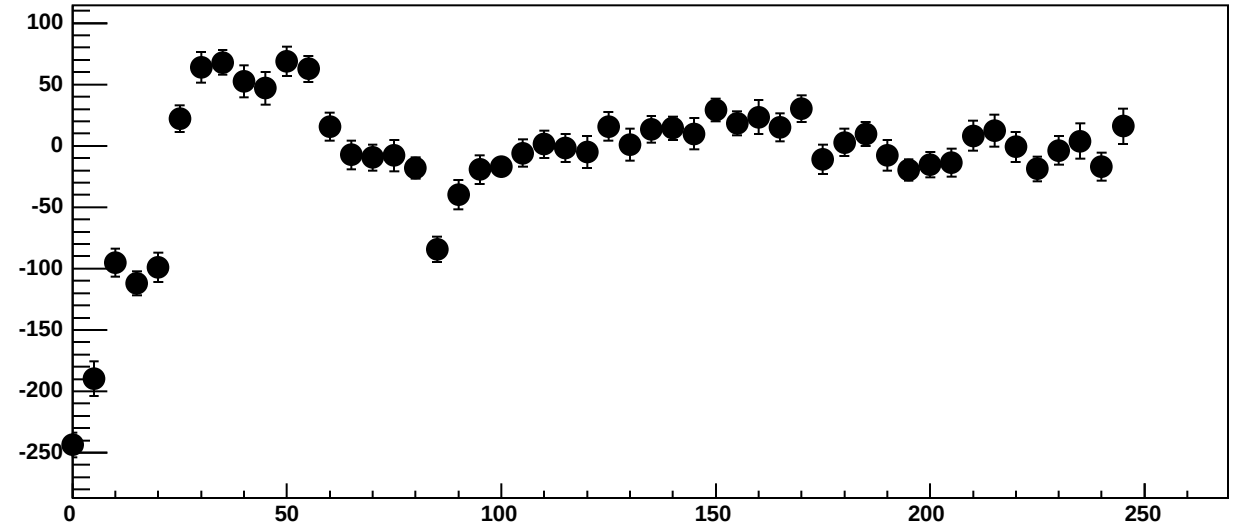
Chip 5, Channel 6, Enable 1, DAC=1600, ADC Mean vs Hold



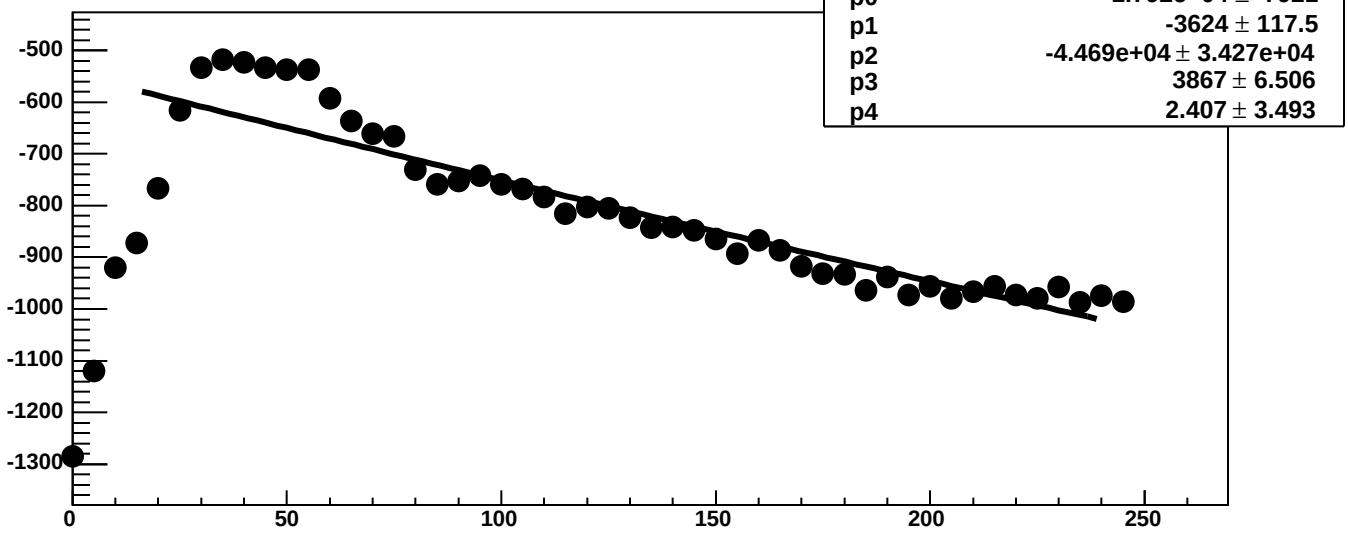
Chip 5, Channel 6, Enable 1, DAC=1600, ADC Noise vs Hold



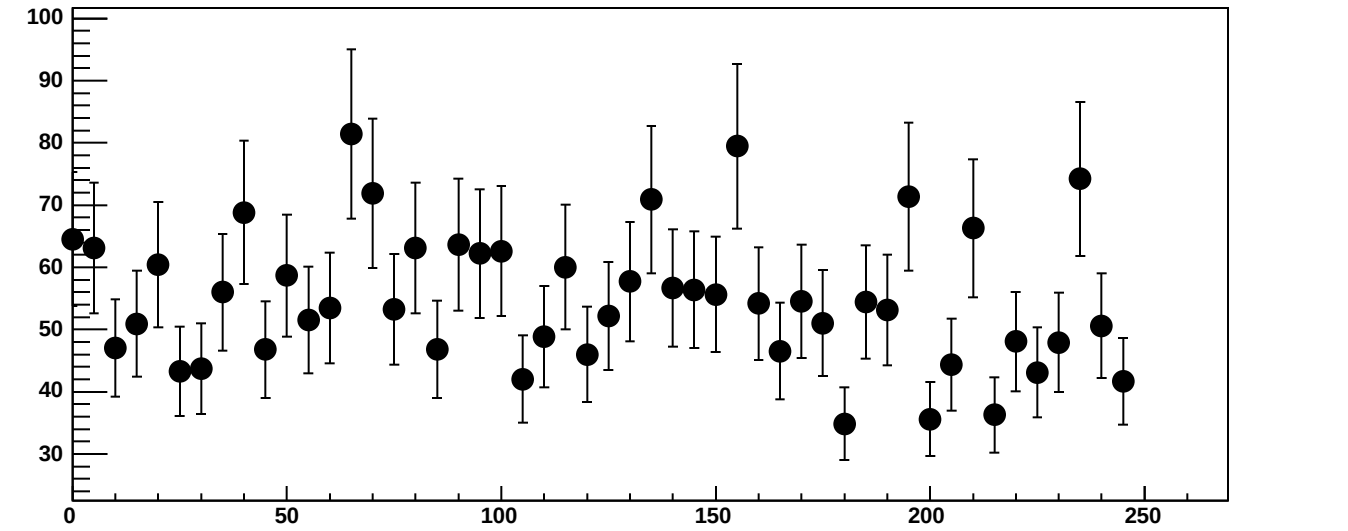
Chip 5, Channel 6, Enable 1, DAC=1600, ADC Residuals vs Hold



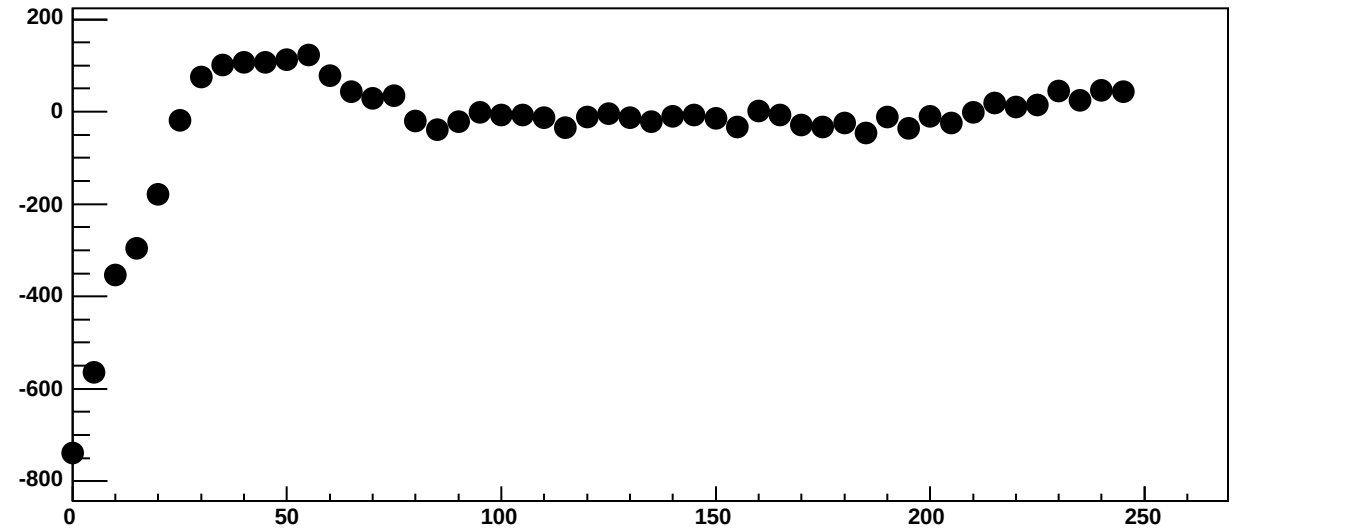
Chip 5, Channel 6, Enable 2, DAC=1600, ADC Mean vs Hold



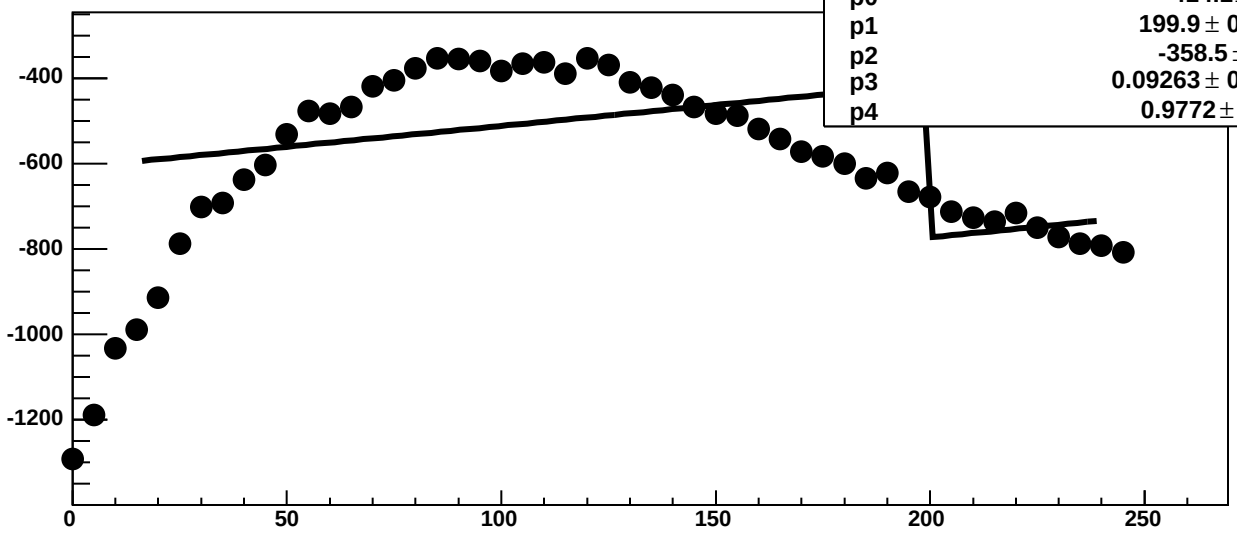
Chip 5, Channel 6, Enable 2, DAC=1600, ADC Noise vs Hold



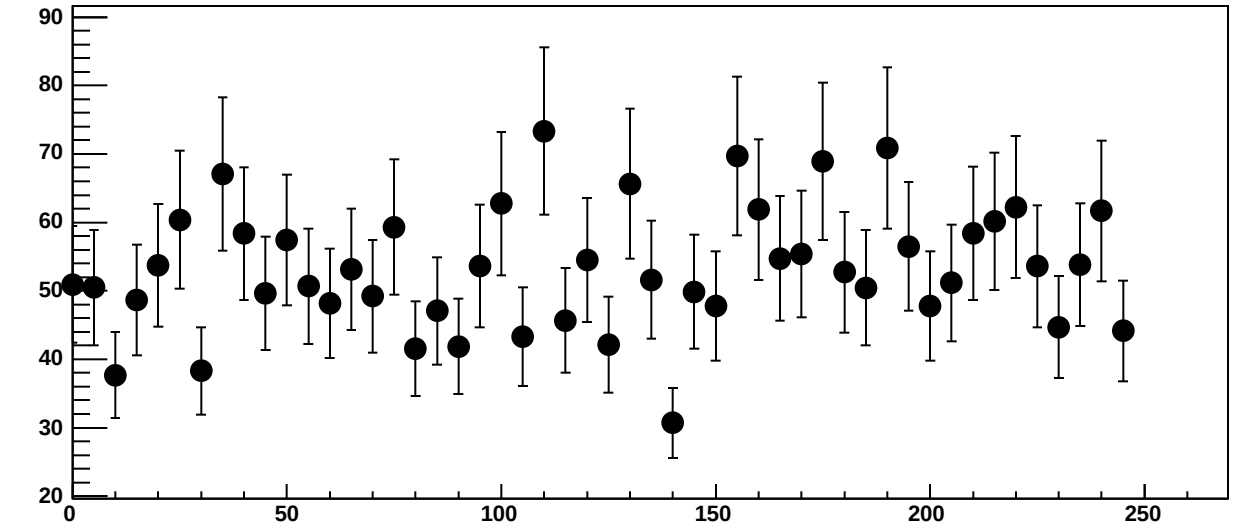
Chip 5, Channel 6, Enable 2, DAC=1600, ADC Residuals vs Hold



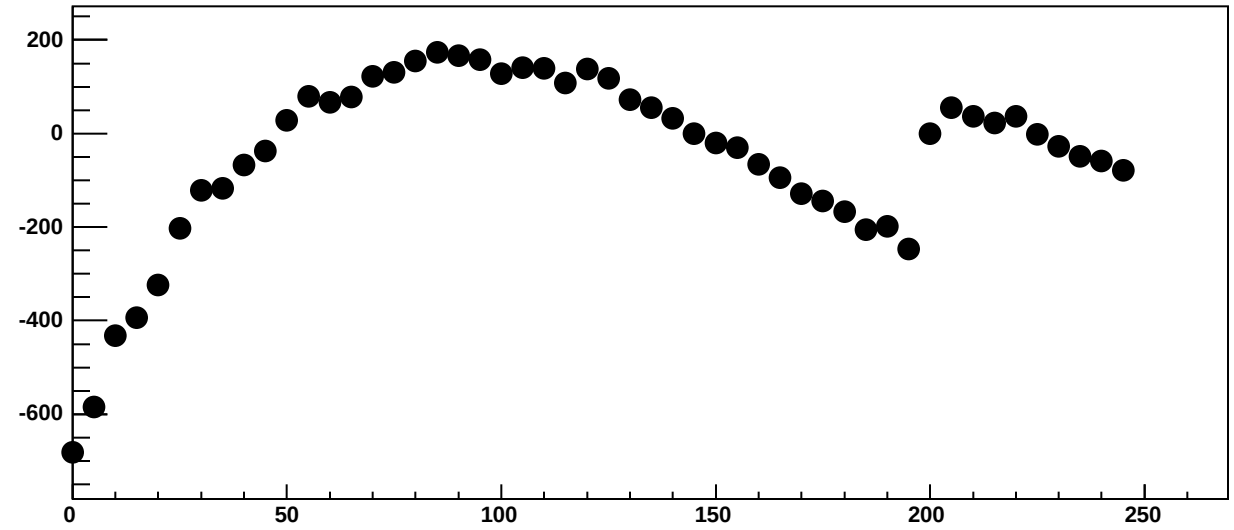
Chip 5, Channel 6, Enable 3, DAC=1600, ADC Mean vs Hold



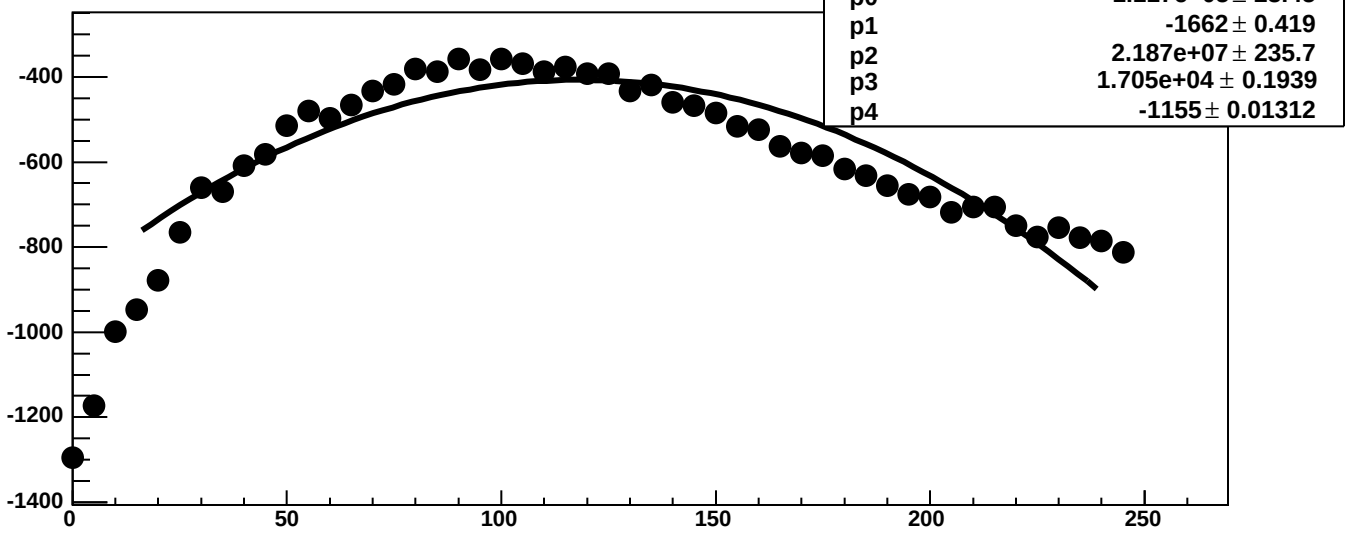
Chip 5, Channel 6, Enable 3, DAC=1600, ADC Noise vs Hold



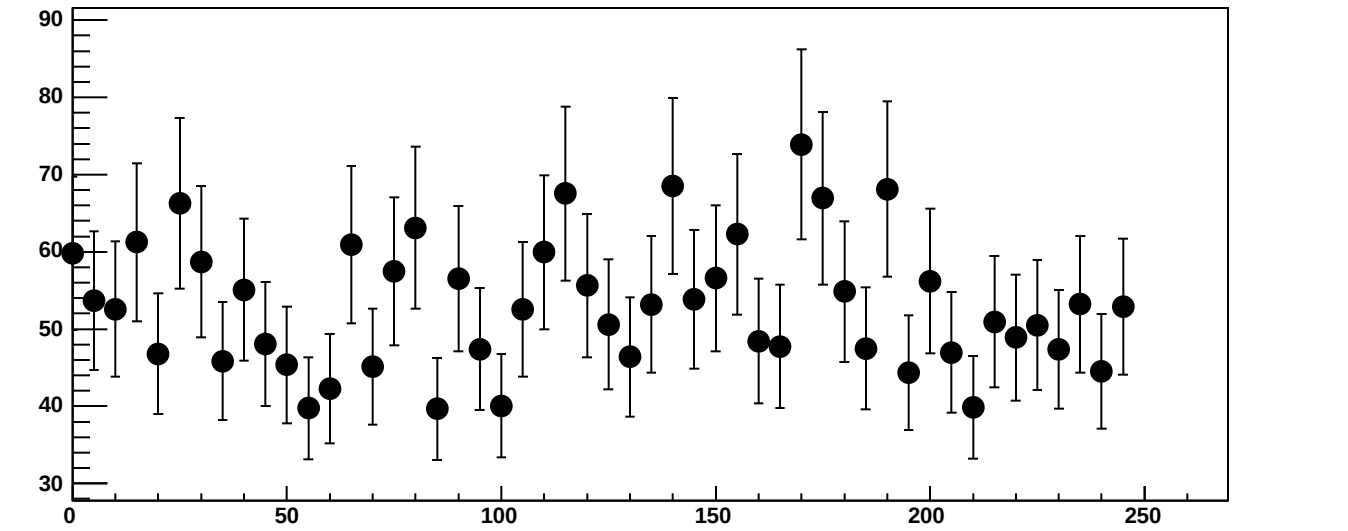
Chip 5, Channel 6, Enable 3, DAC=1600, ADC Residuals vs Hold



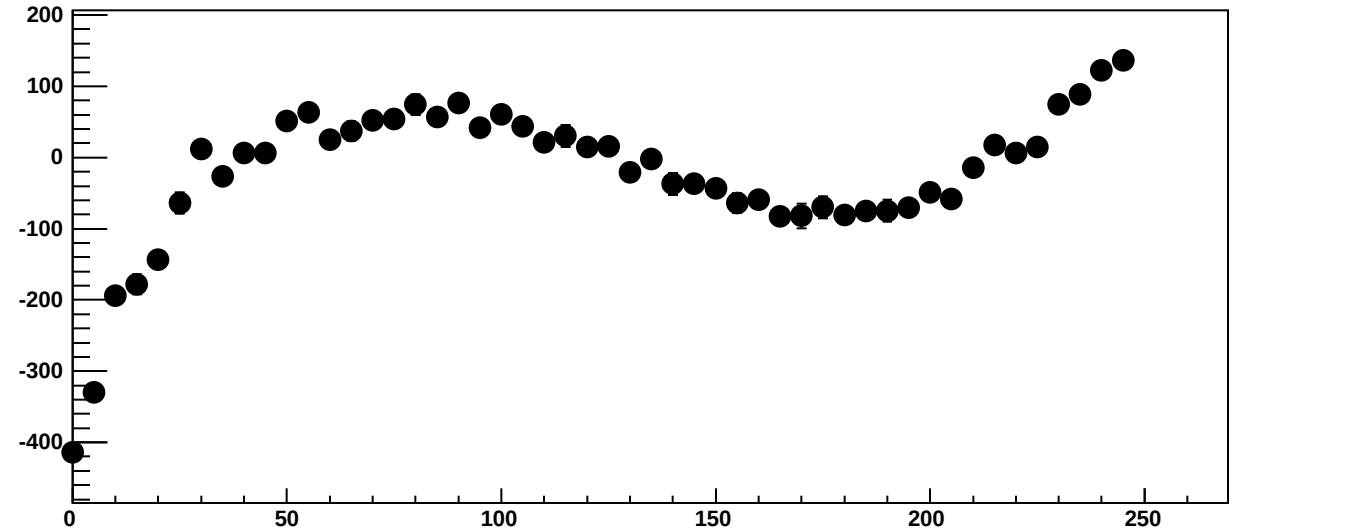
Chip 5, Channel 6, Enable 4, DAC=1600, ADC Mean vs Hold



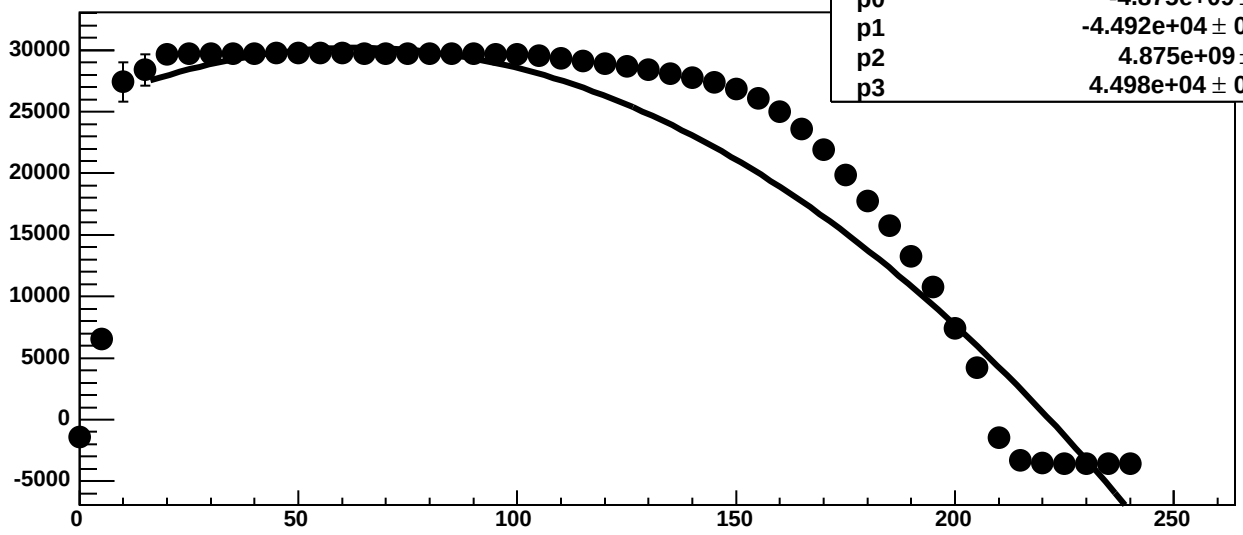
Chip 5, Channel 6, Enable 4, DAC=1600, ADC Noise vs Hold



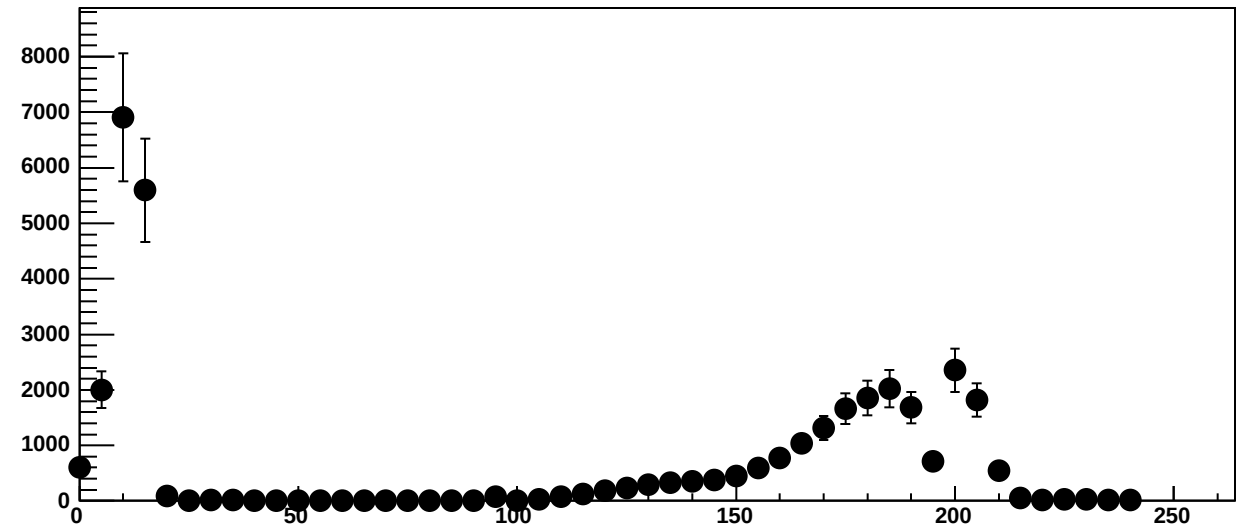
Chip 5, Channel 6, Enable 4, DAC=1600, ADC Residuals vs Hold



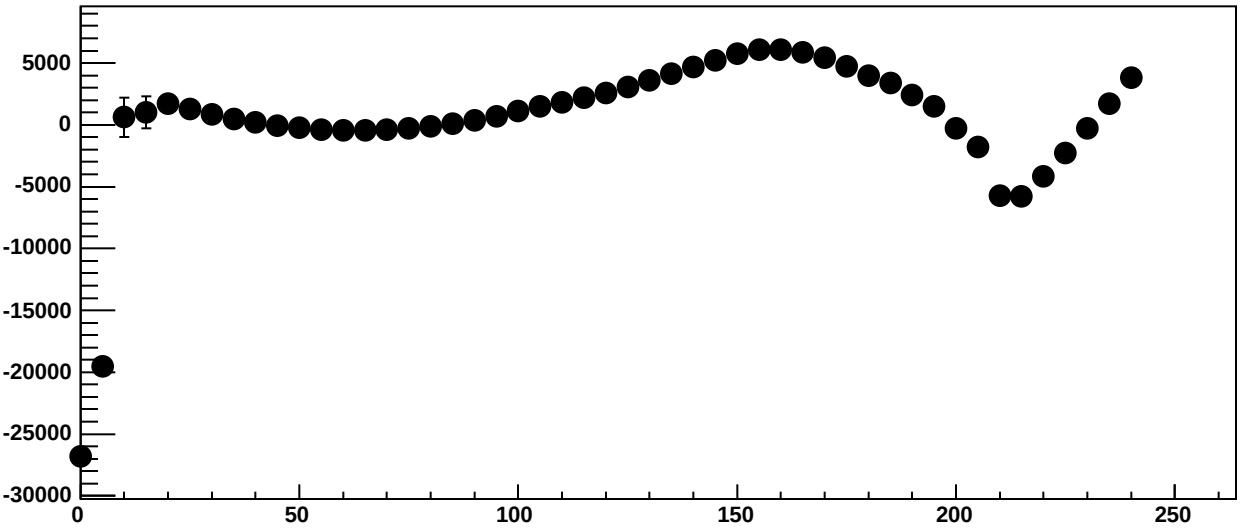
Chip 5, Channel 6, Enable 5!, DAC=1600, ADC Mean vs Hold

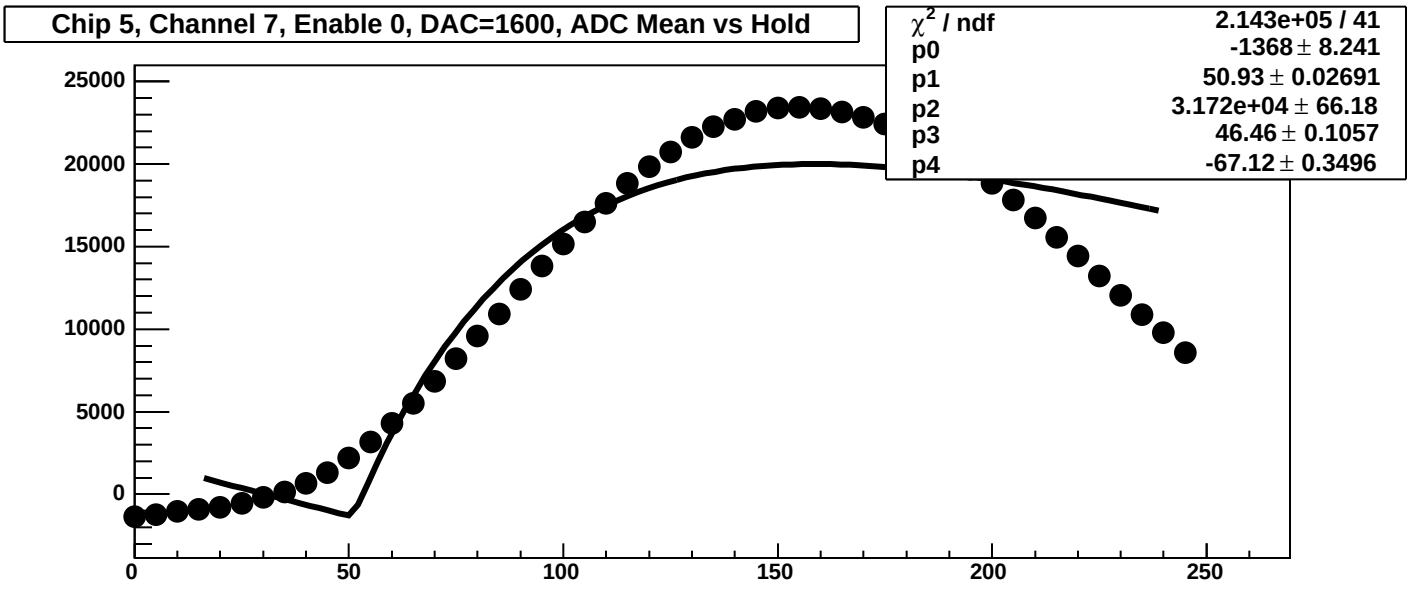


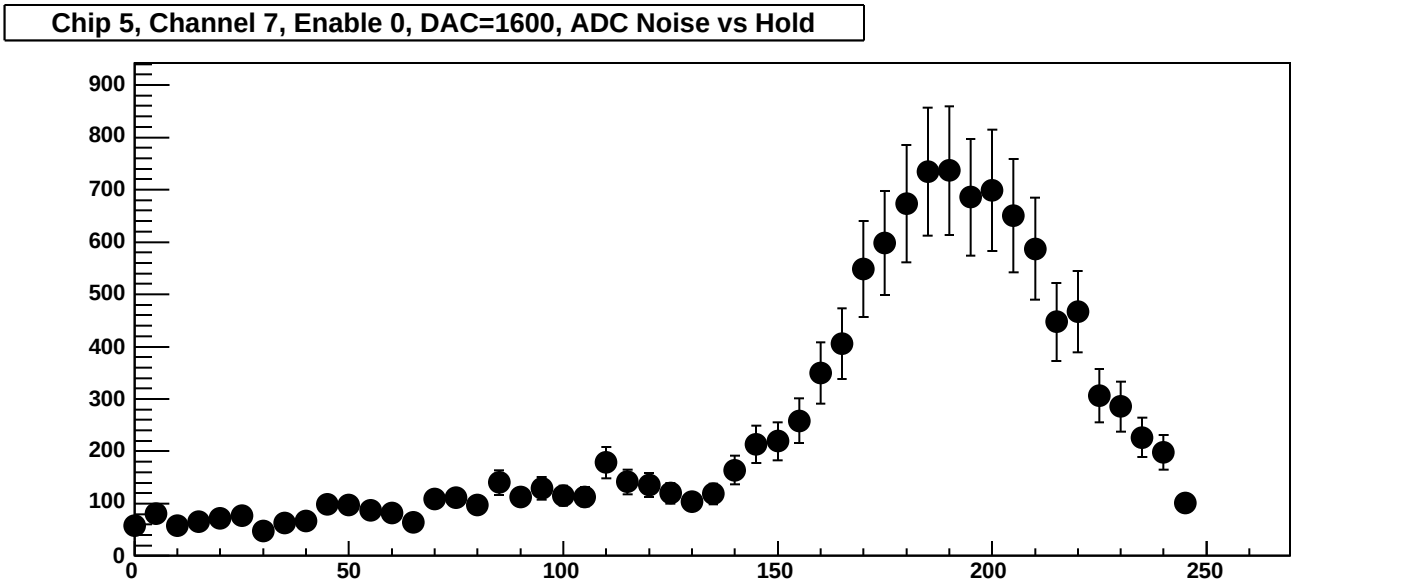
Chip 5, Channel 6, Enable 5!, DAC=1600, ADC Noise vs Hold

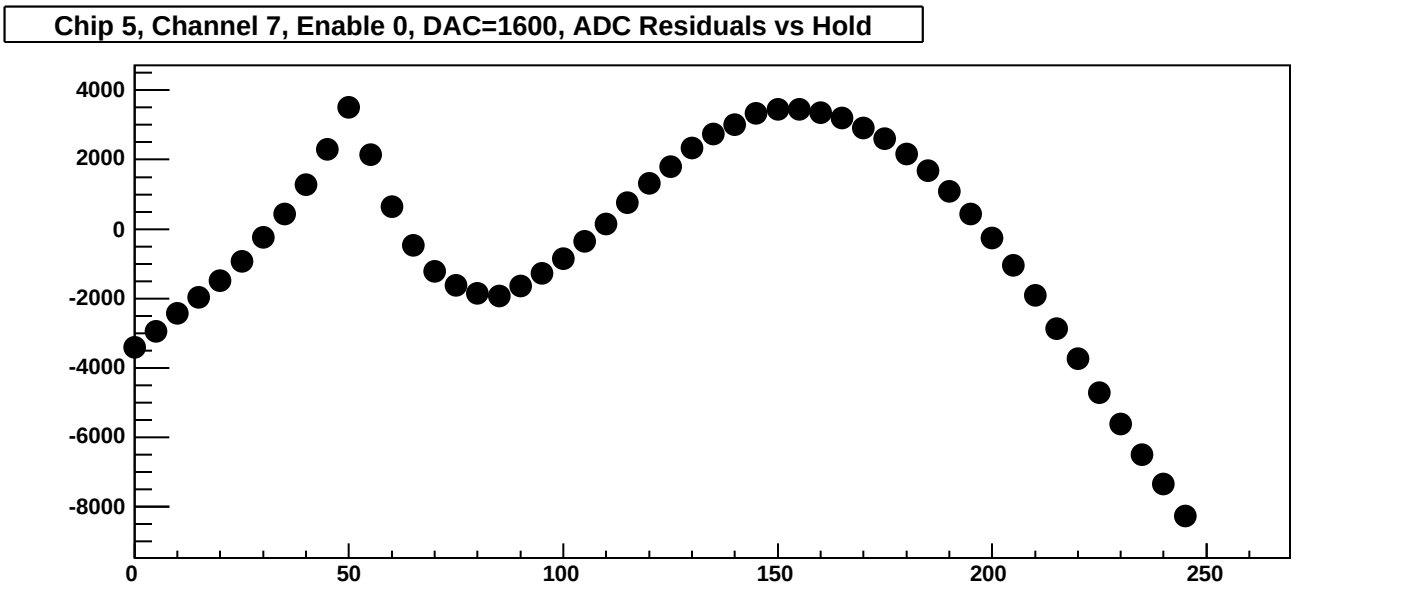


Chip 5, Channel 6, Enable 5!, DAC=1600, ADC Residuals vs Hold

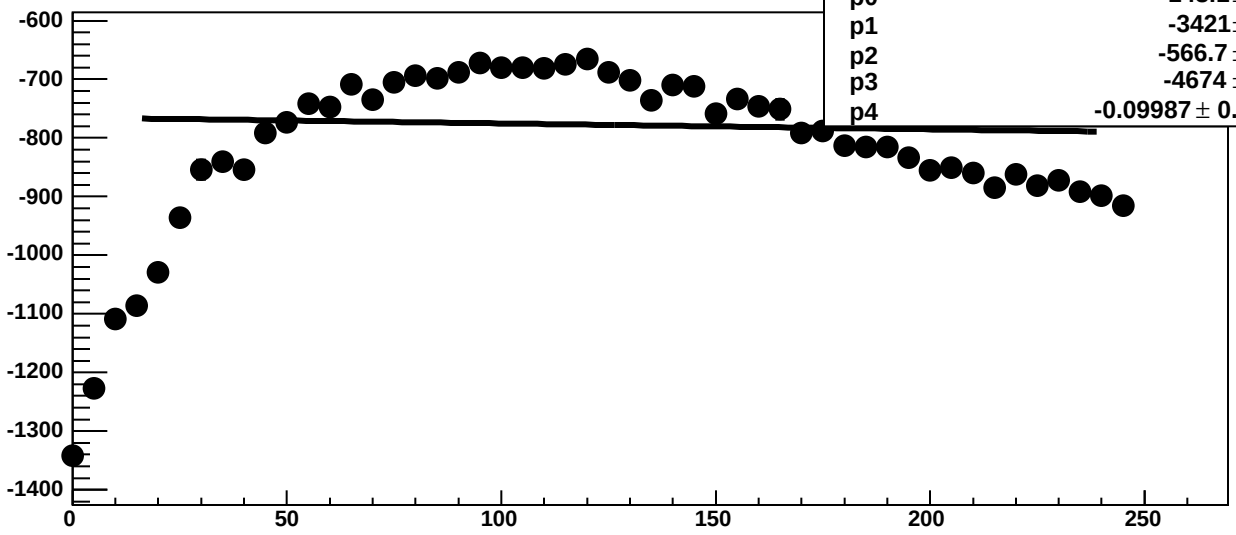



Chip 5, Channel 7, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.143e+05 / 41
p0 -1368 ± 8.241
p1 50.93 ± 0.02691
p2 3.172e+04 ± 66.18
p3 46.46 ± 0.1057
p4 -67.12 ± 0.3496
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

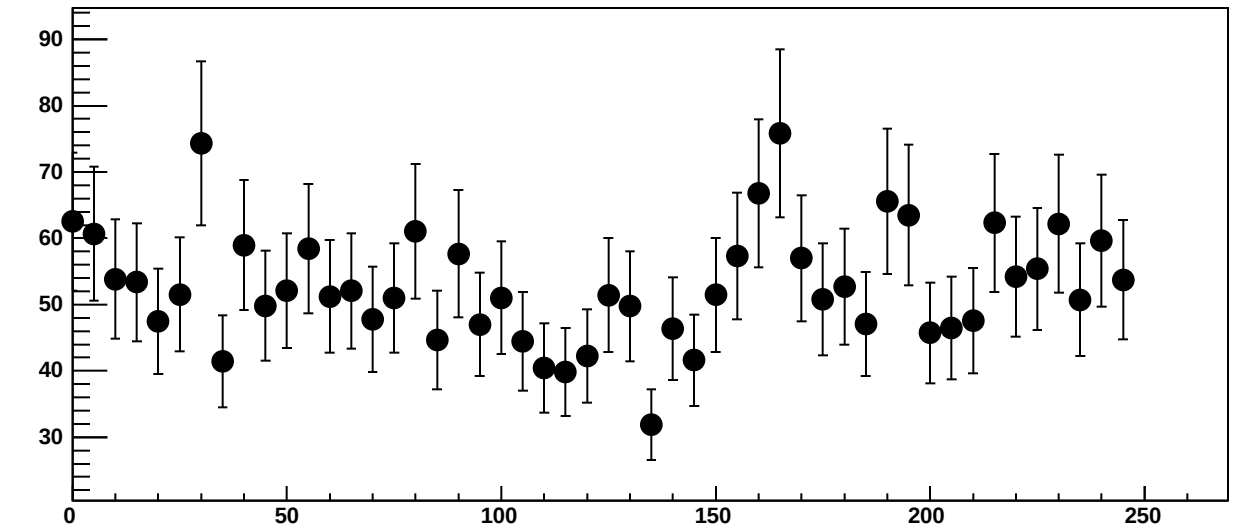

Chip 5, Channel 7, Enable 0, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 5, Channel 7, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250

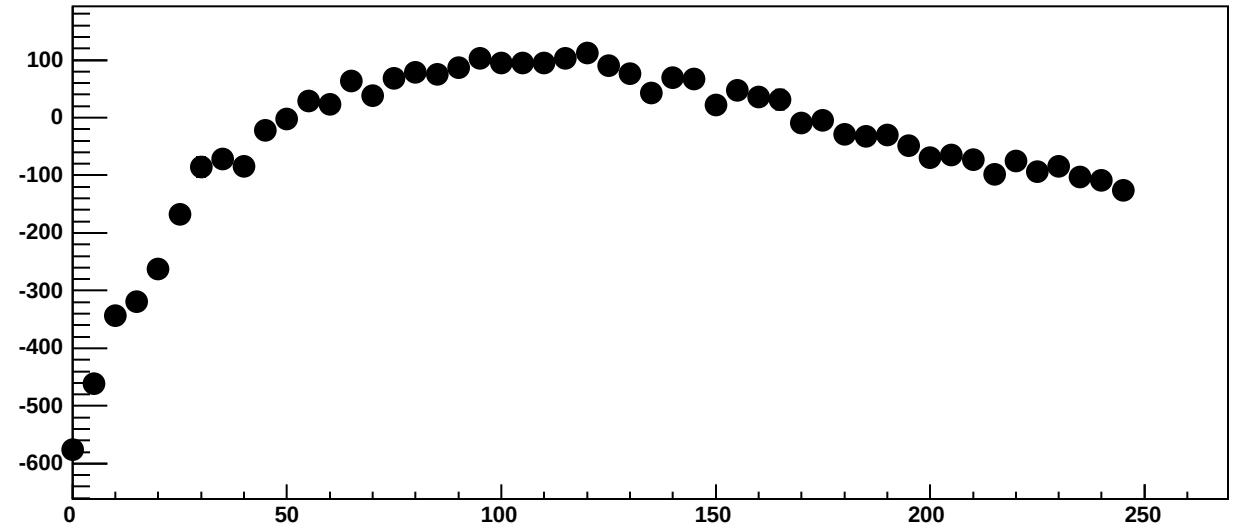
Chip 5, Channel 7, Enable 1, DAC=1600, ADC Mean vs Hold

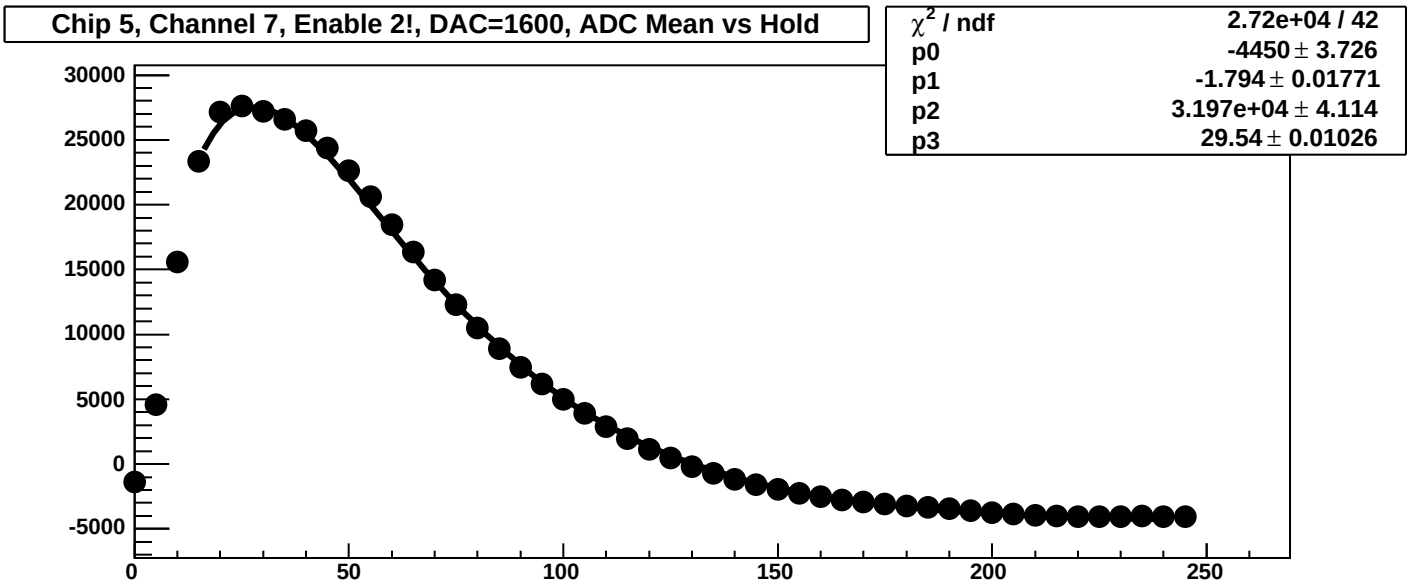


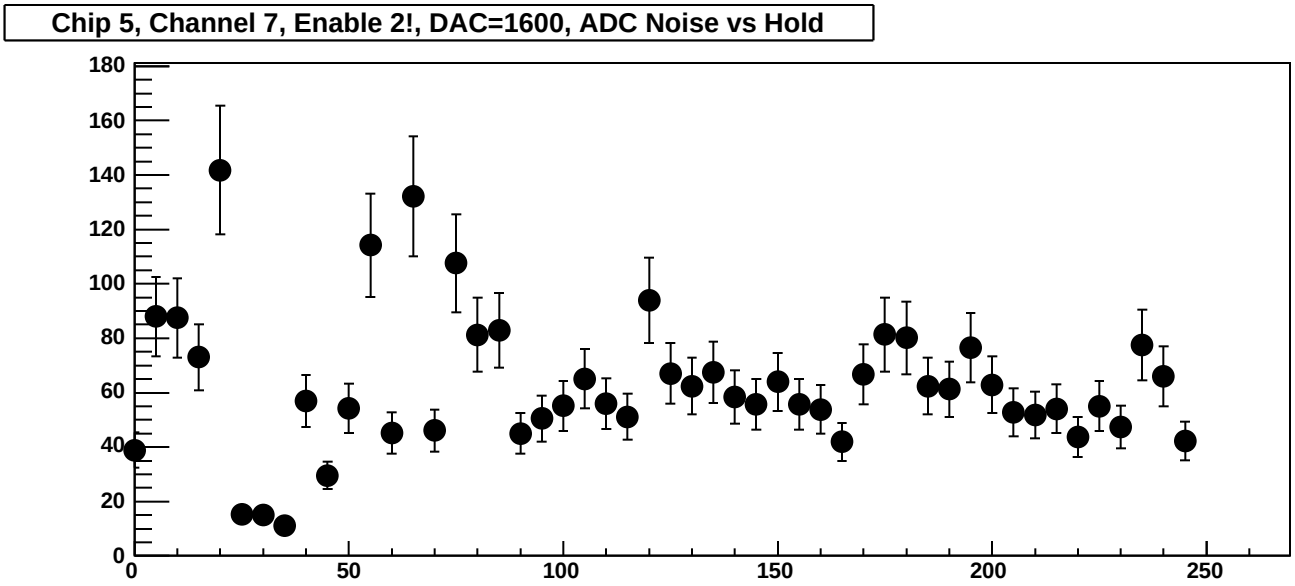
Chip 5, Channel 7, Enable 1, DAC=1600, ADC Noise vs Hold

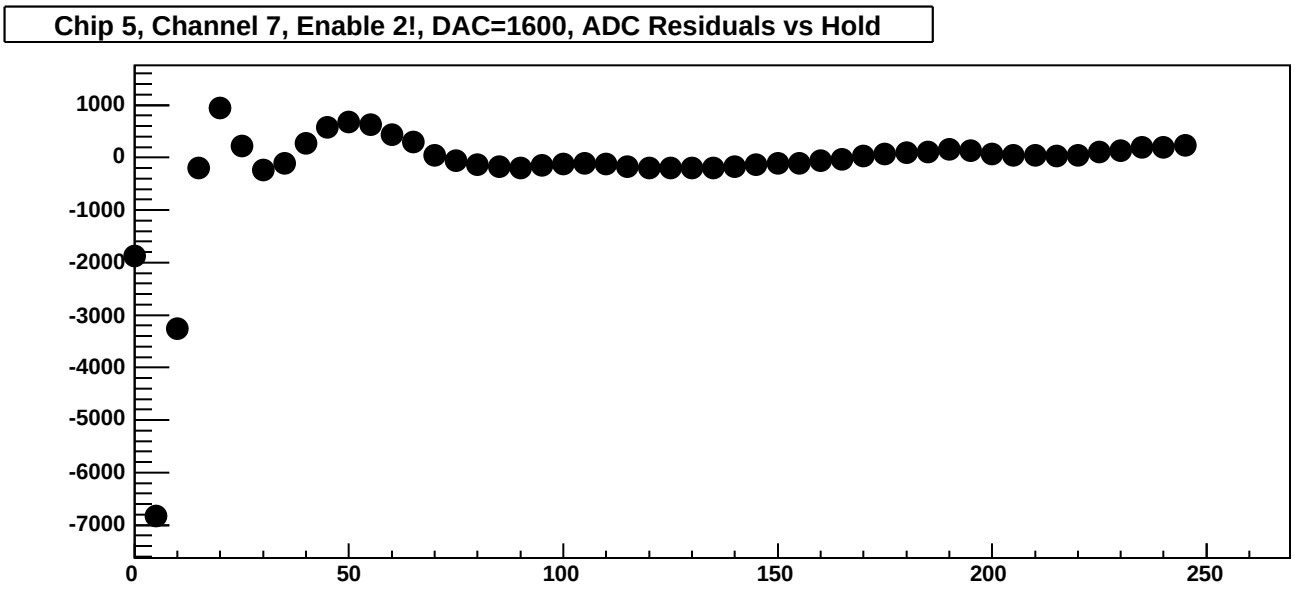


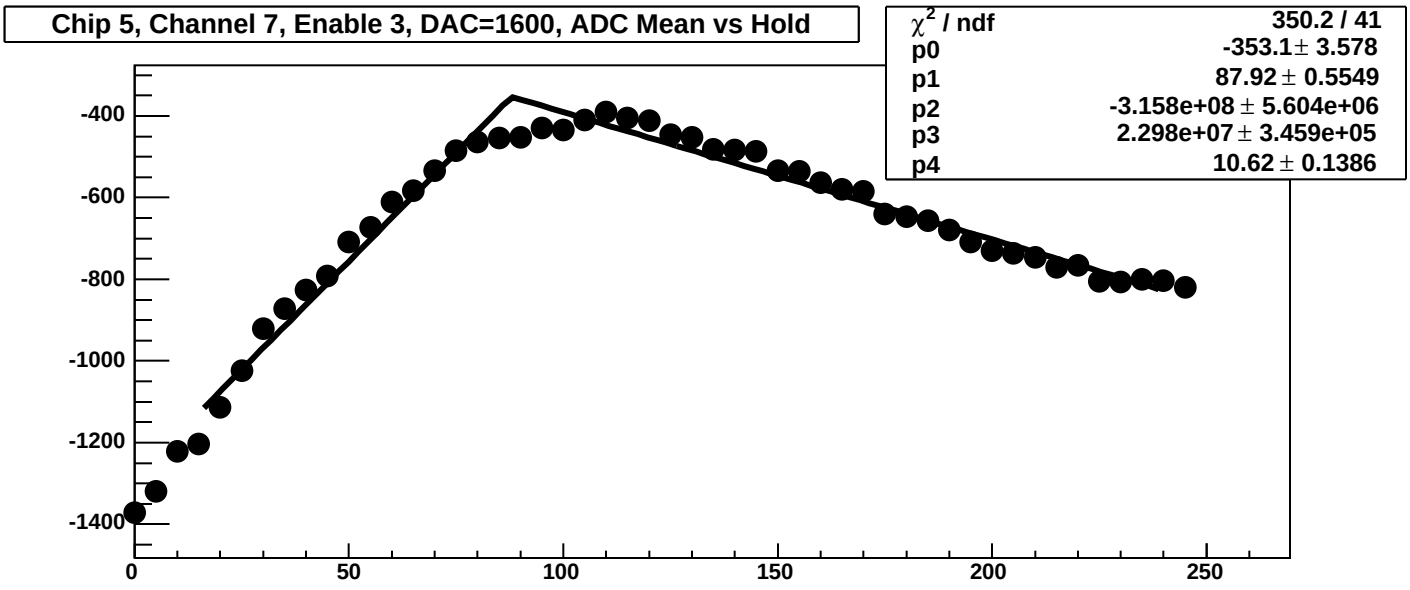
Chip 5, Channel 7, Enable 1, DAC=1600, ADC Residuals vs Hold

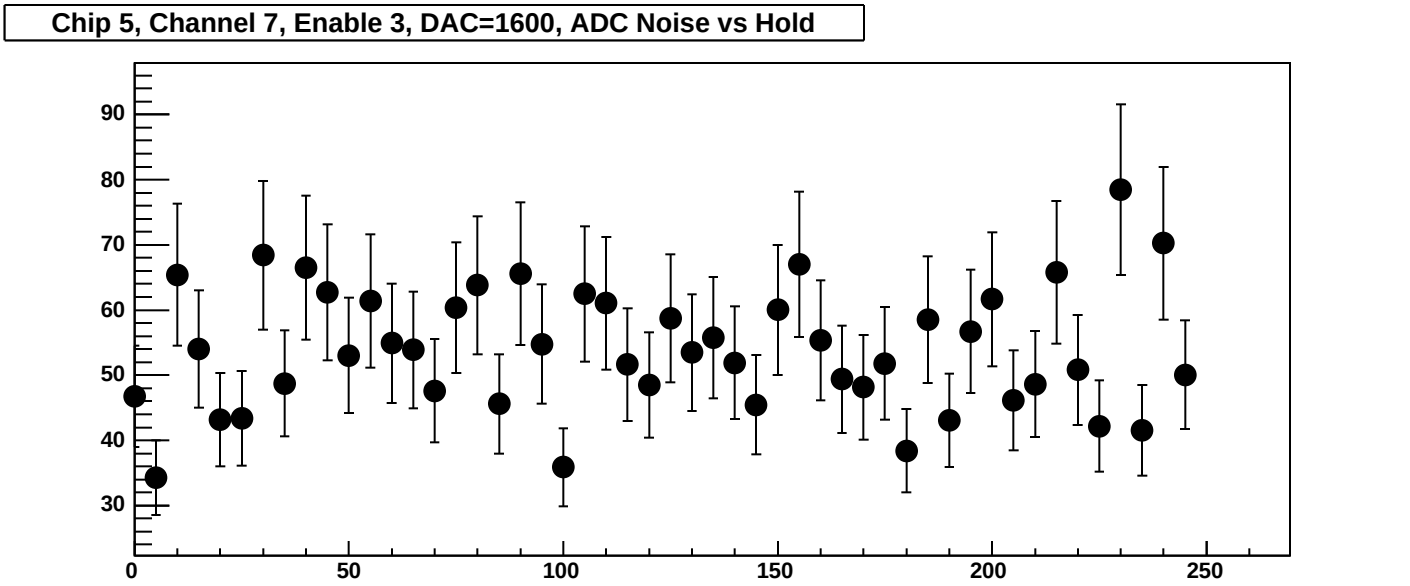


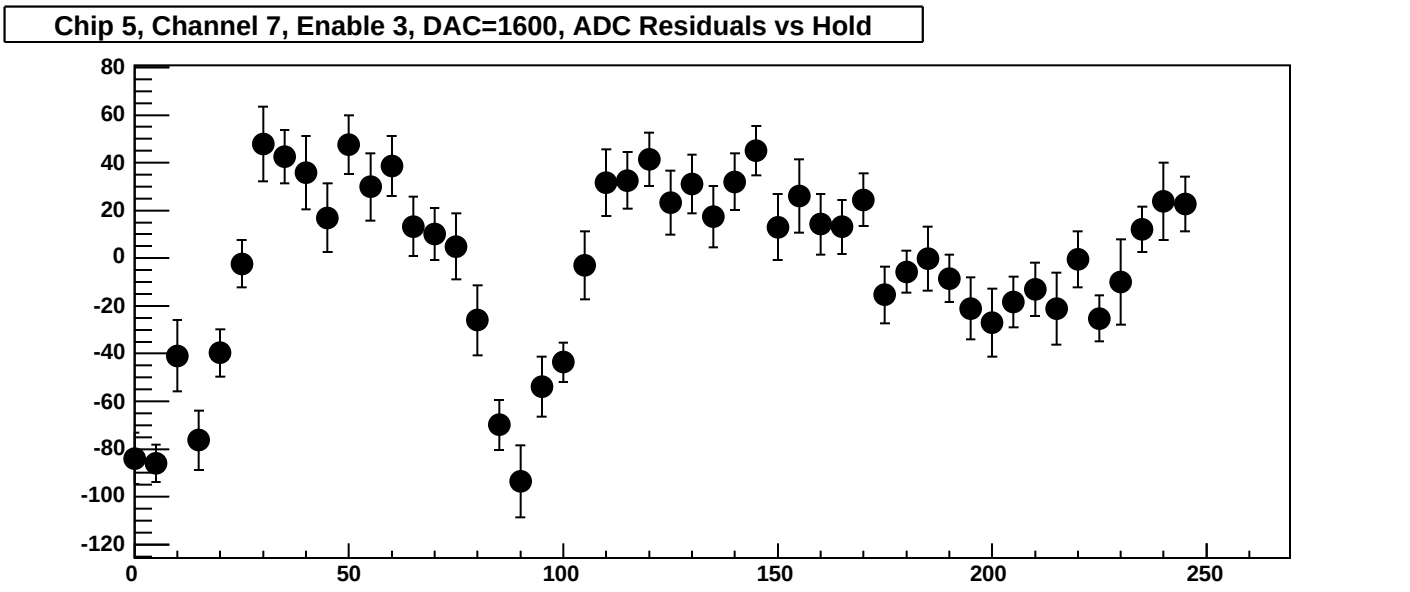

Chip 5, Channel 7, Enable 2!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.72e+04 / 42
p0 -4450 ± 3.726
p1 -1.794 ± 0.01771
p2 3.197e+04 ± 4.114
p3 29.54 ± 0.01026
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

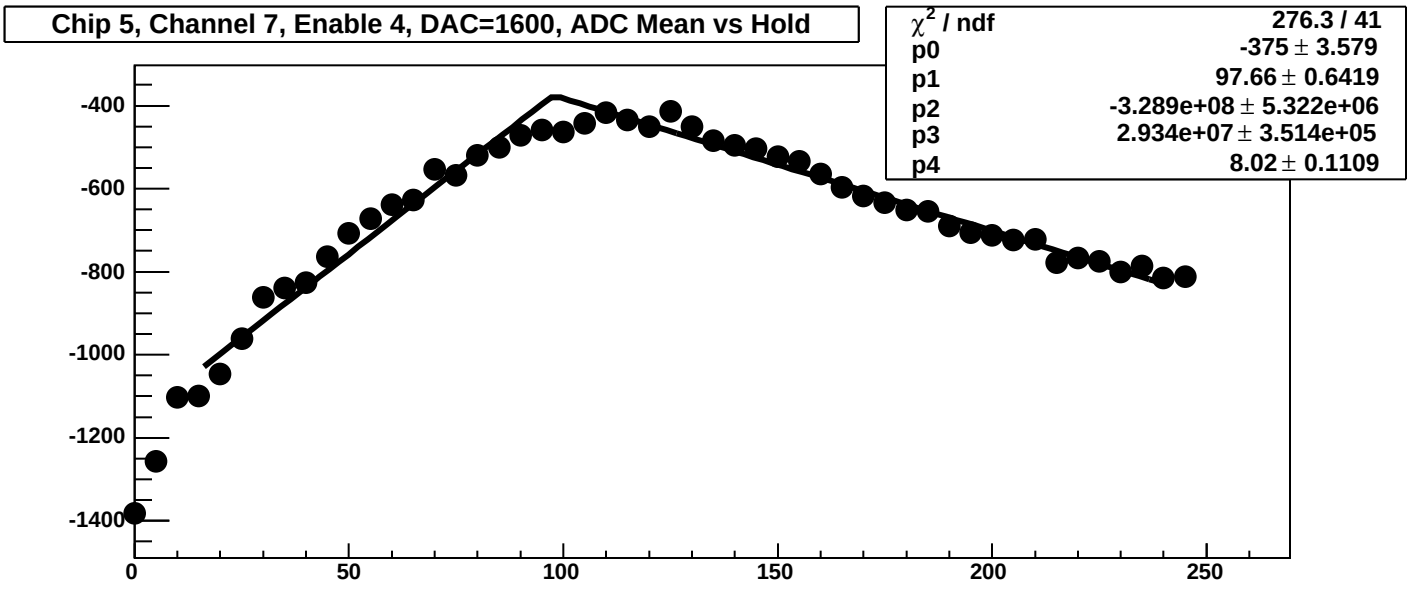

Chip 5, Channel 7, Enable 2!, DAC=1600, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

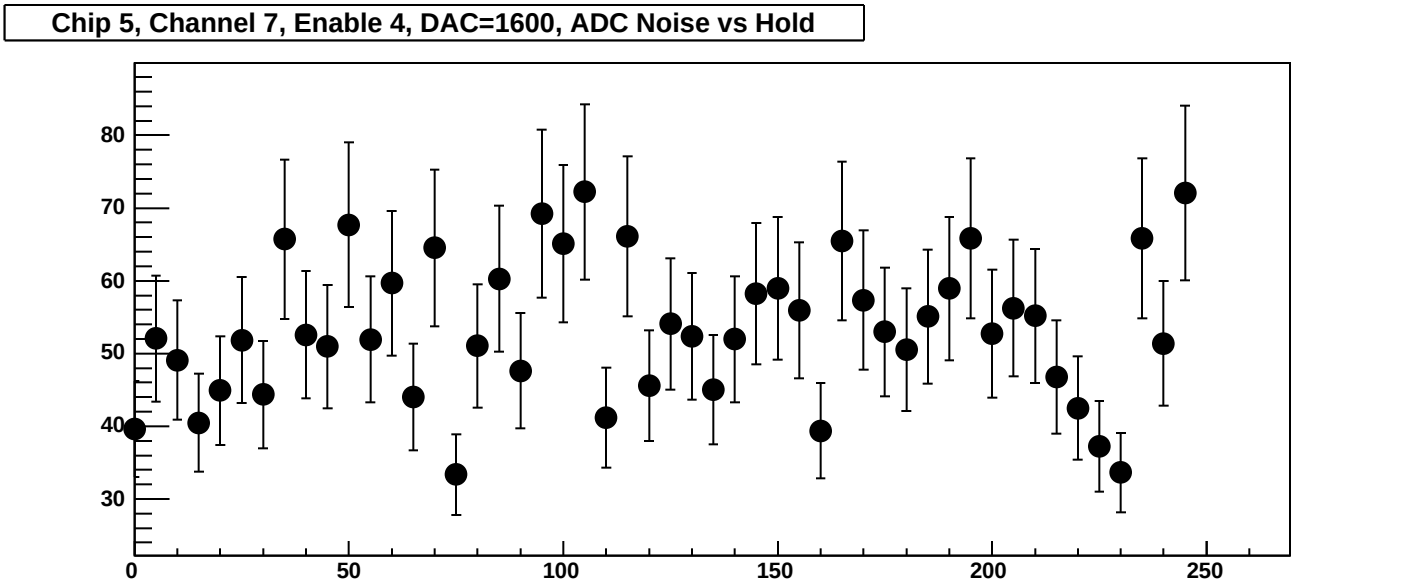

Chip 5, Channel 7, Enable 2!, DAC=1600, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
-7000
0
50
100
150
200
250

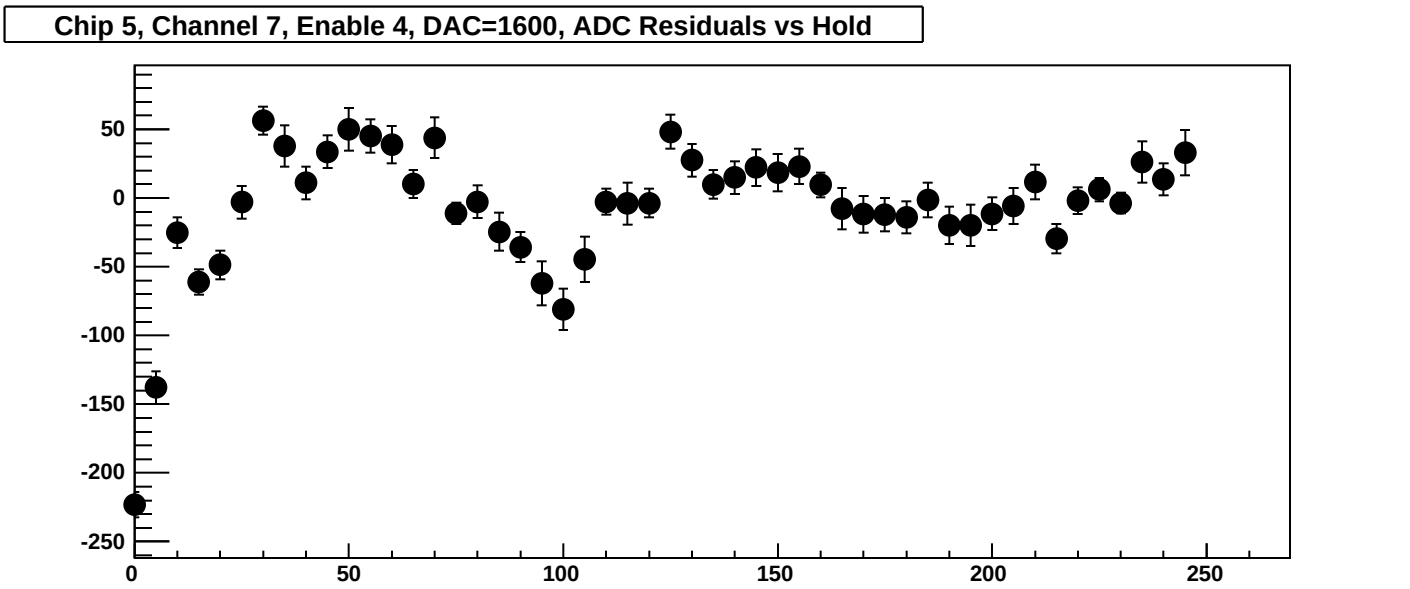

Chip 5, Channel 7, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 350.2 / 41
p0 -353.1 ± 3.578
p1 87.92 ± 0.5549
p2 -3.158e+08 ± 5.604e+06
p3 2.298e+07 ± 3.459e+05
p4 10.62 ± 0.1386
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250


Chip 5, Channel 7, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

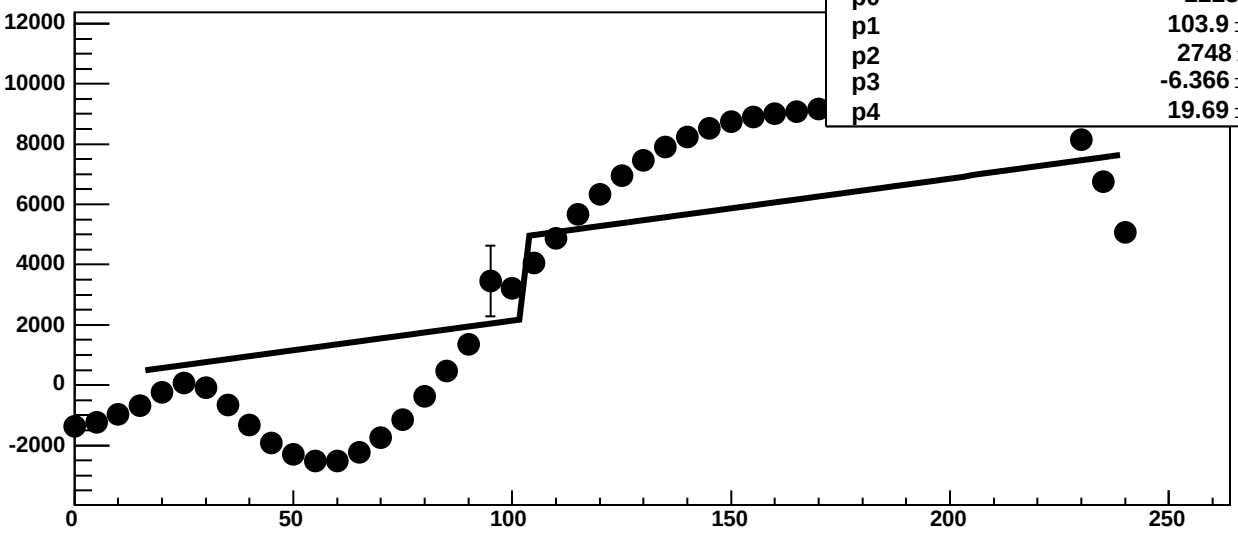

Chip 5, Channel 7, Enable 3, DAC=1600, ADC Residuals vs Hold
80
60
40
20
0
-20
-40
-60
-80
-100
-120
0
50
100
150
200
250


Chip 5, Channel 7, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 276.3 / 41
p0 -375 ± 3.579
p1 97.66 ± 0.6419
p2 -3.289e+08 ± 5.322e+06
p3 2.934e+07 ± 3.514e+05
p4 8.02 ± 0.1109
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

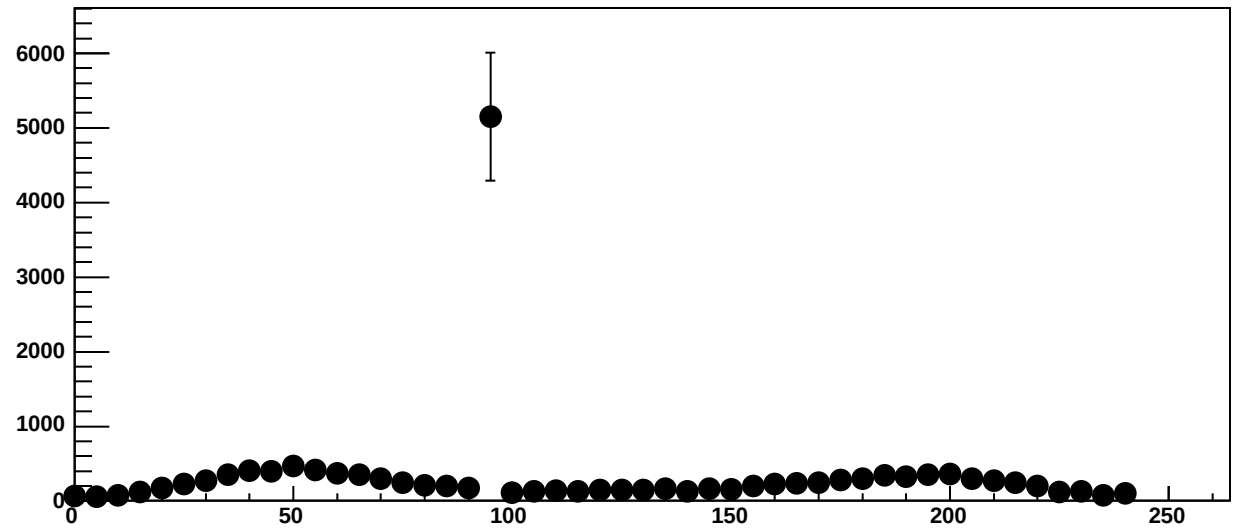

Chip 5, Channel 7, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 5, Channel 7, Enable 4, DAC=1600, ADC Residuals vs Hold
50
0
-50
-100
-150
-200
-250
0
50
100
150
200
250

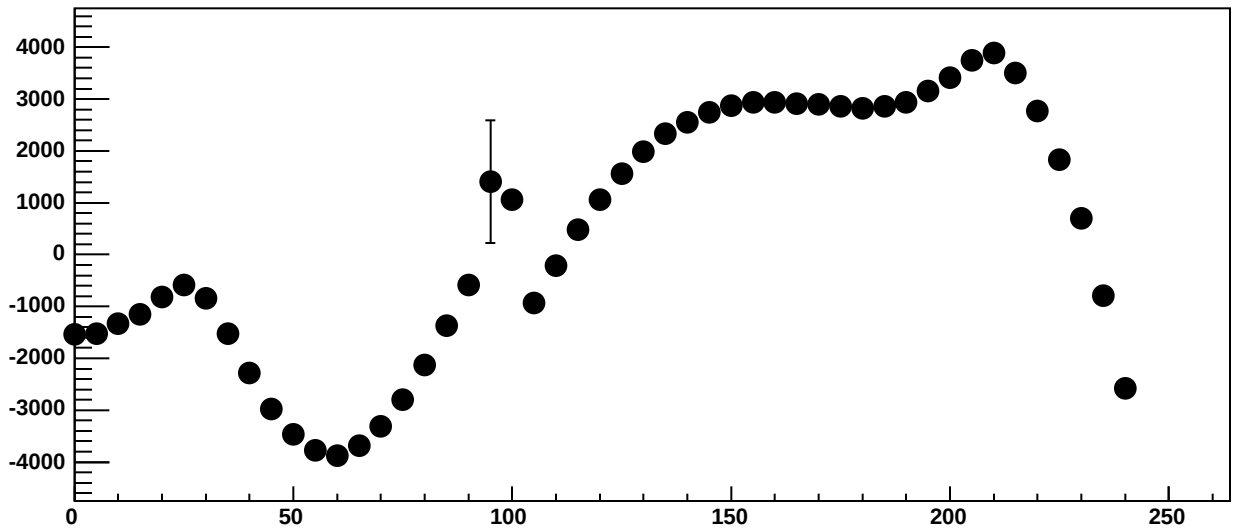
Chip 5, Channel 7, Enable 5, DAC=1600, ADC Mean vs Hold

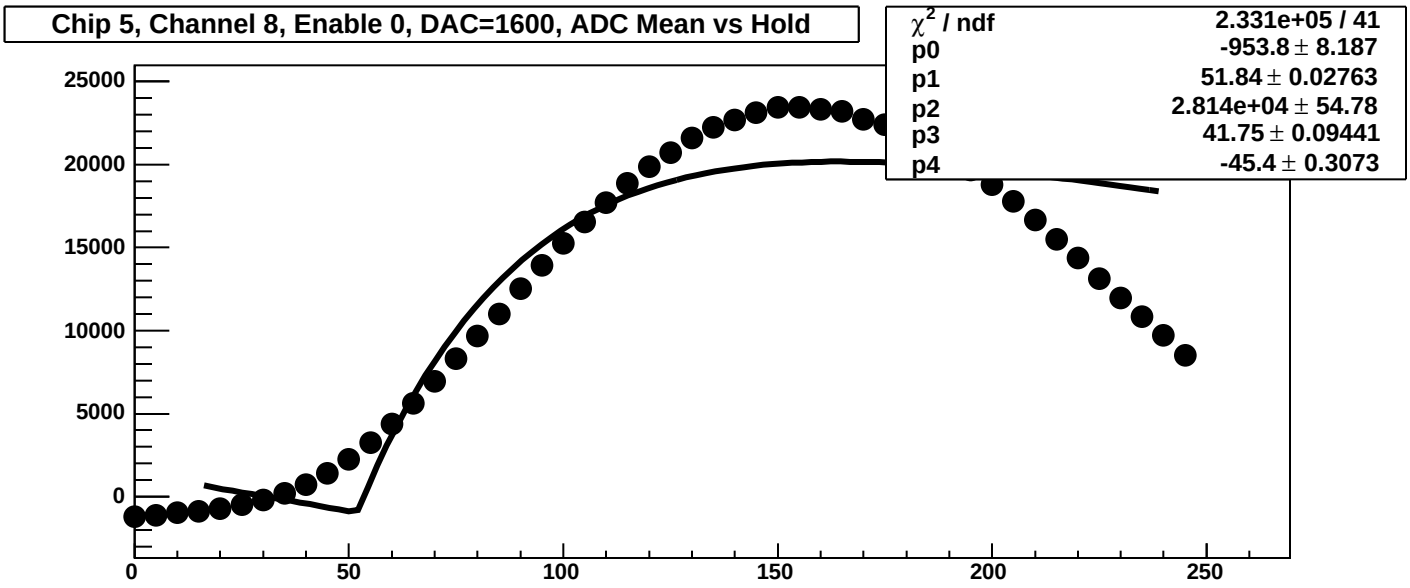


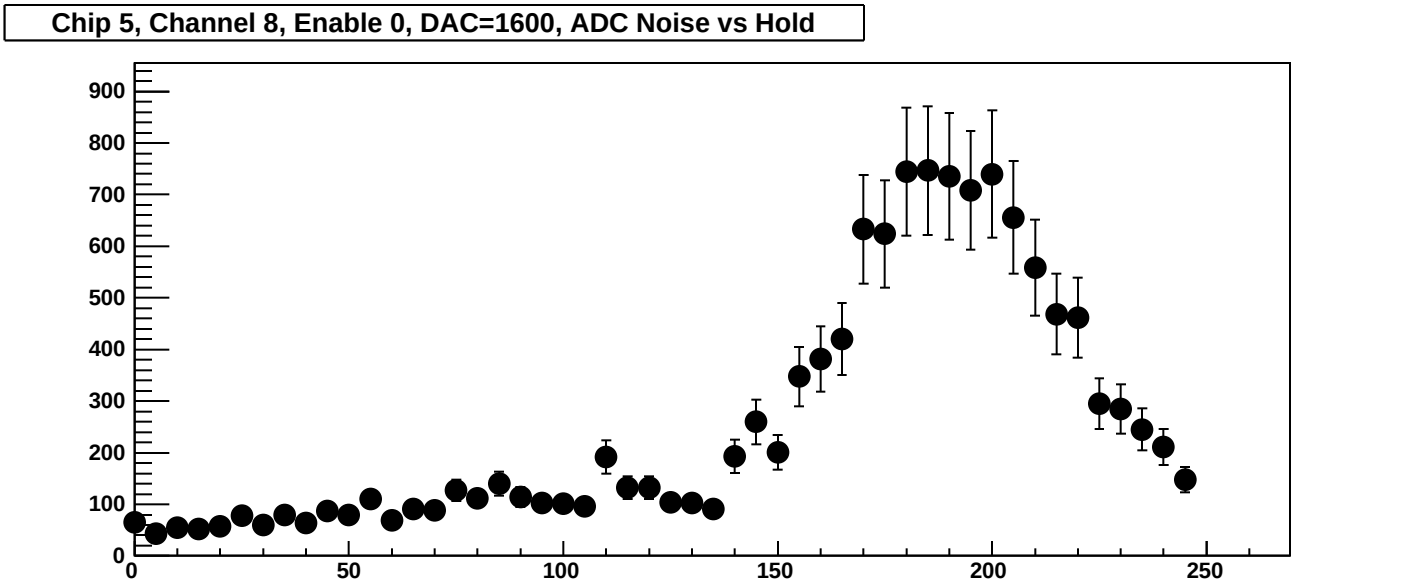
Chip 5, Channel 7, Enable 5, DAC=1600, ADC Noise vs Hold

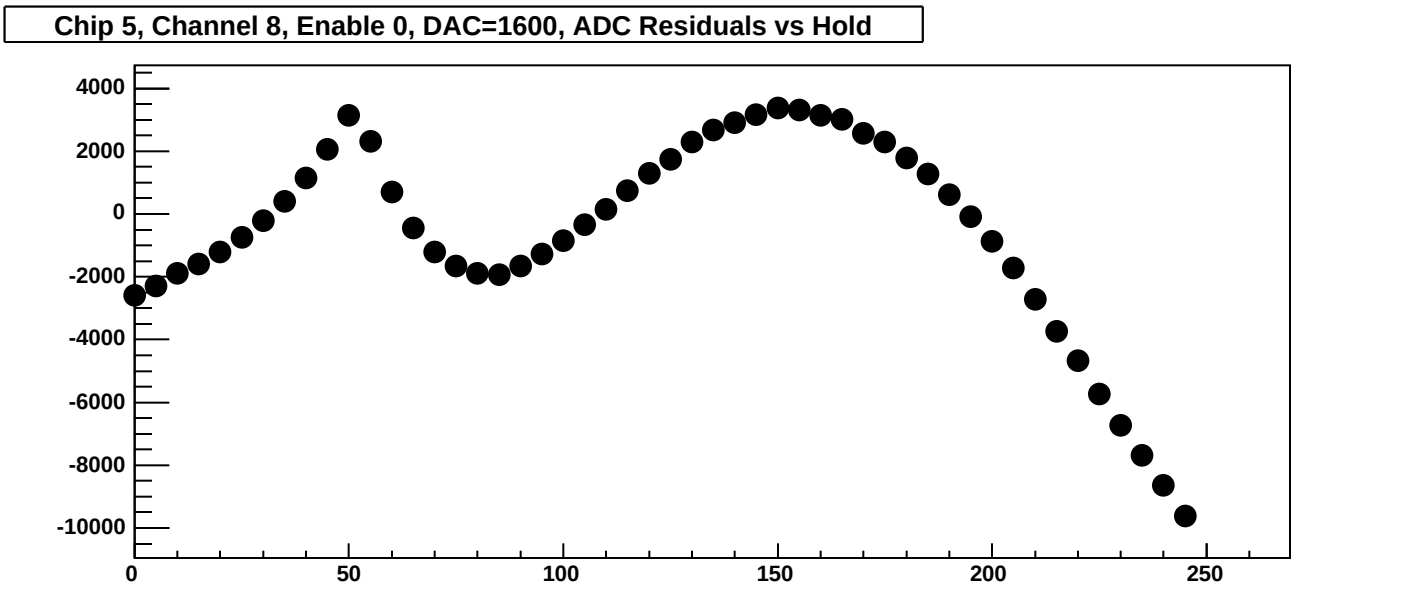


Chip 5, Channel 7, Enable 5, DAC=1600, ADC Residuals vs Hold

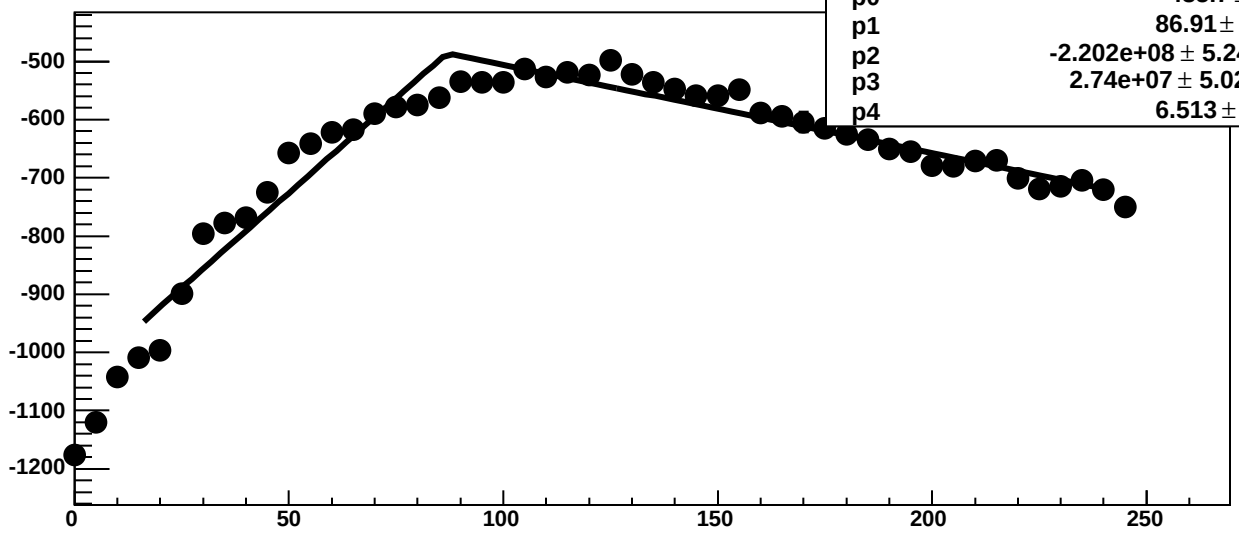



Chip 5, Channel 8, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.331e+05 / 41
p0 -953.8 ± 8.187
p1 51.84 ± 0.02763
p2 2.814e+04 ± 54.78
p3 41.75 ± 0.09441
p4 -45.4 ± 0.3073
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

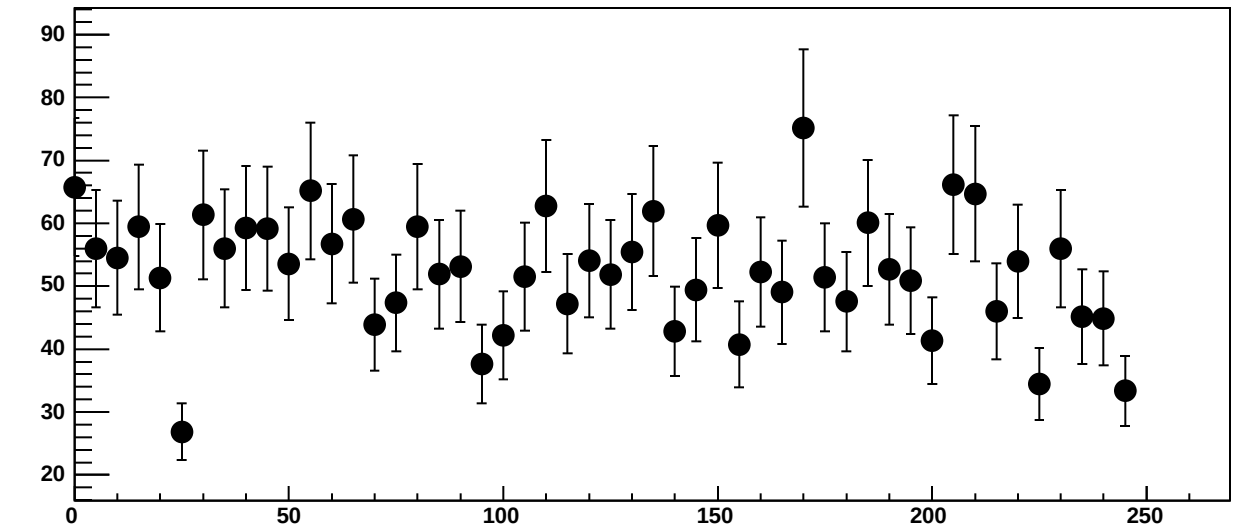

Chip 5, Channel 8, Enable 0, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 5, Channel 8, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

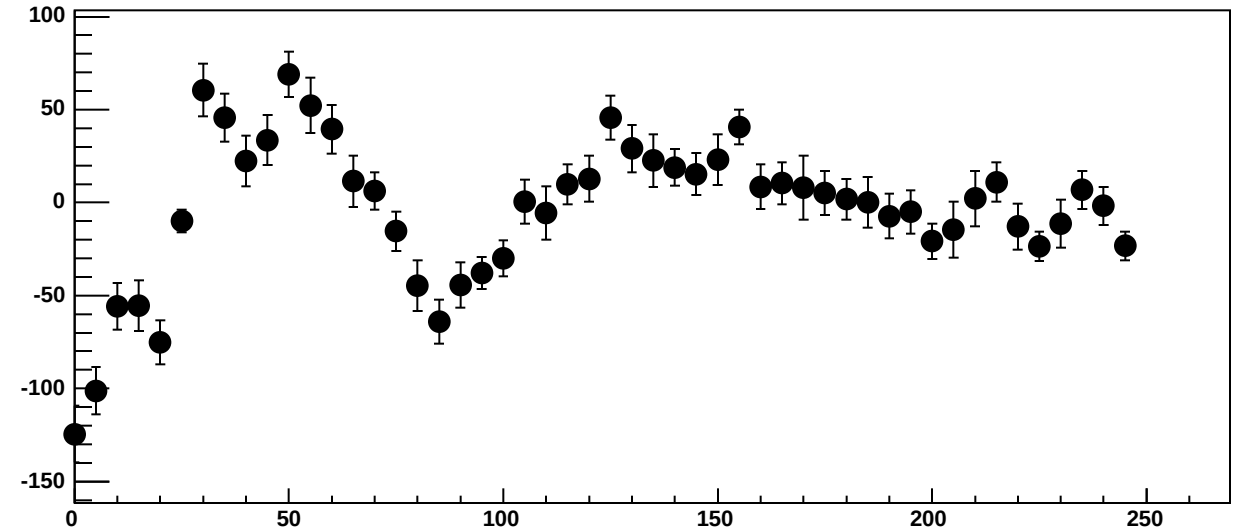
Chip 5, Channel 8, Enable 1, DAC=1600, ADC Mean vs Hold



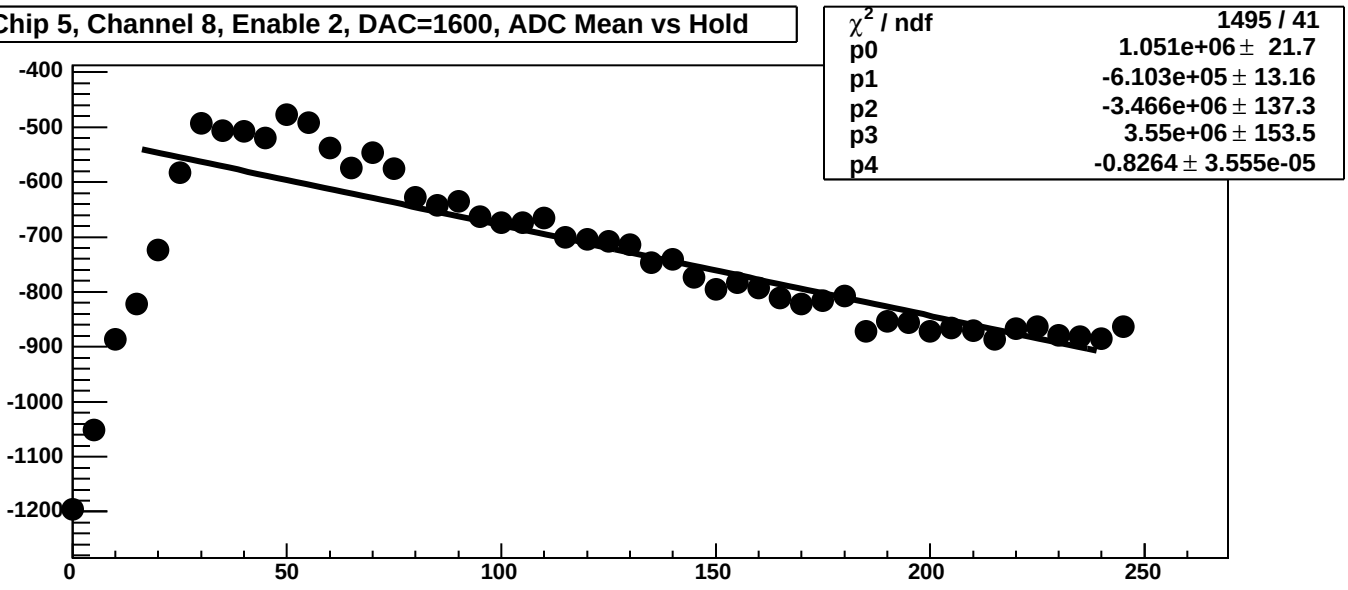
Chip 5, Channel 8, Enable 1, DAC=1600, ADC Noise vs Hold



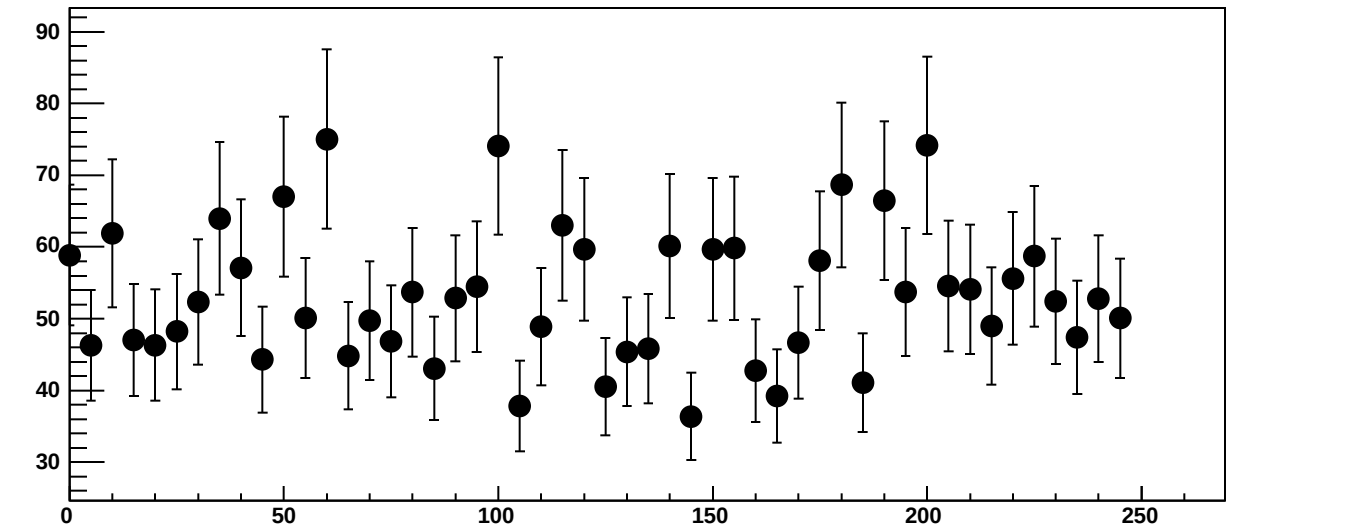
Chip 5, Channel 8, Enable 1, DAC=1600, ADC Residuals vs Hold



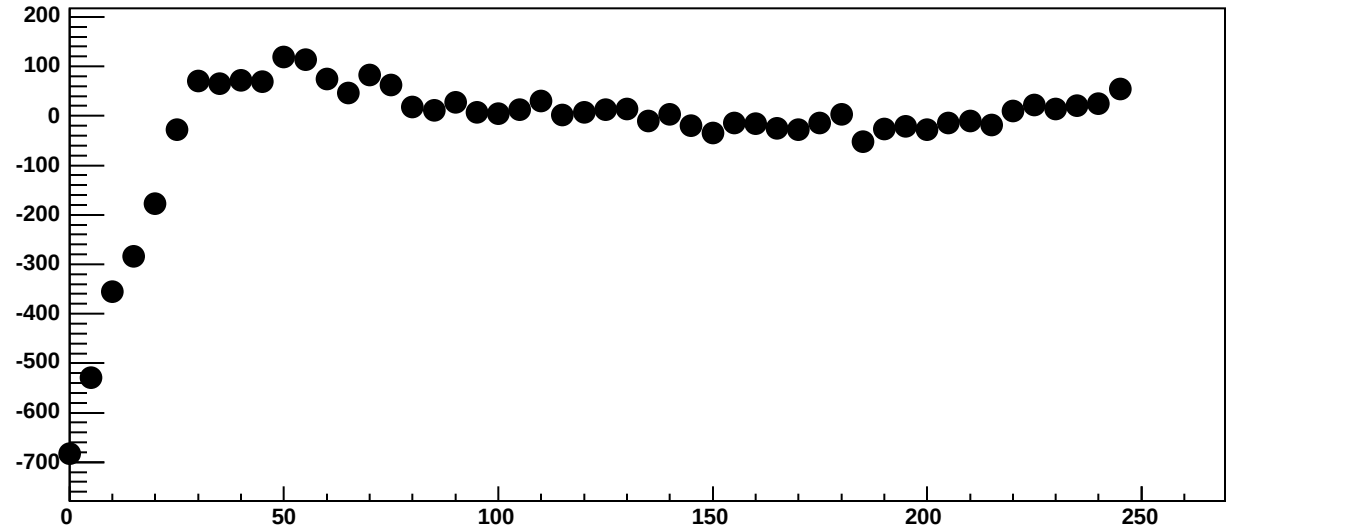
Chip 5, Channel 8, Enable 2, DAC=1600, ADC Mean vs Hold



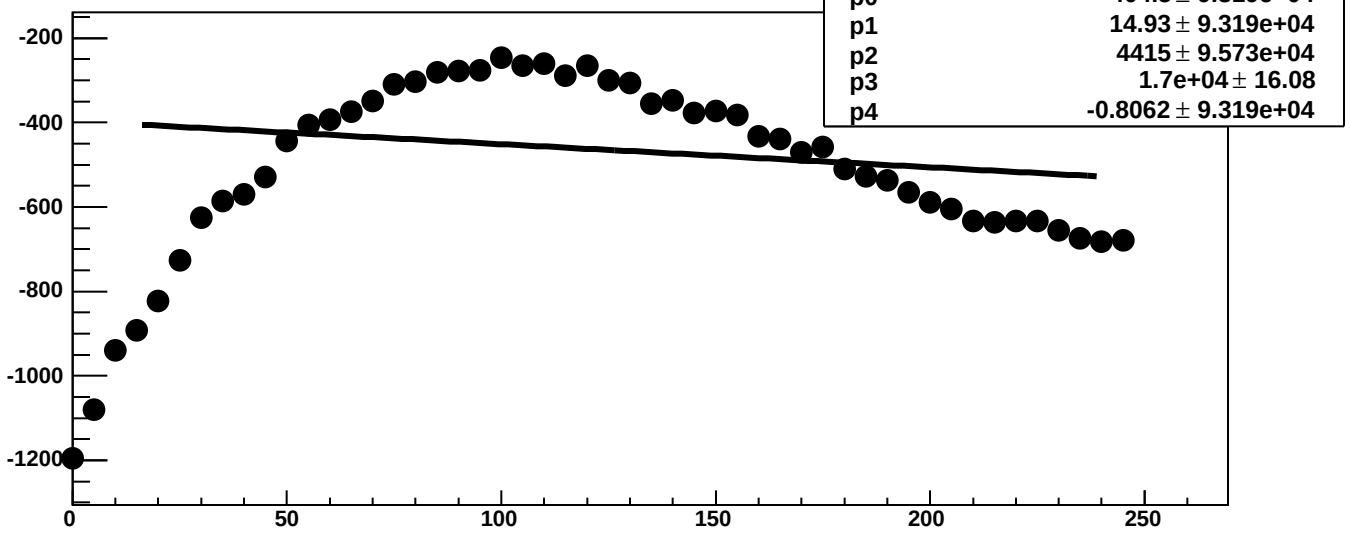
Chip 5, Channel 8, Enable 2, DAC=1600, ADC Noise vs Hold



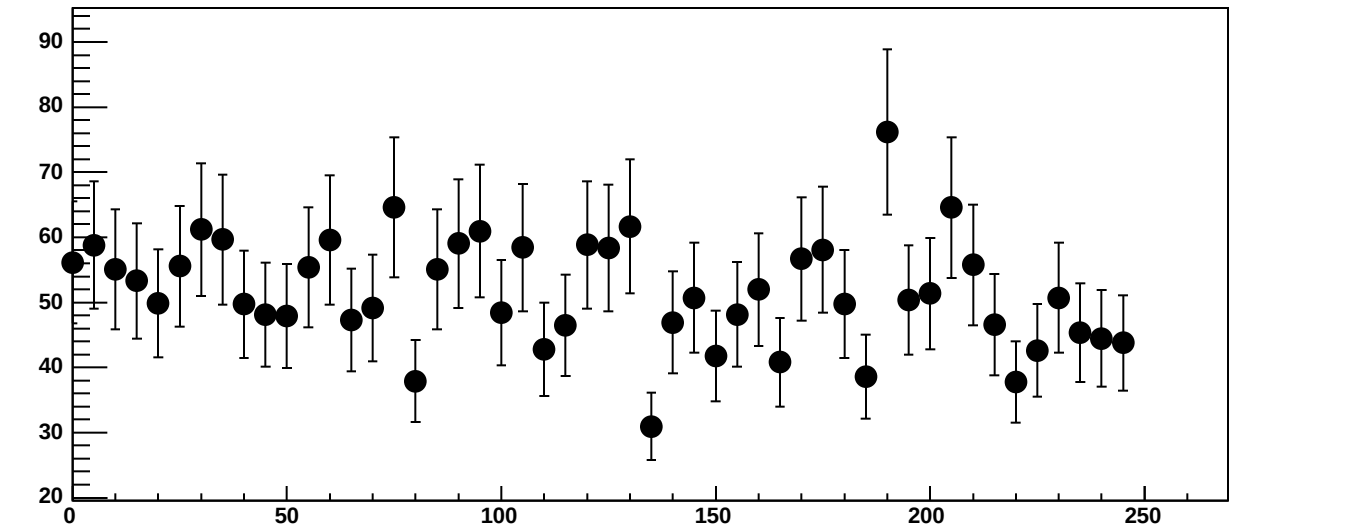
Chip 5, Channel 8, Enable 2, DAC=1600, ADC Residuals vs Hold



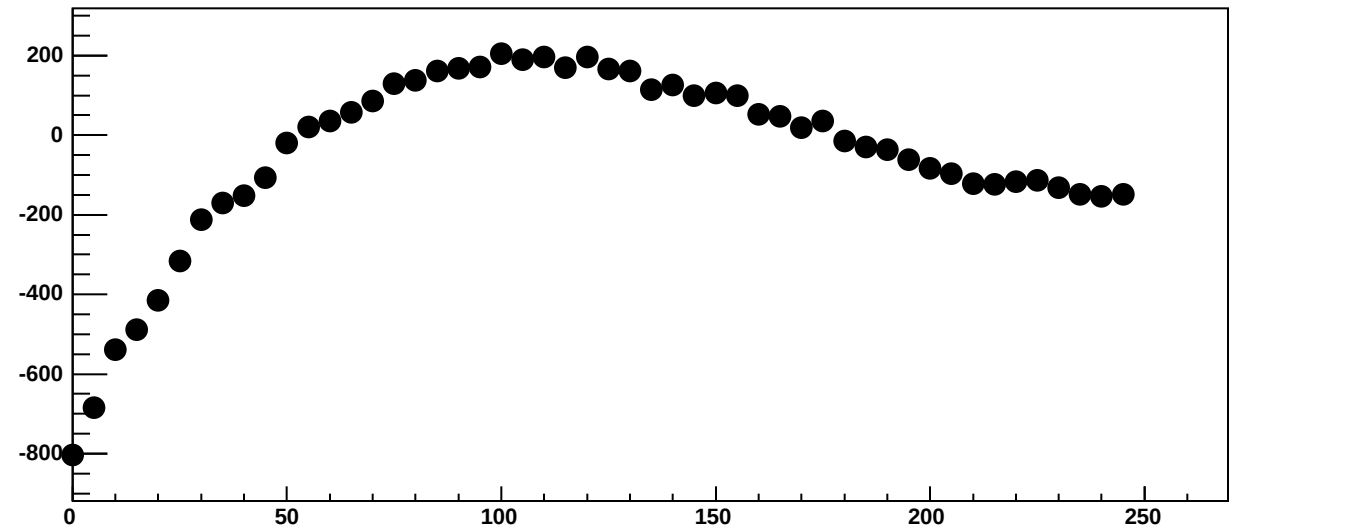
Chip 5, Channel 8, Enable 3, DAC=1600, ADC Mean vs Hold



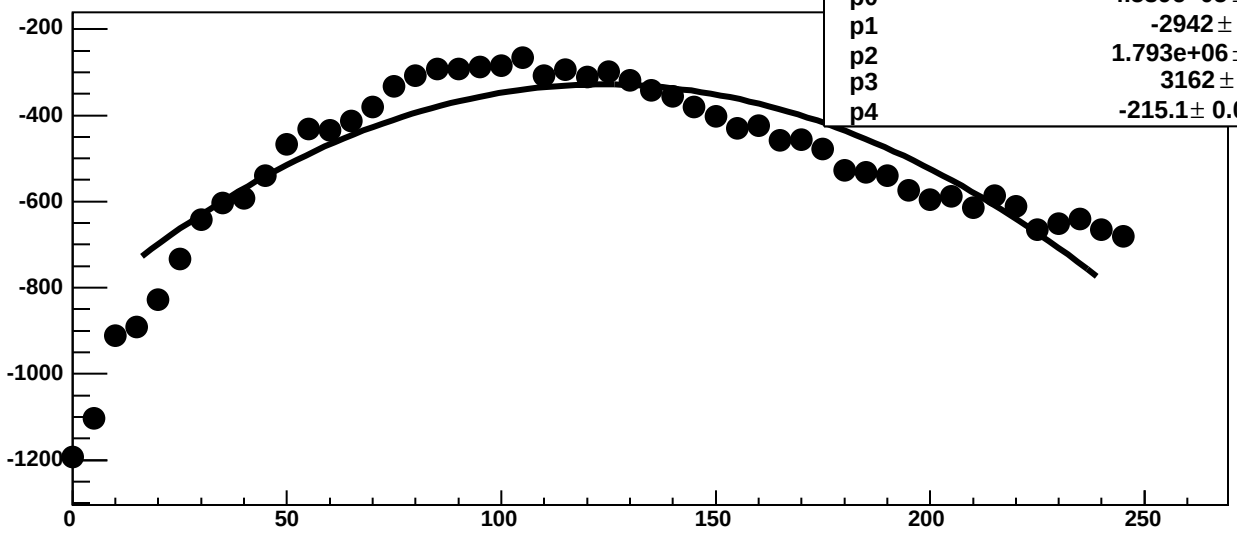
Chip 5, Channel 8, Enable 3, DAC=1600, ADC Noise vs Hold



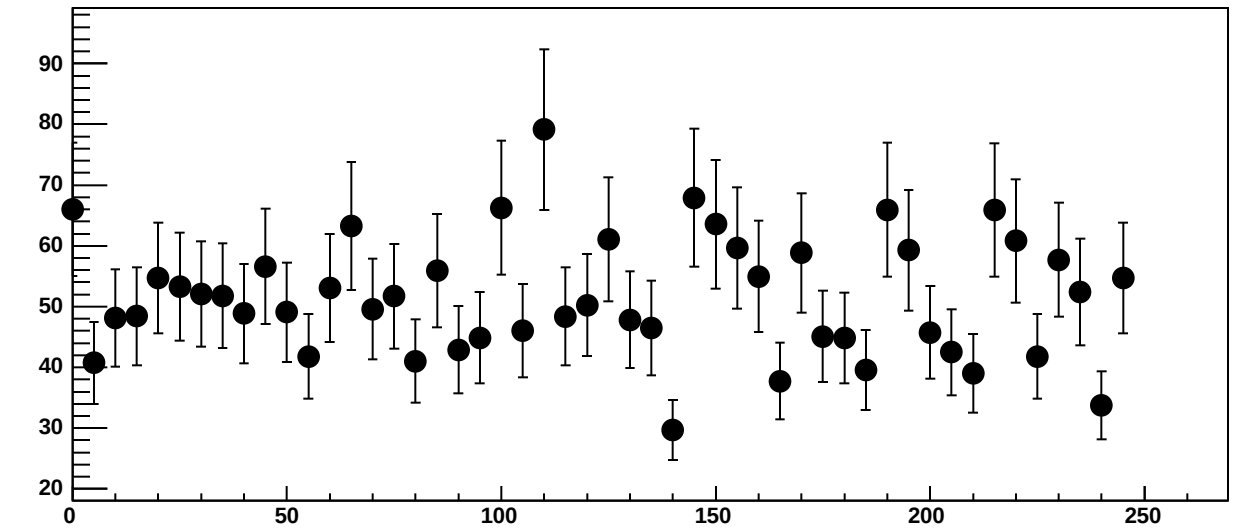
Chip 5, Channel 8, Enable 3, DAC=1600, ADC Residuals vs Hold



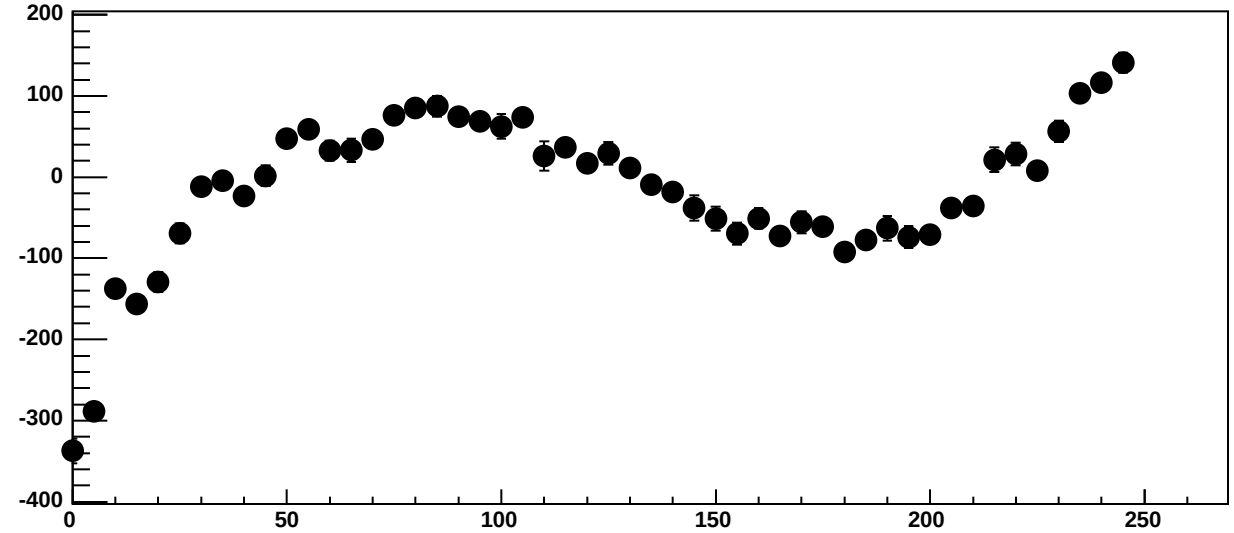
Chip 5, Channel 8, Enable 4, DAC=1600, ADC Mean vs Hold



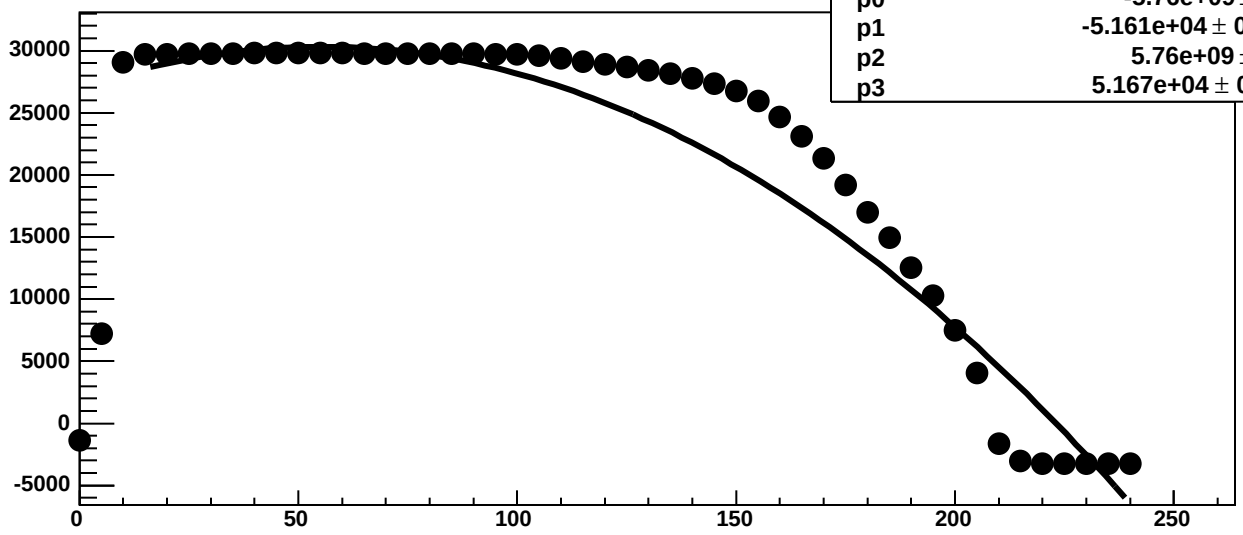
Chip 5, Channel 8, Enable 4, DAC=1600, ADC Noise vs Hold



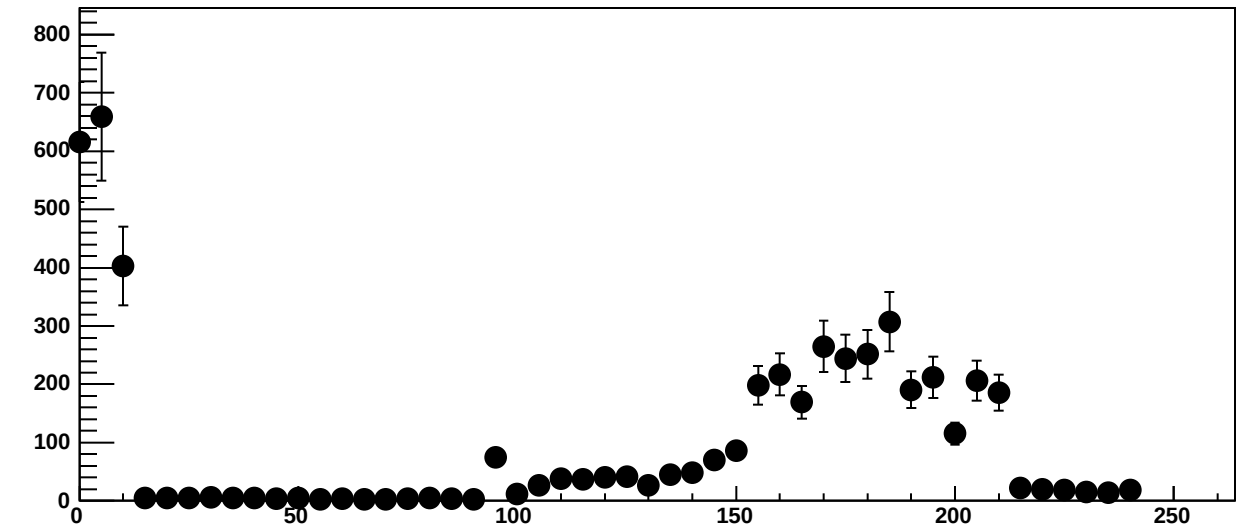
Chip 5, Channel 8, Enable 4, DAC=1600, ADC Residuals vs Hold



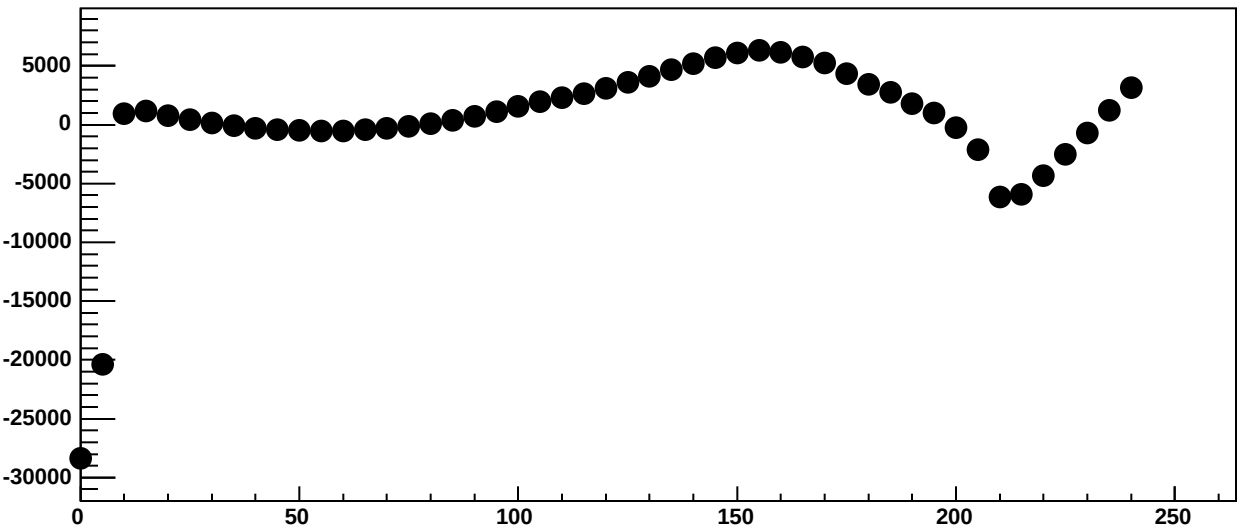
Chip 5, Channel 8, Enable 5!, DAC=1600, ADC Mean vs Hold



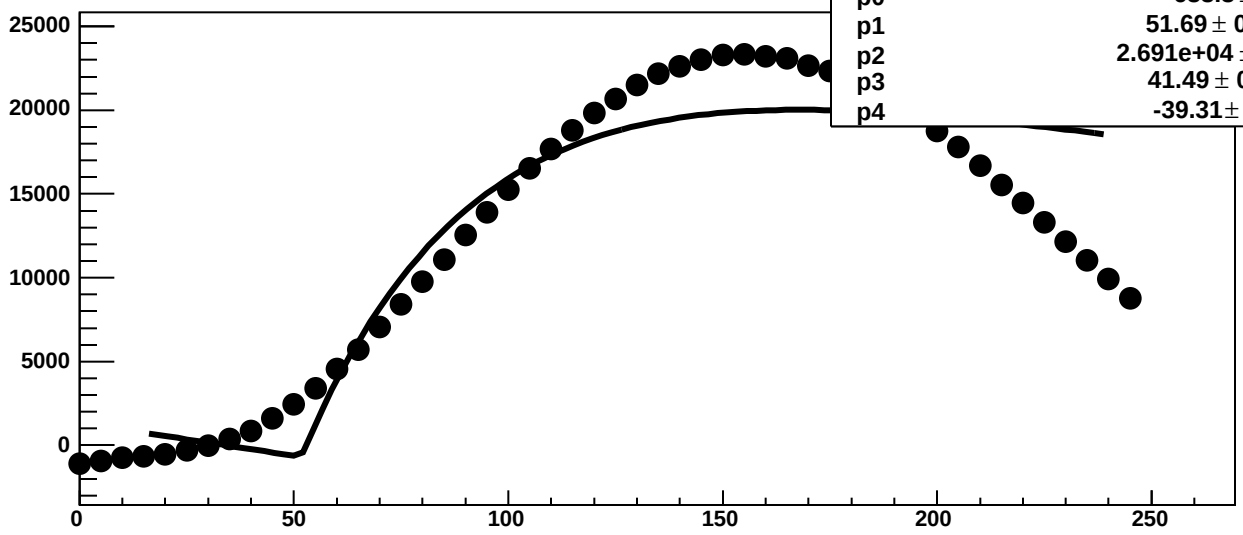
Chip 5, Channel 8, Enable 5!, DAC=1600, ADC Noise vs Hold



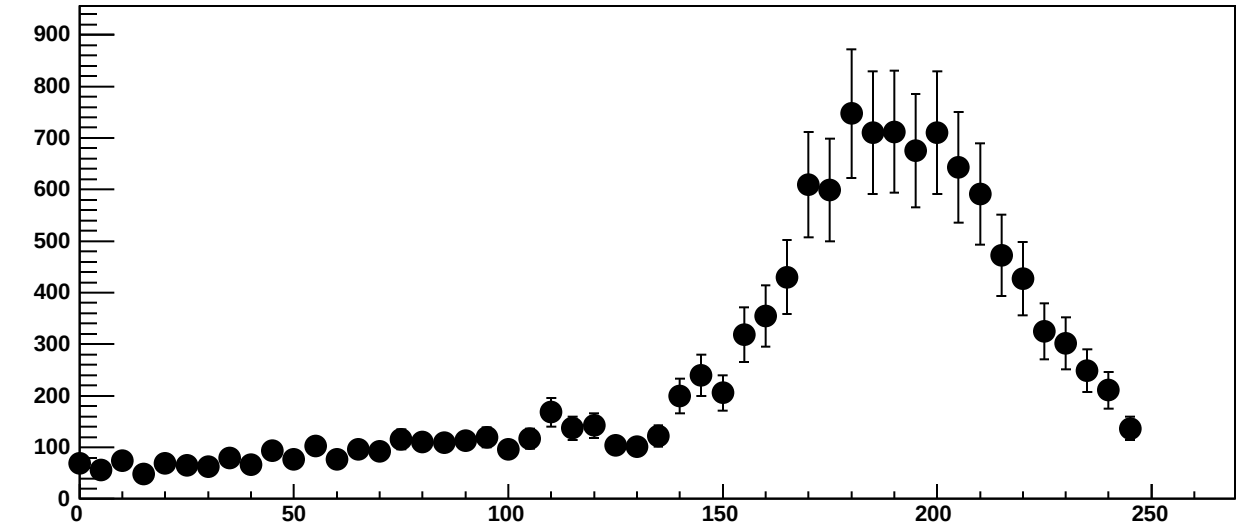
Chip 5, Channel 8, Enable 5!, DAC=1600, ADC Residuals vs Hold



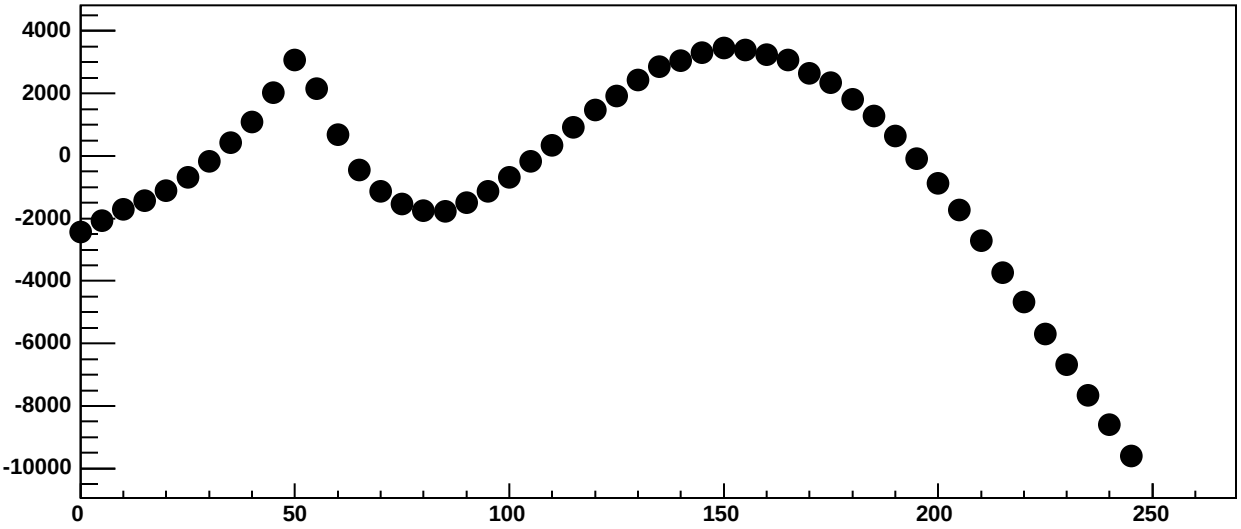
Chip 5, Channel 9, Enable 0, DAC=1600, ADC Mean vs Hold



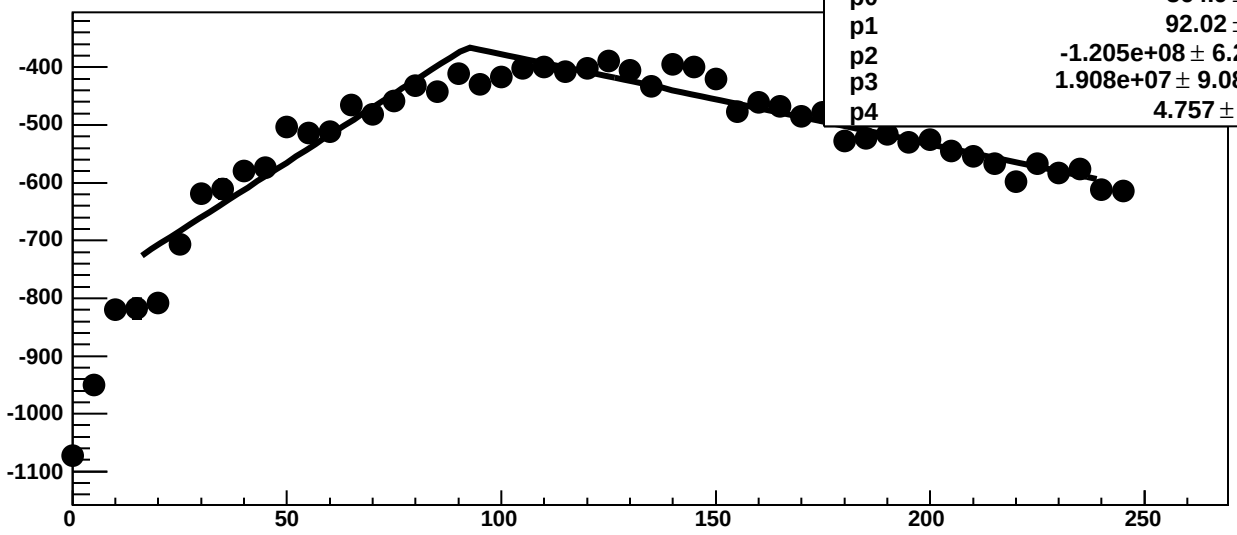
Chip 5, Channel 9, Enable 0, DAC=1600, ADC Noise vs Hold



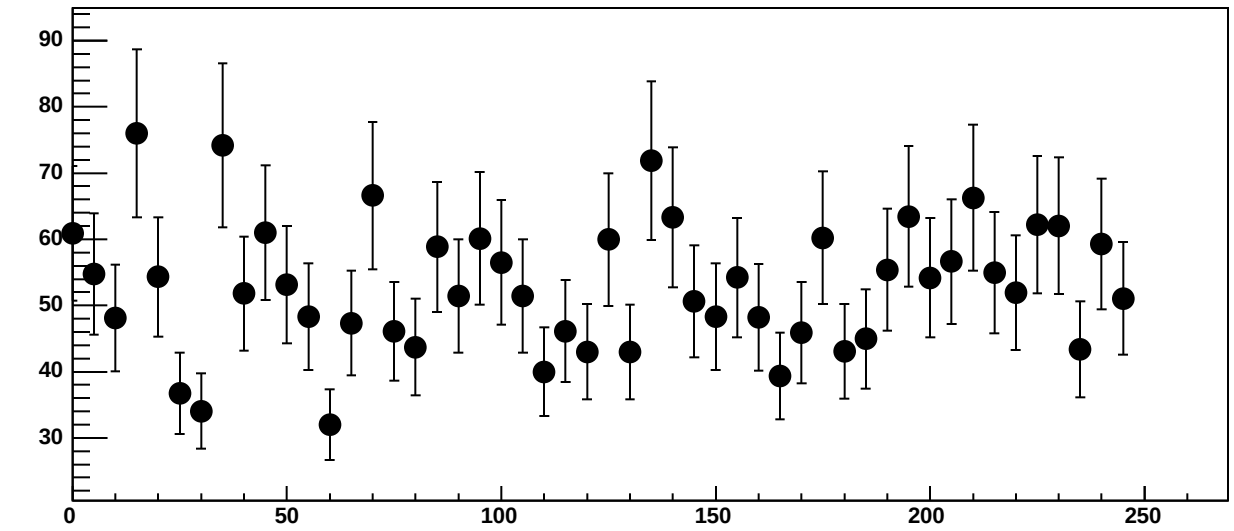
Chip 5, Channel 9, Enable 0, DAC=1600, ADC Residuals vs Hold



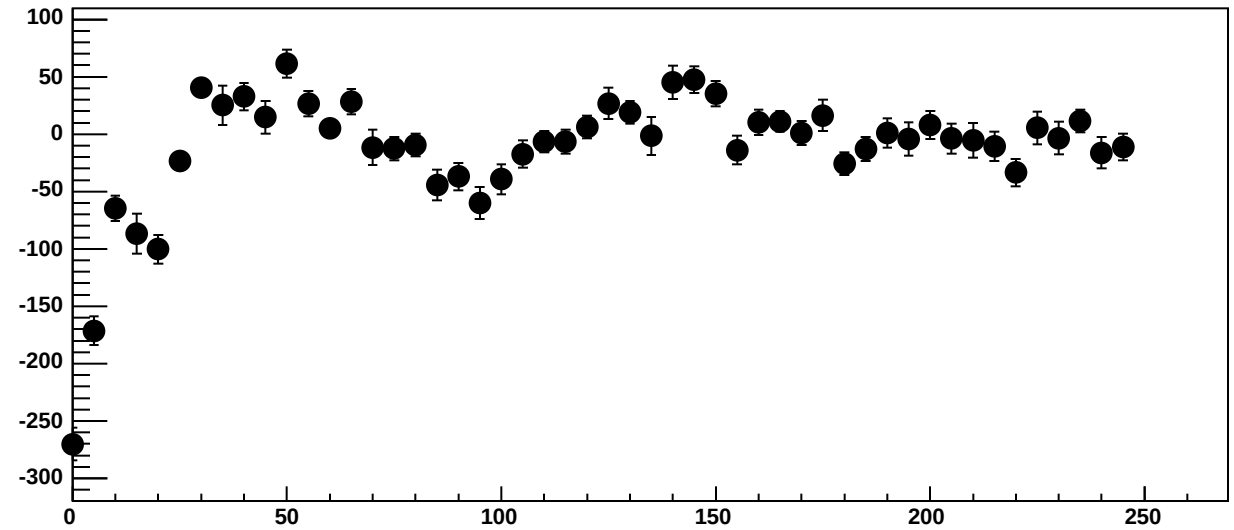
Chip 5, Channel 9, Enable 1, DAC=1600, ADC Mean vs Hold



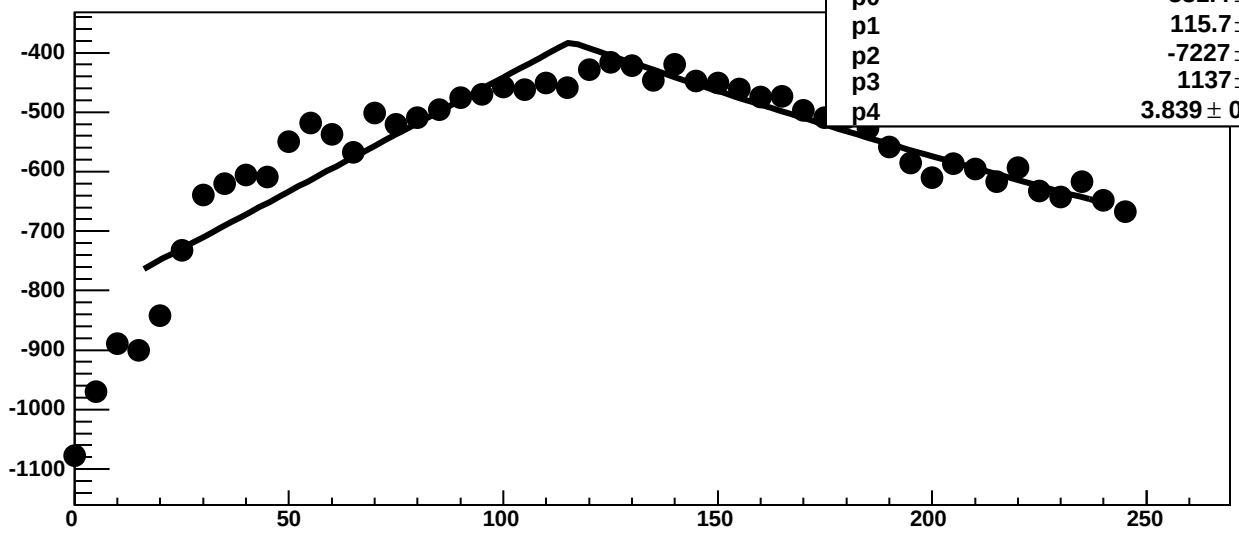
Chip 5, Channel 9, Enable 1, DAC=1600, ADC Noise vs Hold



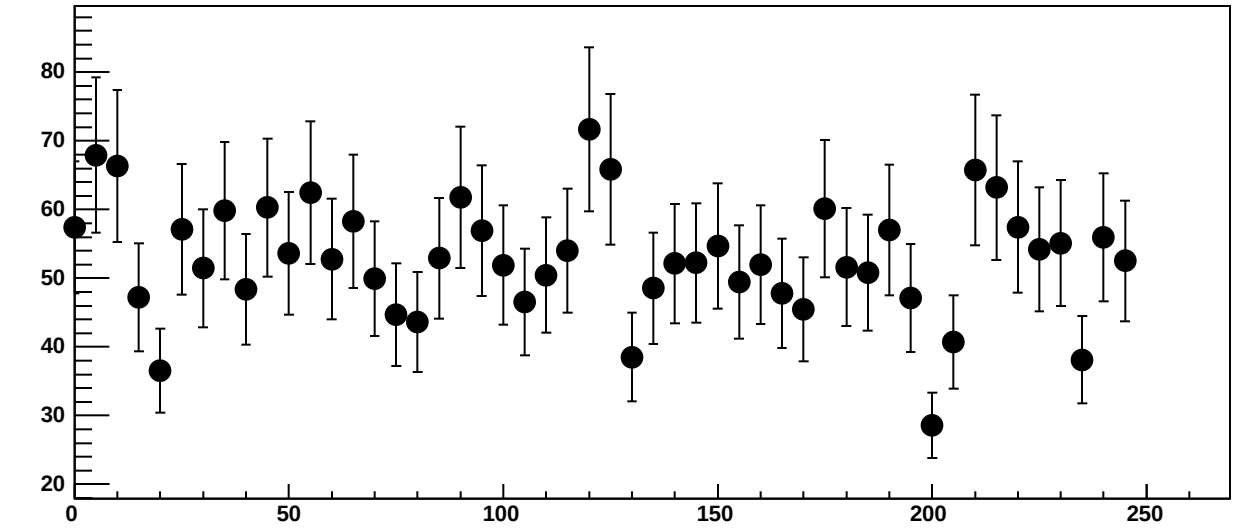
Chip 5, Channel 9, Enable 1, DAC=1600, ADC Residuals vs Hold



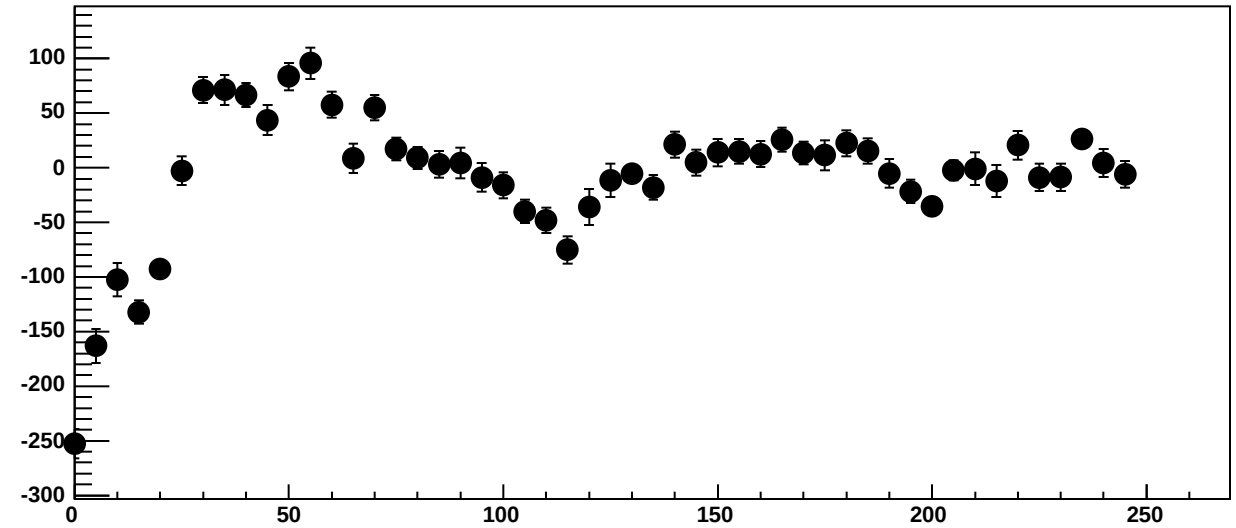
Chip 5, Channel 9, Enable 2, DAC=1600, ADC Mean vs Hold



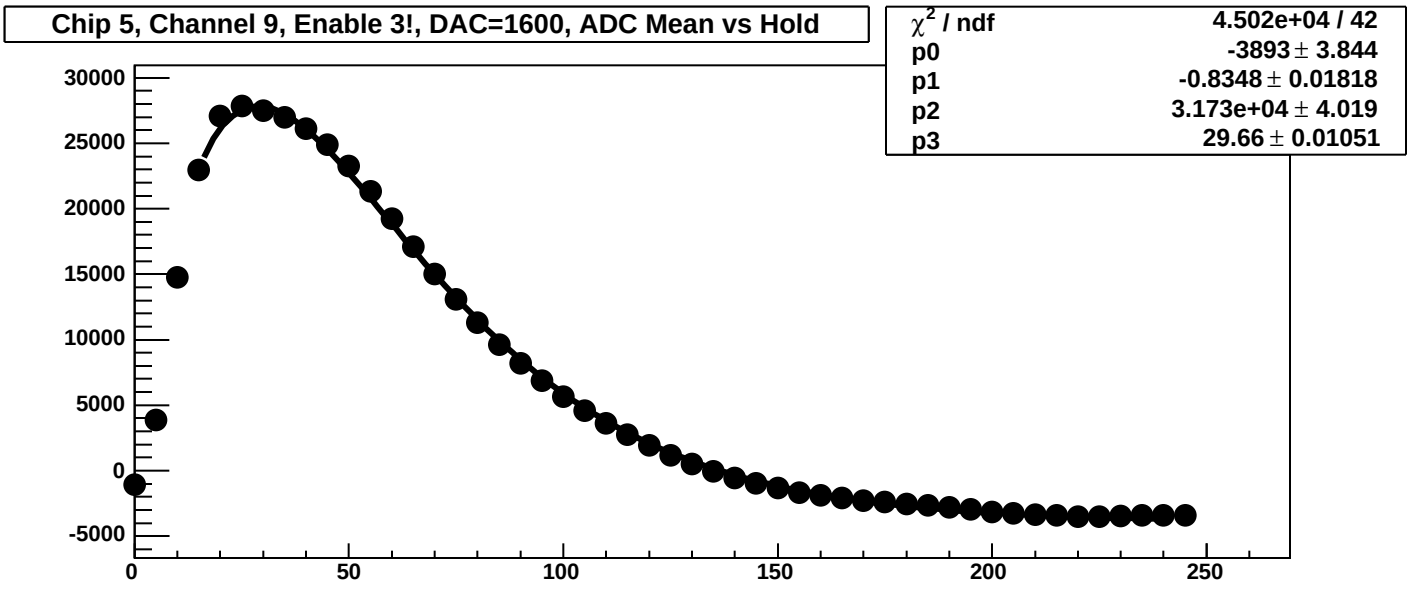
Chip 5, Channel 9, Enable 2, DAC=1600, ADC Noise vs Hold



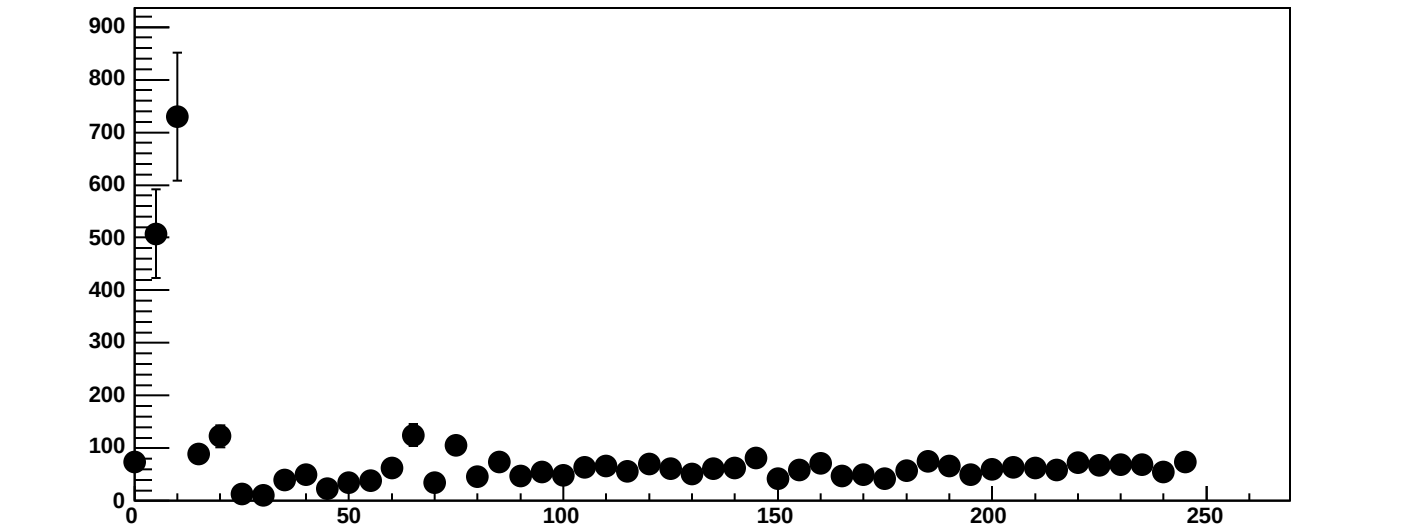
Chip 5, Channel 9, Enable 2, DAC=1600, ADC Residuals vs Hold



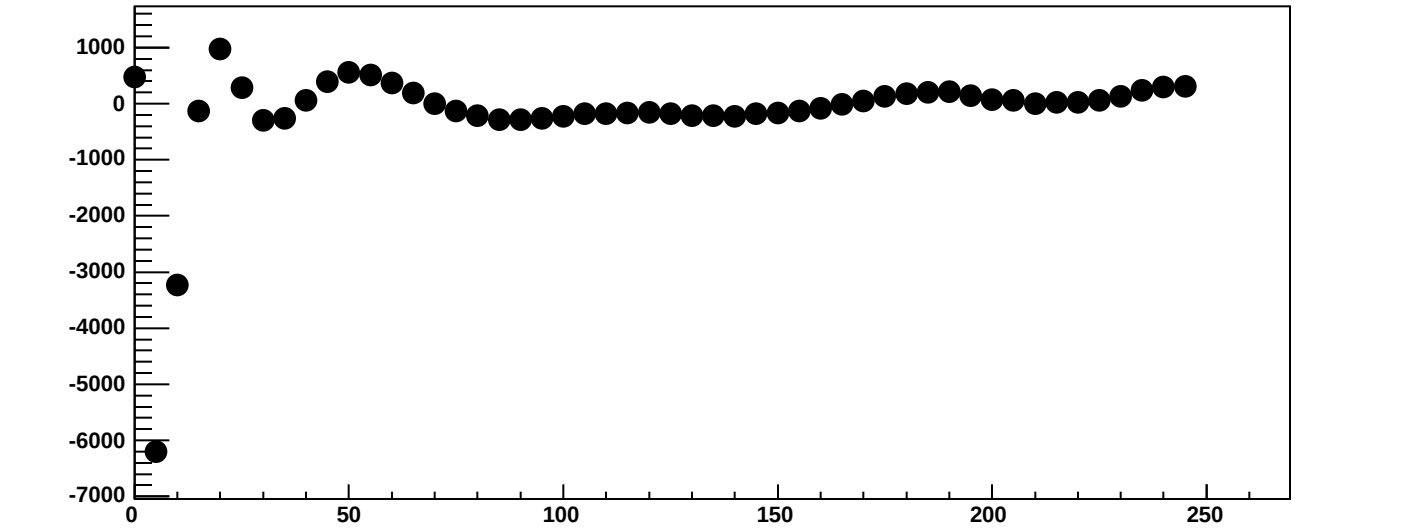
Chip 5, Channel 9, Enable 3!, DAC=1600, ADC Mean vs Hold



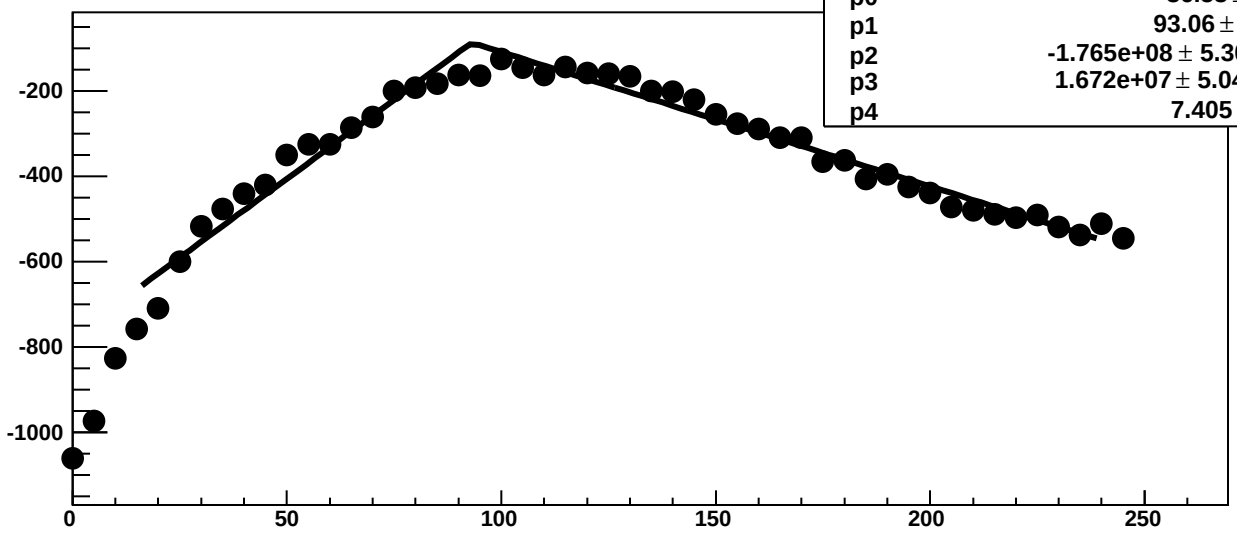
Chip 5, Channel 9, Enable 3!, DAC=1600, ADC Noise vs Hold



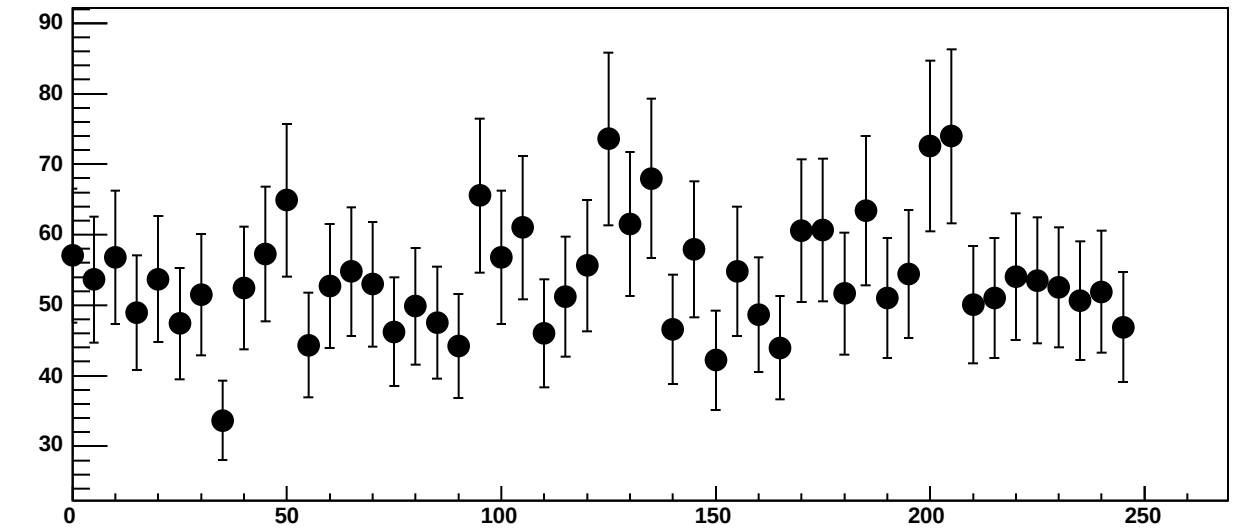
Chip 5, Channel 9, Enable 3!, DAC=1600, ADC Residuals vs Hold



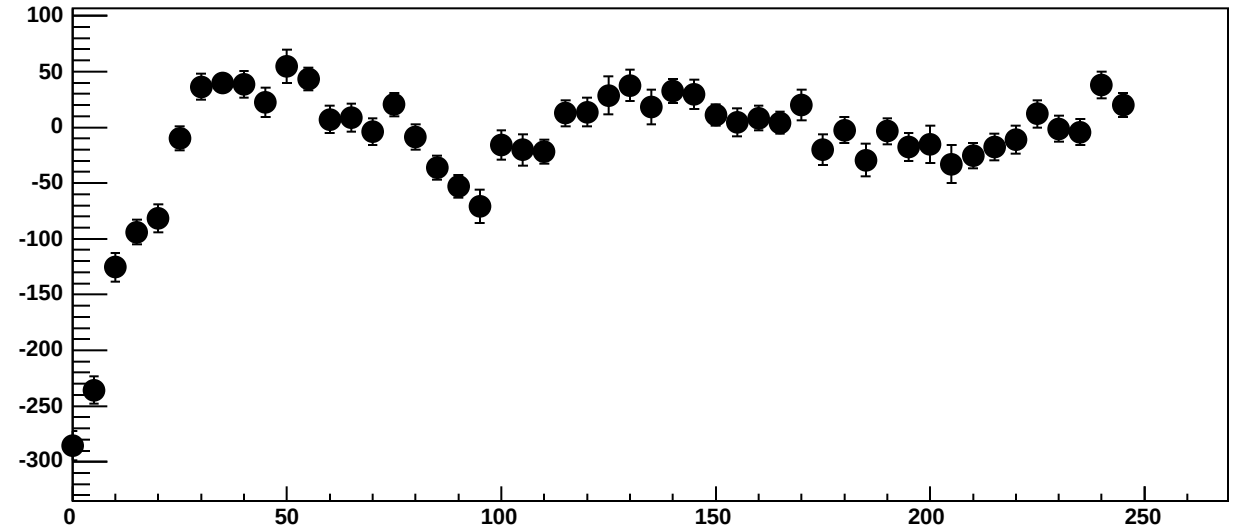
Chip 5, Channel 9, Enable 4, DAC=1600, ADC Mean vs Hold

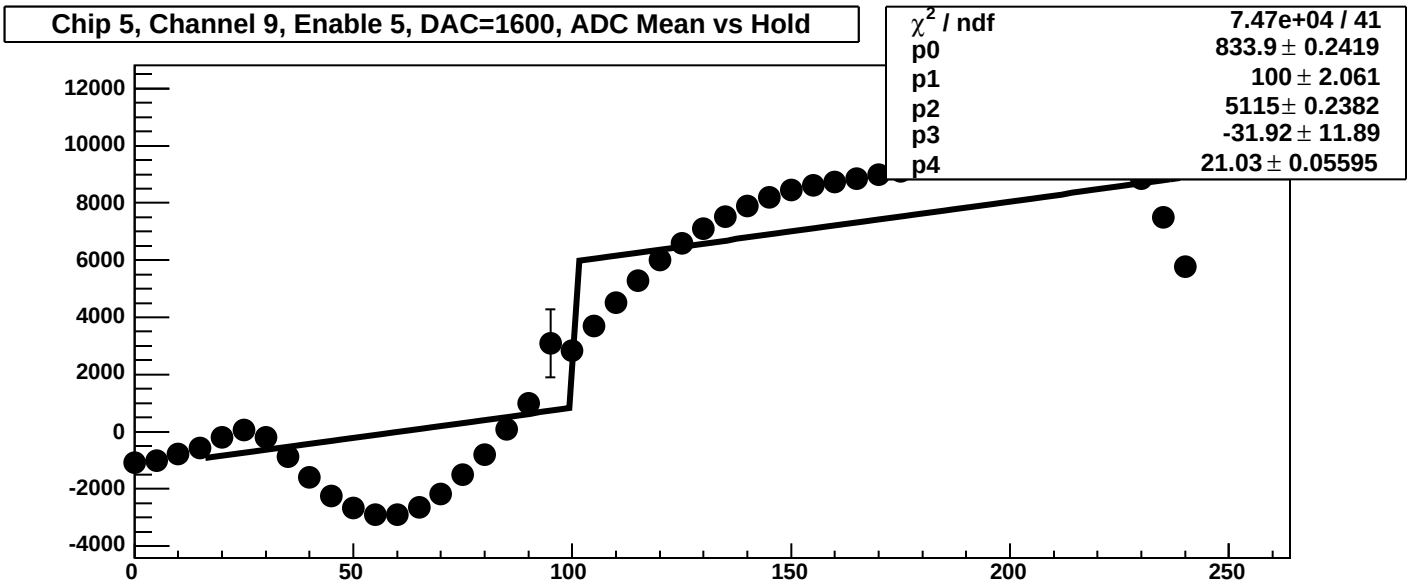


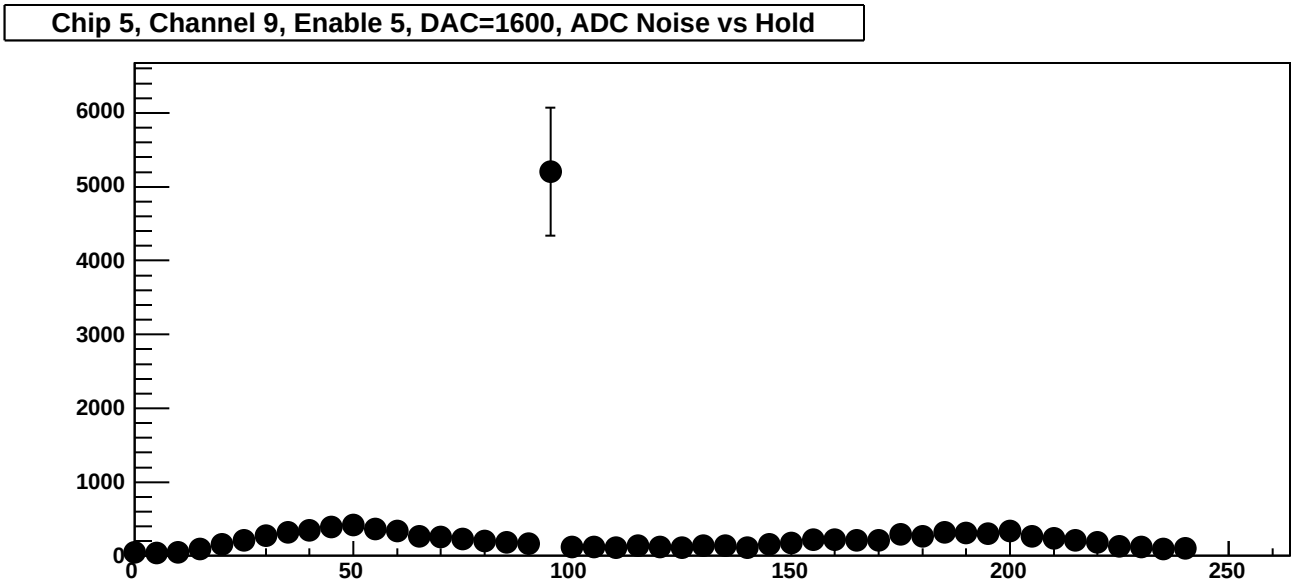
Chip 5, Channel 9, Enable 4, DAC=1600, ADC Noise vs Hold

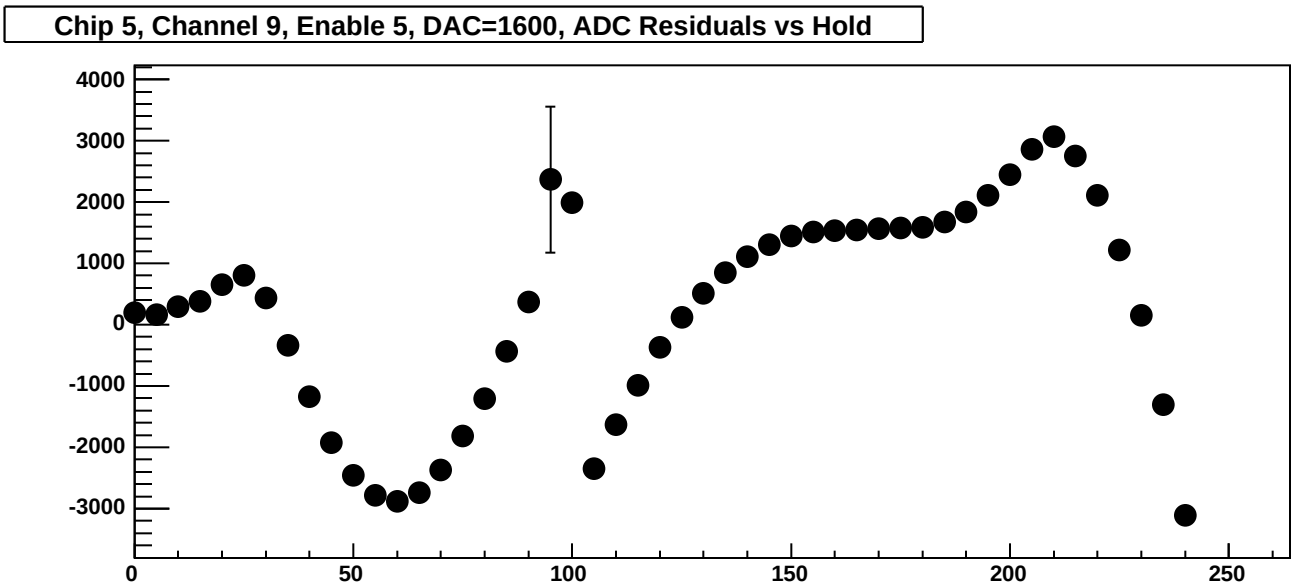


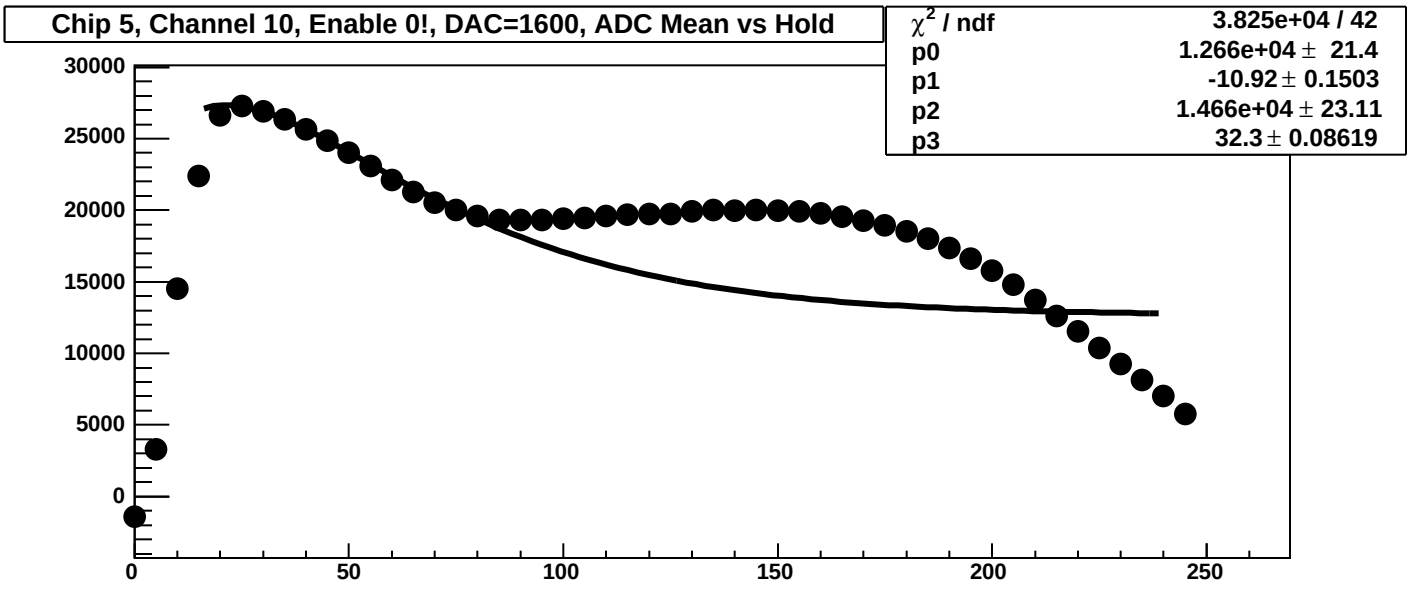
Chip 5, Channel 9, Enable 4, DAC=1600, ADC Residuals vs Hold

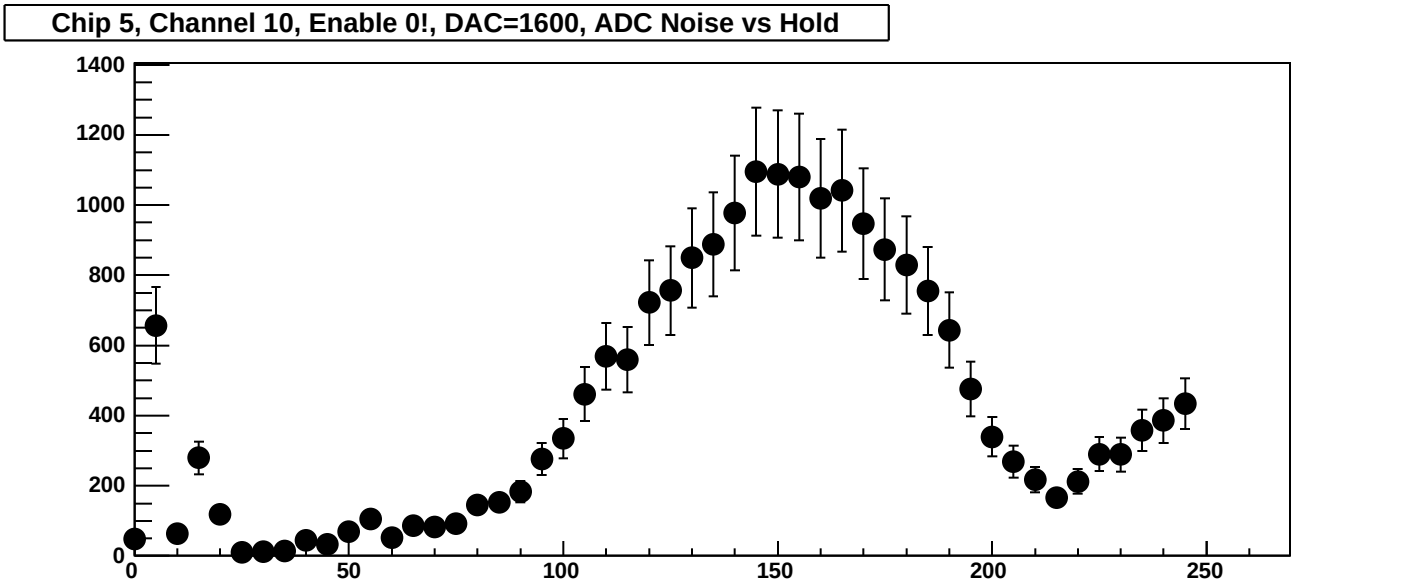


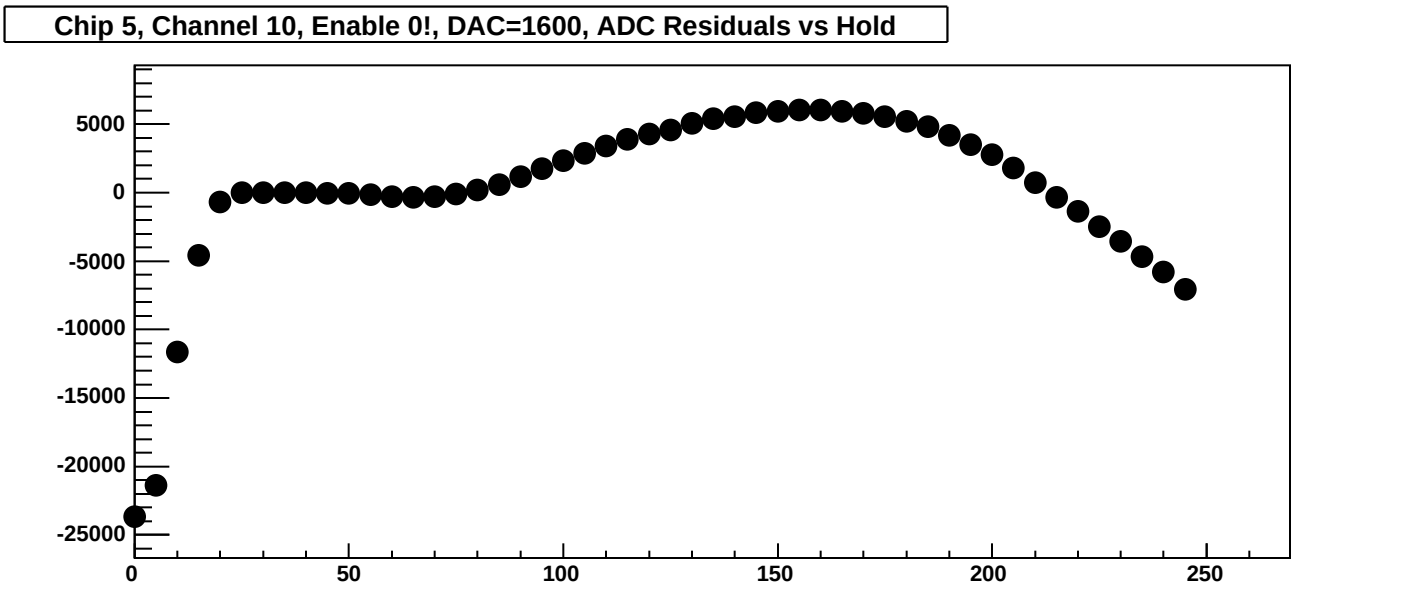

Chip 5, Channel 9, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.47e+04 / 41
p0 833.9 ± 0.2419
p1 100 ± 2.061
p2 5115 ± 0.2382
p3 -31.92 ± 11.89
p4 21.03 ± 0.05595
12000
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

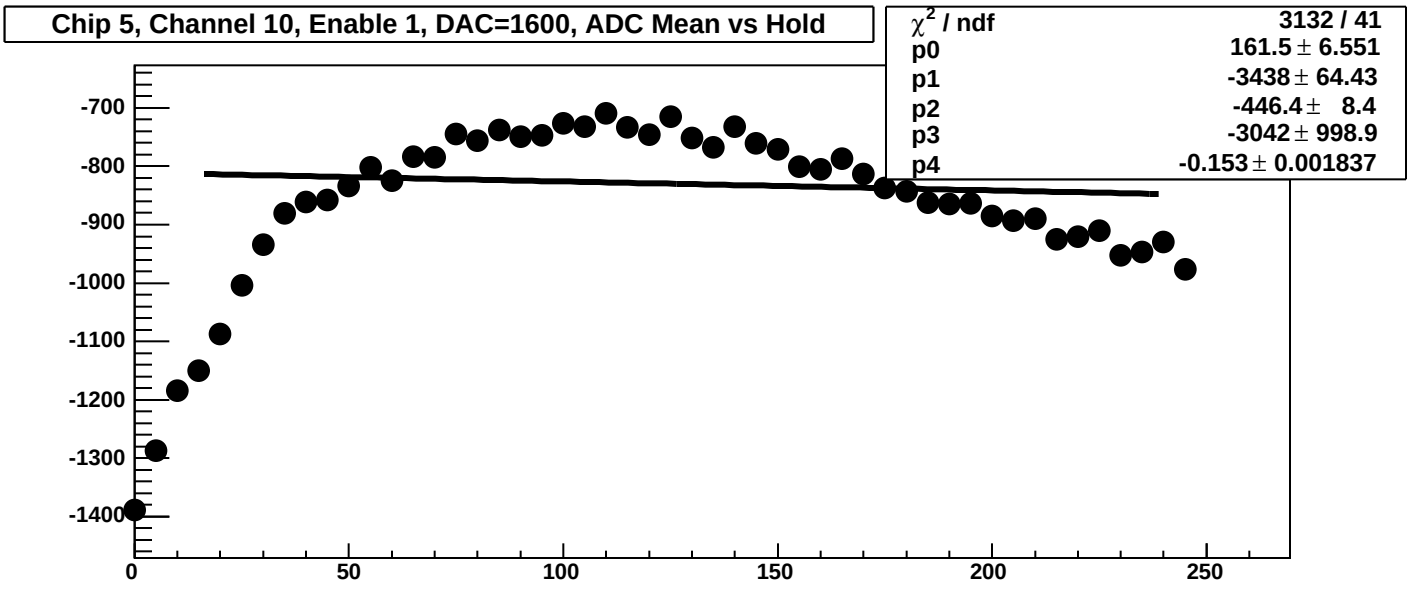

Chip 5, Channel 9, Enable 5, DAC=1600, ADC Noise vs Hold
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

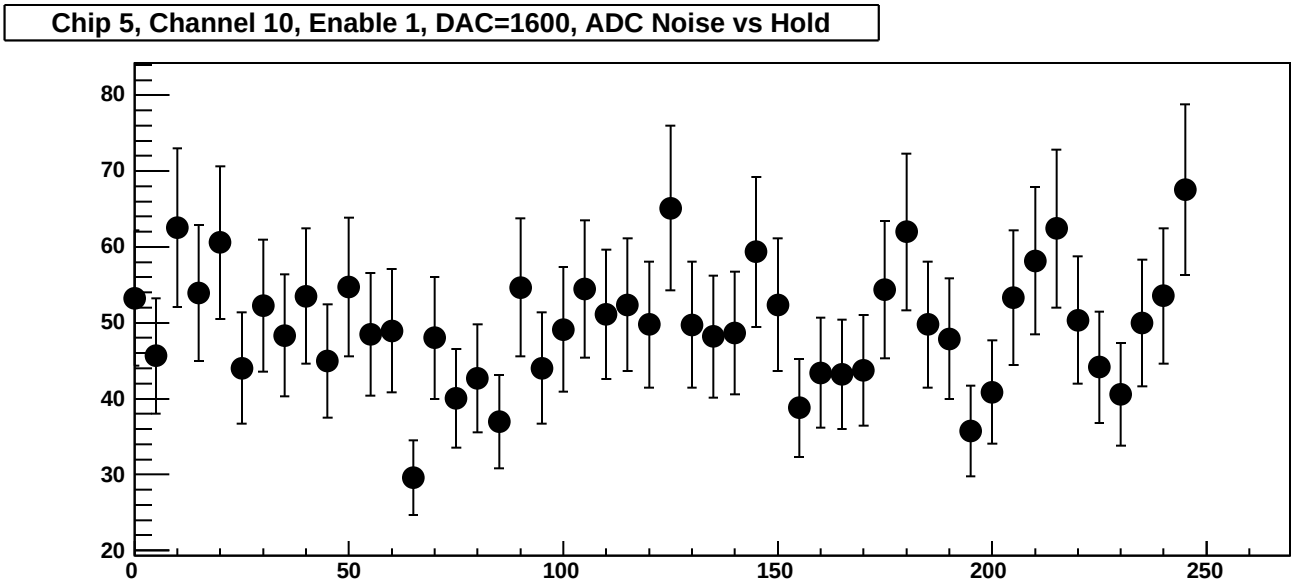

Chip 5, Channel 9, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

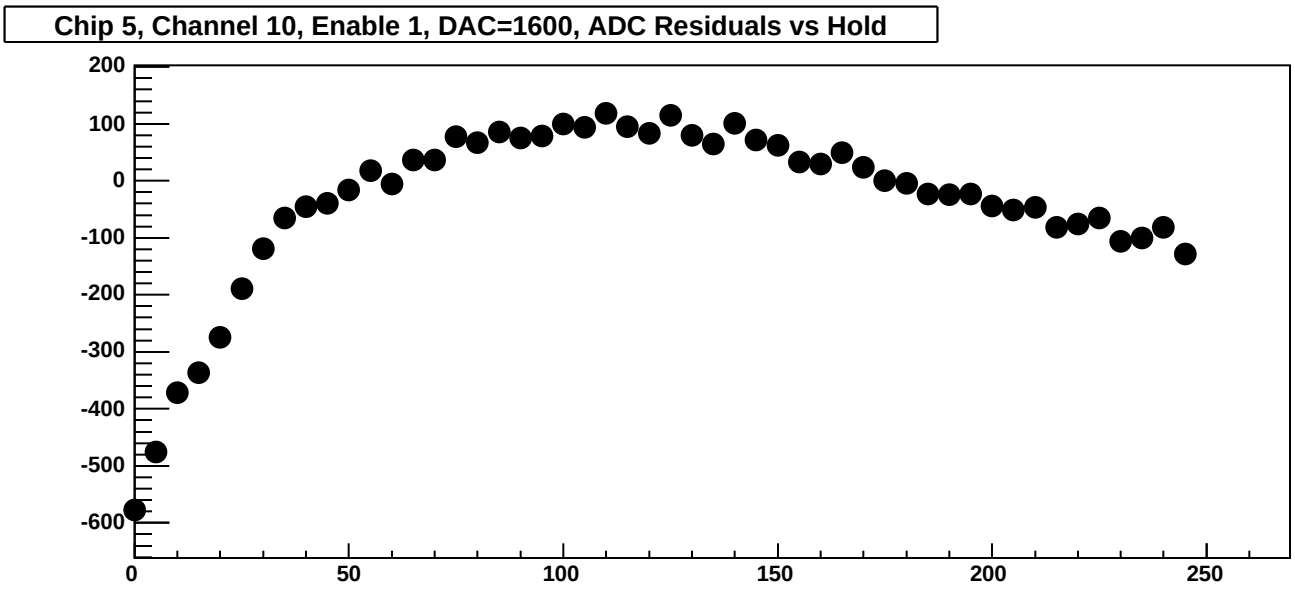

Chip 5, Channel 10, Enable 0!, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.825e+04 / 42
p0 1.266e+04 ± 21.4
p1 -10.92 ± 0.1503
p2 1.466e+04 ± 23.11
p3 32.3 ± 0.08619
30000
25000
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 5, Channel 10, Enable 0!, DAC=1600, ADC Noise vs Hold
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

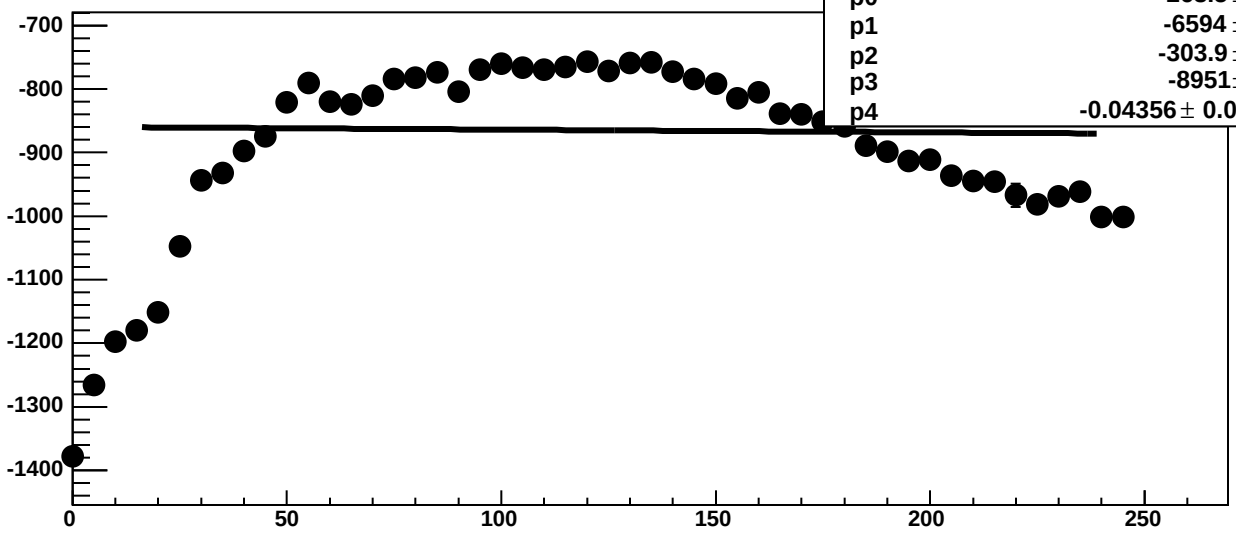

Chip 5, Channel 10, Enable 0!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
0
50
100
150
200
250


Chip 5, Channel 10, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 3132 / 41
p0 161.5 ± 6.551
p1 -3438 ± 64.43
p2 -446.4 ± 8.4
p3 -3042 ± 998.9
p4 -0.153 ± 0.001837
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
50
100
150
200
250

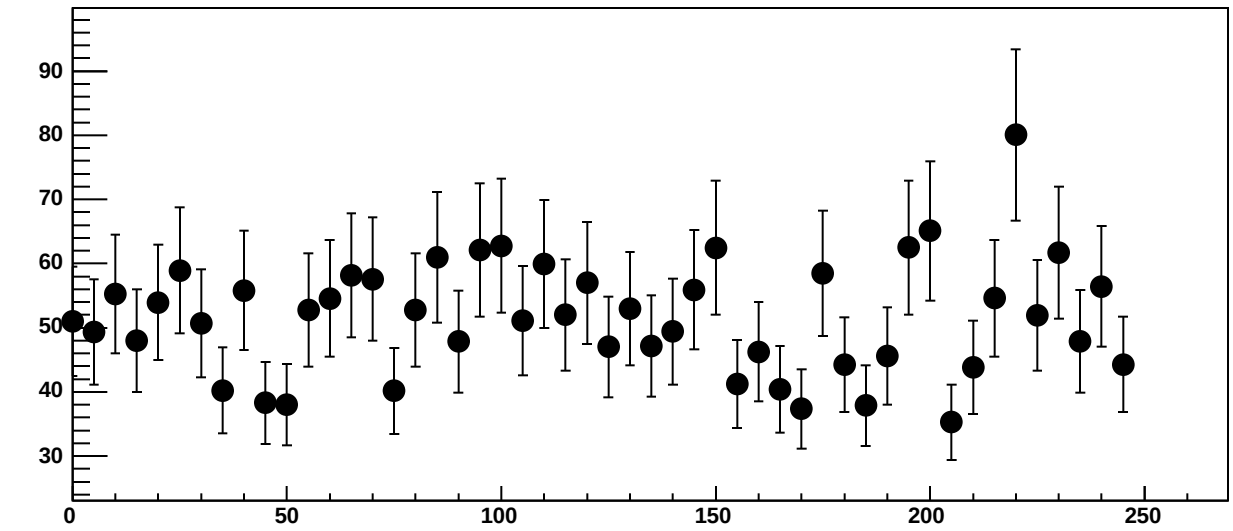

Chip 5, Channel 10, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 5, Channel 10, Enable 1, DAC=1600, ADC Residuals vs Hold
200
100
0
-100
-200
-300
-400
-500
-600
0
50
100
150
200
250

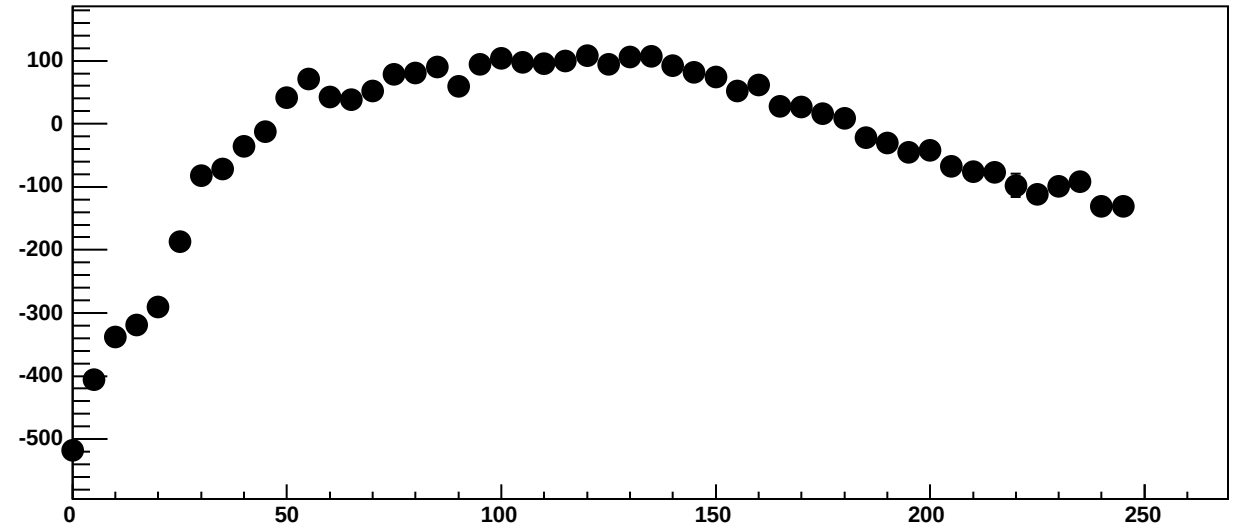
Chip 5, Channel 10, Enable 2, DAC=1600, ADC Mean vs Hold



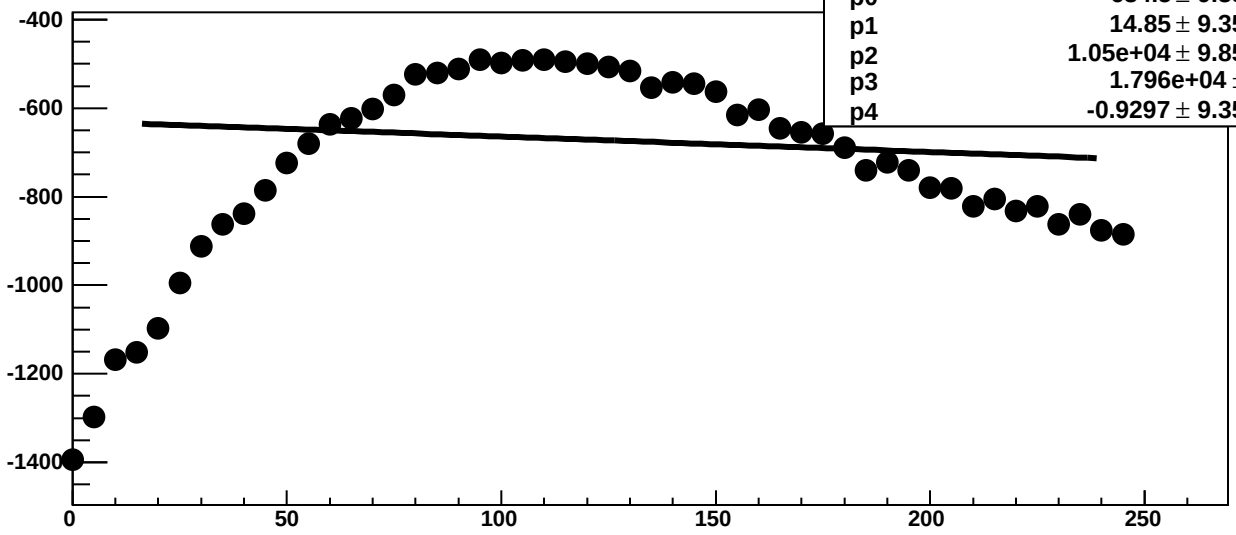
Chip 5, Channel 10, Enable 2, DAC=1600, ADC Noise vs Hold



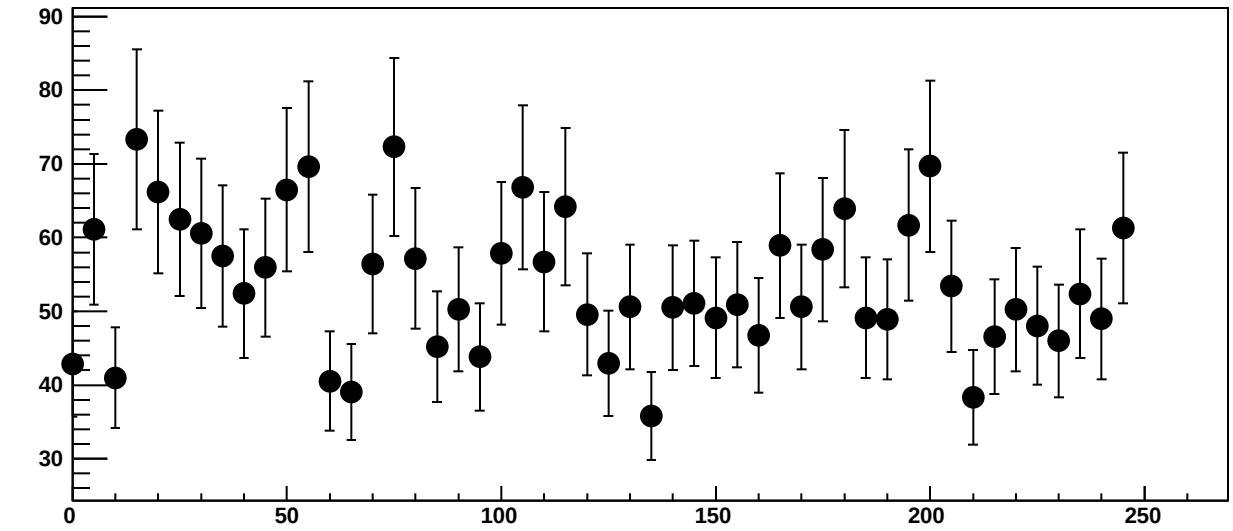
Chip 5, Channel 10, Enable 2, DAC=1600, ADC Residuals vs Hold



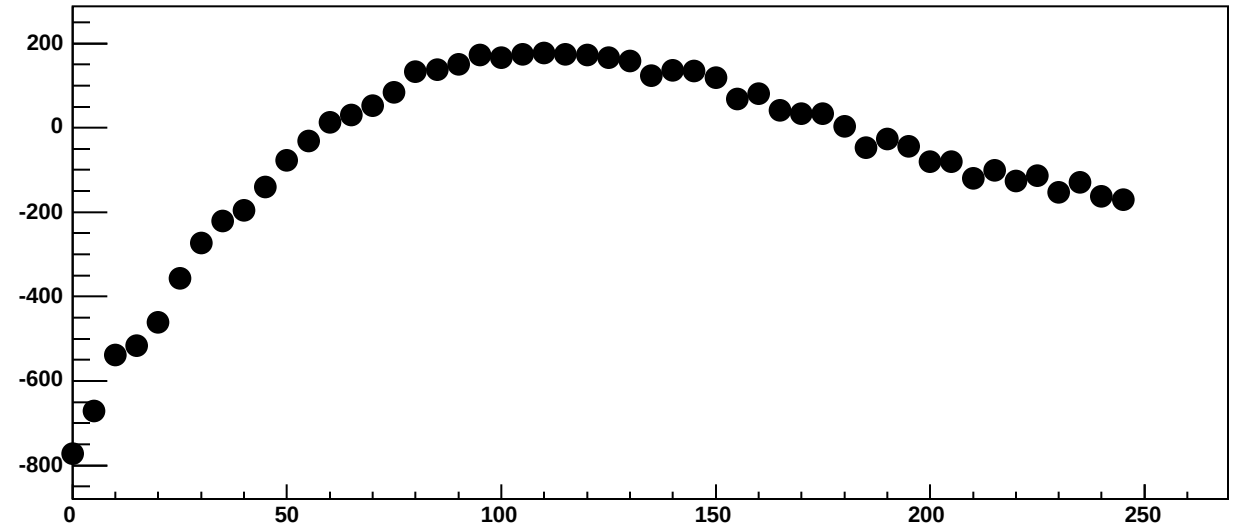
Chip 5, Channel 10, Enable 3, DAC=1600, ADC Mean vs Hold



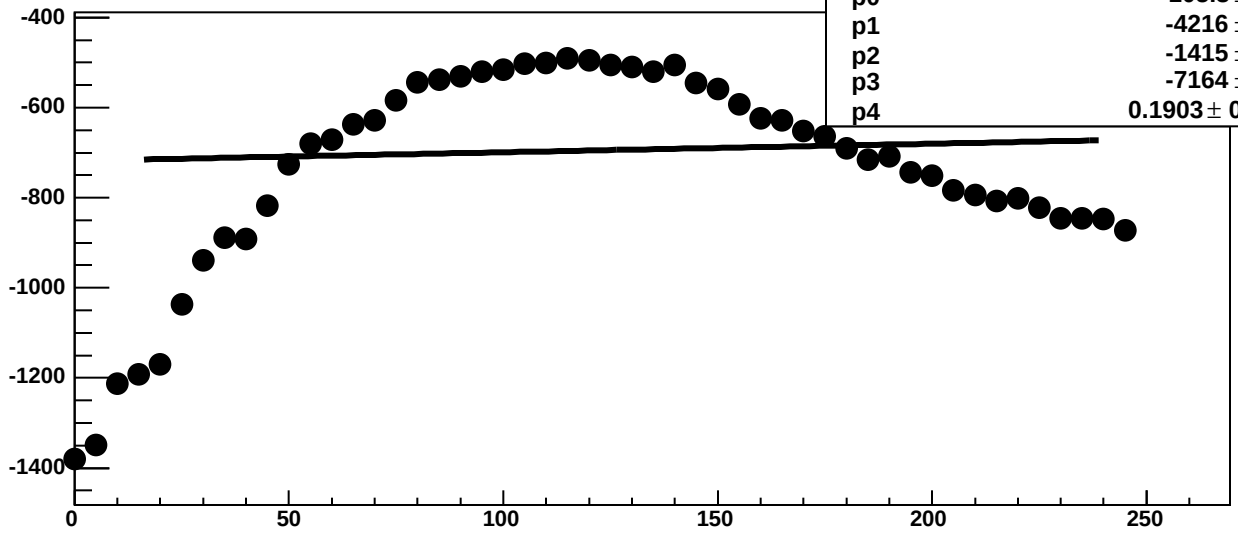
Chip 5, Channel 10, Enable 3, DAC=1600, ADC Noise vs Hold



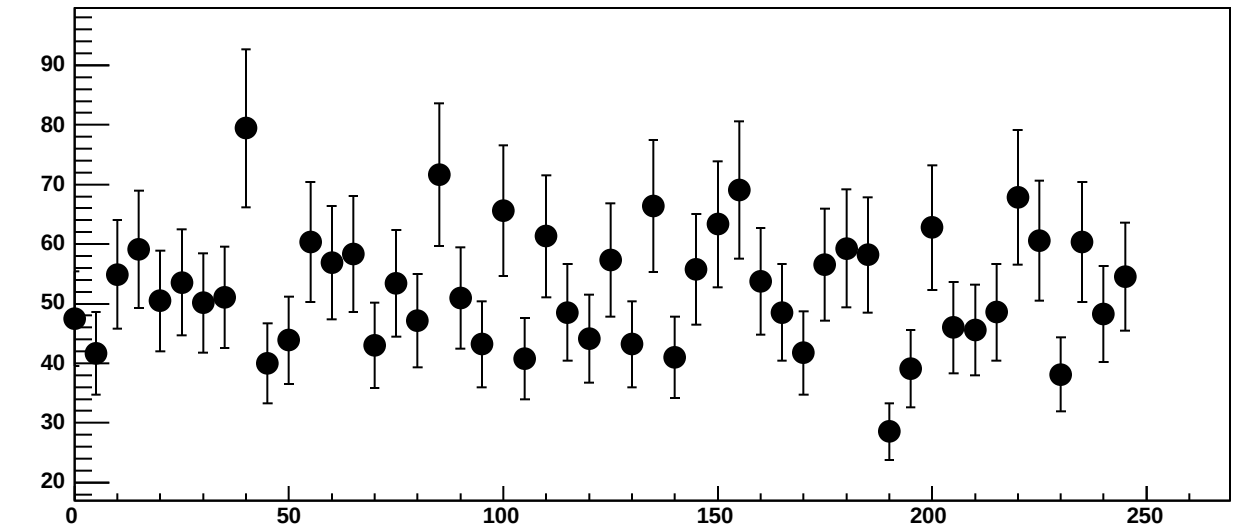
Chip 5, Channel 10, Enable 3, DAC=1600, ADC Residuals vs Hold



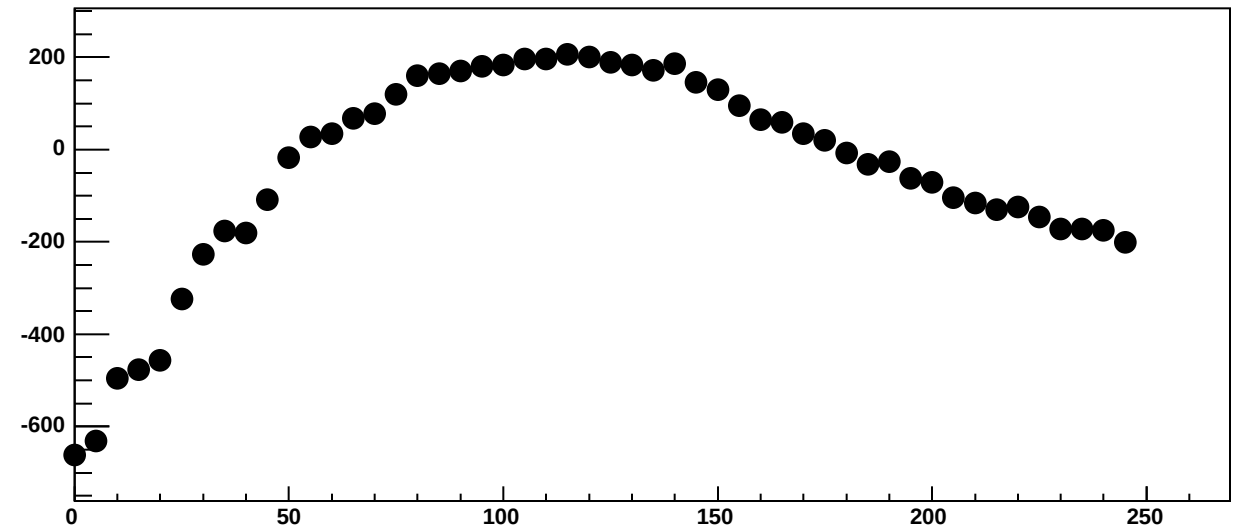
Chip 5, Channel 10, Enable 4, DAC=1600, ADC Mean vs Hold

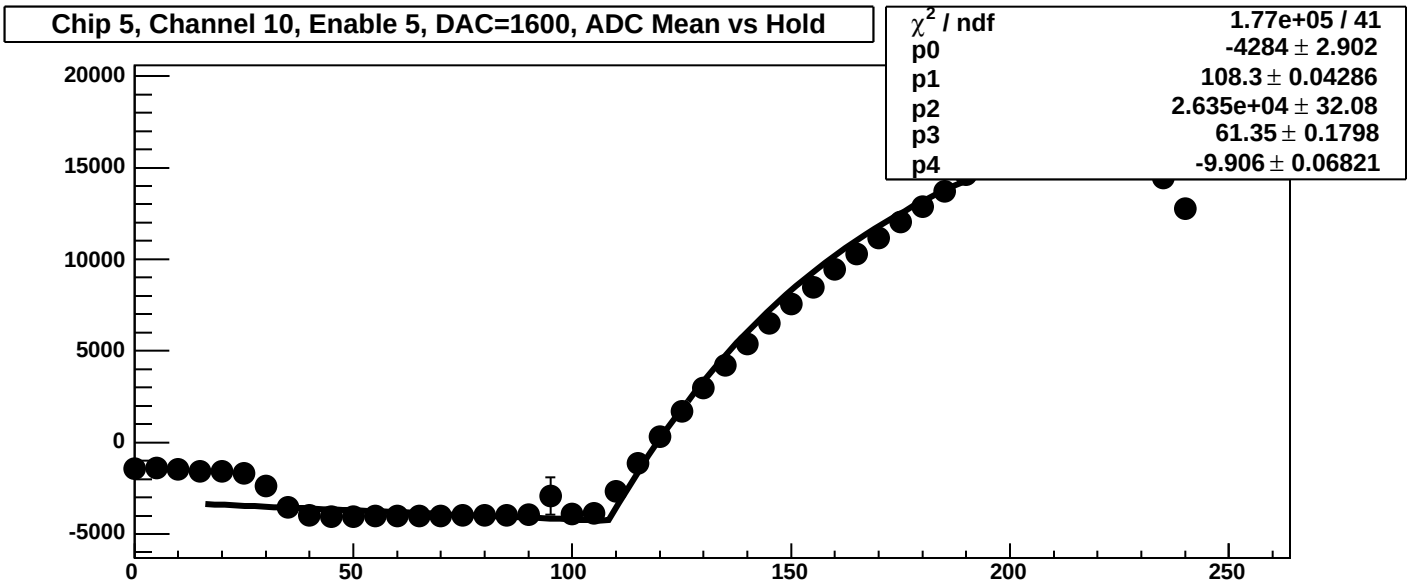


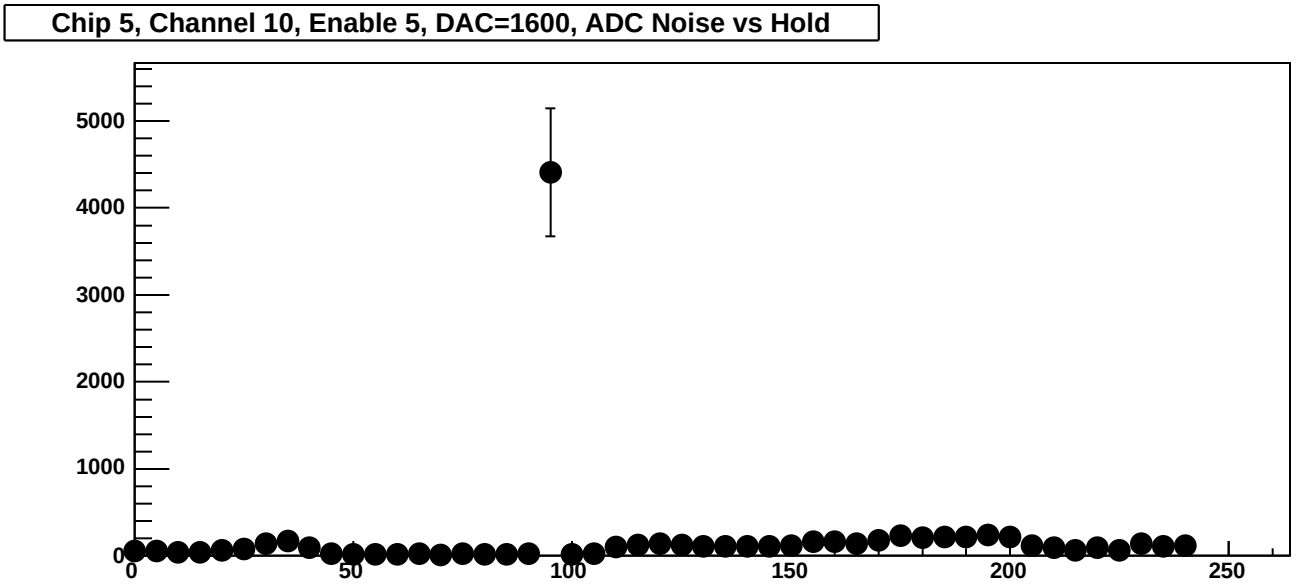
Chip 5, Channel 10, Enable 4, DAC=1600, ADC Noise vs Hold

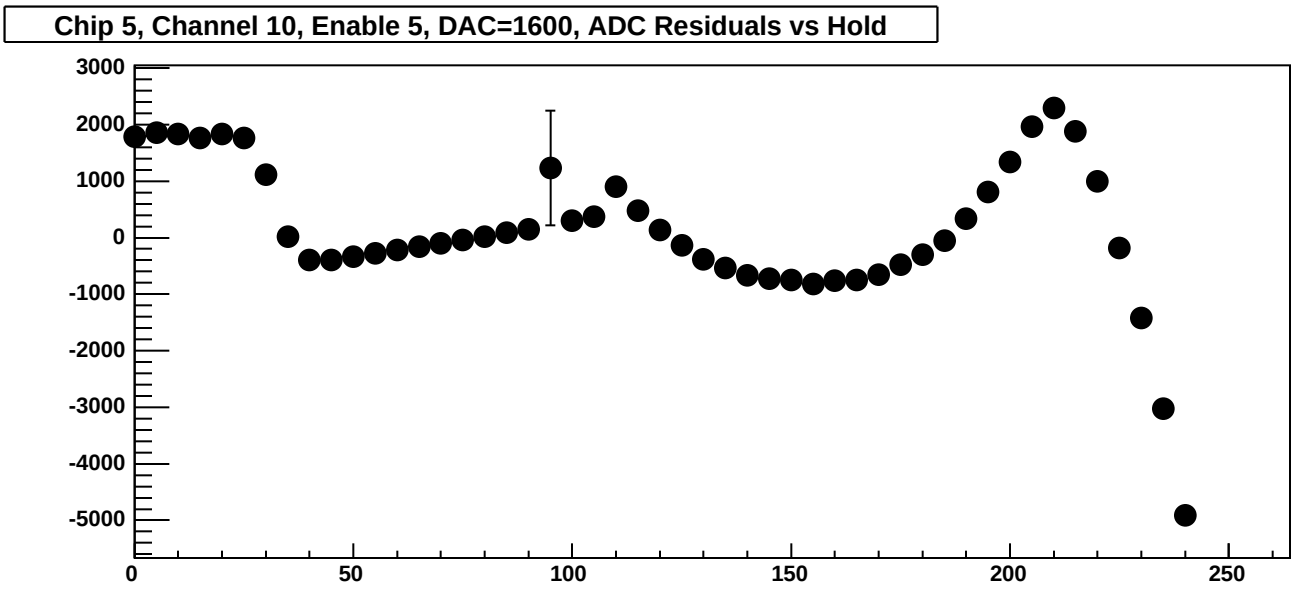


Chip 5, Channel 10, Enable 4, DAC=1600, ADC Residuals vs Hold

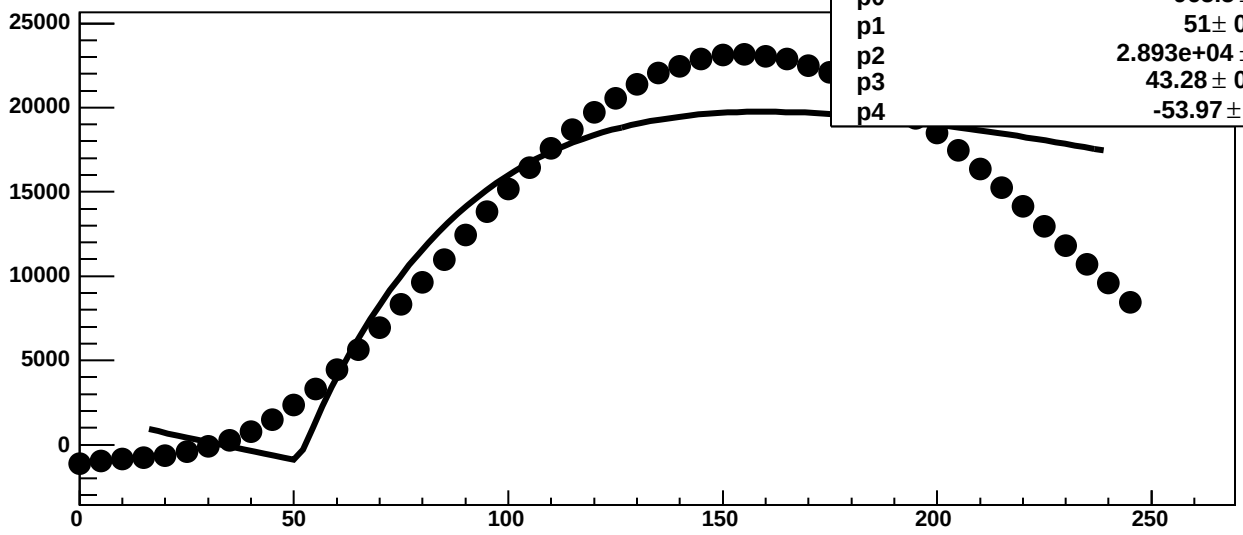



Chip 5, Channel 10, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.77e+05 / 41
p0 -4284 ± 2.902
p1 108.3 ± 0.04286
p2 2.635e+04 ± 32.08
p3 61.35 ± 0.1798
p4 -9.906 ± 0.06821
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

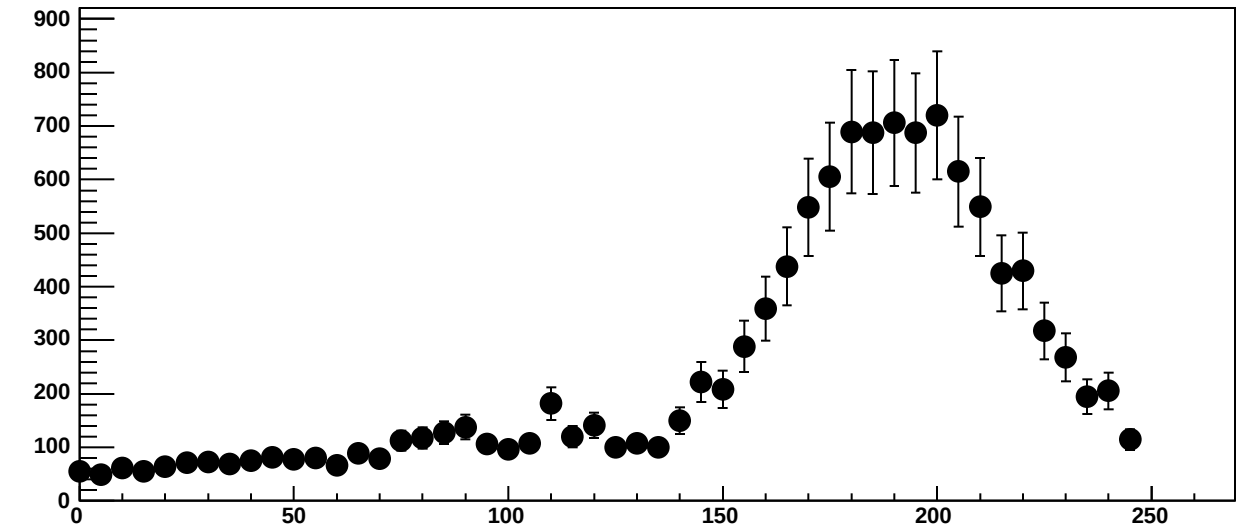

Chip 5, Channel 10, Enable 5, DAC=1600, ADC Noise vs Hold
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 5, Channel 10, Enable 5, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

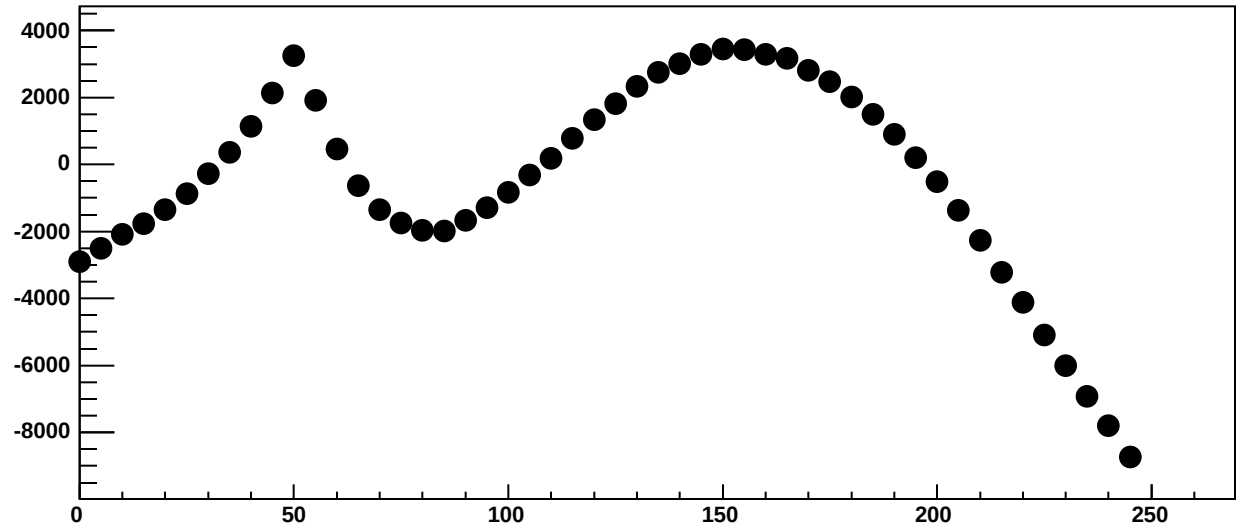
Chip 5, Channel 11, Enable 0, DAC=1600, ADC Mean vs Hold

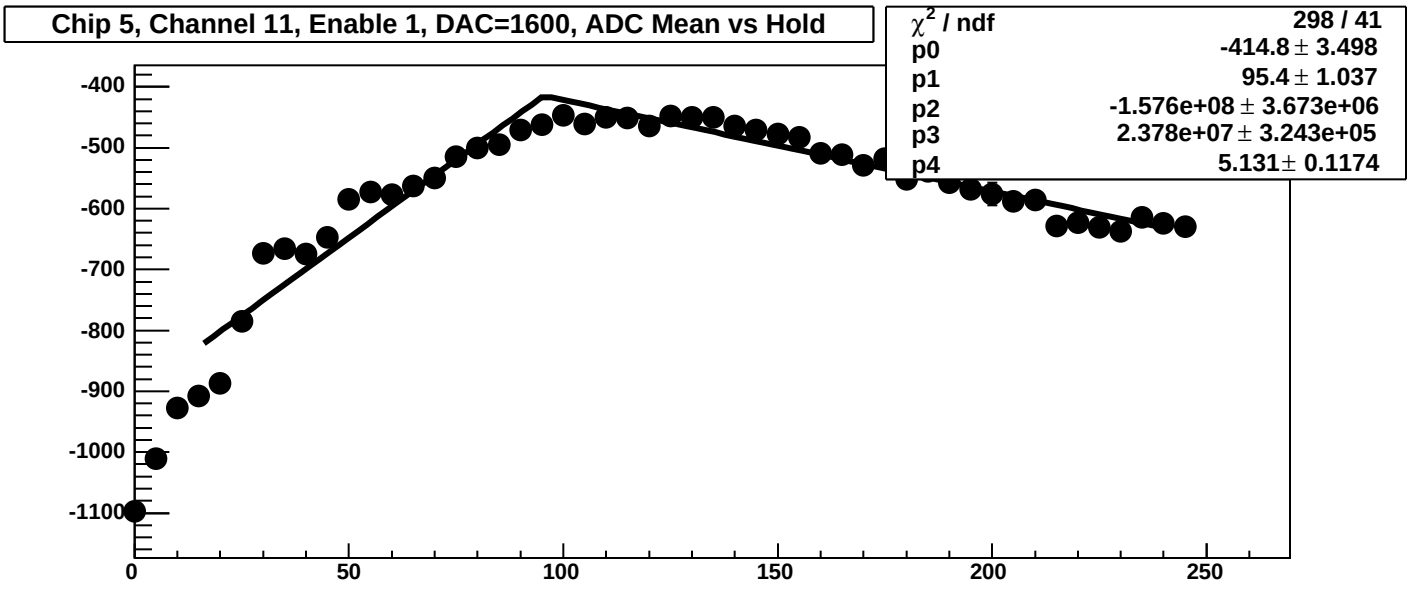


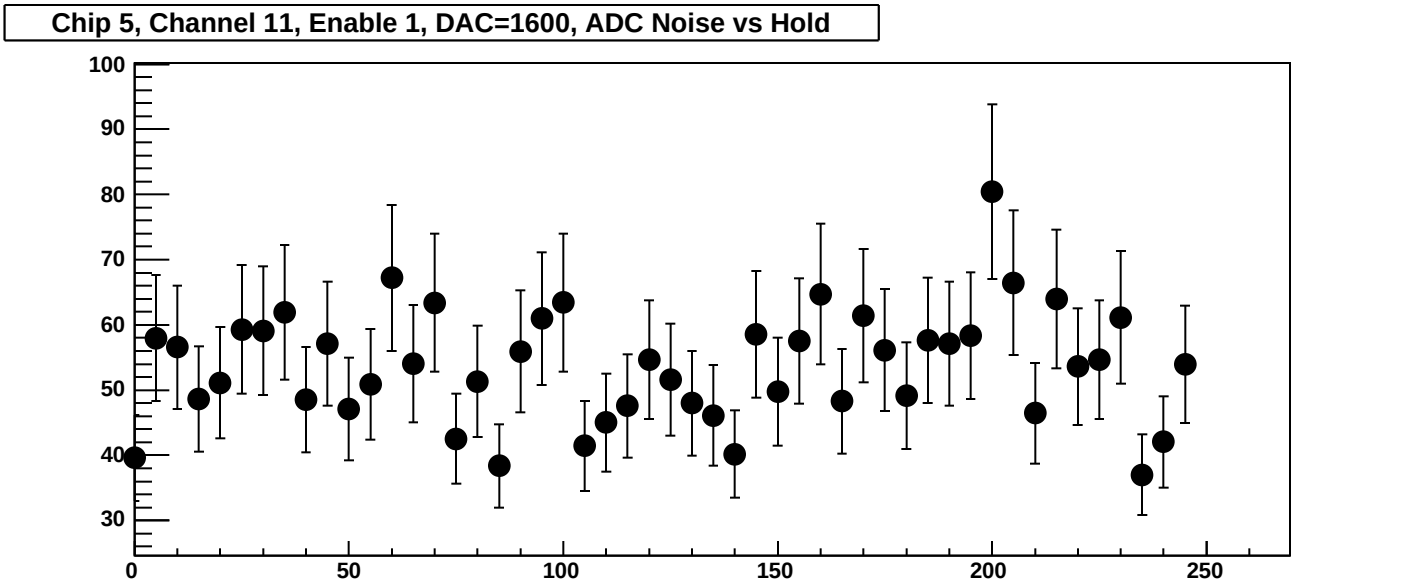
Chip 5, Channel 11, Enable 0, DAC=1600, ADC Noise vs Hold

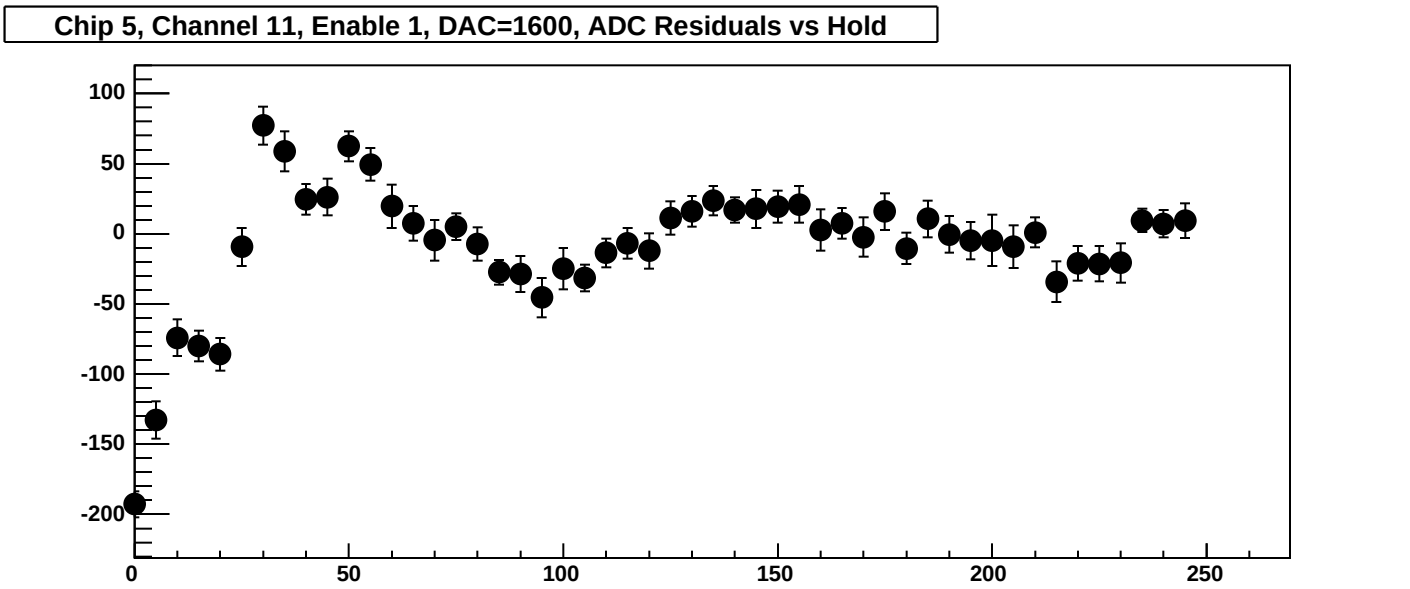


Chip 5, Channel 11, Enable 0, DAC=1600, ADC Residuals vs Hold

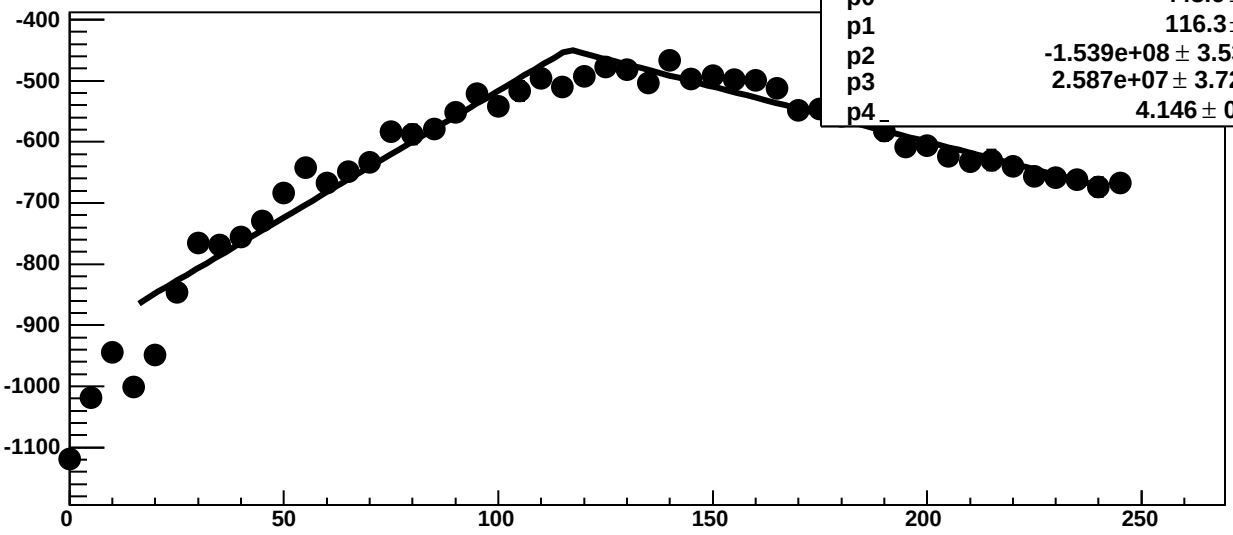



Chip 5, Channel 11, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 298 / 41
p0 -414.8 ± 3.498
p1 95.4 ± 1.037
p2 -1.576e+08 ± 3.673e+06
p3 2.378e+07 ± 3.243e+05
p4 5.131 ± 0.1174
-400
-500
-600
-700
-800
-900
-1000
-1100
0
50
100
150
200
250

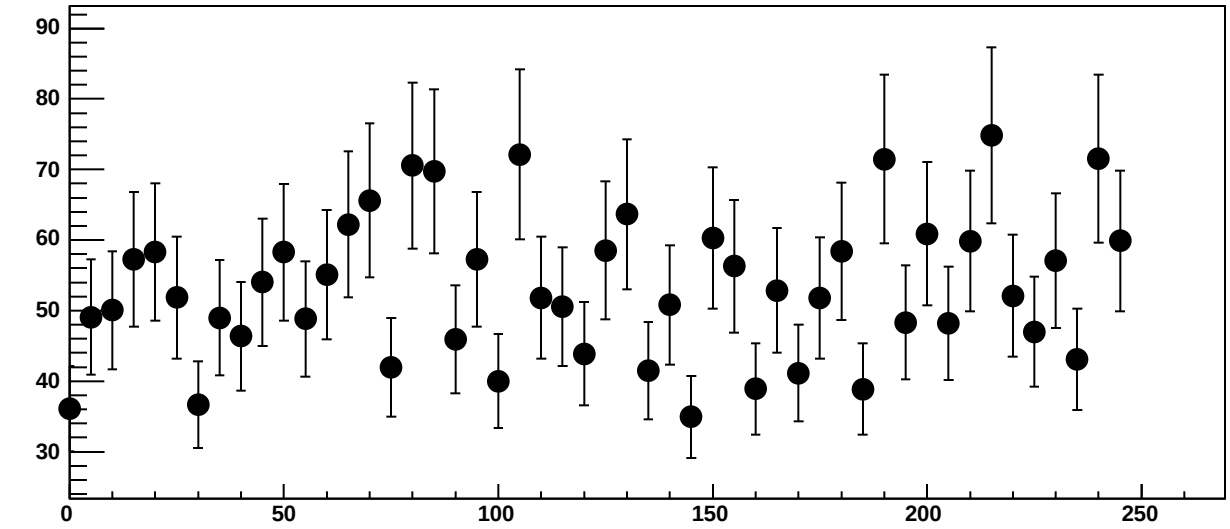

Chip 5, Channel 11, Enable 1, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 5, Channel 11, Enable 1, DAC=1600, ADC Residuals vs Hold
100
50
0
-50
-100
-150
-200
0
50
100
150
200
250

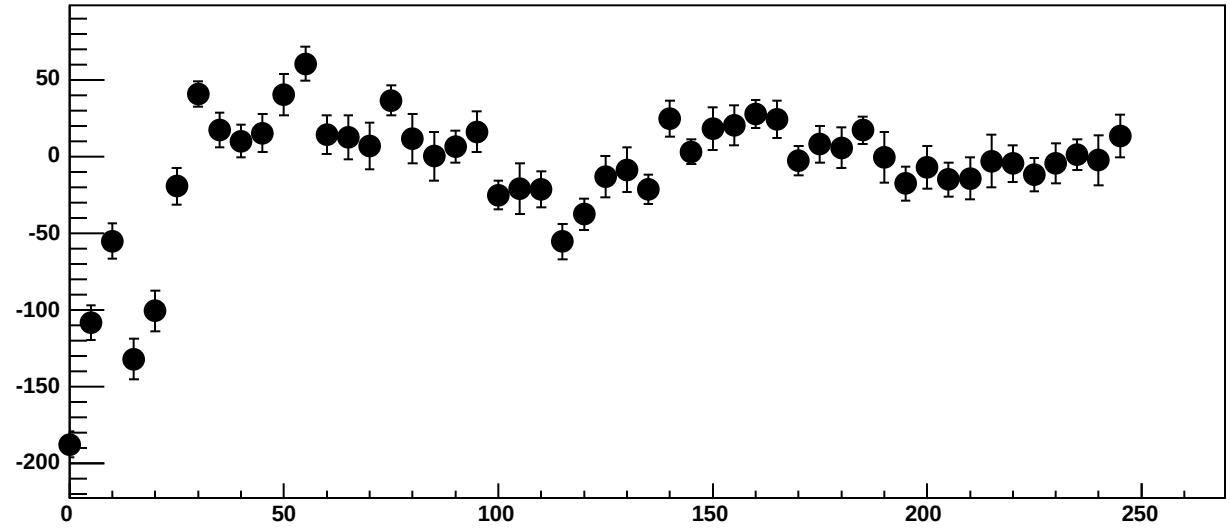
Chip 5, Channel 11, Enable 2, DAC=1600, ADC Mean vs Hold

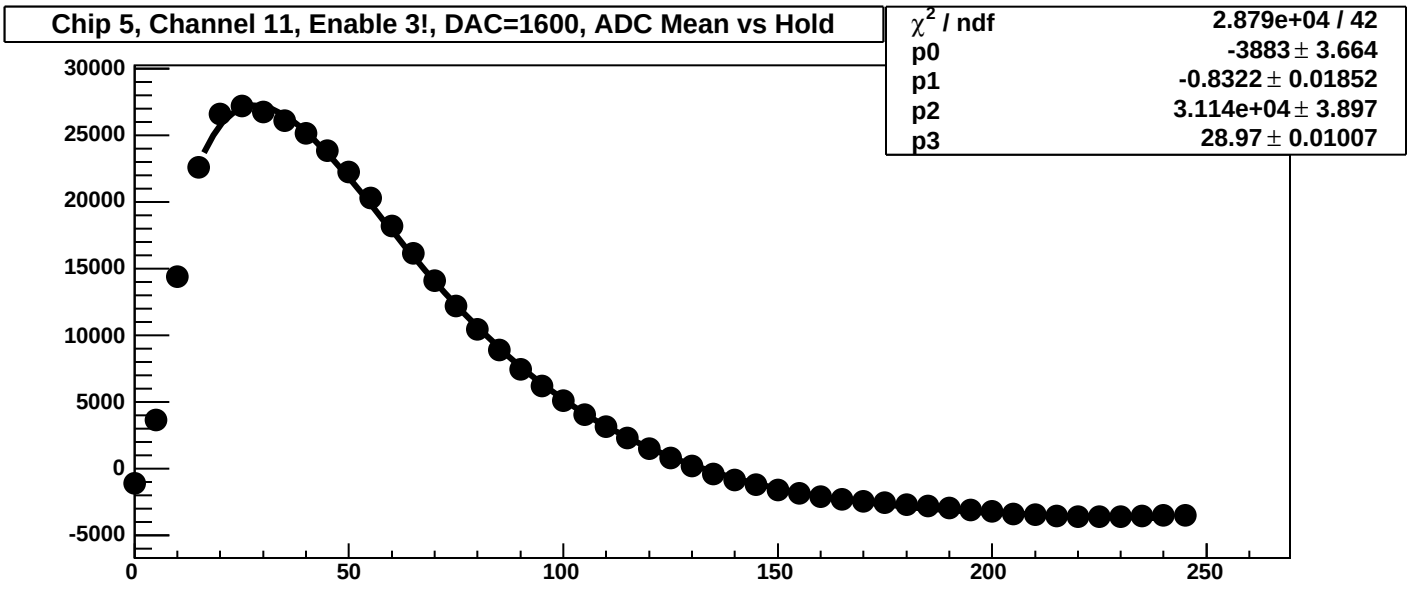


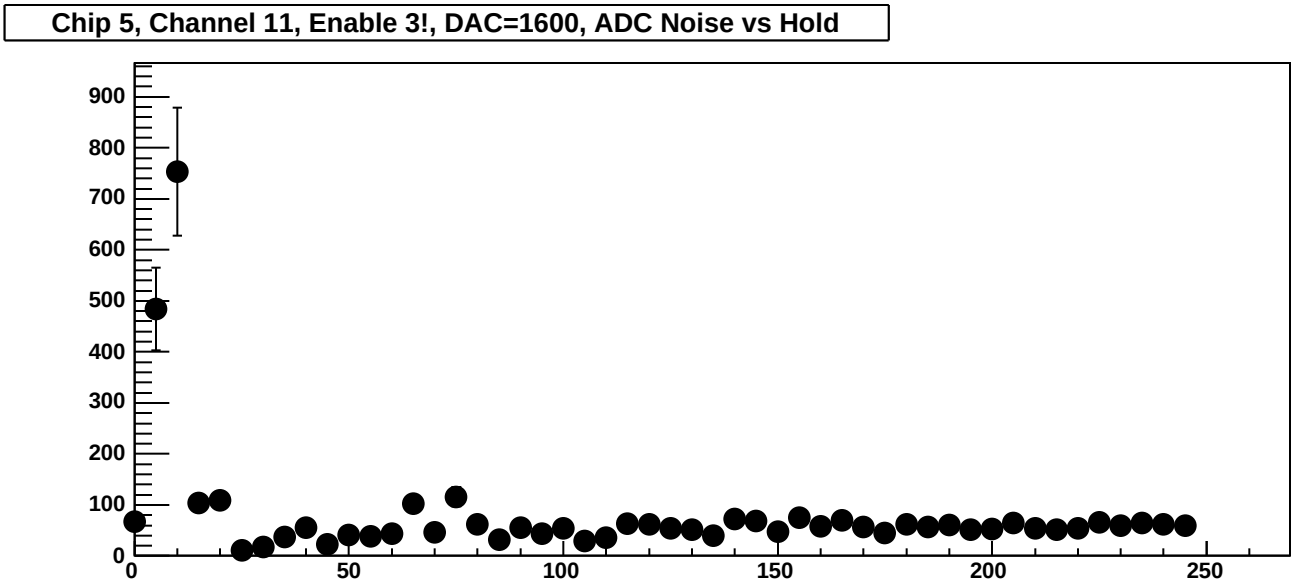
Chip 5, Channel 11, Enable 2, DAC=1600, ADC Noise vs Hold

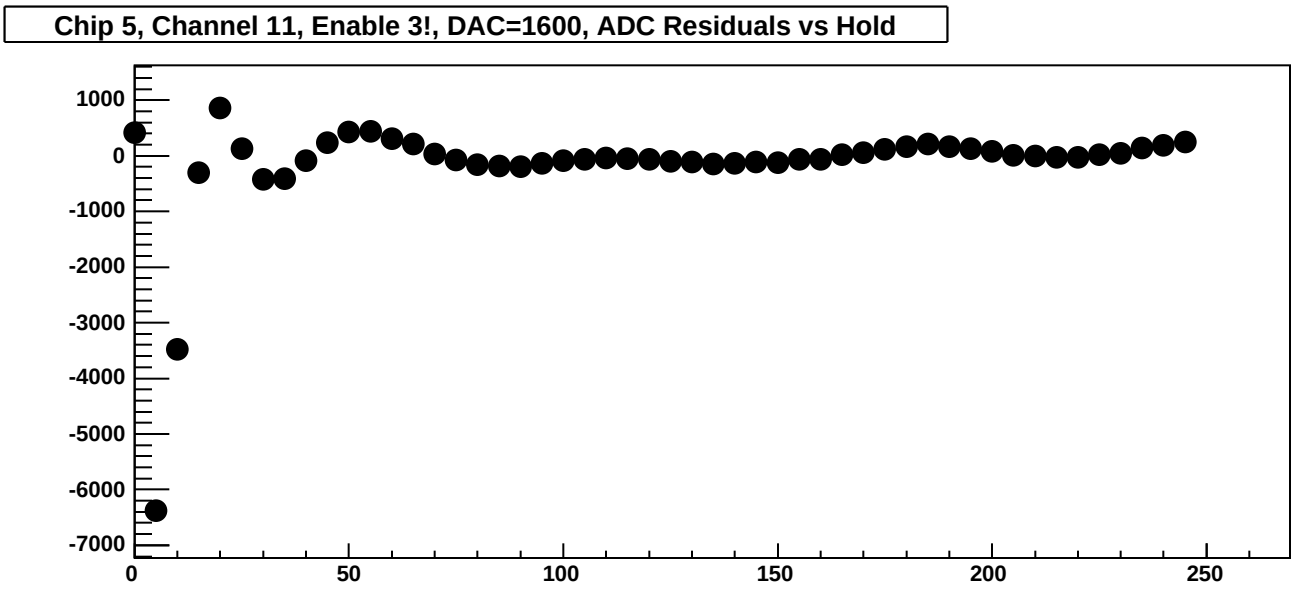


Chip 5, Channel 11, Enable 2, DAC=1600, ADC Residuals vs Hold

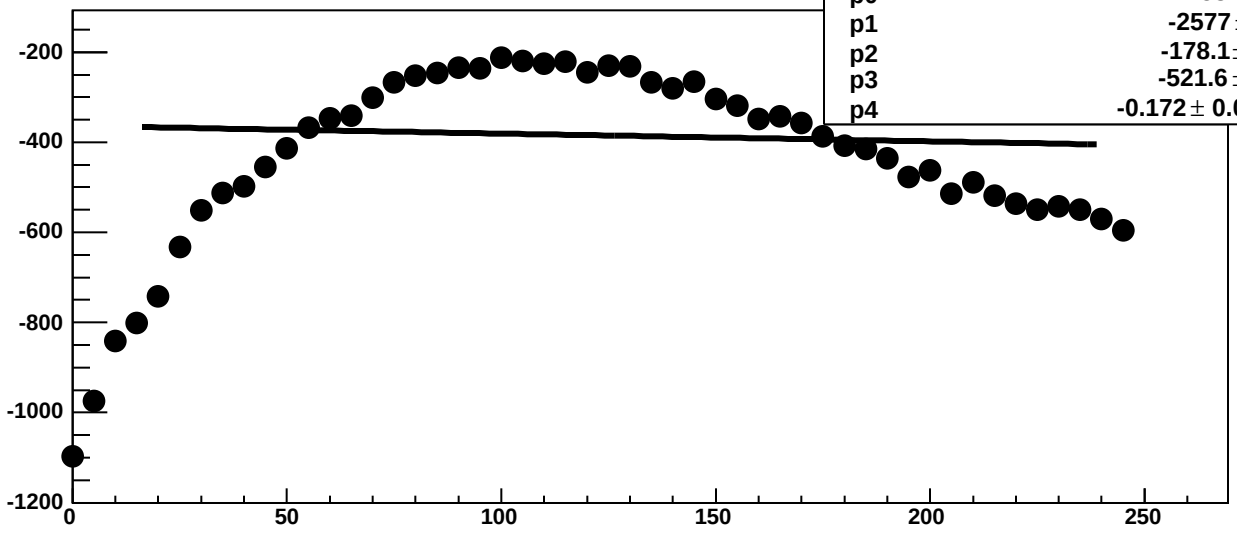



Chip 5, Channel 11, Enable 3!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.879e+04 / 42
p0 -3883 ± 3.664
p1 -0.8322 ± 0.01852
p2 3.114e+04 ± 3.897
p3 28.97 ± 0.01007
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

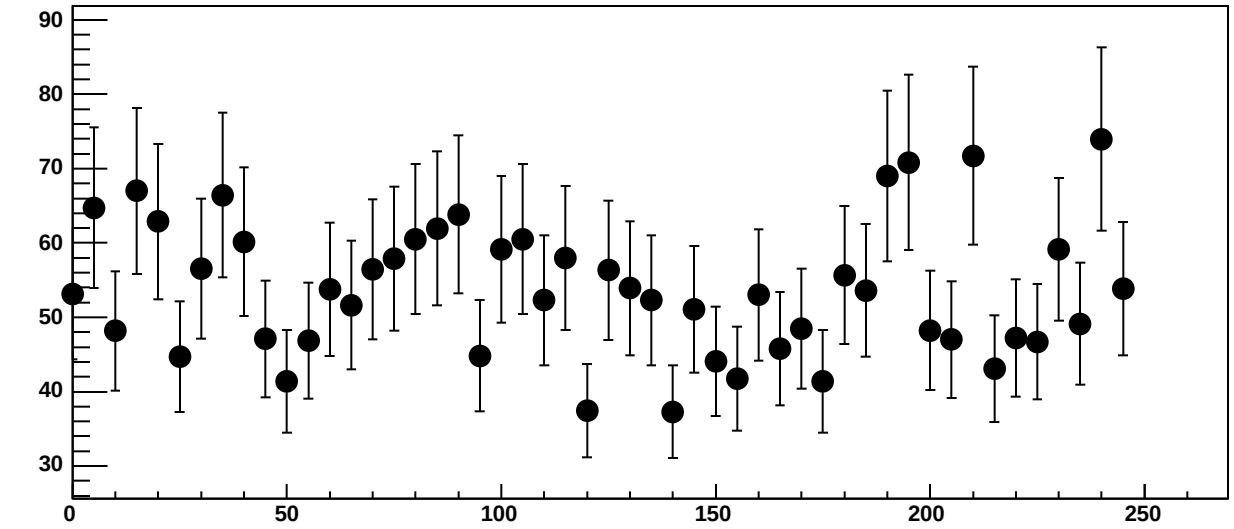

Chip 5, Channel 11, Enable 3!, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 5, Channel 11, Enable 3!, DAC=1600, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
-7000
0
50
100
150
200
250

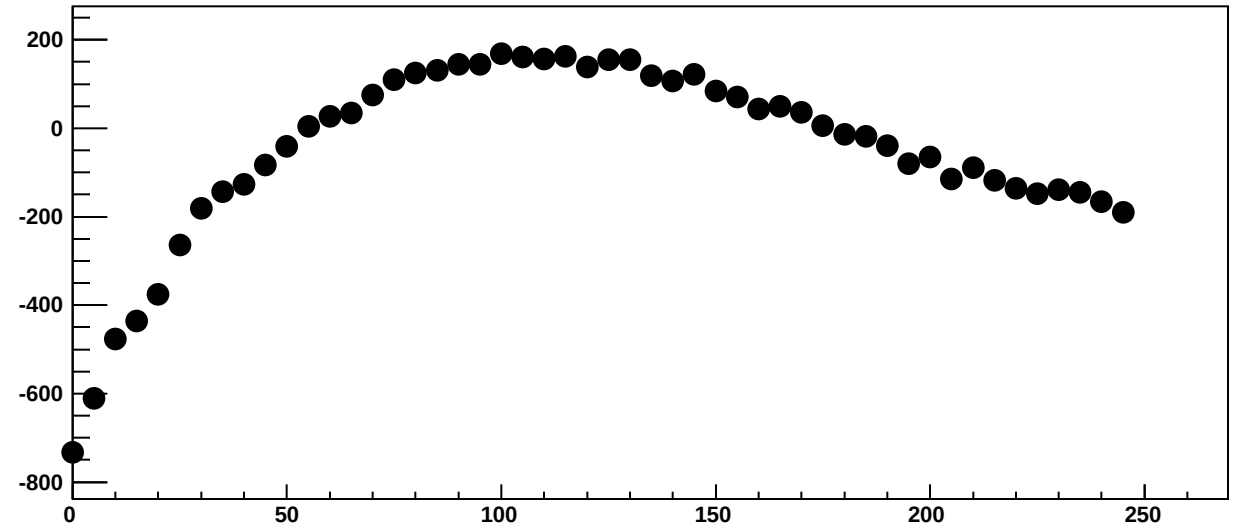
Chip 5, Channel 11, Enable 4, DAC=1600, ADC Mean vs Hold

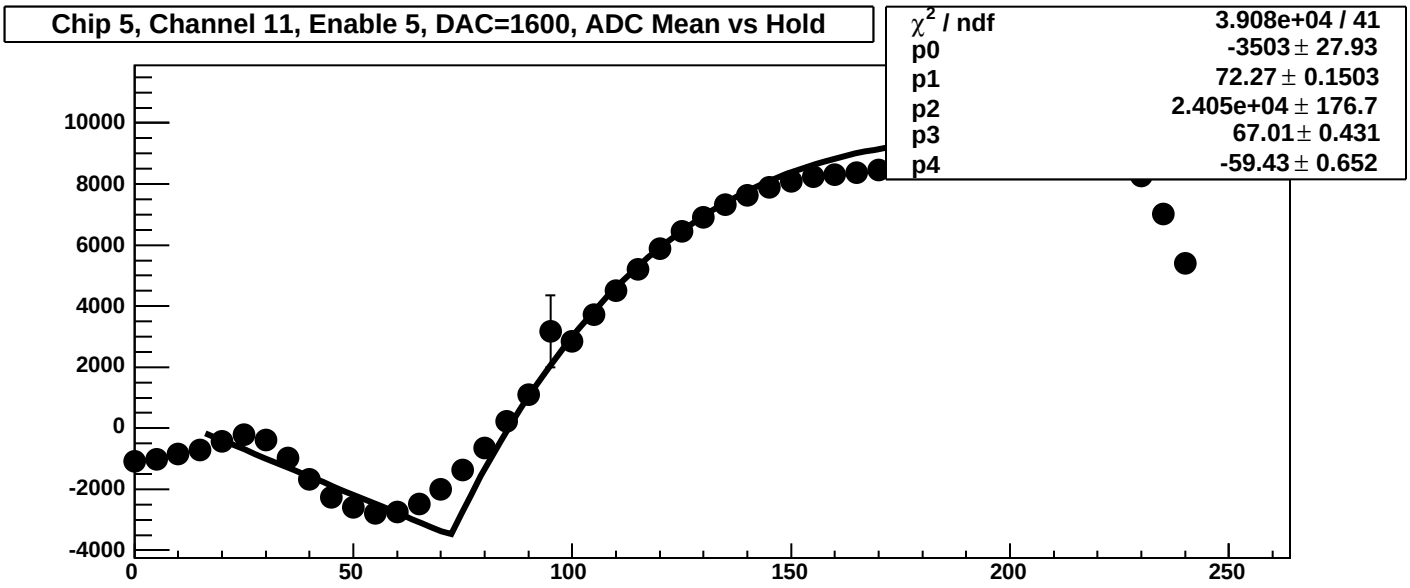


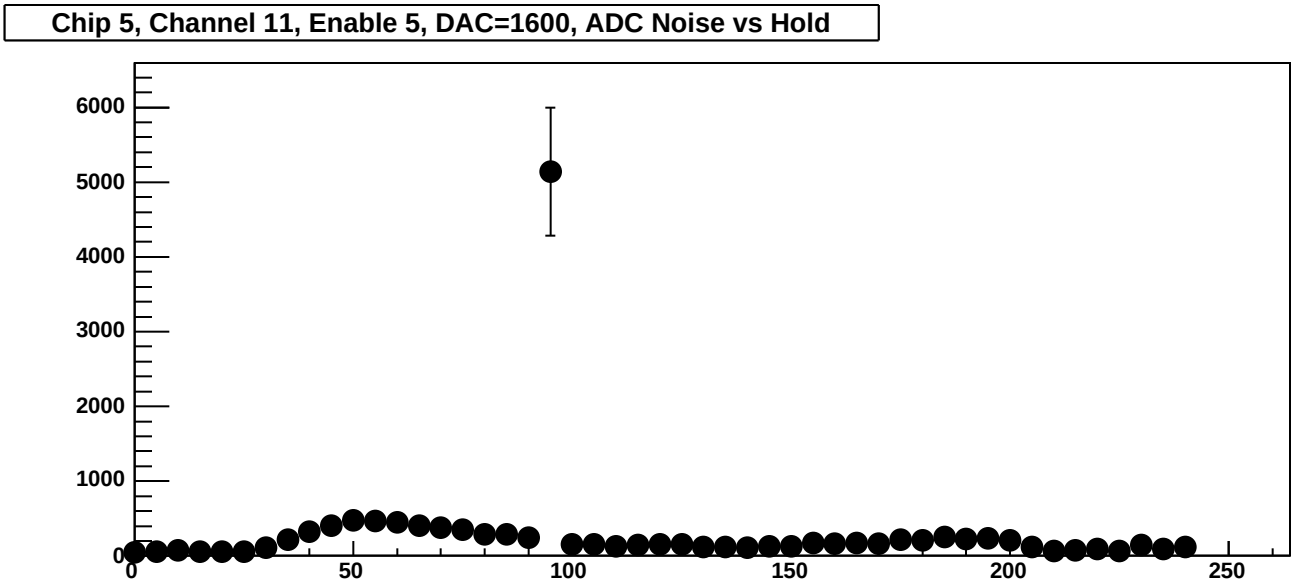
Chip 5, Channel 11, Enable 4, DAC=1600, ADC Noise vs Hold

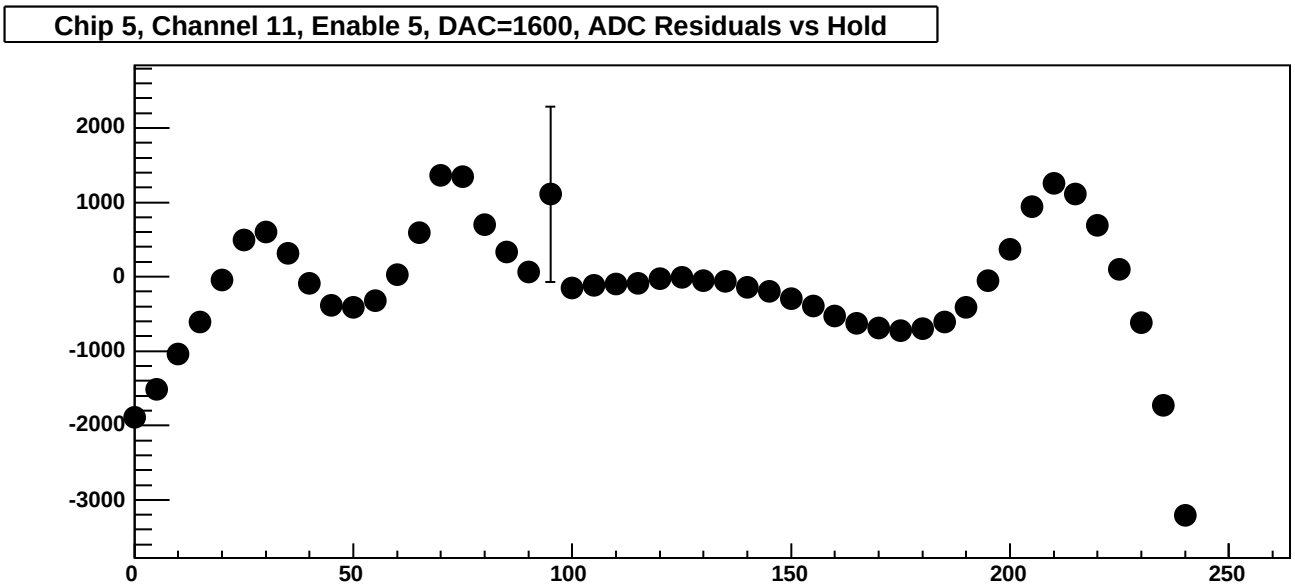


Chip 5, Channel 11, Enable 4, DAC=1600, ADC Residuals vs Hold

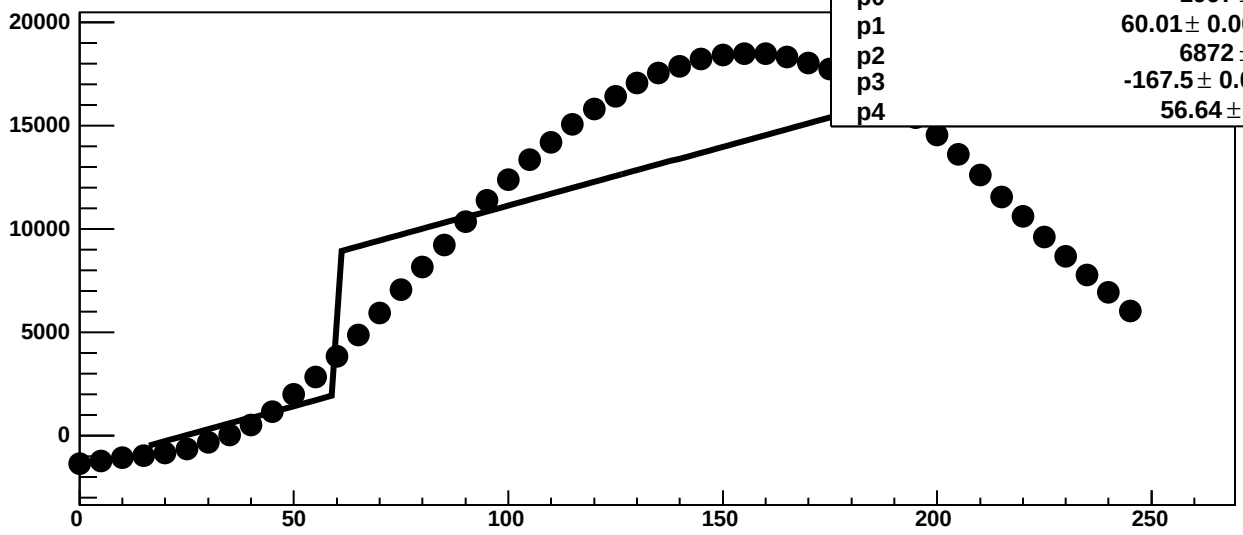



Chip 5, Channel 11, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.908e+04 / 41
p0 -3503 ± 27.93
p1 72.27 ± 0.1503
p2 2.405e+04 ± 176.7
p3 67.01 ± 0.431
p4 -59.43 ± 0.652
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

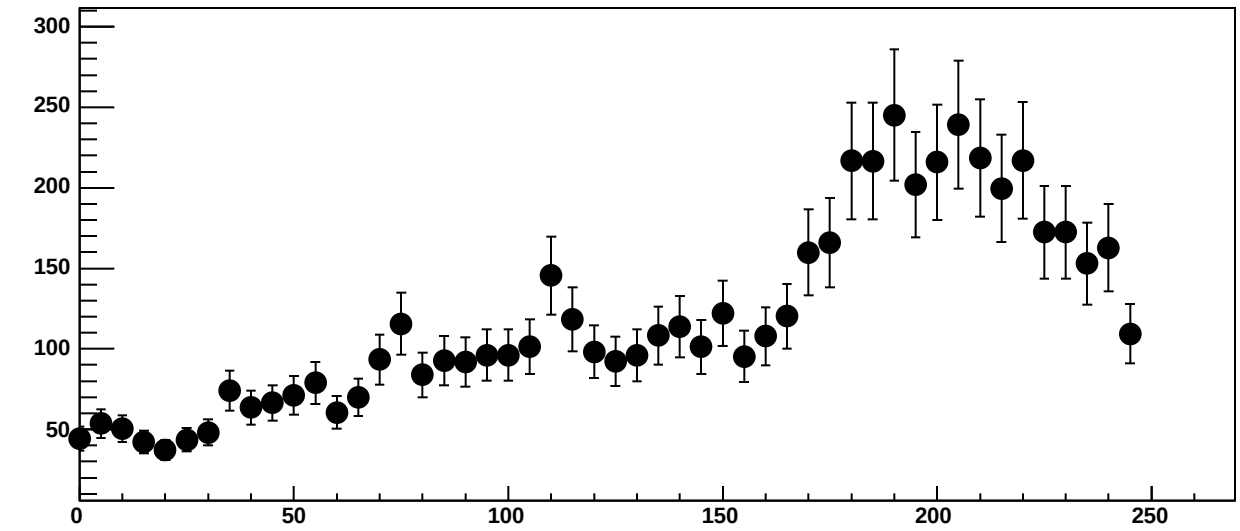

Chip 5, Channel 11, Enable 5, DAC=1600, ADC Noise vs Hold
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 5, Channel 11, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

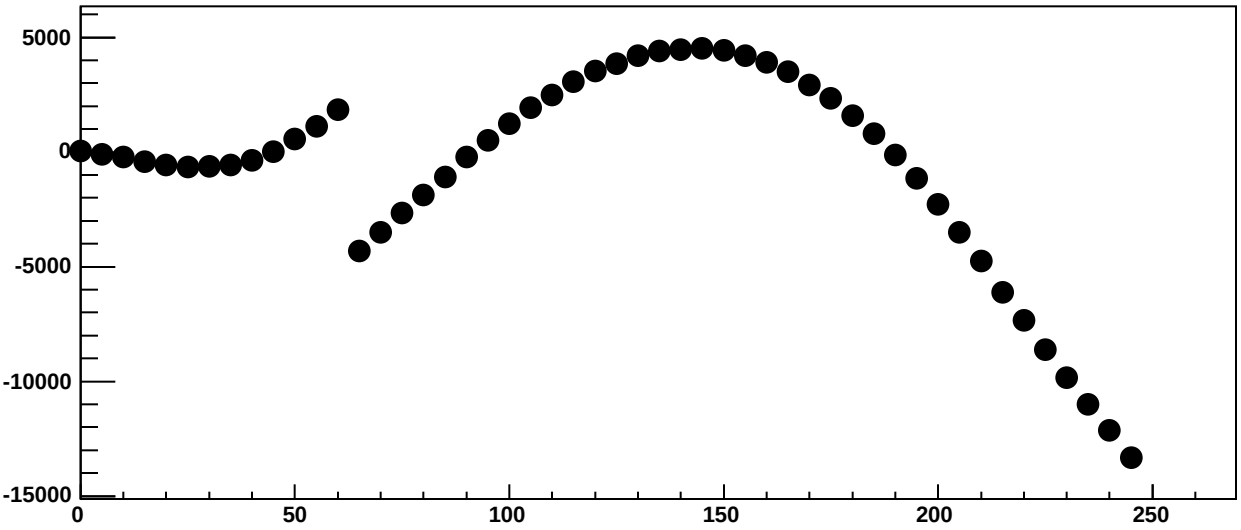
Chip 5, Channel 12, Enable 0, DAC=1600, ADC Mean vs Hold



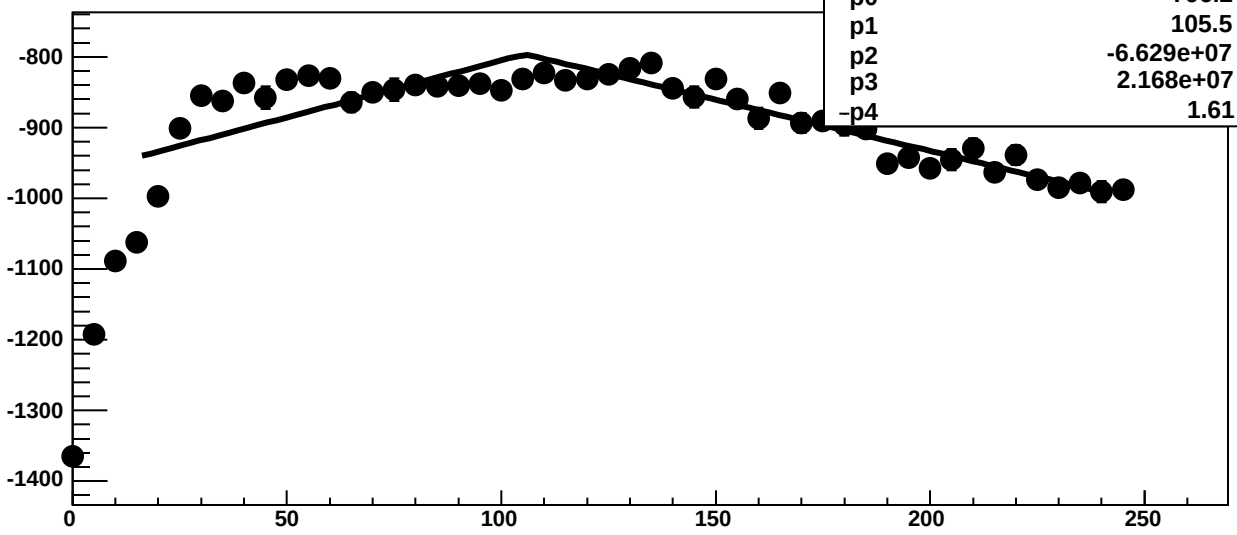
Chip 5, Channel 12, Enable 0, DAC=1600, ADC Noise vs Hold



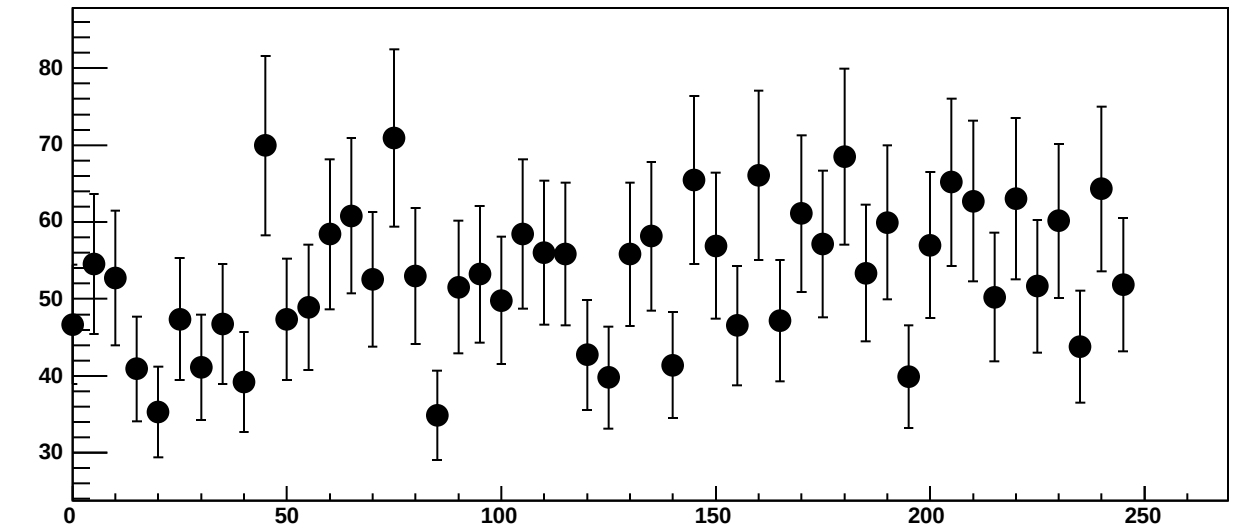
Chip 5, Channel 12, Enable 0, DAC=1600, ADC Residuals vs Hold



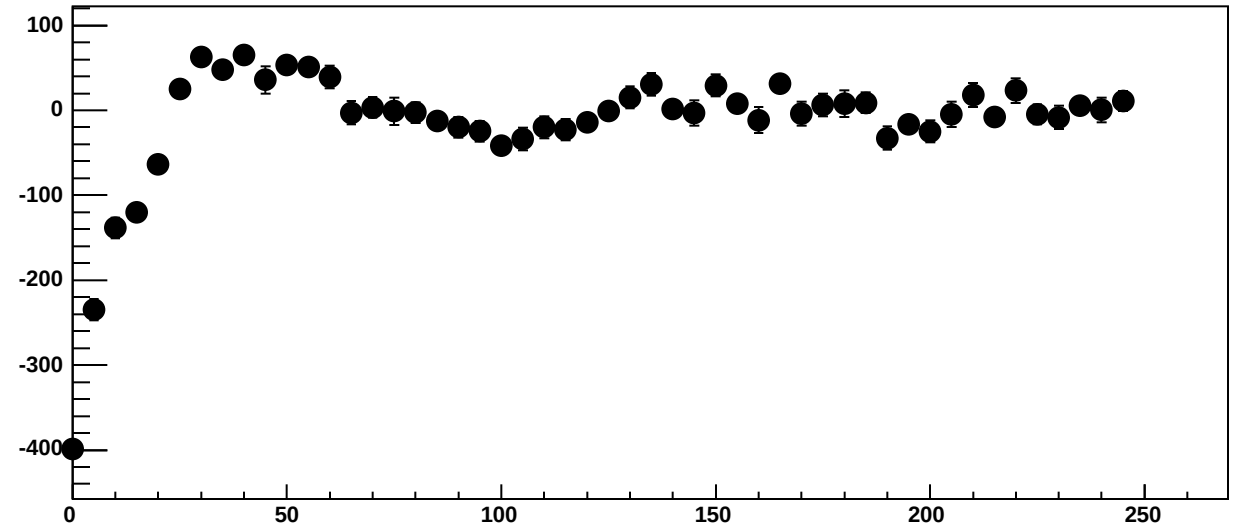
Chip 5, Channel 12, Enable 1, DAC=1600, ADC Mean vs Hold



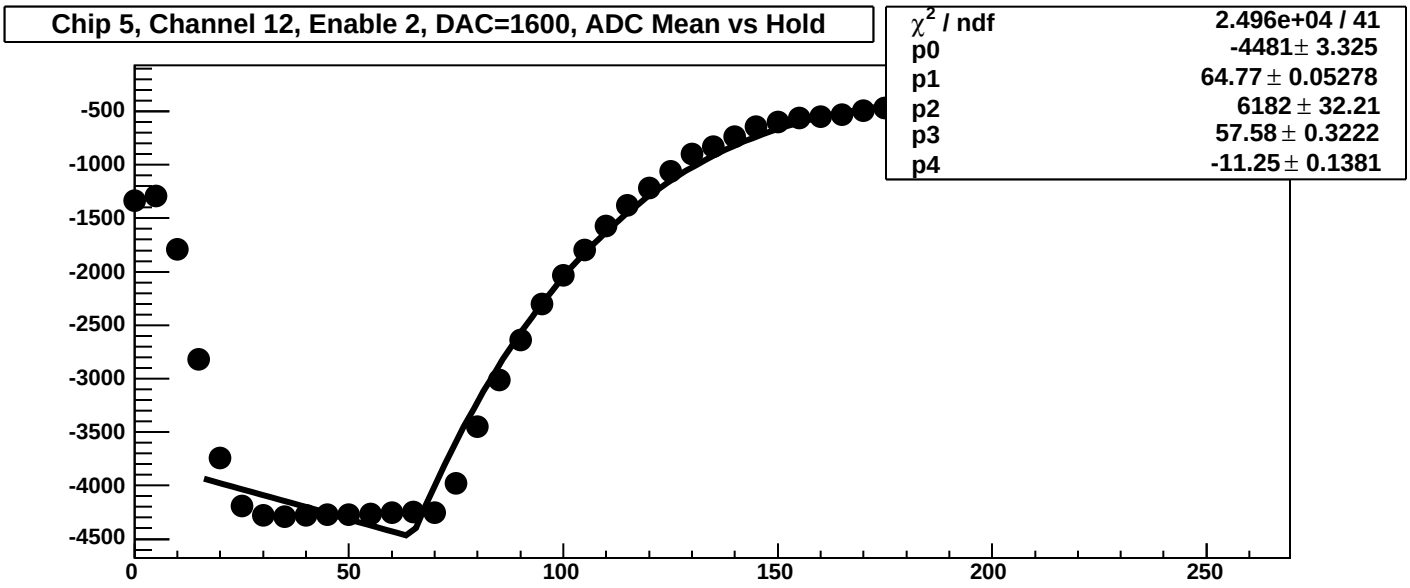
Chip 5, Channel 12, Enable 1, DAC=1600, ADC Noise vs Hold



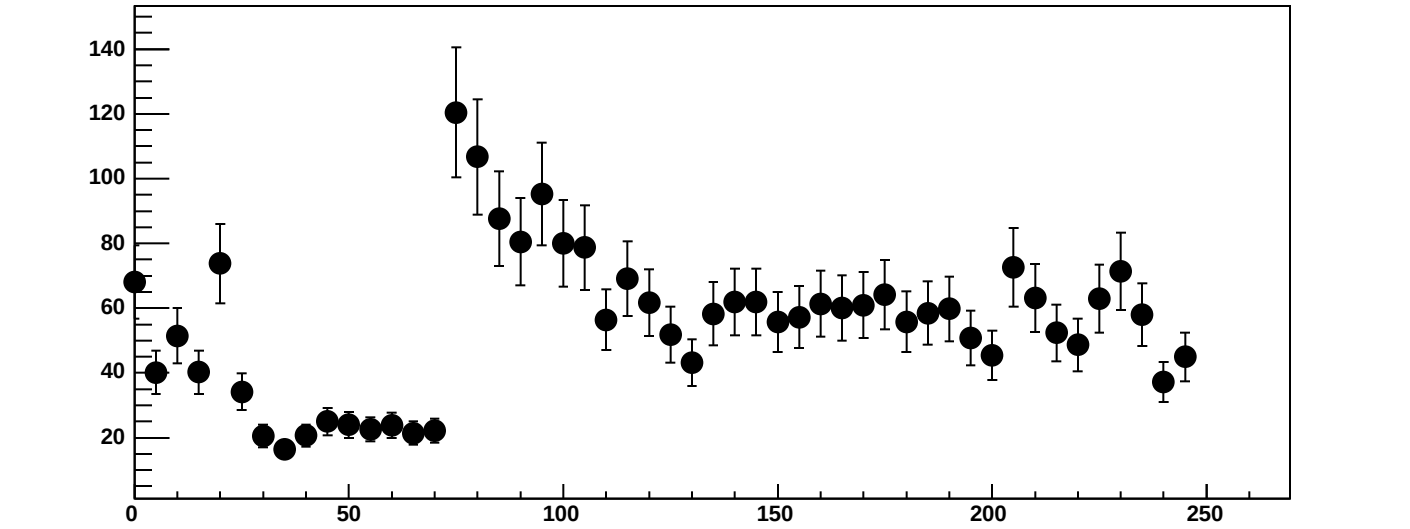
Chip 5, Channel 12, Enable 1, DAC=1600, ADC Residuals vs Hold



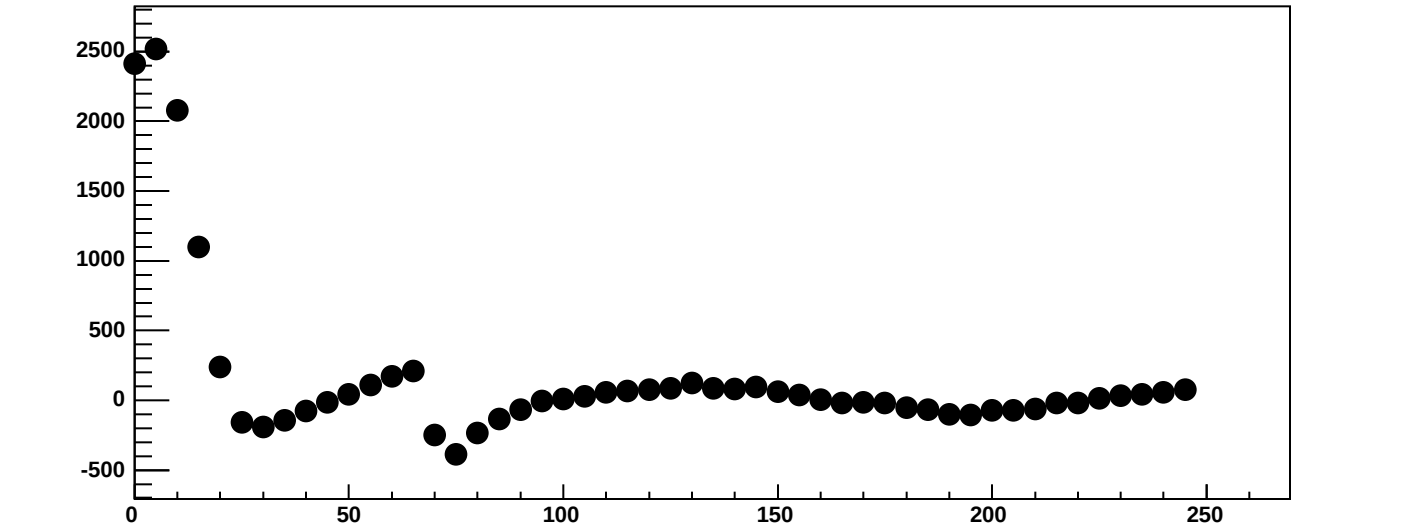
Chip 5, Channel 12, Enable 2, DAC=1600, ADC Mean vs Hold



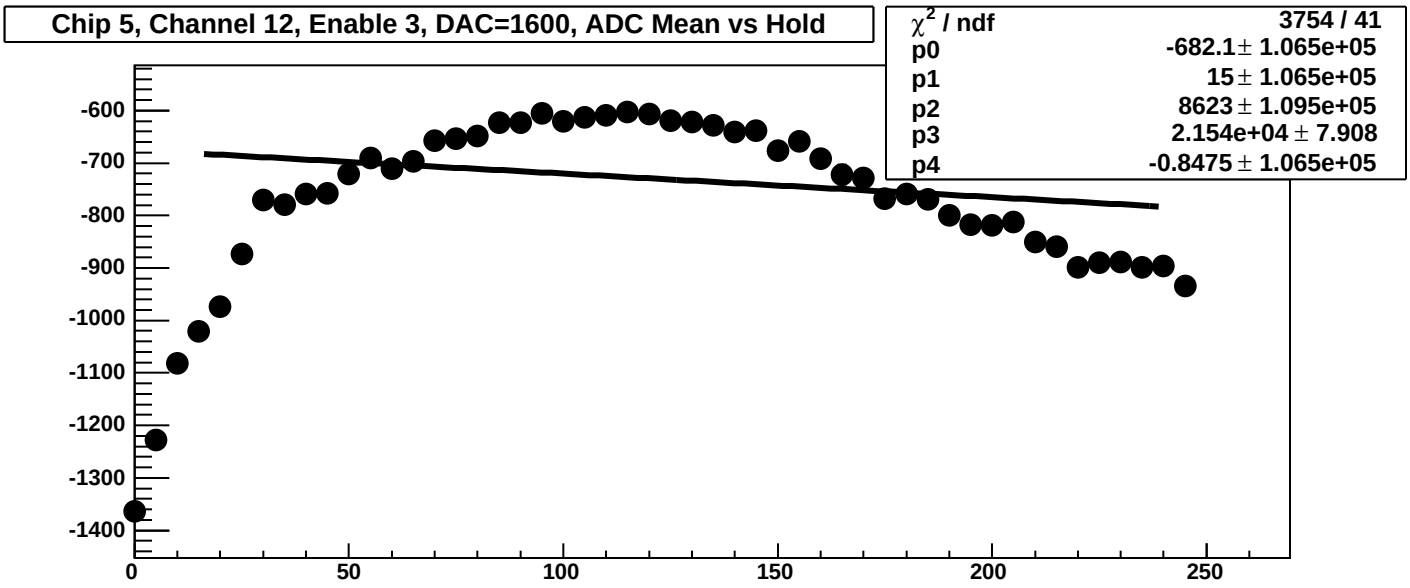
Chip 5, Channel 12, Enable 2, DAC=1600, ADC Noise vs Hold



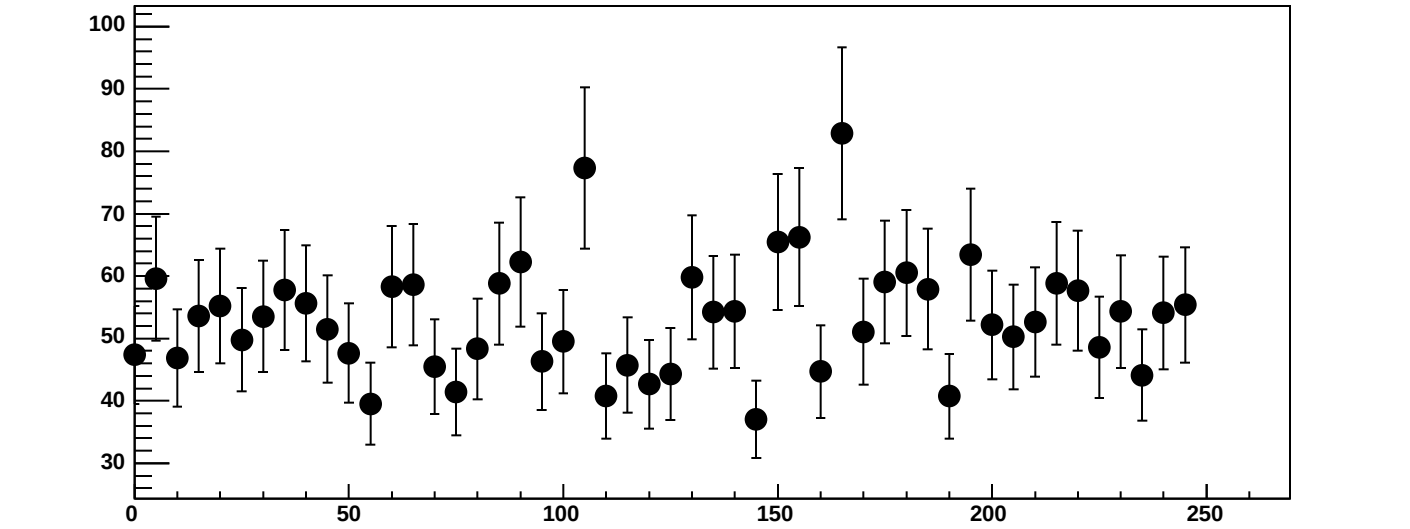
Chip 5, Channel 12, Enable 2, DAC=1600, ADC Residuals vs Hold



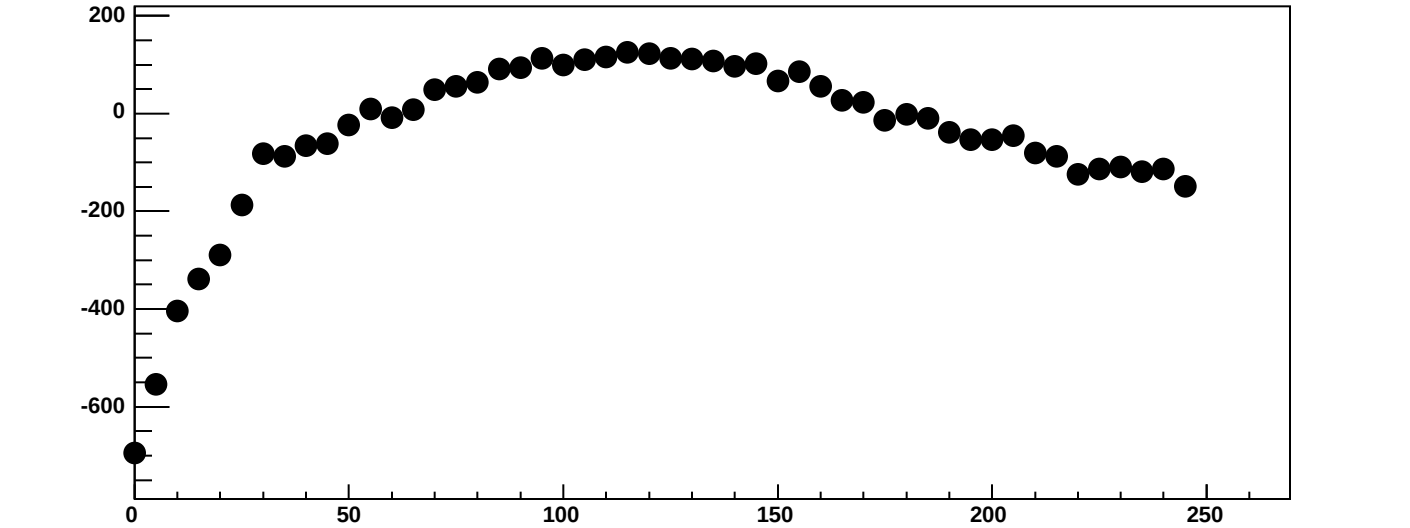
Chip 5, Channel 12, Enable 3, DAC=1600, ADC Mean vs Hold

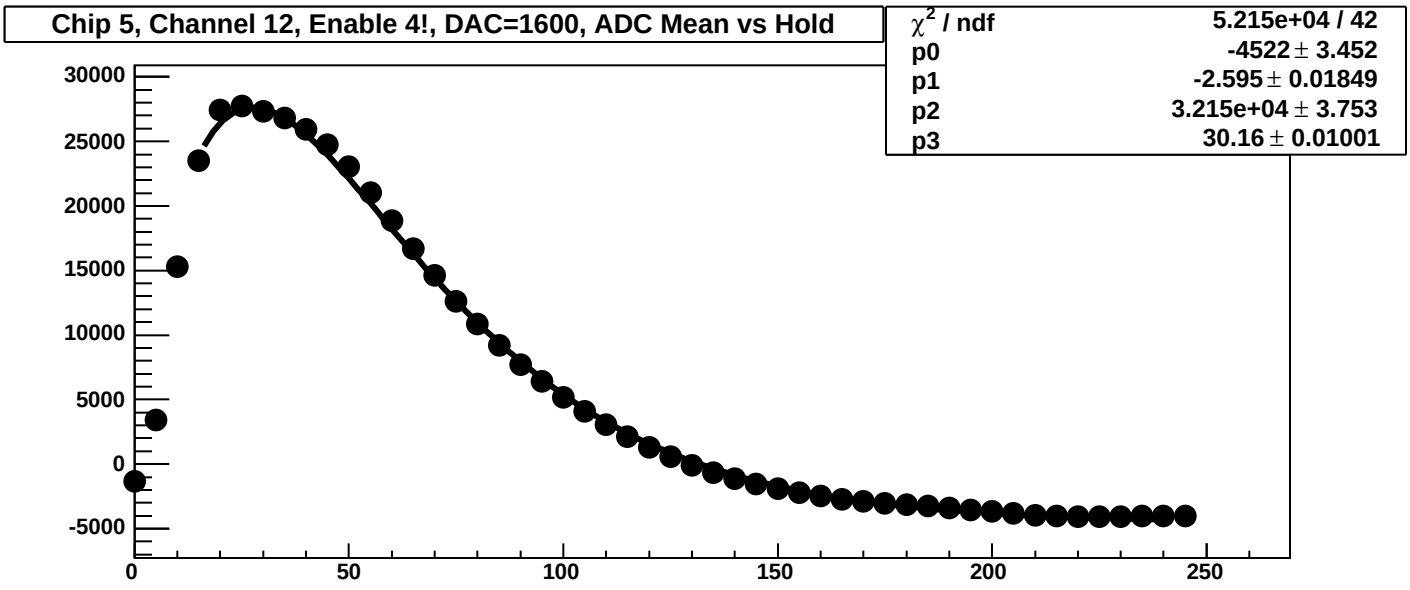


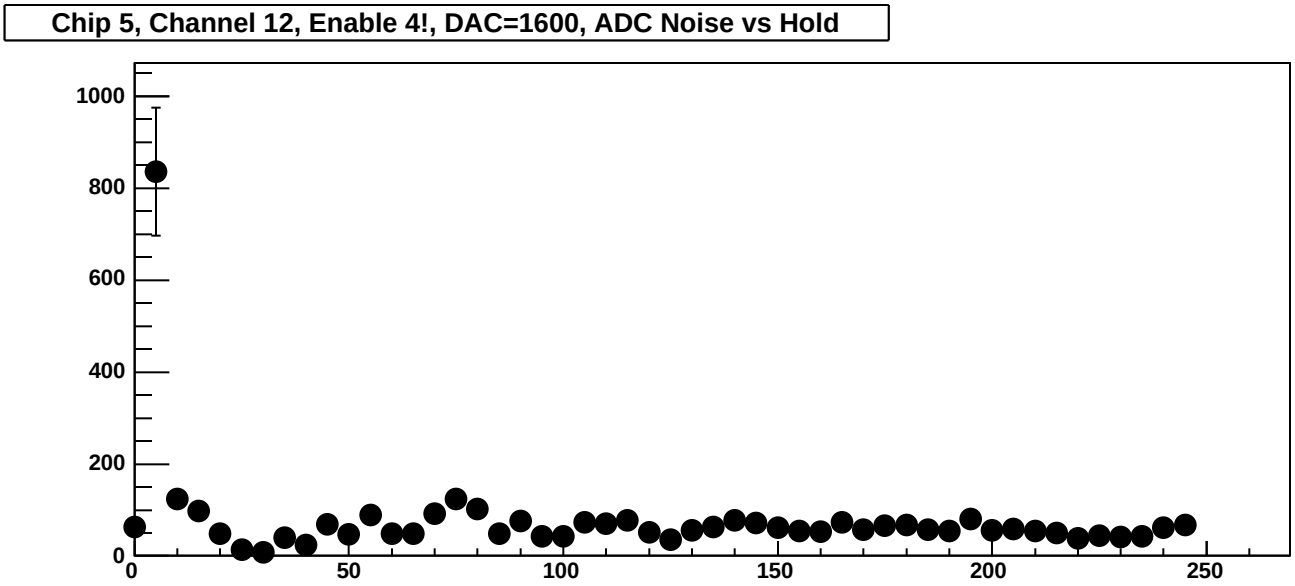
Chip 5, Channel 12, Enable 3, DAC=1600, ADC Noise vs Hold

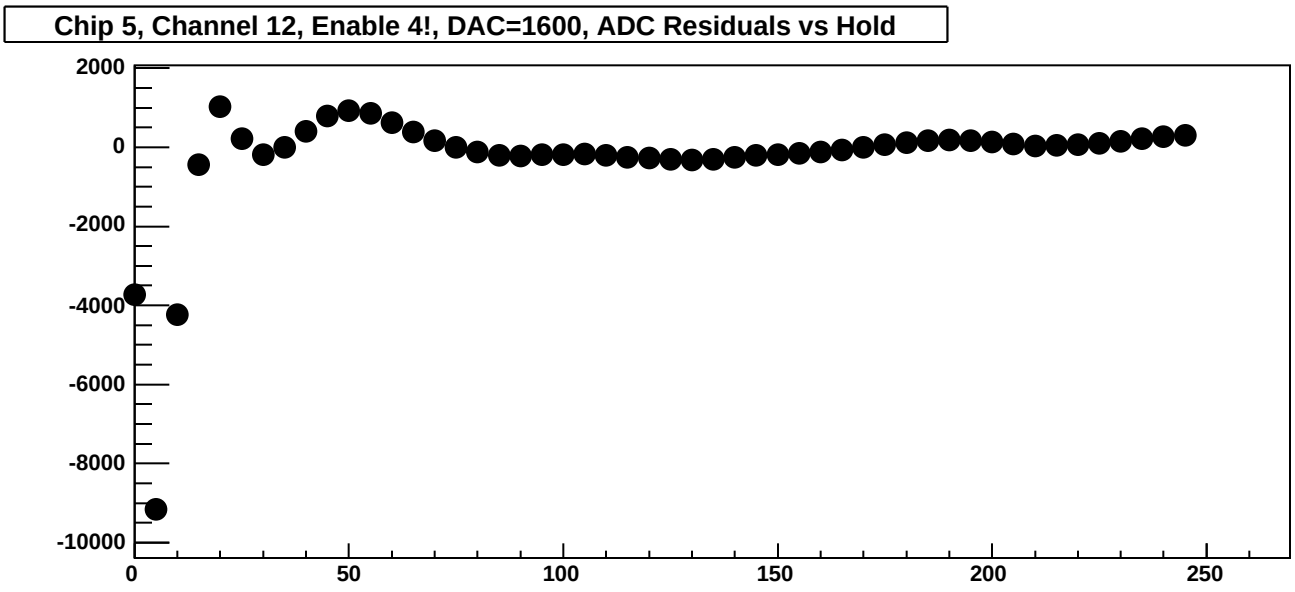


Chip 5, Channel 12, Enable 3, DAC=1600, ADC Residuals vs Hold



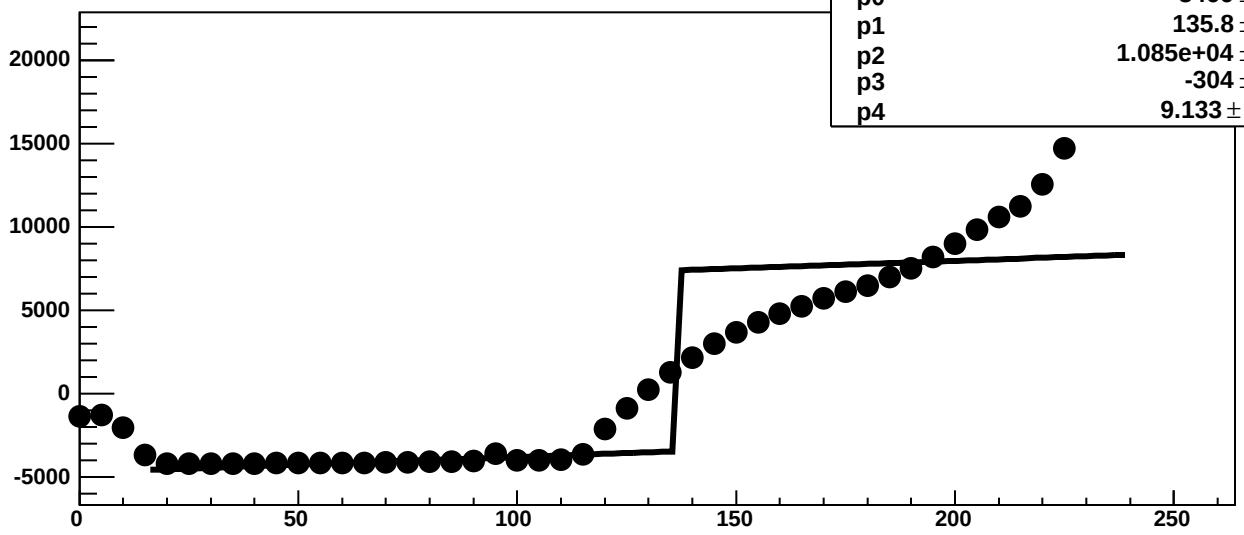

Chip 5, Channel 12, Enable 4!, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.215e+04 / 42
p0 -4522 ± 3.452
p1 -2.595 ± 0.01849
p2 3.215e+04 ± 3.753
p3 30.16 ± 0.01001
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 5, Channel 12, Enable 4!, DAC=1600, ADC Noise vs Hold
1000
800
600
400
200
0
0
50
100
150
200
250

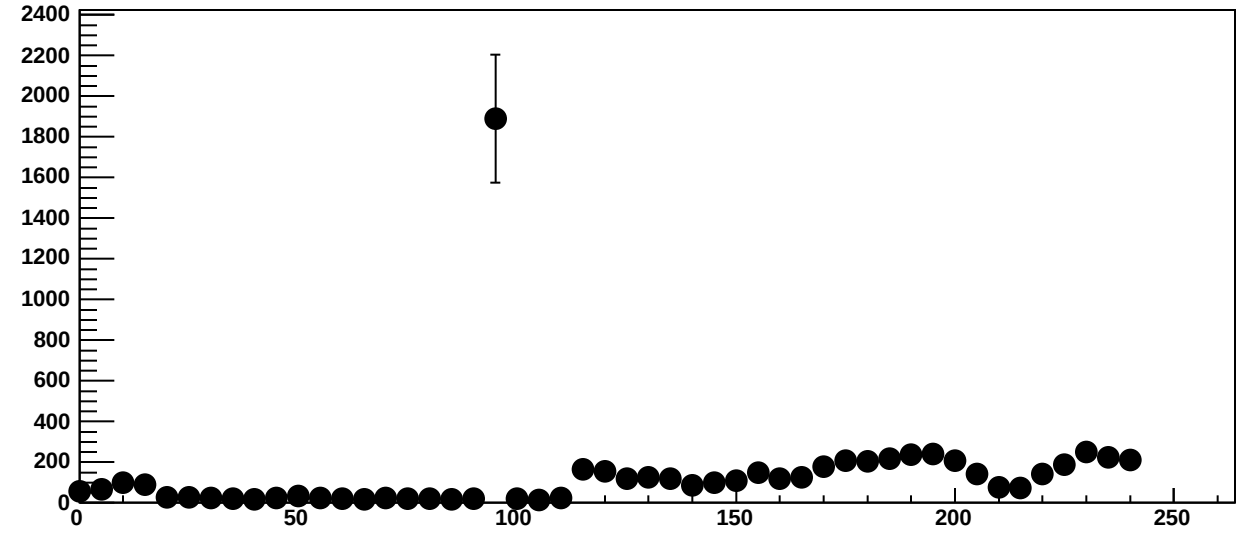

Chip 5, Channel 12, Enable 4!, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

Chip 5, Channel 12, Enable 5, DAC=1600, ADC Mean vs Hold

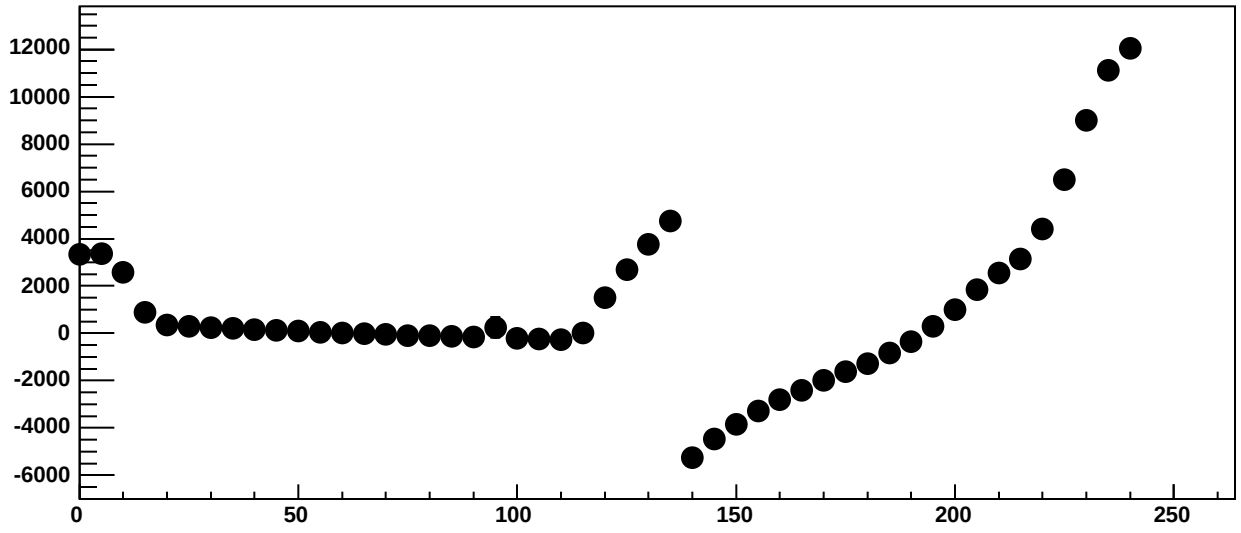
χ^2 / ndf	4.788e+05 / 41
p0	-3466 ± 3.574
p1	135.8 ± 1.635
p2	1.085e+04 ± 5.734
p3	-304 ± 6.156
p4	9.133 ± 0.1138

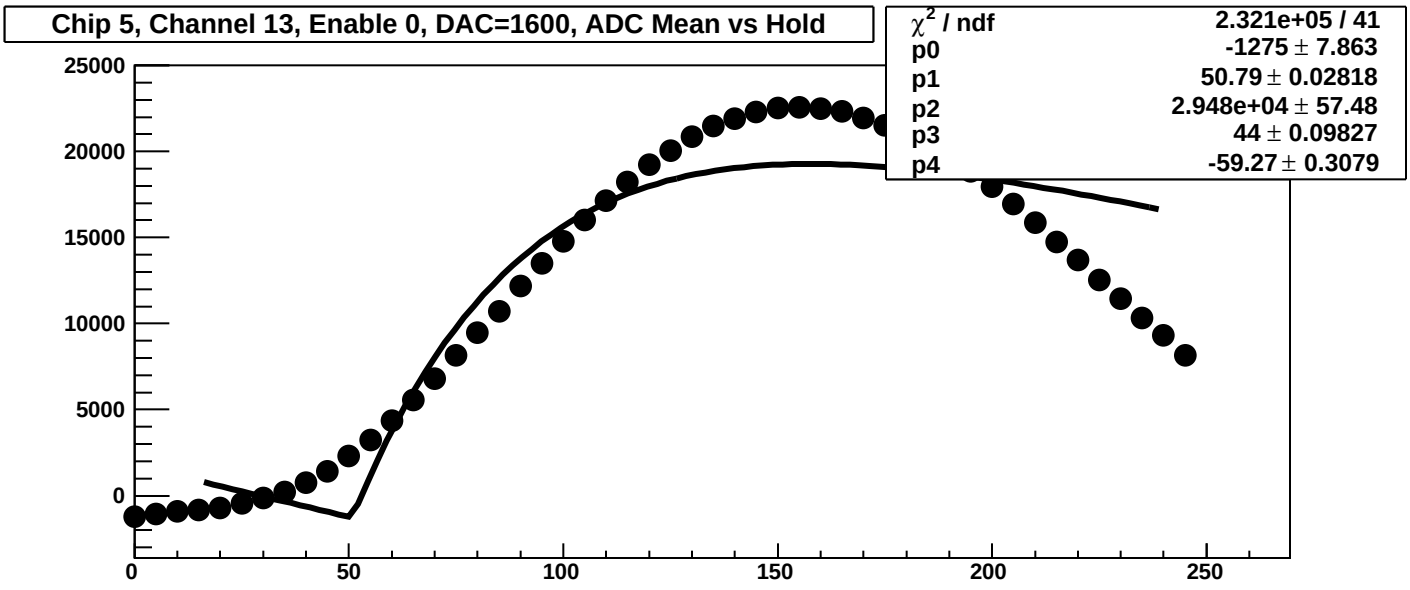


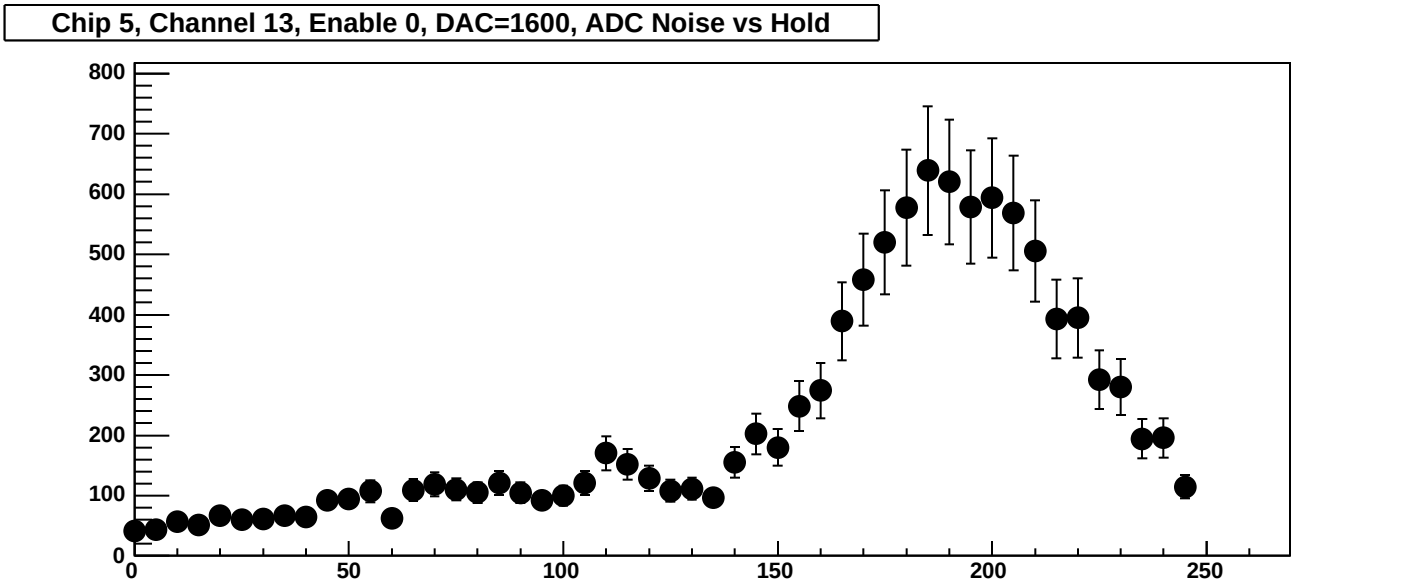
Chip 5, Channel 12, Enable 5, DAC=1600, ADC Noise vs Hold

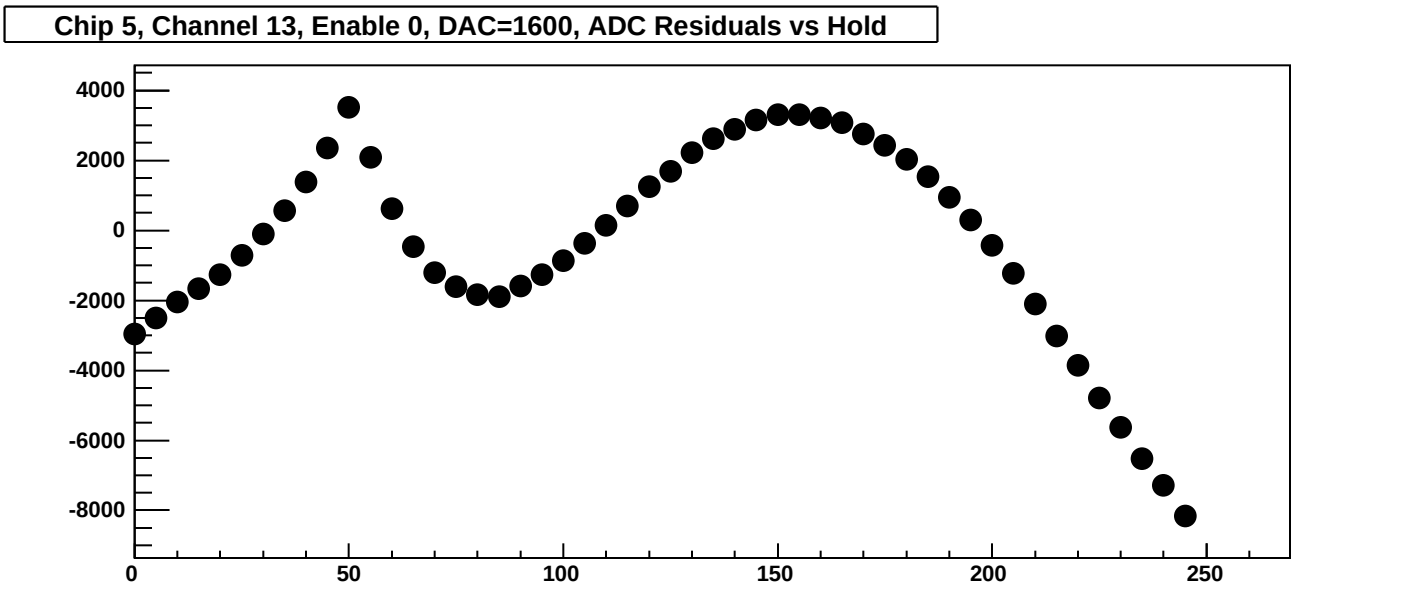


Chip 5, Channel 12, Enable 5, DAC=1600, ADC Residuals vs Hold



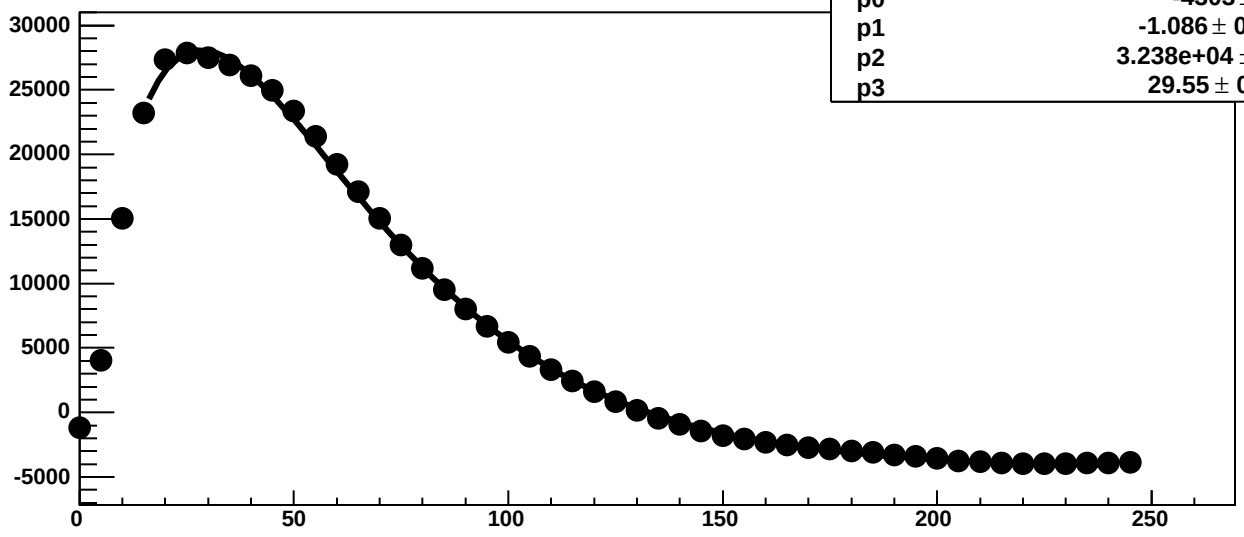

Chip 5, Channel 13, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.321e+05 / 41
p0 -1275 ± 7.863
p1 50.79 ± 0.02818
p2 2.948e+04 ± 57.48
p3 44 ± 0.09827
p4 -59.27 ± 0.3079
25000
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 5, Channel 13, Enable 0, DAC=1600, ADC Noise vs Hold
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250

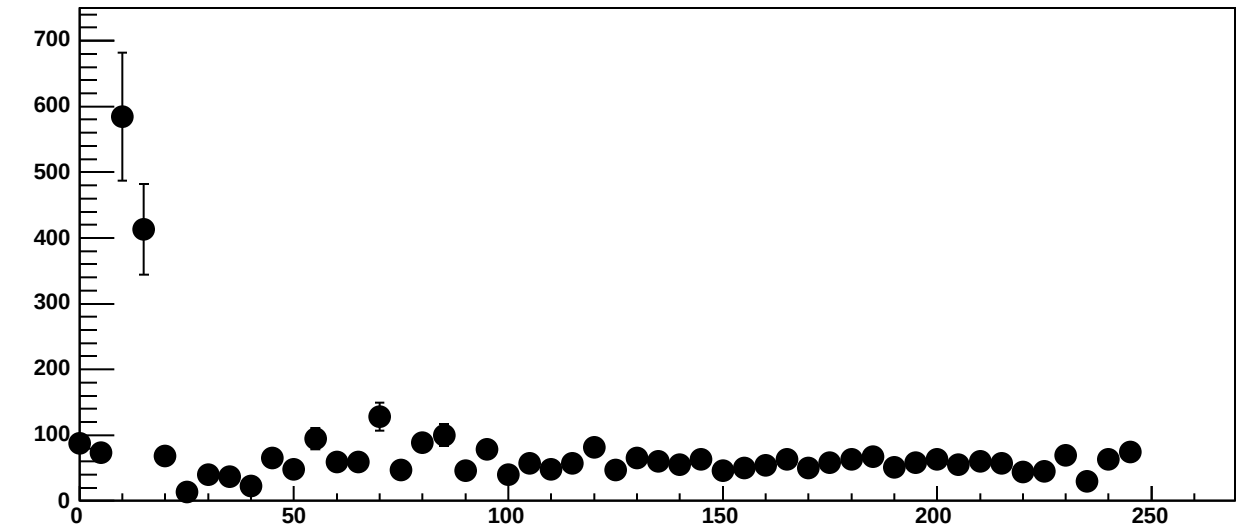

Chip 5, Channel 13, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250

Chip 5, Channel 13, Enable 1!, DAC=1600, ADC Mean vs Hold

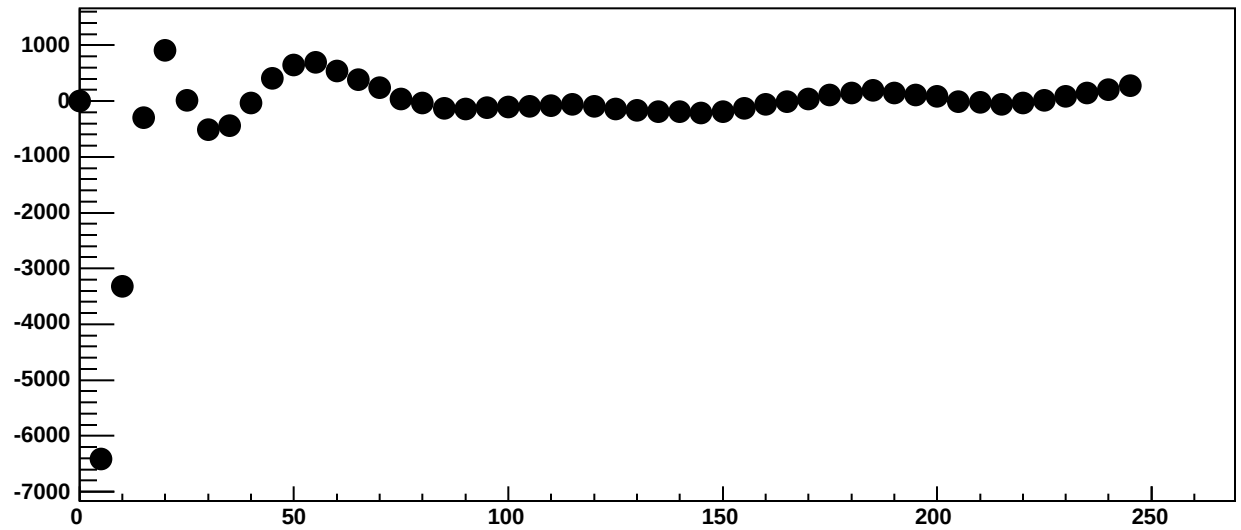
χ^2 / ndf	2.009e+04 / 42
p0	-4303 ± 3.453
p1	-1.086 ± 0.02065
p2	3.238e+04 ± 3.877
p3	29.55 ± 0.01119



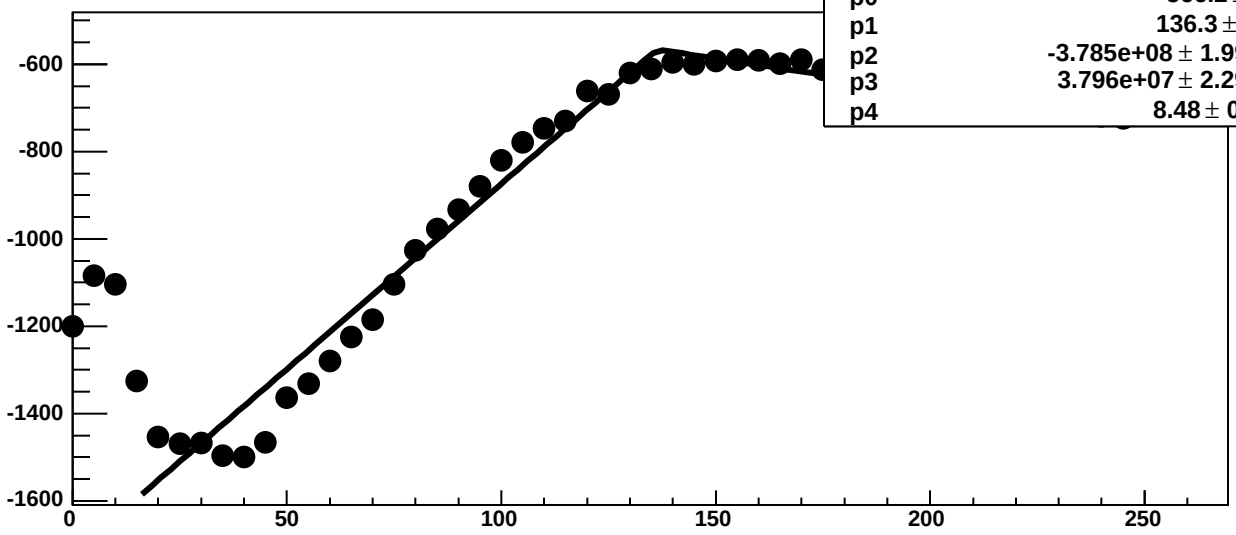
Chip 5, Channel 13, Enable 1!, DAC=1600, ADC Noise vs Hold



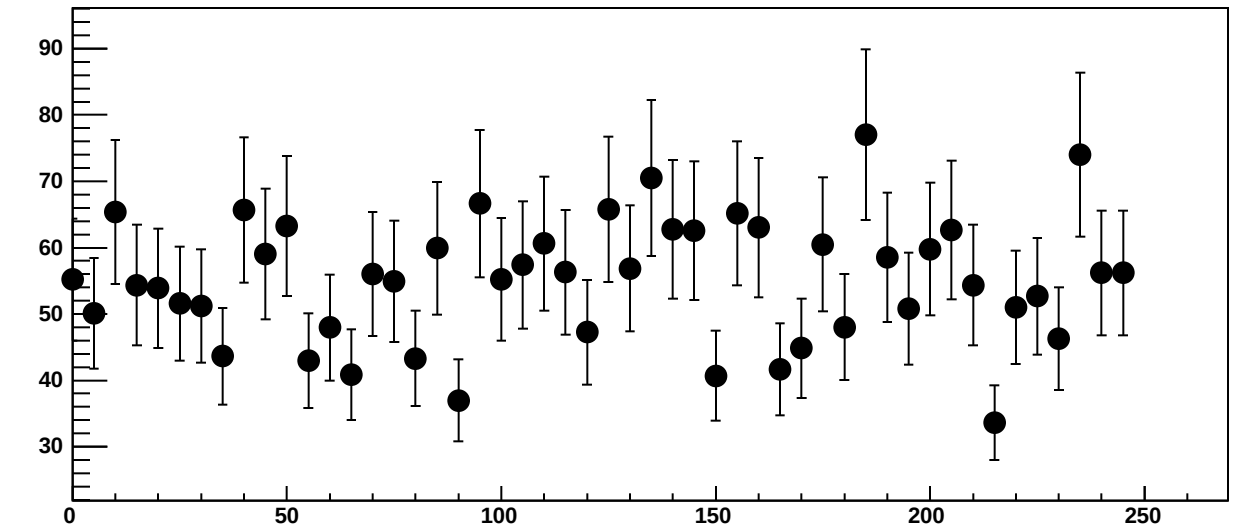
Chip 5, Channel 13, Enable 1!, DAC=1600, ADC Residuals vs Hold



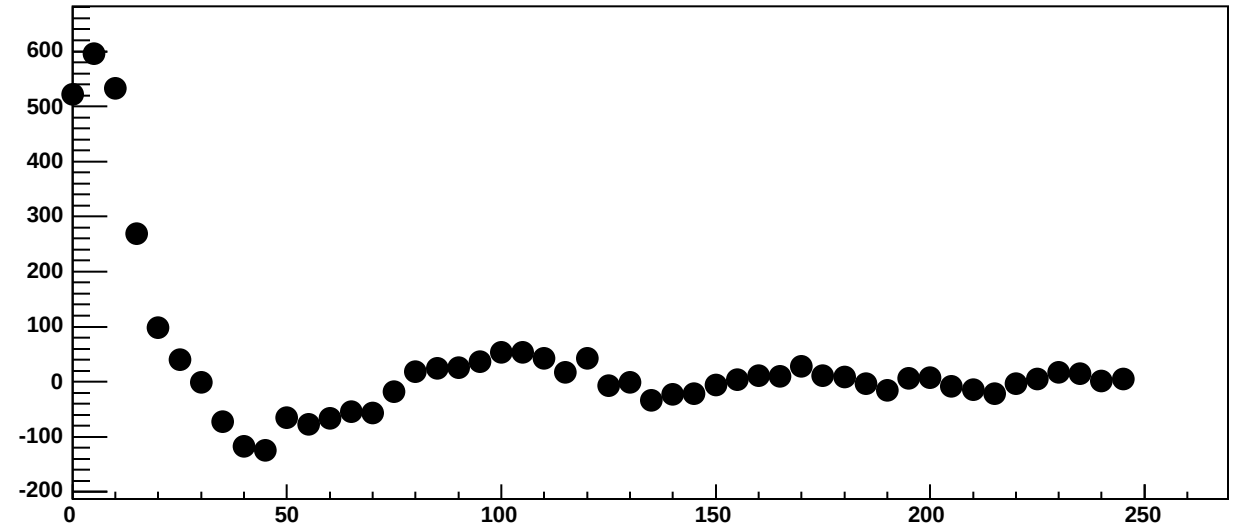
Chip 5, Channel 13, Enable 2, DAC=1600, ADC Mean vs Hold

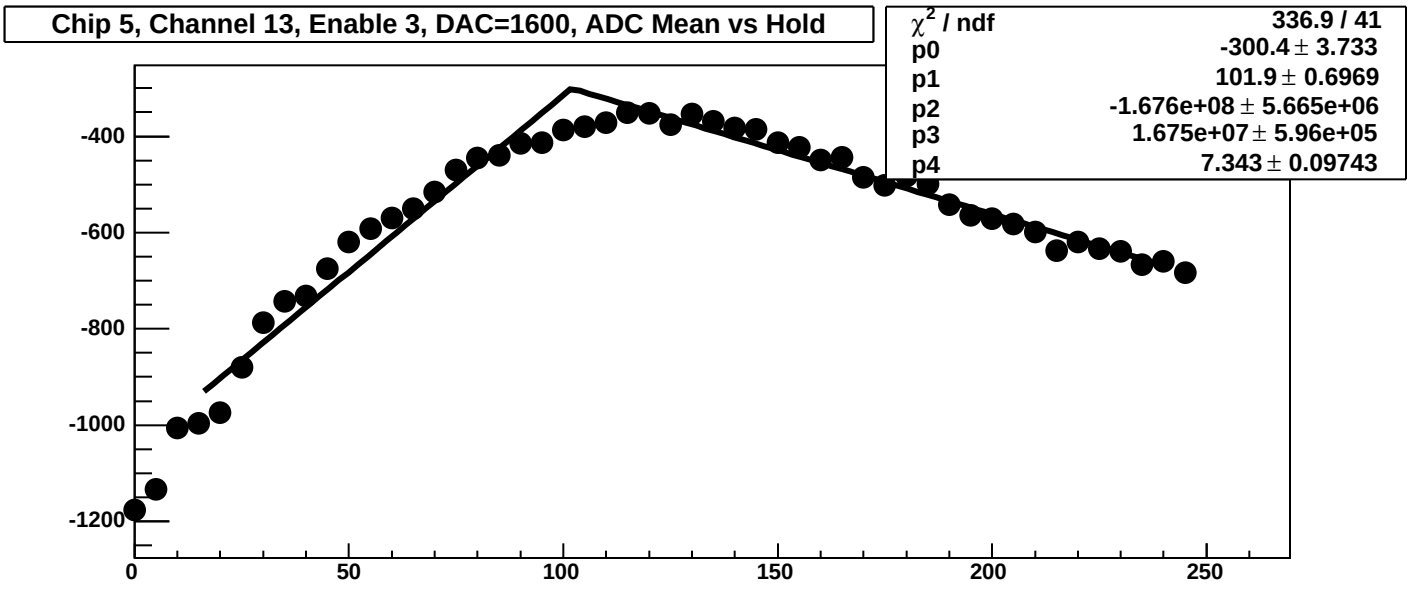


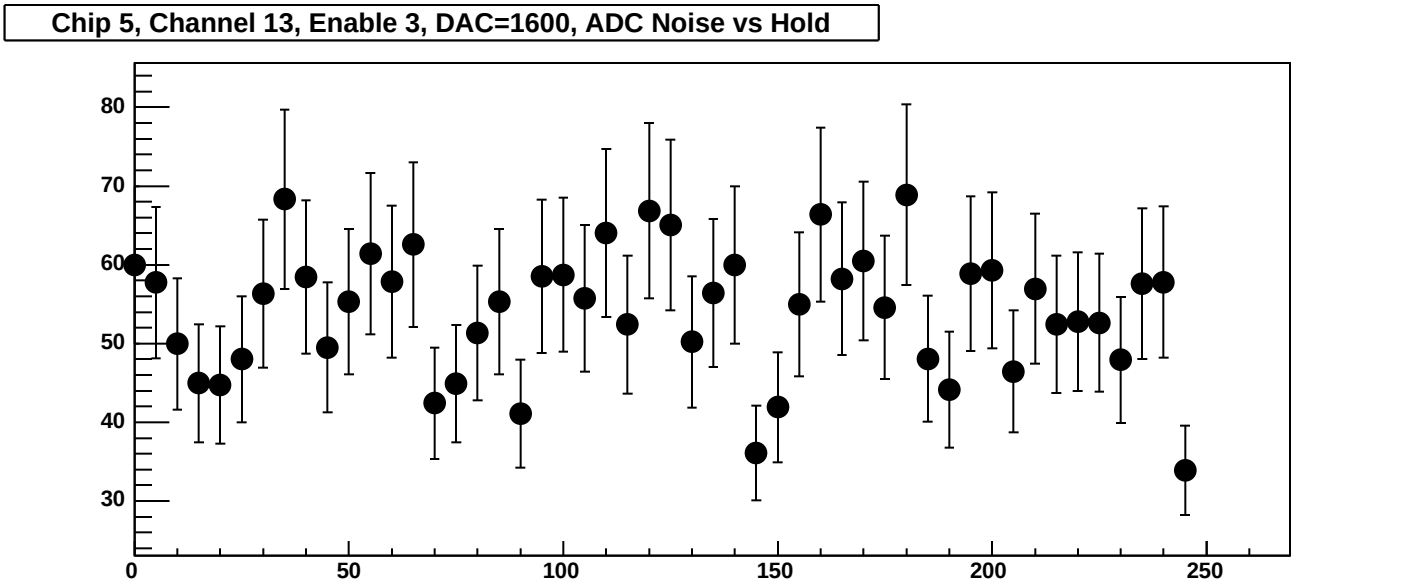
Chip 5, Channel 13, Enable 2, DAC=1600, ADC Noise vs Hold

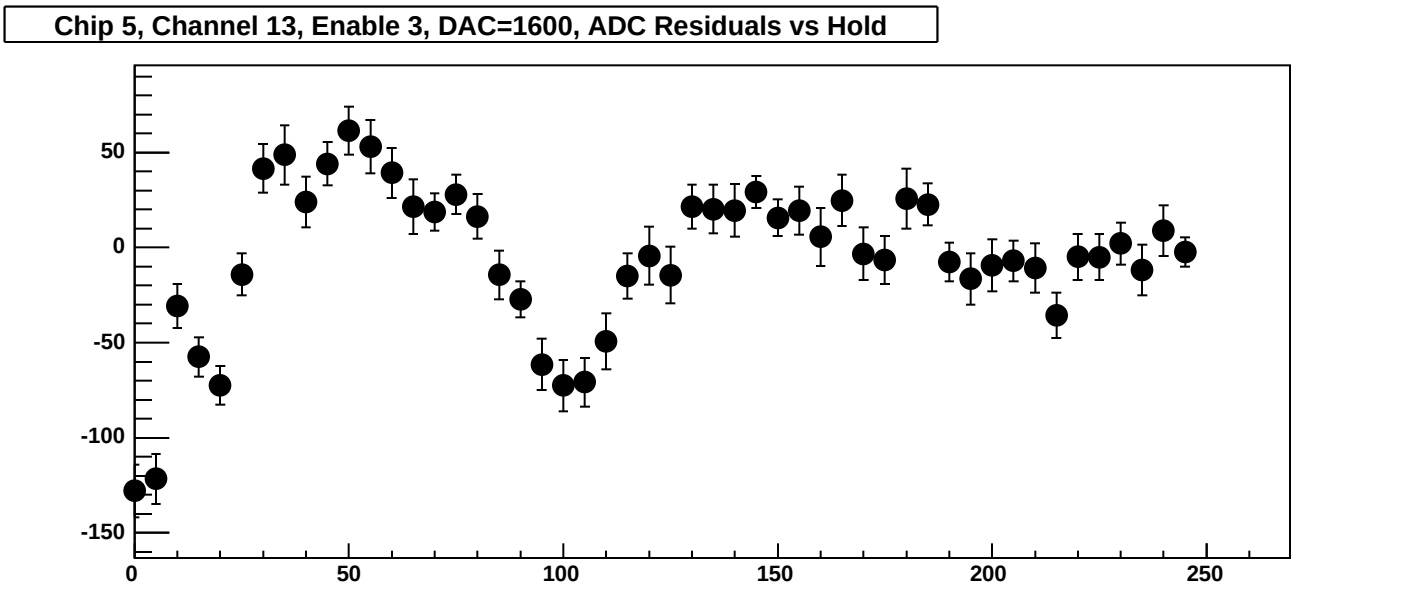


Chip 5, Channel 13, Enable 2, DAC=1600, ADC Residuals vs Hold

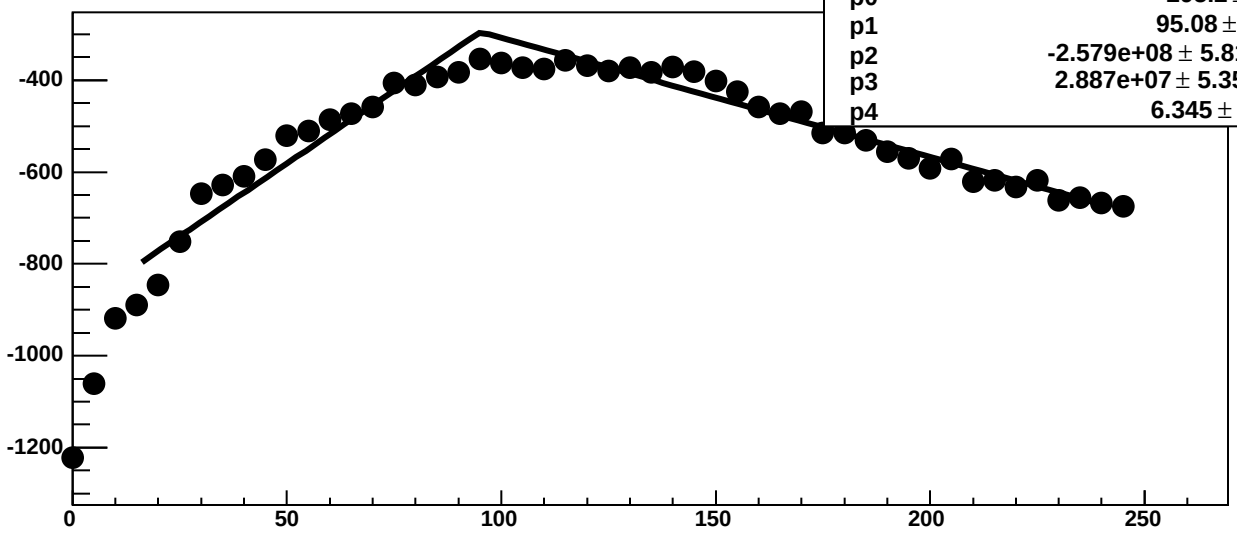



Chip 5, Channel 13, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 336.9 / 41
p0 -300.4 ± 3.733
p1 101.9 ± 0.6969
p2 -1.676e+08 ± 5.665e+06
p3 1.675e+07 ± 5.96e+05
p4 7.343 ± 0.09743
-400
-600
-800
-1000
-1200
0
50
100
150
200
250

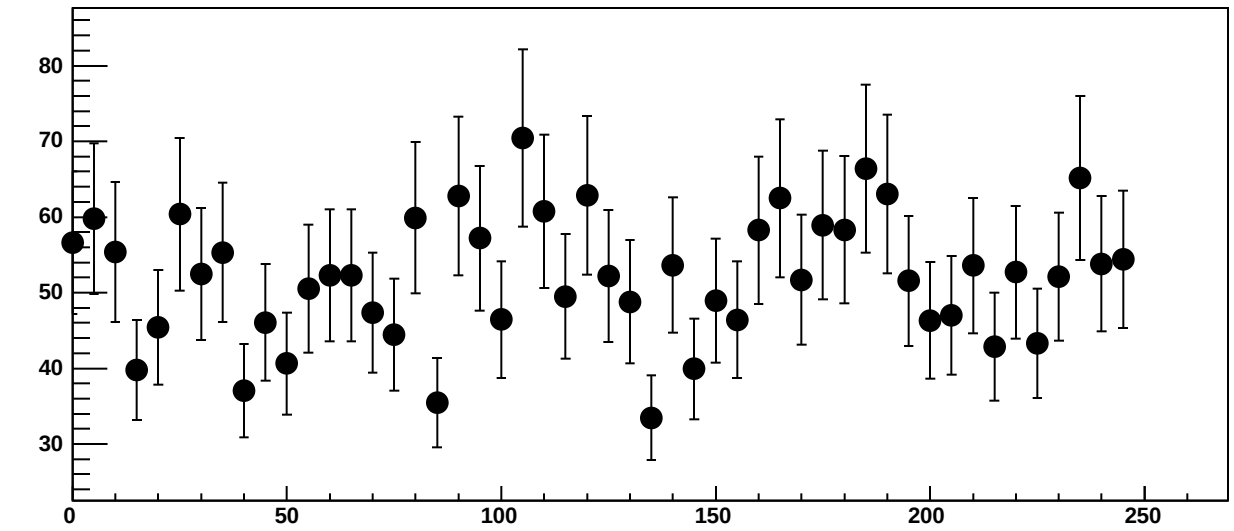

Chip 5, Channel 13, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 5, Channel 13, Enable 3, DAC=1600, ADC Residuals vs Hold
50
0
-50
-100
-150
0
50
100
150
200
250

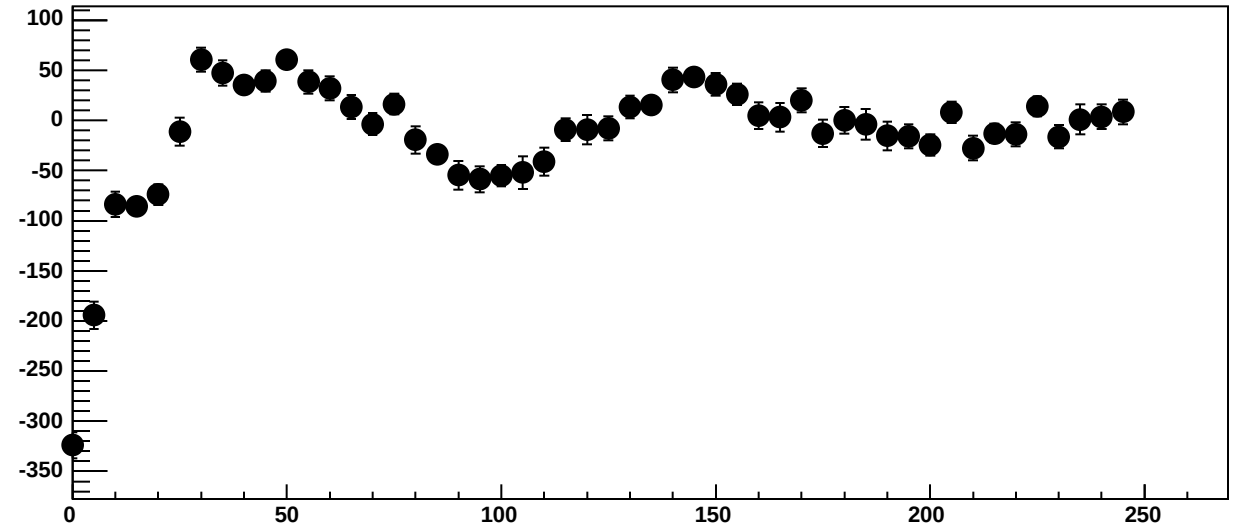
Chip 5, Channel 13, Enable 4, DAC=1600, ADC Mean vs Hold



Chip 5, Channel 13, Enable 4, DAC=1600, ADC Noise vs Hold

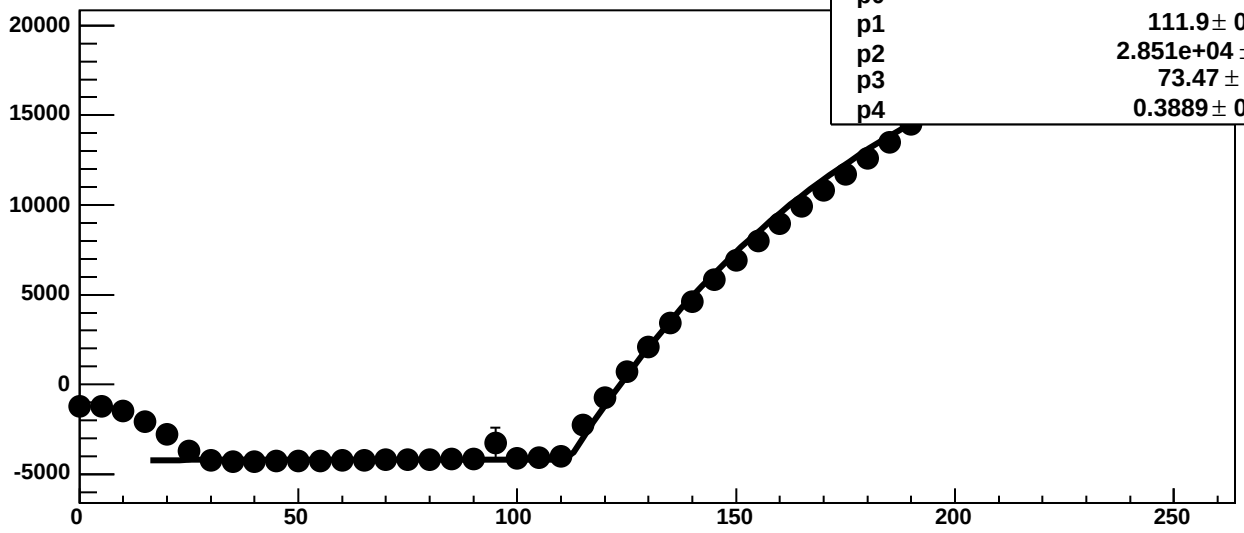


Chip 5, Channel 13, Enable 4, DAC=1600, ADC Residuals vs Hold

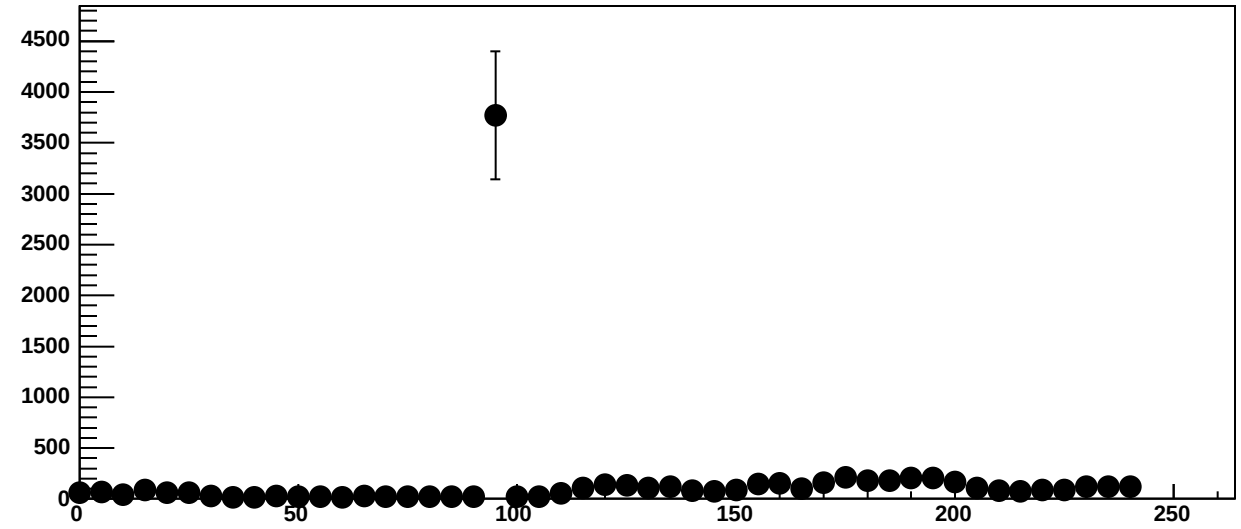


Chip 5, Channel 13, Enable 5, DAC=1600, ADC Mean vs Hold

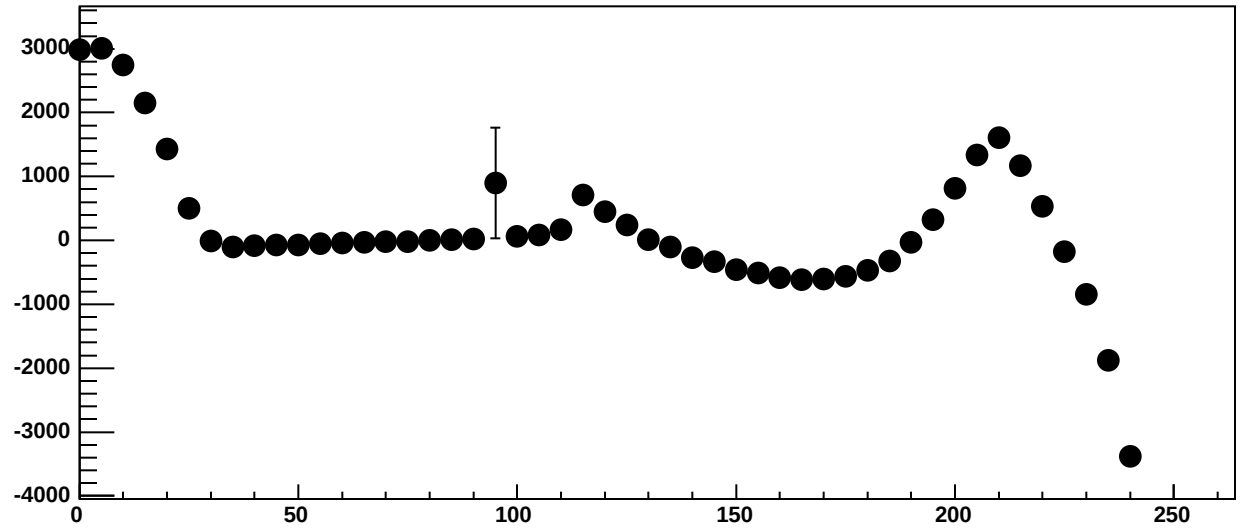
χ^2 / ndf	6.651e+04 / 41
p0	-4172 ± 2.844
p1	111.9 ± 0.05083
p2	2.851e+04 ± 46.22
p3	73.47 ± 0.2625
p4	0.3889 ± 0.05296



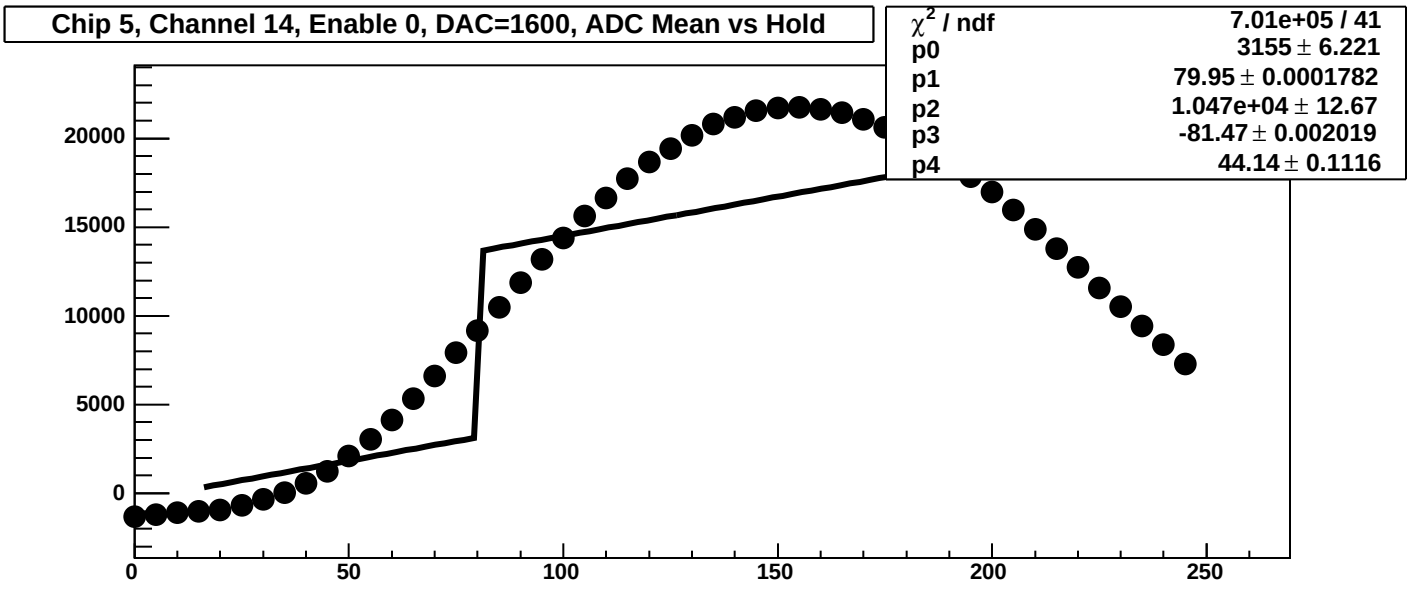
Chip 5, Channel 13, Enable 5, DAC=1600, ADC Noise vs Hold



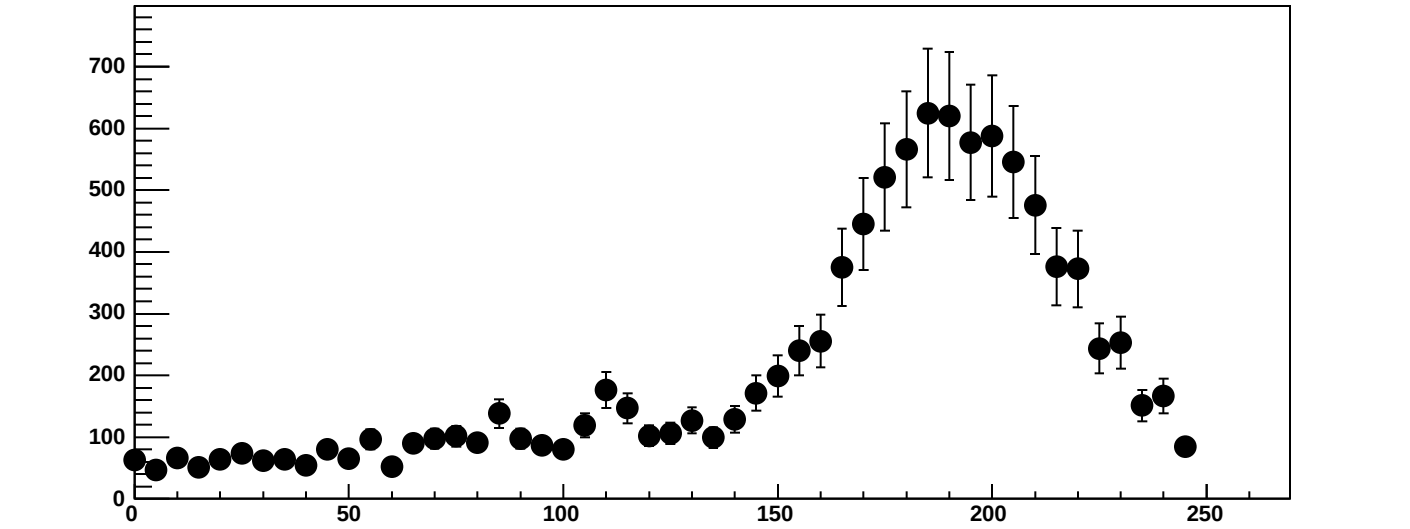
Chip 5, Channel 13, Enable 5, DAC=1600, ADC Residuals vs Hold



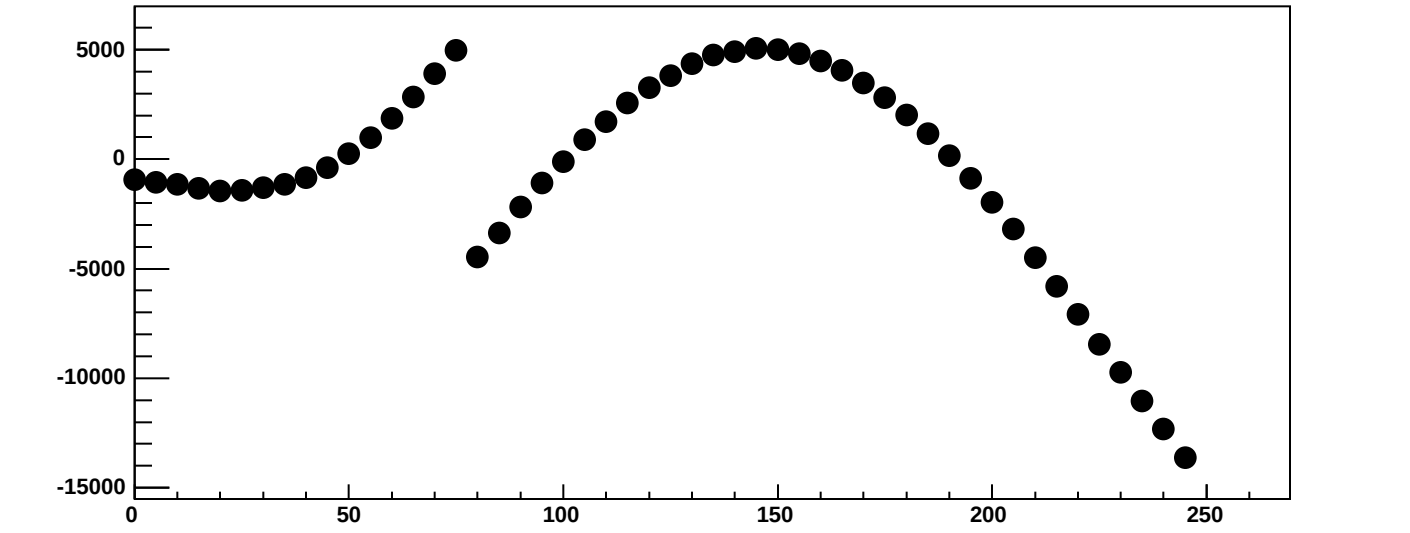
Chip 5, Channel 14, Enable 0, DAC=1600, ADC Mean vs Hold



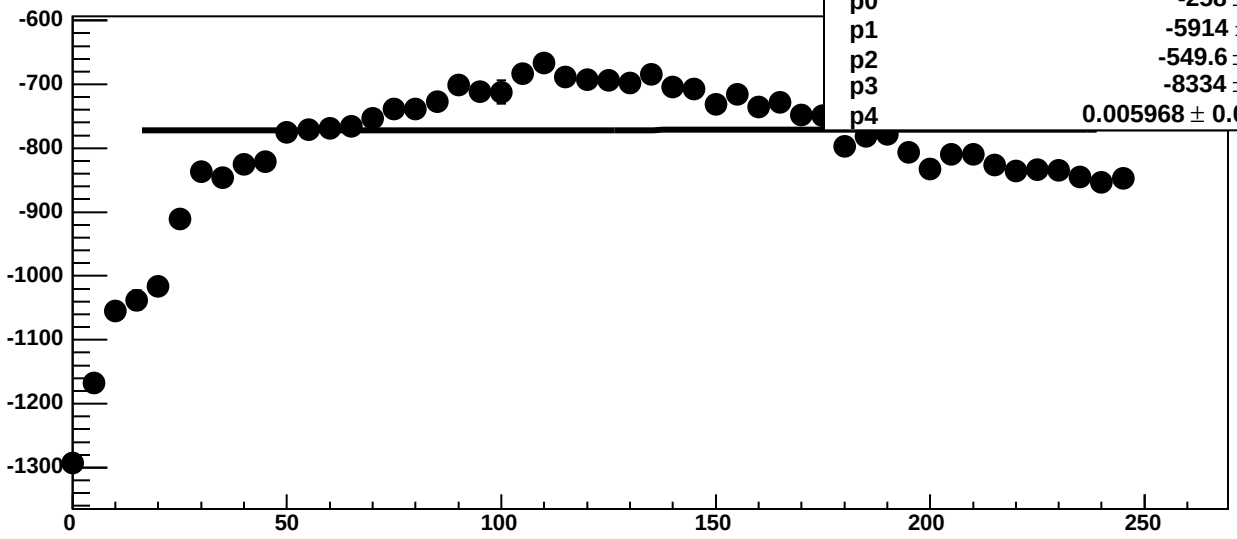
Chip 5, Channel 14, Enable 0, DAC=1600, ADC Noise vs Hold



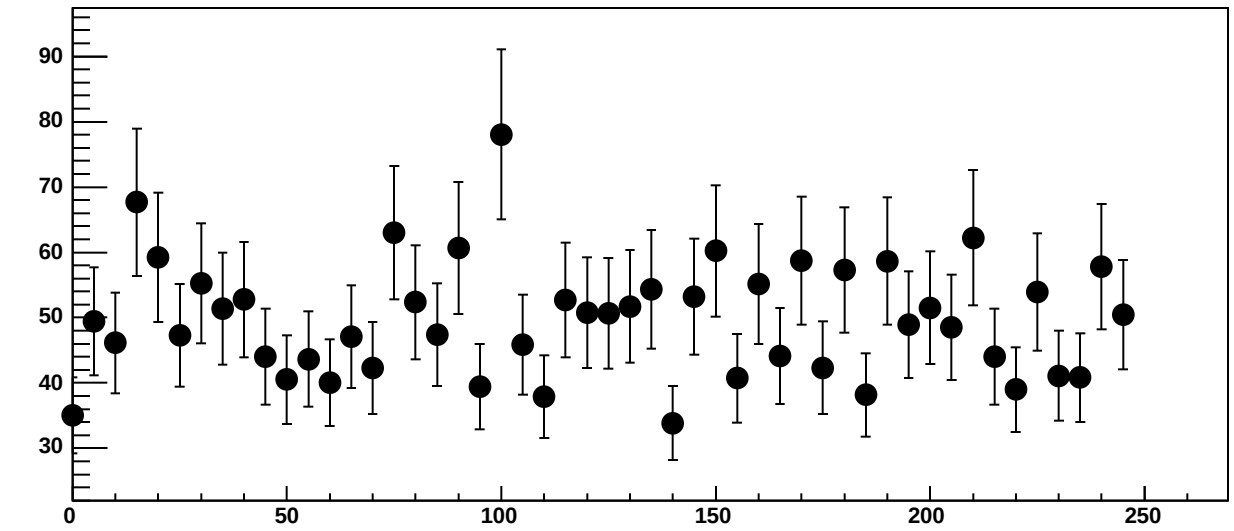
Chip 5, Channel 14, Enable 0, DAC=1600, ADC Residuals vs Hold



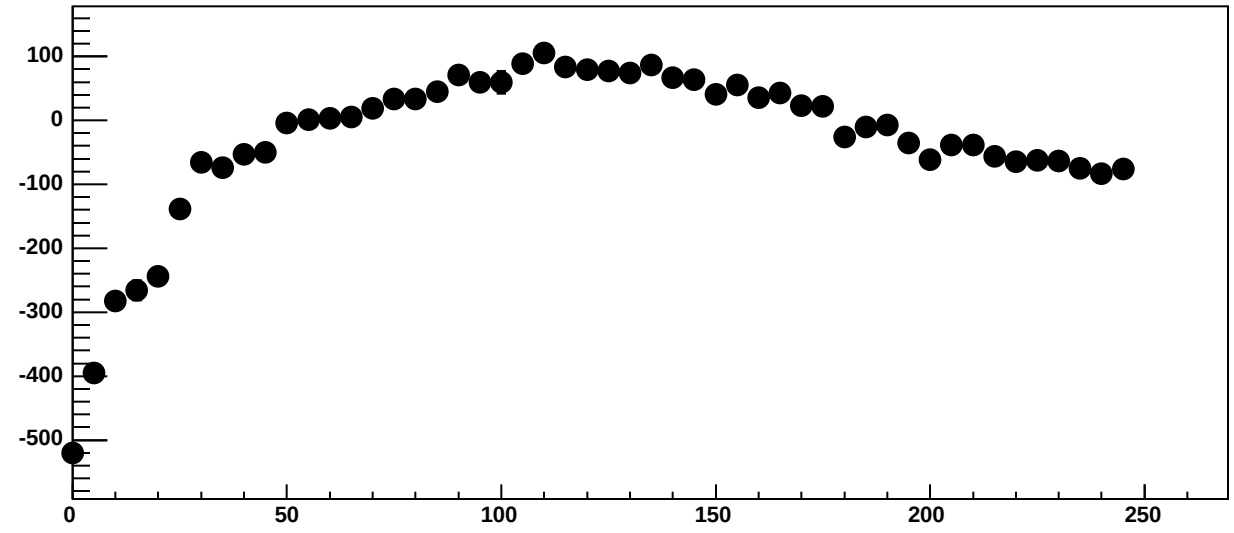
Chip 5, Channel 14, Enable 1, DAC=1600, ADC Mean vs Hold



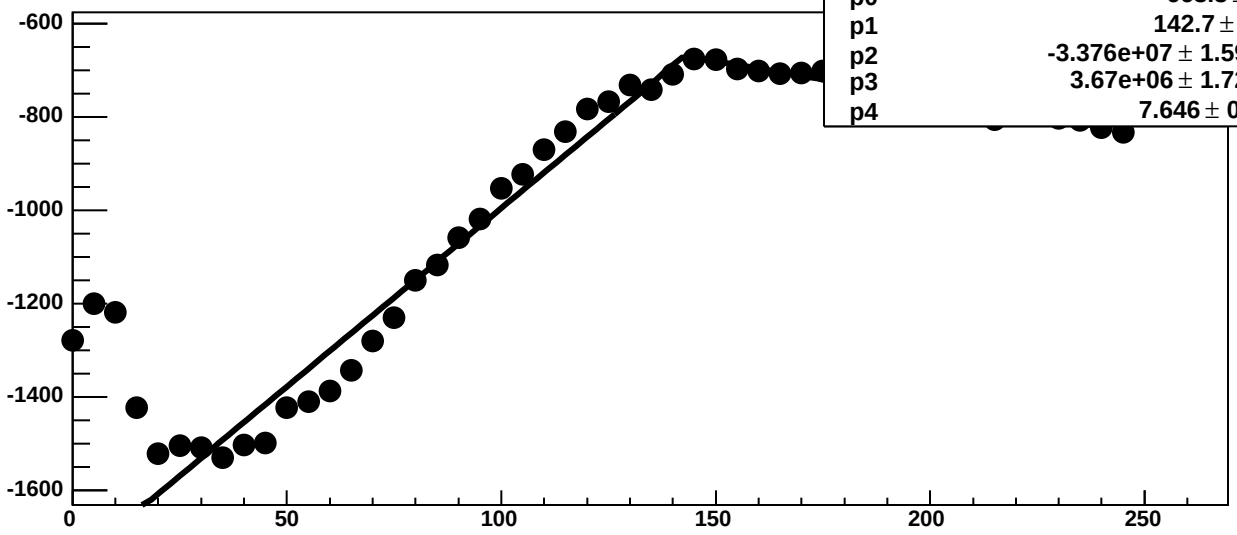
Chip 5, Channel 14, Enable 1, DAC=1600, ADC Noise vs Hold



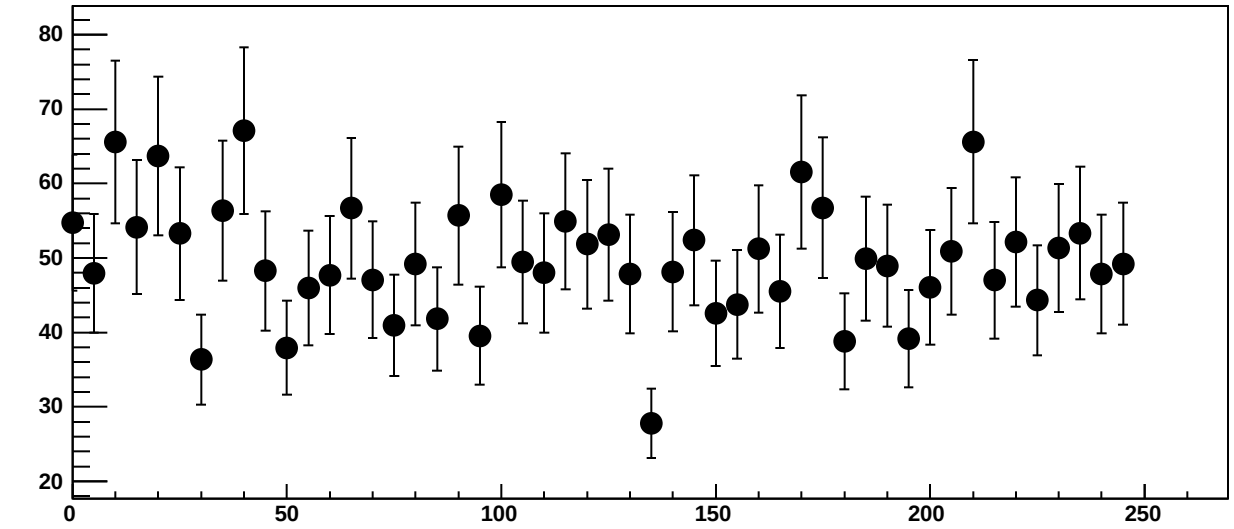
Chip 5, Channel 14, Enable 1, DAC=1600, ADC Residuals vs Hold



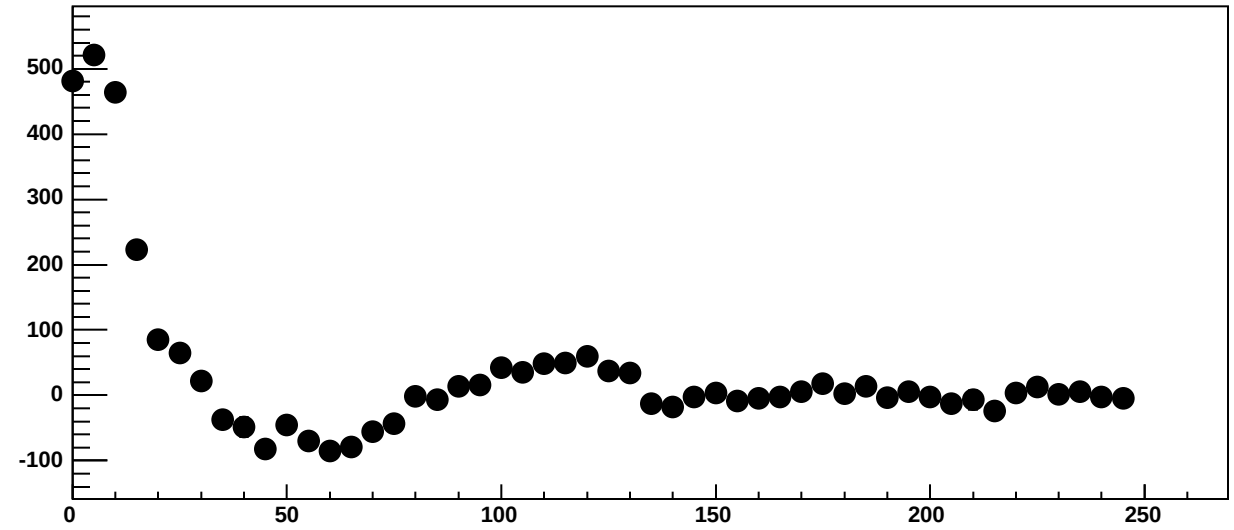
Chip 5, Channel 14, Enable 2, DAC=1600, ADC Mean vs Hold



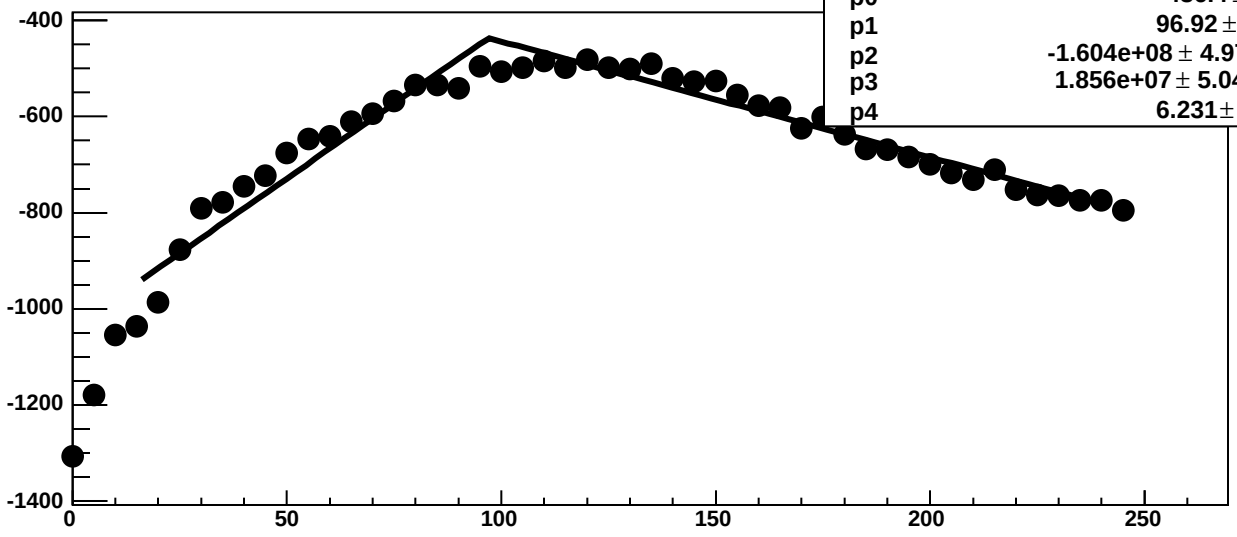
Chip 5, Channel 14, Enable 2, DAC=1600, ADC Noise vs Hold



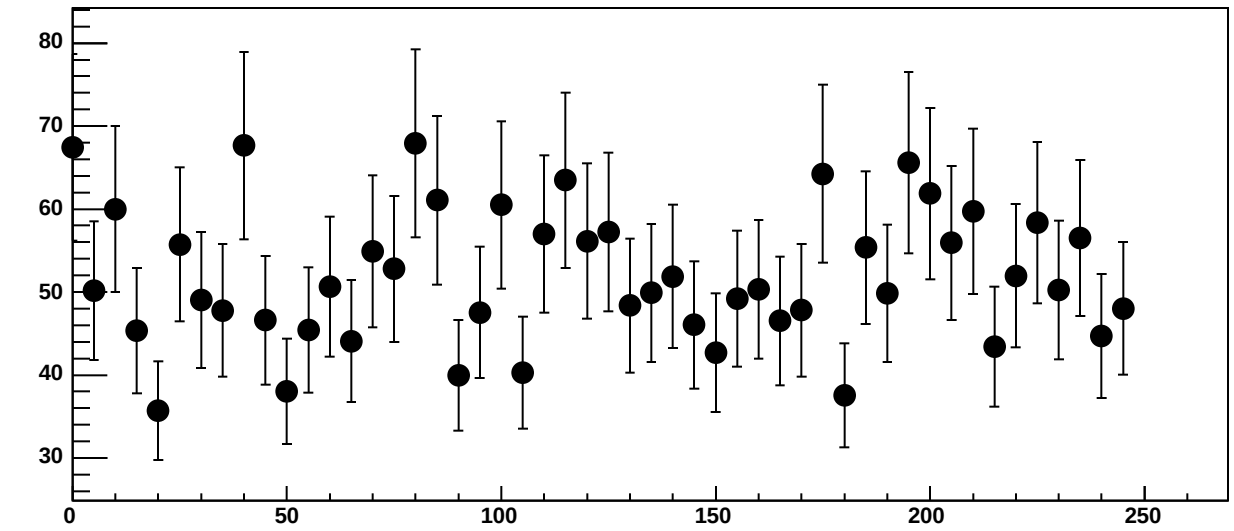
Chip 5, Channel 14, Enable 2, DAC=1600, ADC Residuals vs Hold



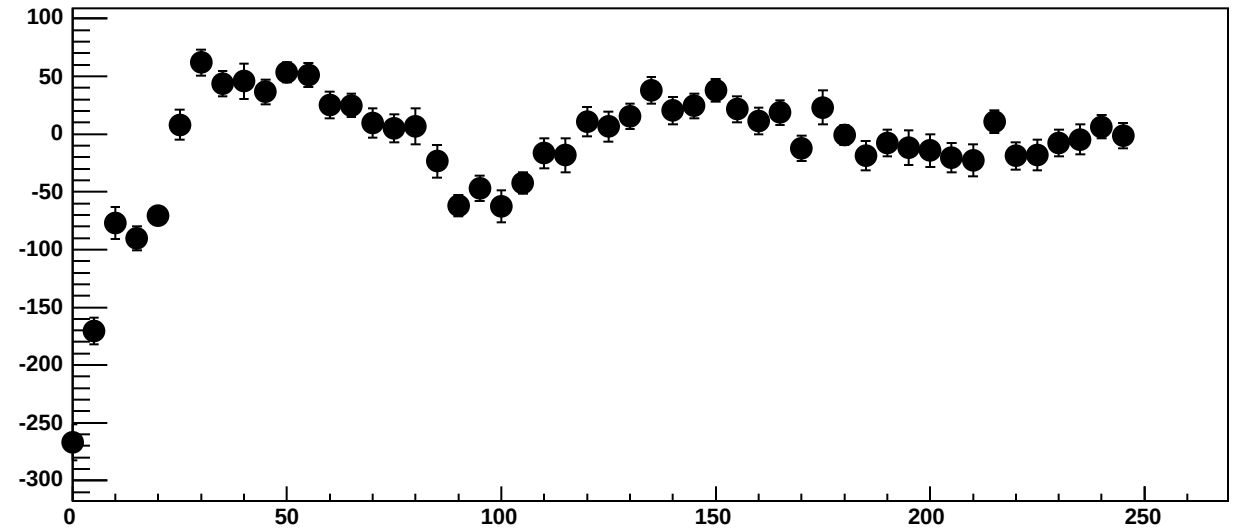
Chip 5, Channel 14, Enable 3, DAC=1600, ADC Mean vs Hold



Chip 5, Channel 14, Enable 3, DAC=1600, ADC Noise vs Hold

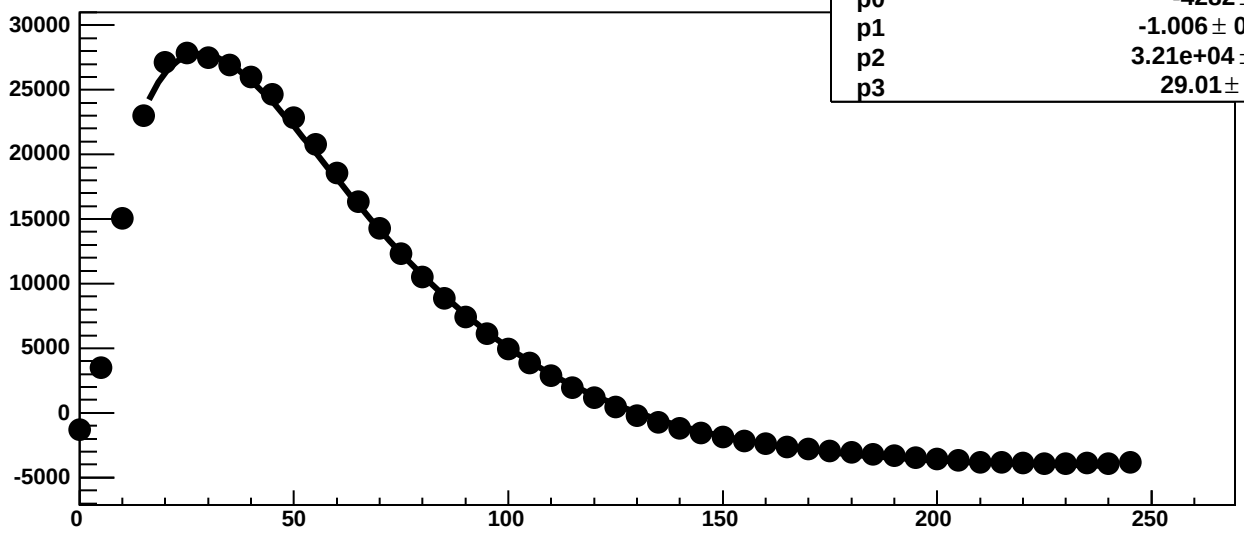


Chip 5, Channel 14, Enable 3, DAC=1600, ADC Residuals vs Hold

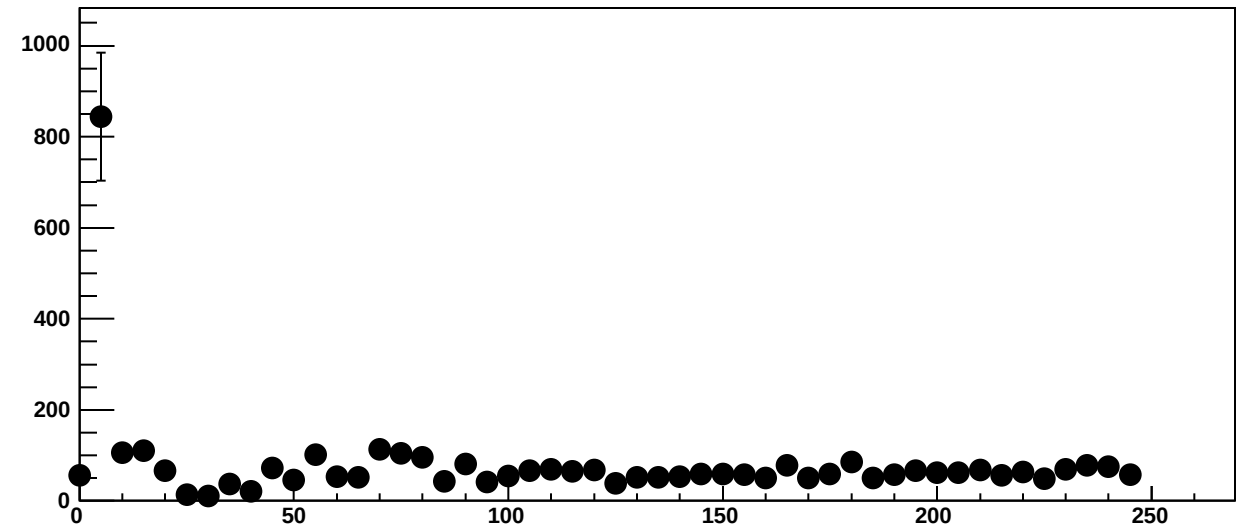


Chip 5, Channel 14, Enable 4!, DAC=1600, ADC Mean vs Hold

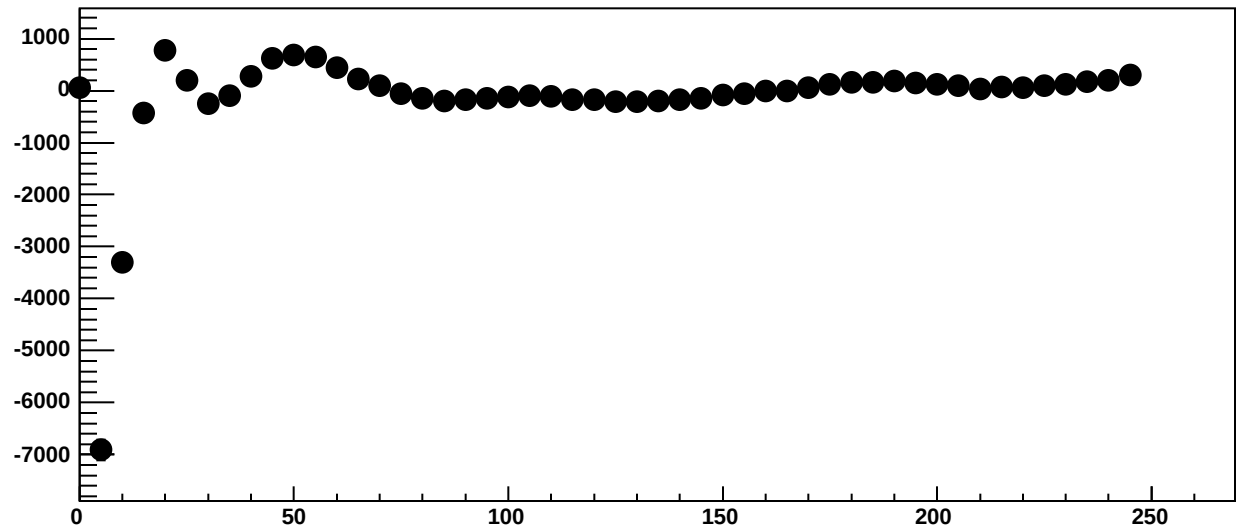
χ^2 / ndf	3.198e+04 / 42
p0	-4282 ± 3.895
p1	-1.006 ± 0.01873
p2	3.21e+04 ± 4.163
p3	29.01 ± 0.0106

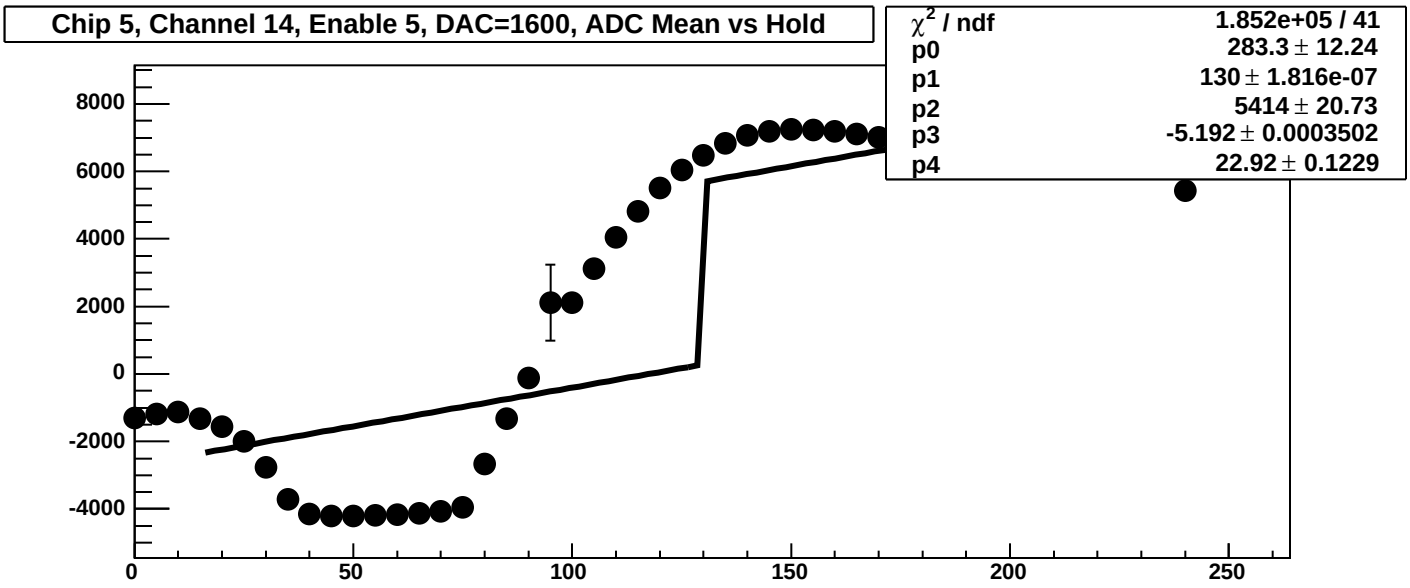


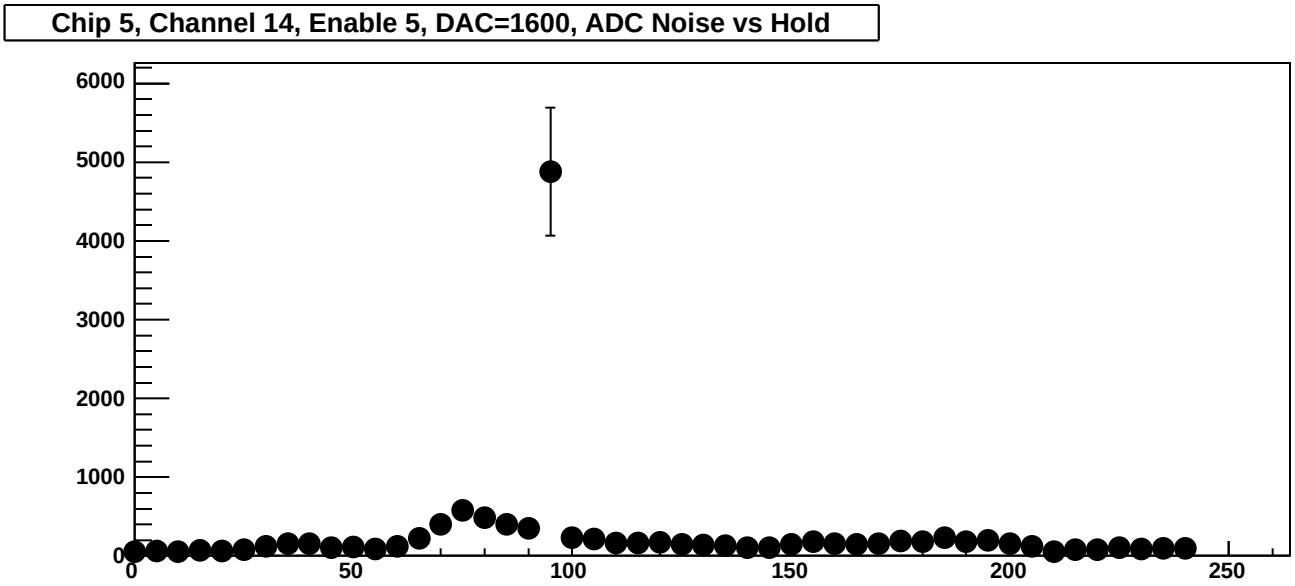
Chip 5, Channel 14, Enable 4!, DAC=1600, ADC Noise vs Hold

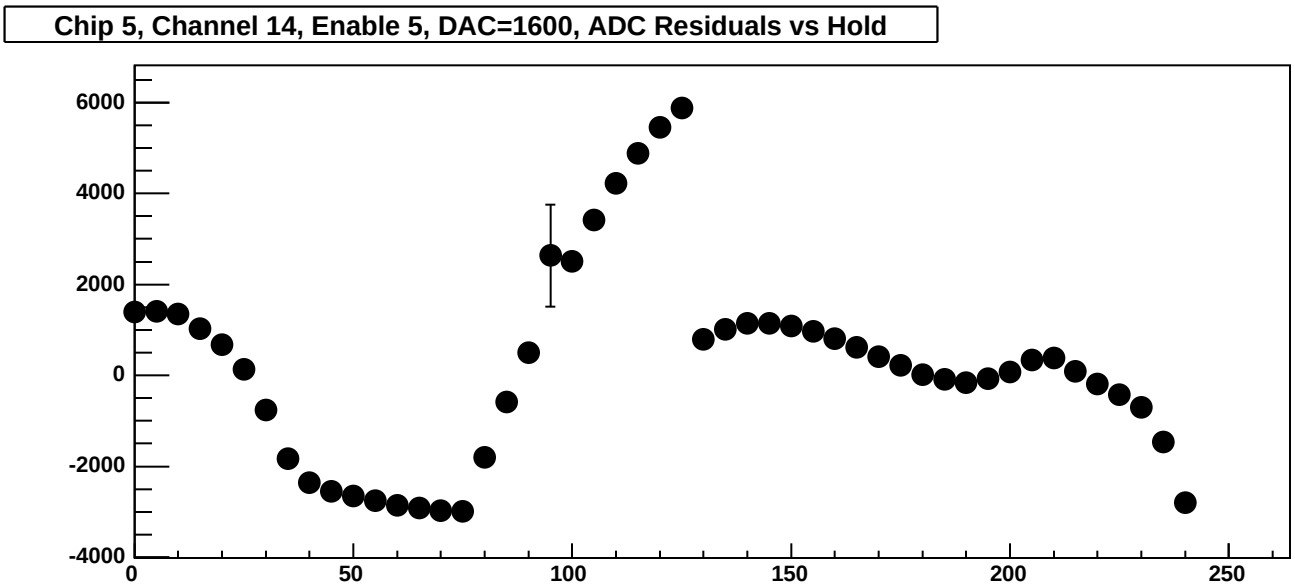


Chip 5, Channel 14, Enable 4!, DAC=1600, ADC Residuals vs Hold

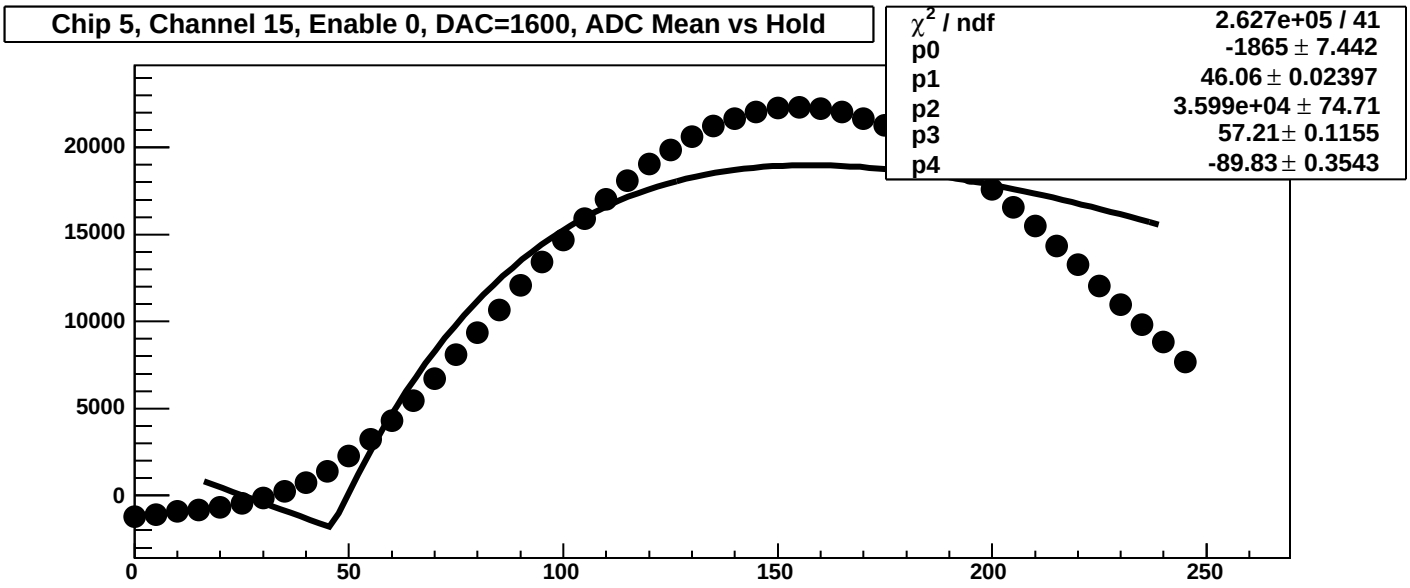



Chip 5, Channel 14, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.852e+05 / 41
p0 283.3 ± 12.24
p1 130 ± 1.816e-07
p2 5414 ± 20.73
p3 -5.192 ± 0.0003502
p4 22.92 ± 0.1229
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

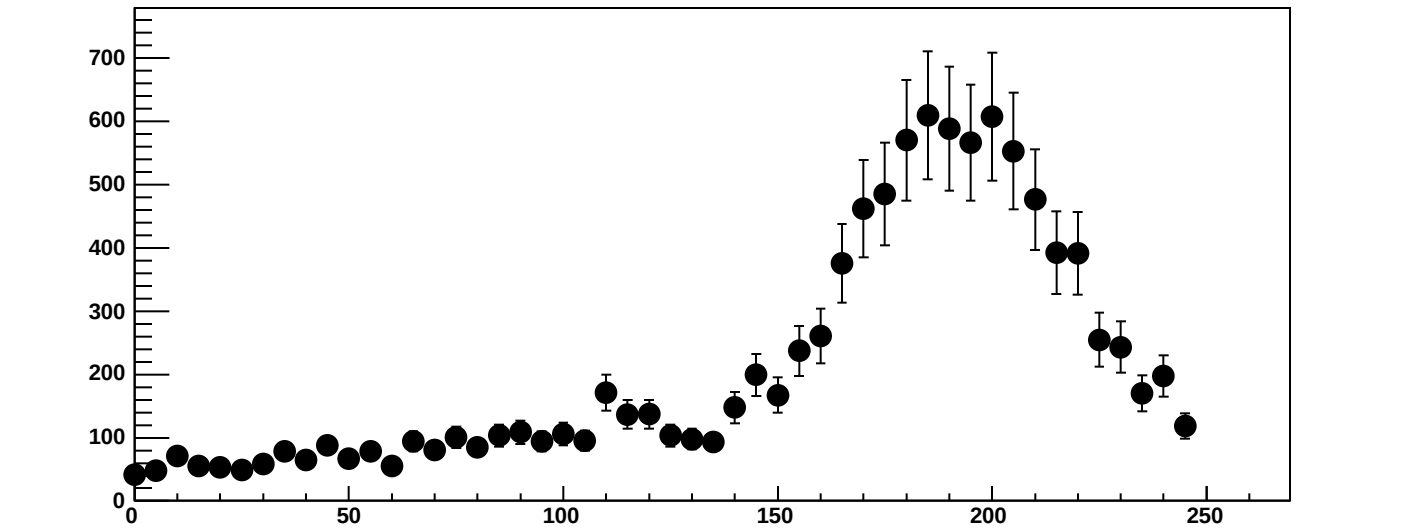

Chip 5, Channel 14, Enable 5, DAC=1600, ADC Noise vs Hold
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 5, Channel 14, Enable 5, DAC=1600, ADC Residuals vs Hold
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

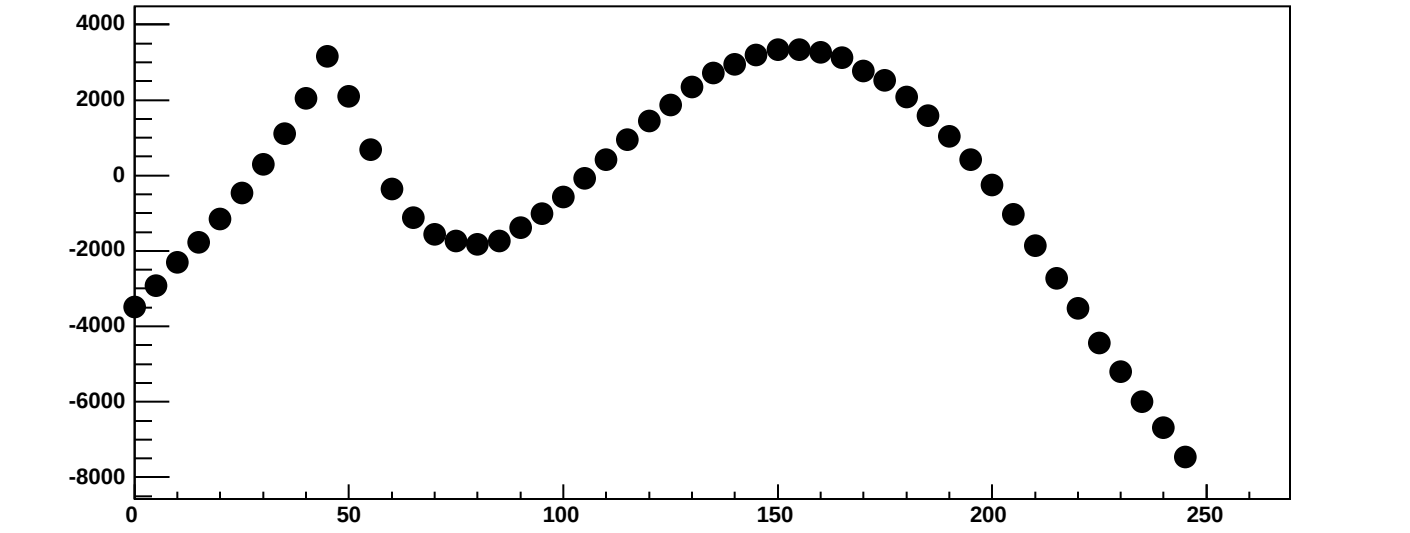
Chip 5, Channel 15, Enable 0, DAC=1600, ADC Mean vs Hold

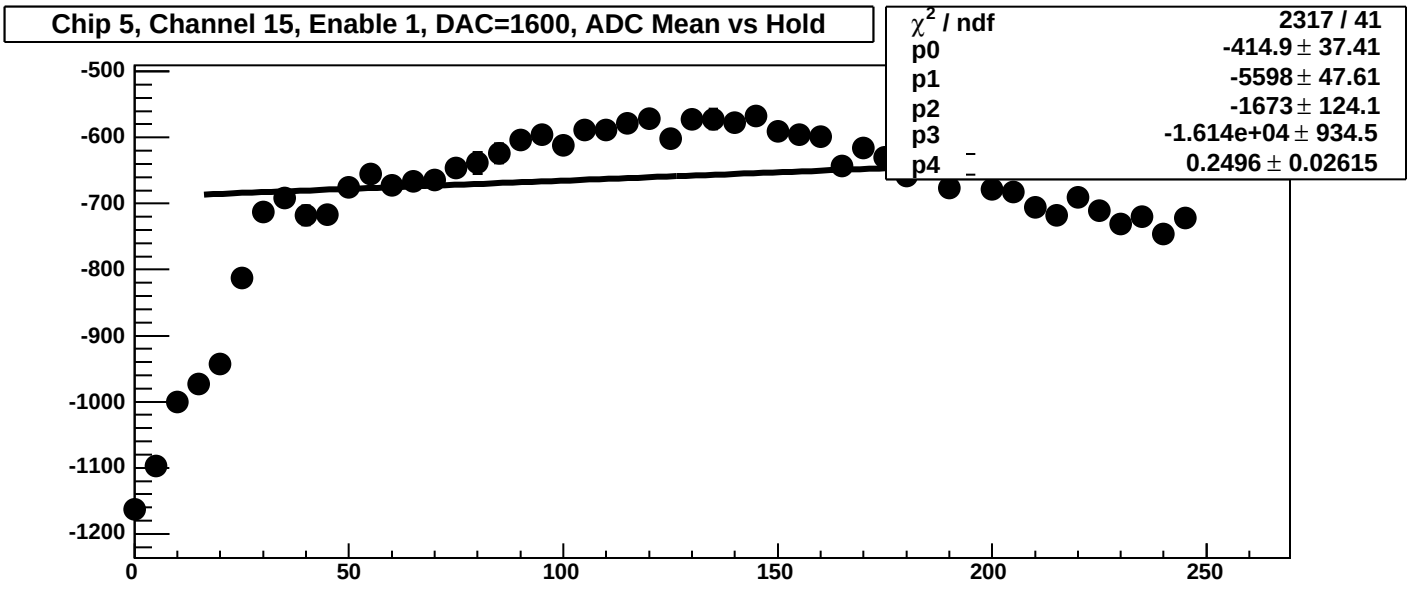


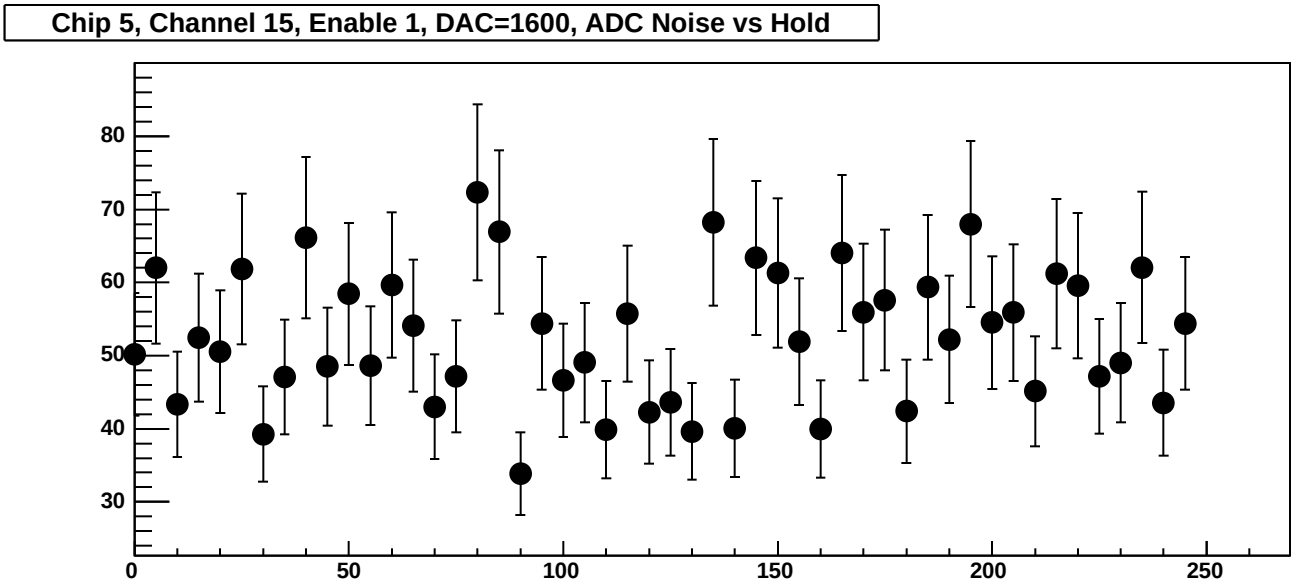
Chip 5, Channel 15, Enable 0, DAC=1600, ADC Noise vs Hold

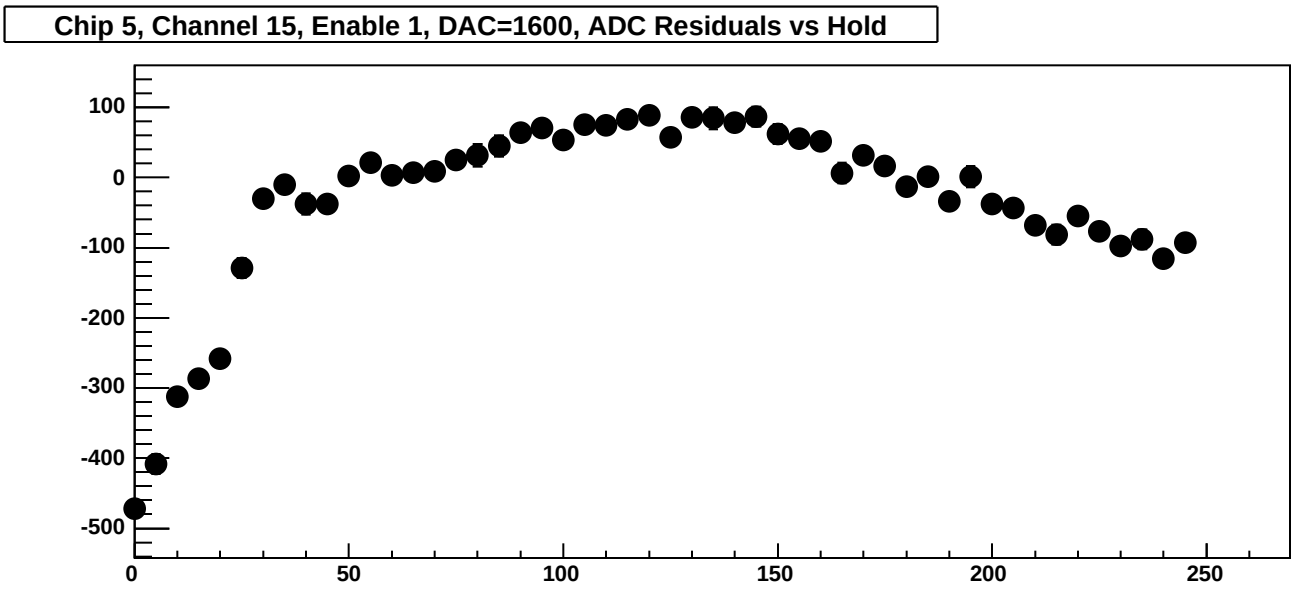


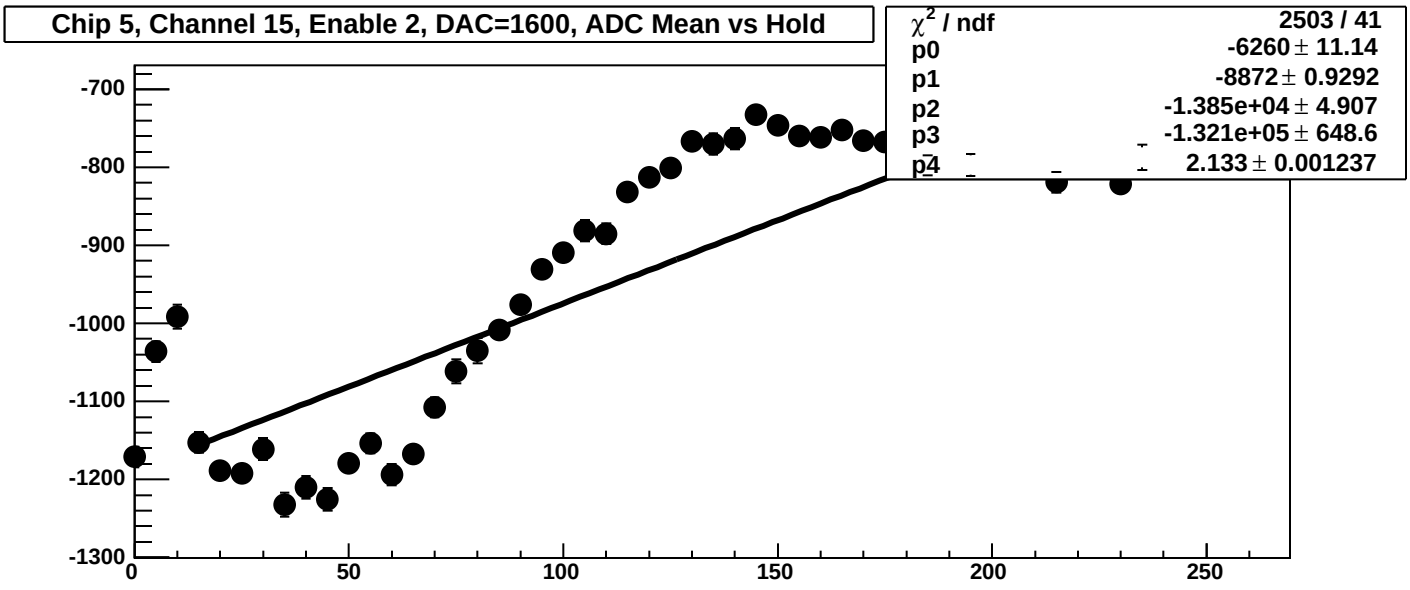
Chip 5, Channel 15, Enable 0, DAC=1600, ADC Residuals vs Hold

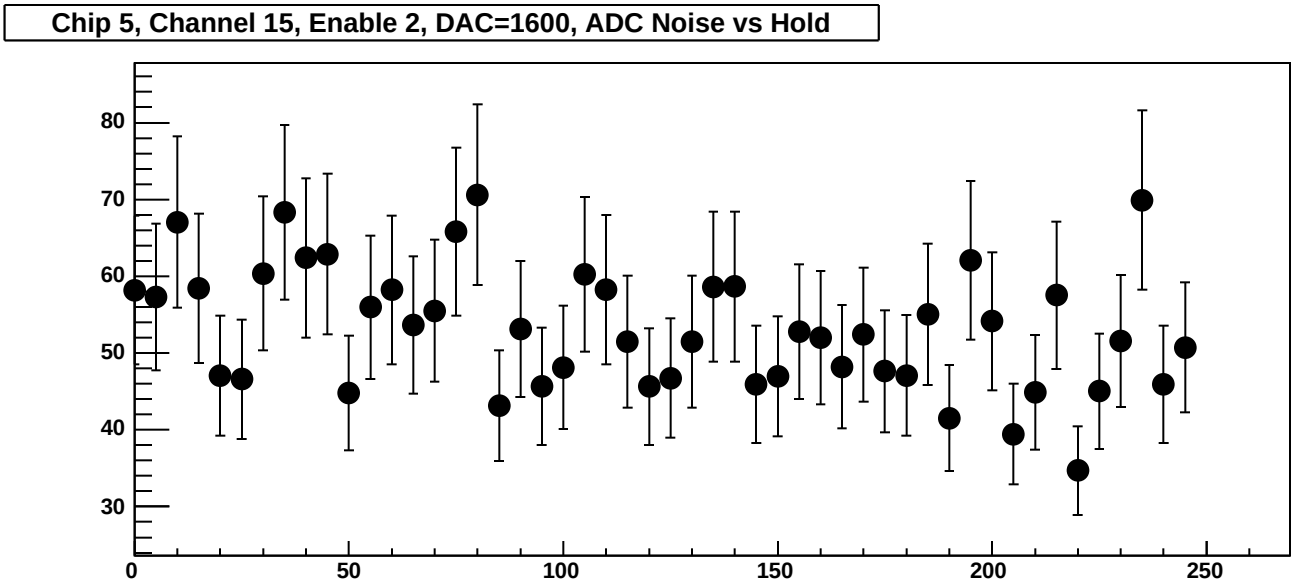


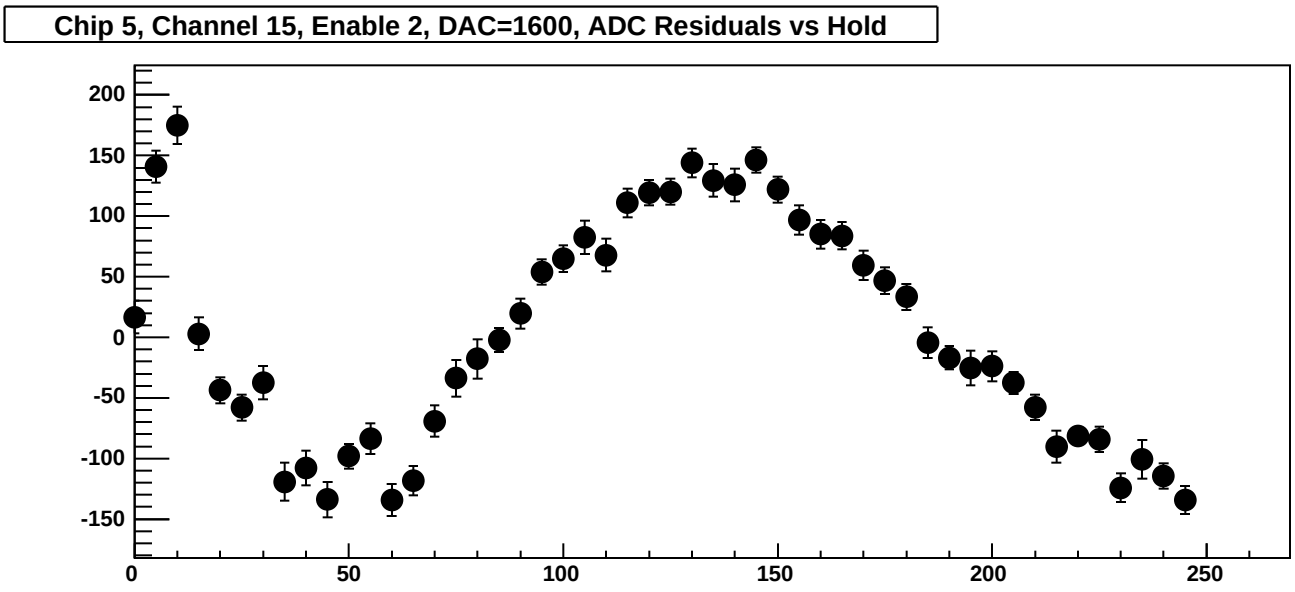

Chip 5, Channel 15, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 2317 / 41
p0 -414.9 ± 37.41
p1 -5598 ± 47.61
p2 -1673 ± 124.1
p3 -1.614e+04 ± 934.5
p4 0.2496 ± 0.02615
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
50
100
150
200
250

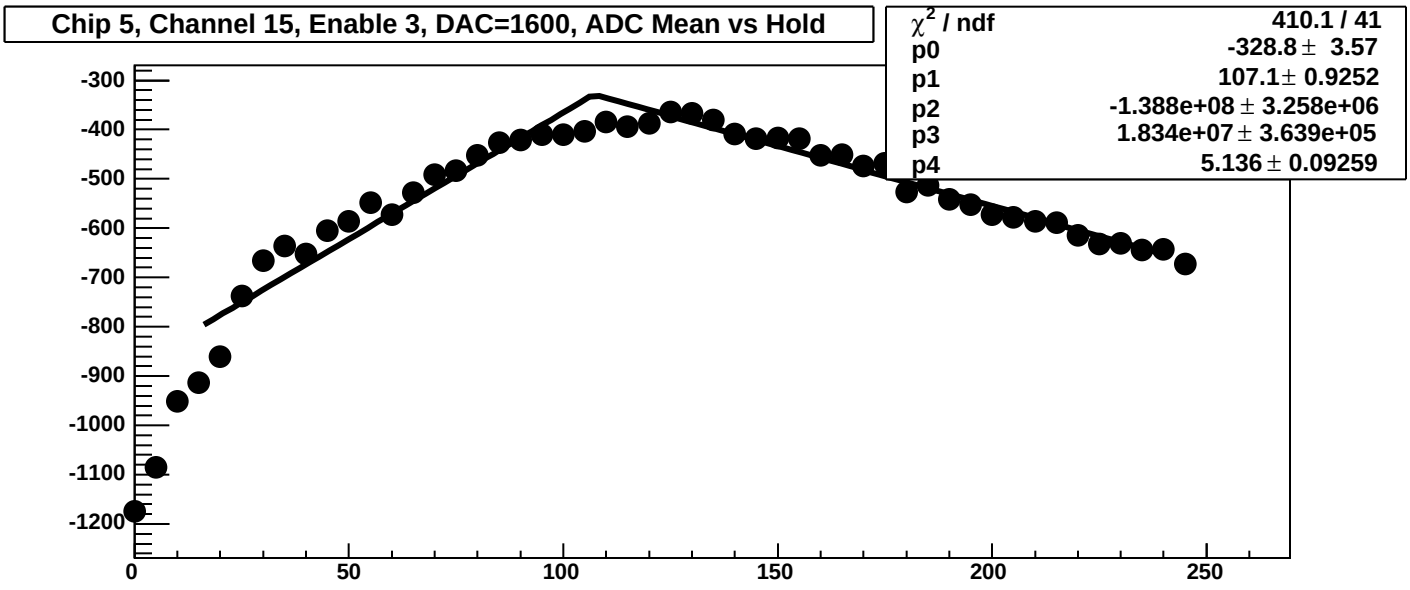

Chip 5, Channel 15, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

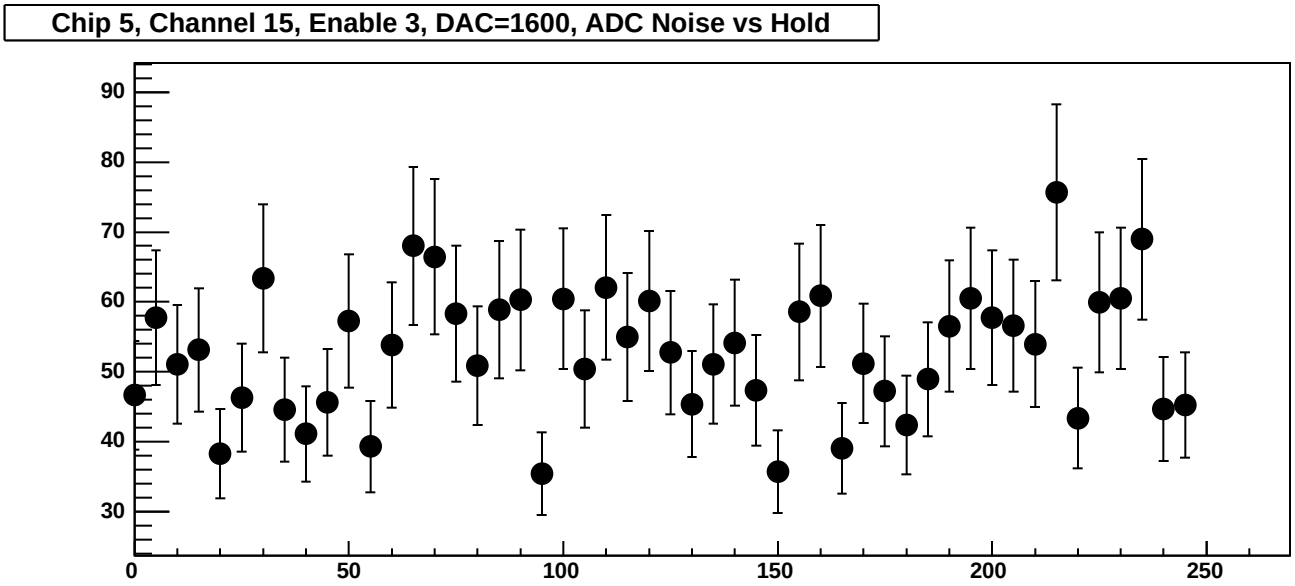

Chip 5, Channel 15, Enable 1, DAC=1600, ADC Residuals vs Hold
100
0
-100
-200
-300
-400
-500
0
50
100
150
200
250

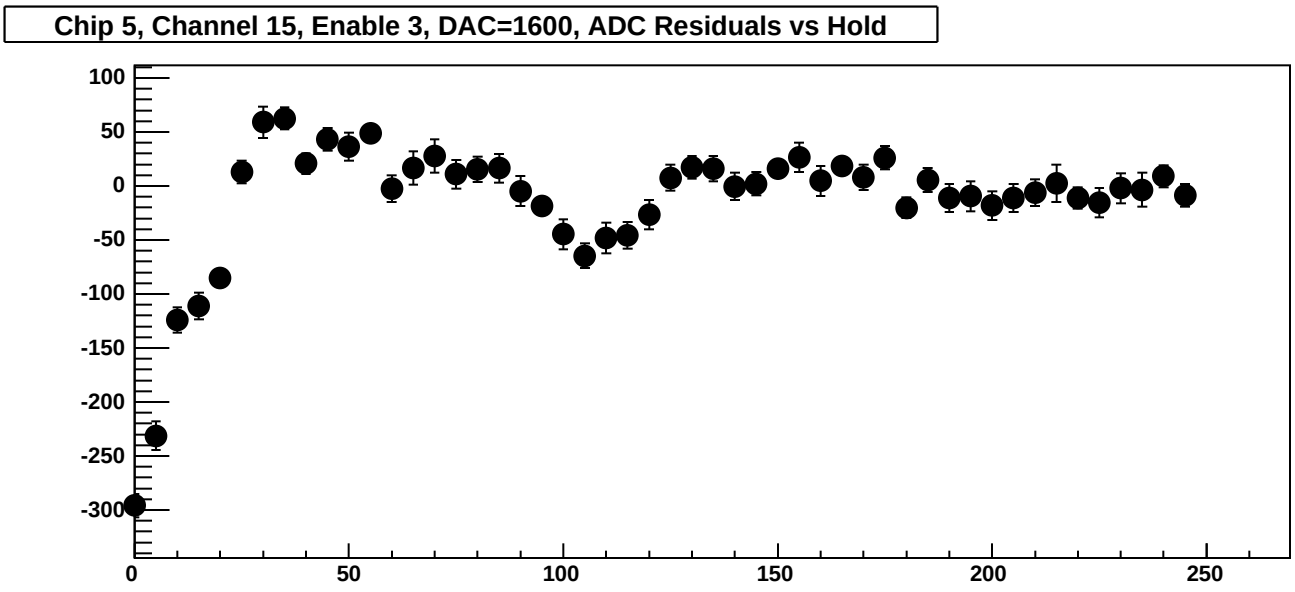

Chip 5, Channel 15, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 2503 / 41
p0 -6260 ± 11.14
p1 -8872 ± 0.9292
p2 -1.385e+04 ± 4.907
p3 -1.321e+05 ± 648.6
p4 2.133 ± 0.001237
-700
-800
-900
-1000
-1100
-1200
-1300
0
50
100
150
200
250


Chip 5, Channel 15, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

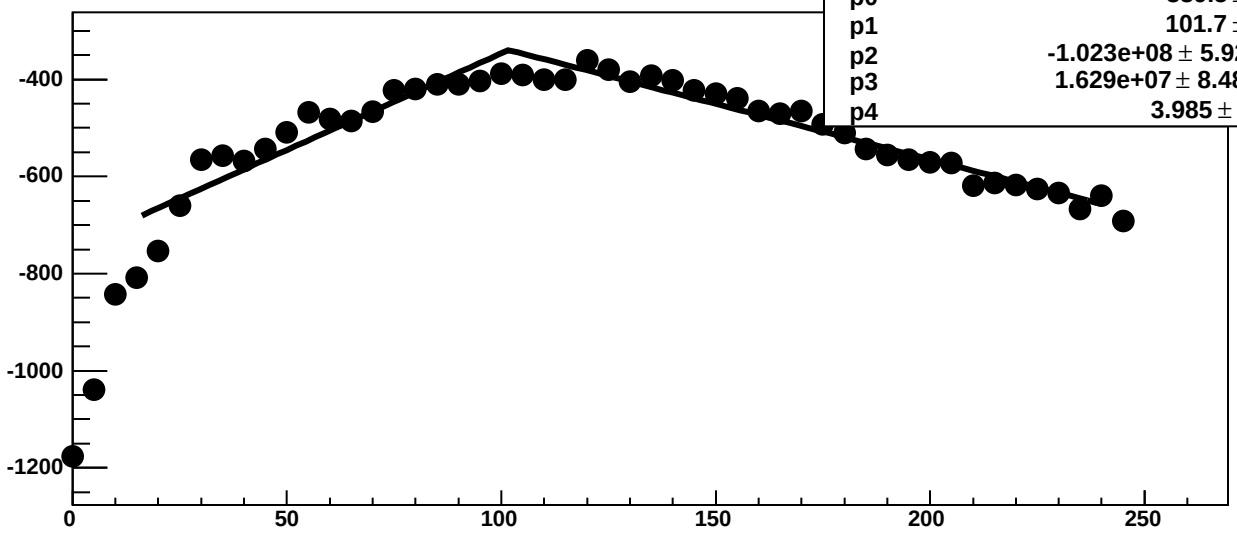

Chip 5, Channel 15, Enable 2, DAC=1600, ADC Residuals vs Hold
200
150
100
50
0
-50
-100
-150
0
50
100
150
200
250


Chip 5, Channel 15, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 410.1 / 41
p0 -328.8 ± 3.57
p1 107.1 ± 0.9252
p2 -1.388e+08 ± 3.258e+06
p3 1.834e+07 ± 3.639e+05
p4 5.136 ± 0.09259
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
50
100
150
200
250

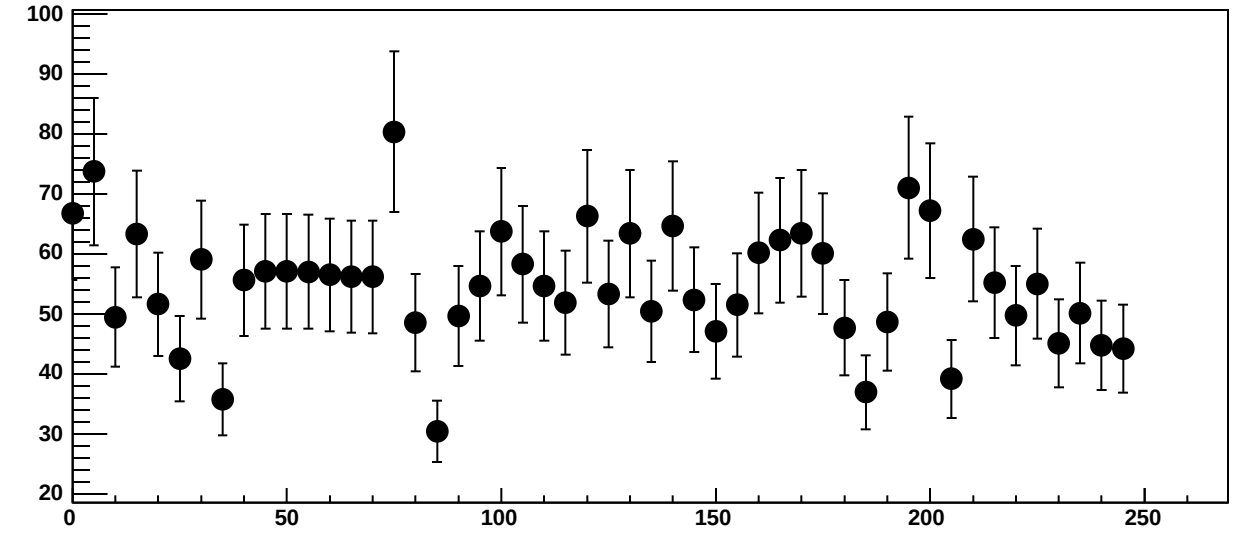

Chip 5, Channel 15, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 5, Channel 15, Enable 3, DAC=1600, ADC Residuals vs Hold
100
50
0
-50
-100
-150
-200
-250
-300
0
50
100
150
200
250

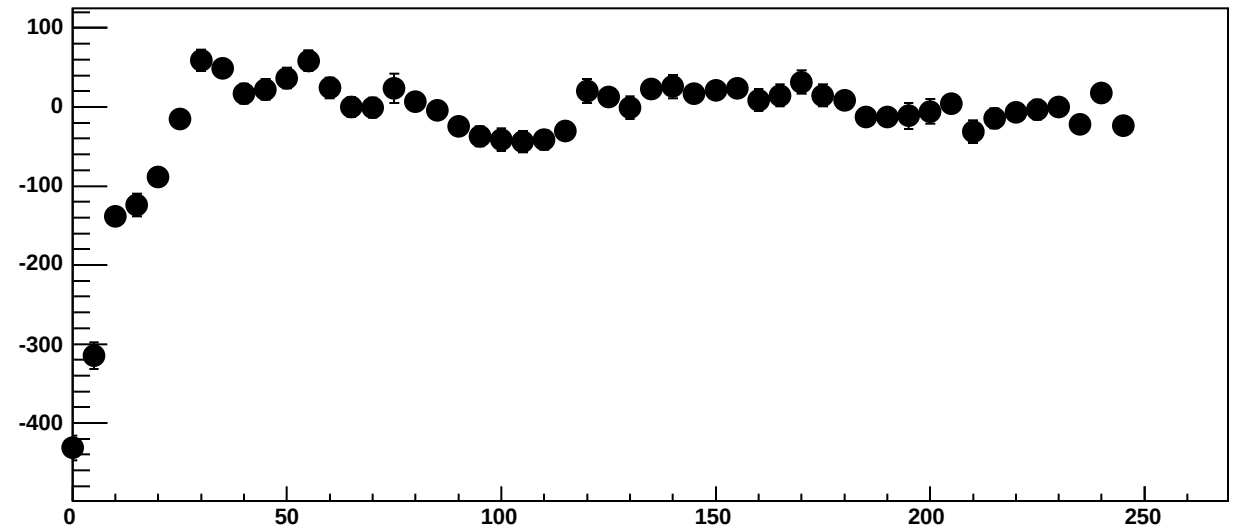
Chip 5, Channel 15, Enable 4, DAC=1600, ADC Mean vs Hold



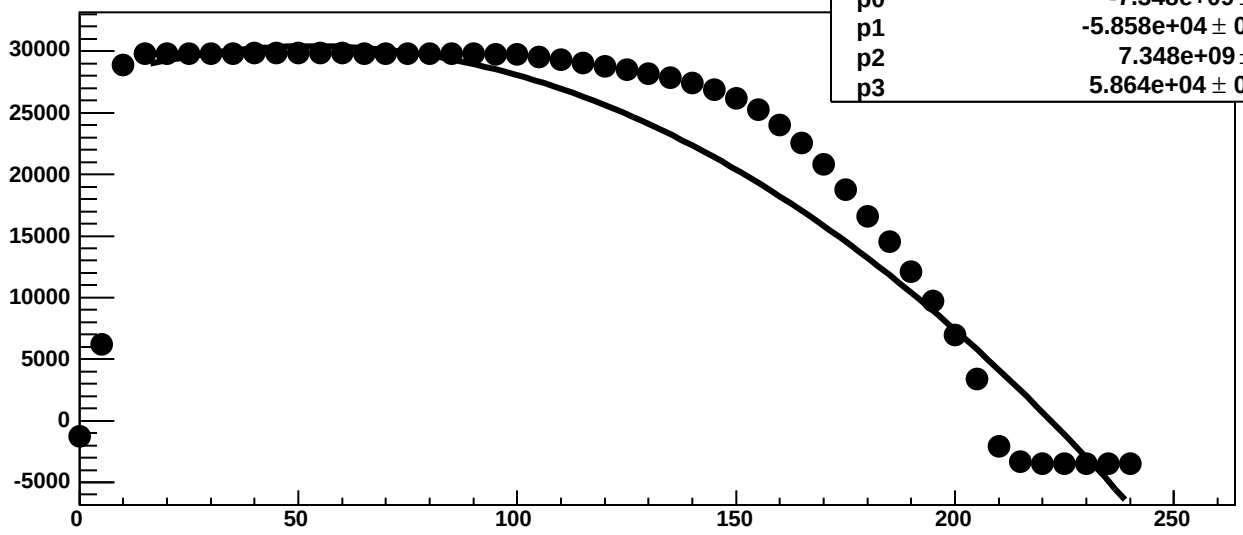
Chip 5, Channel 15, Enable 4, DAC=1600, ADC Noise vs Hold



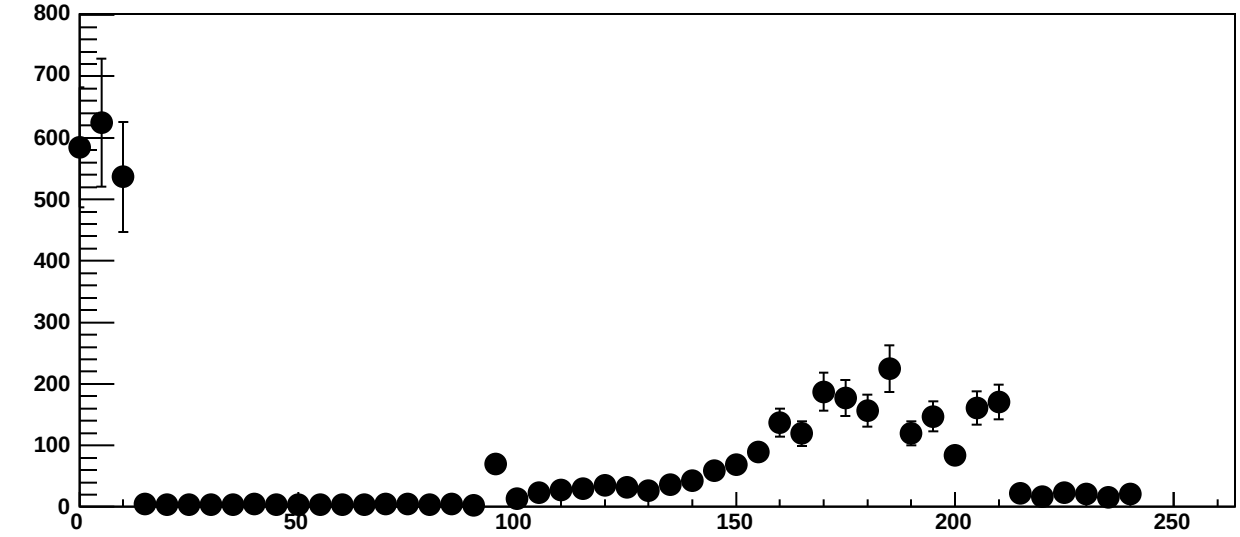
Chip 5, Channel 15, Enable 4, DAC=1600, ADC Residuals vs Hold



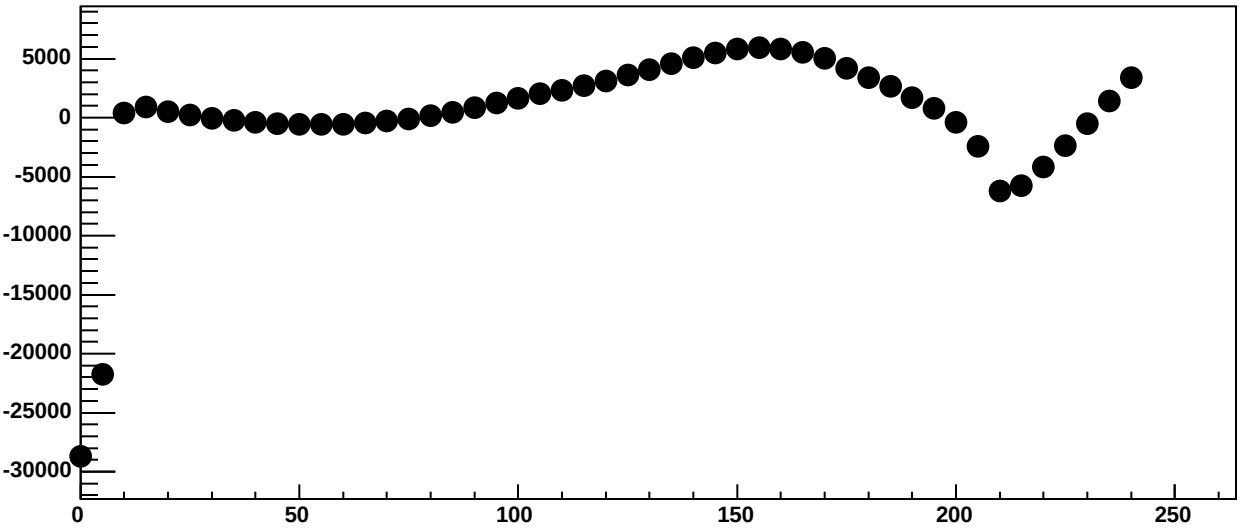
Chip 5, Channel 15, Enable 5!, DAC=1600, ADC Mean vs Hold

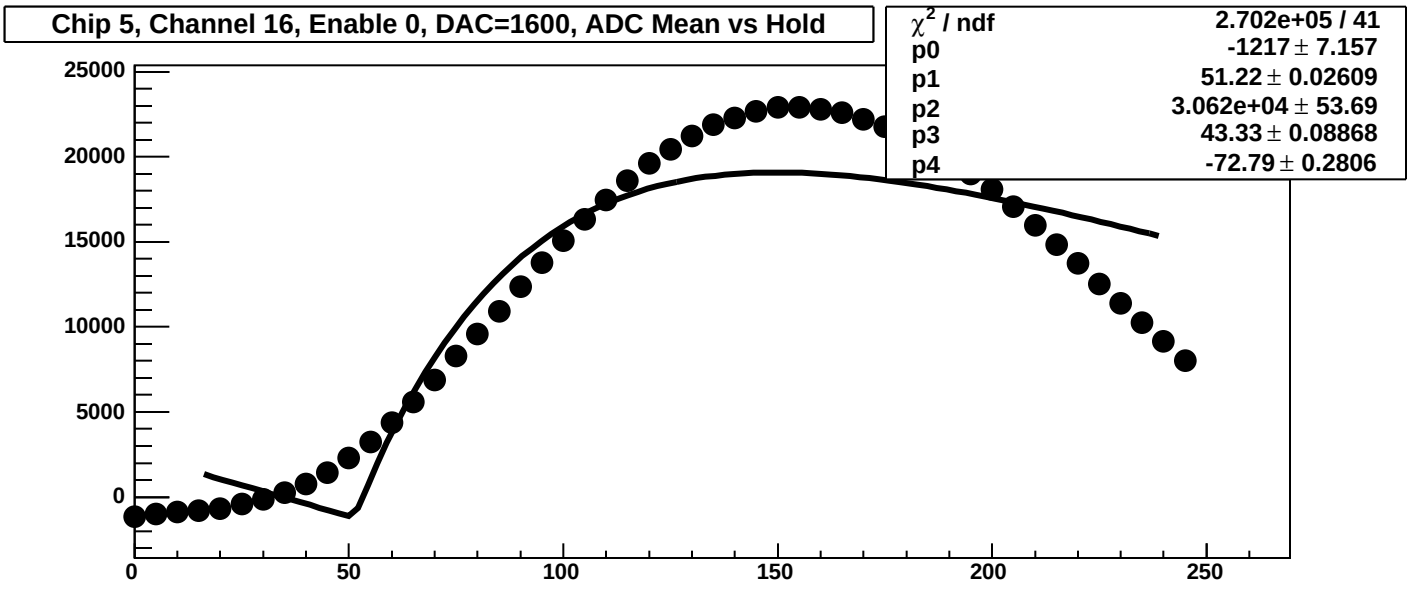


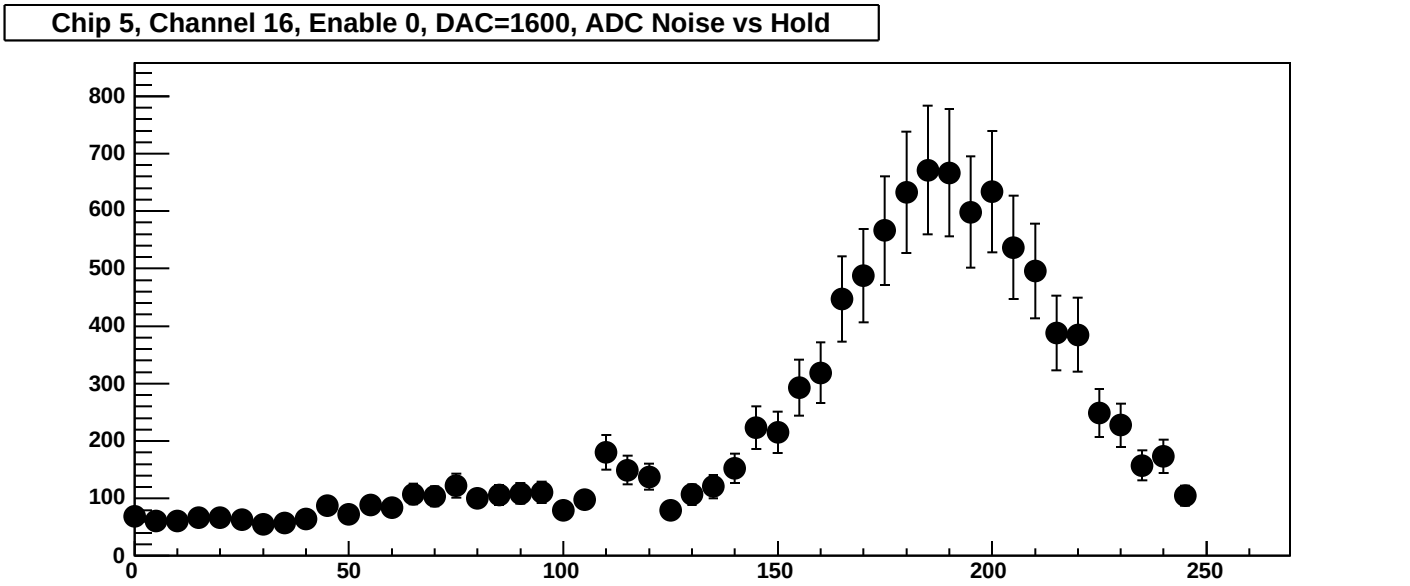
Chip 5, Channel 15, Enable 5!, DAC=1600, ADC Noise vs Hold

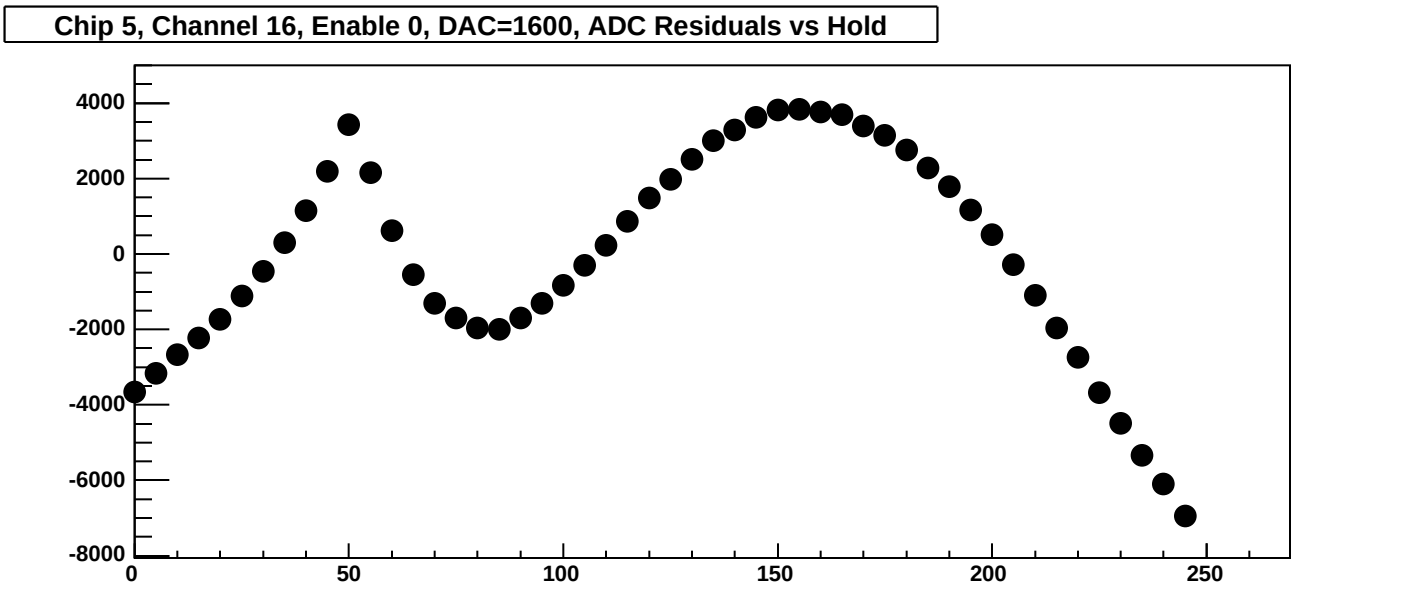


Chip 5, Channel 15, Enable 5!, DAC=1600, ADC Residuals vs Hold

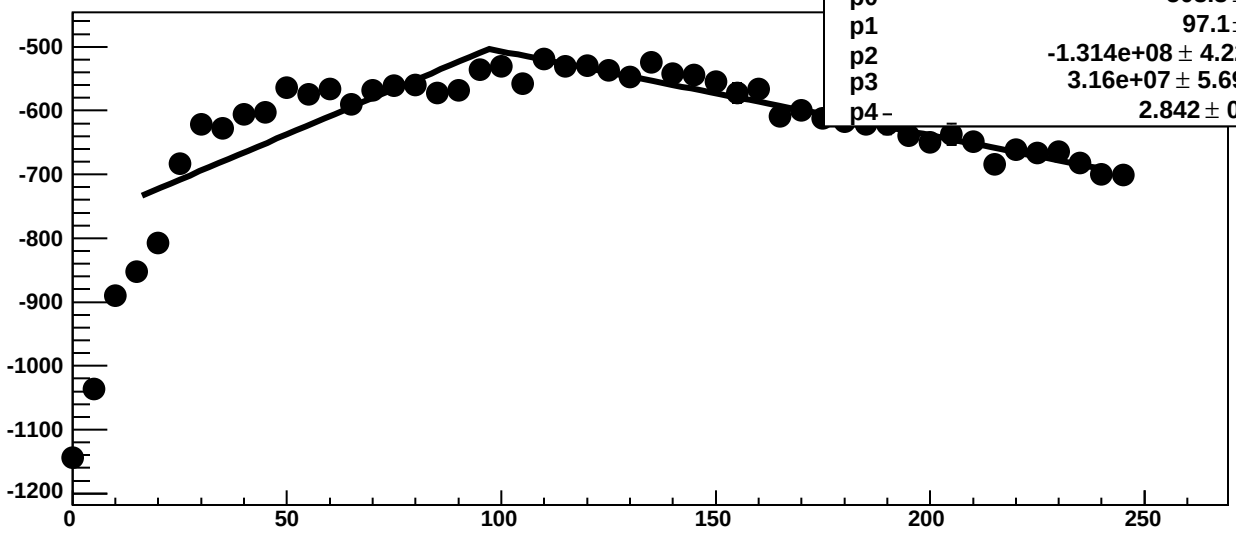



Chip 5, Channel 16, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.702e+05 / 41
p0 -1217 ± 7.157
p1 51.22 ± 0.02609
p2 3.062e+04 ± 53.69
p3 43.33 ± 0.08868
p4 -72.79 ± 0.2806
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

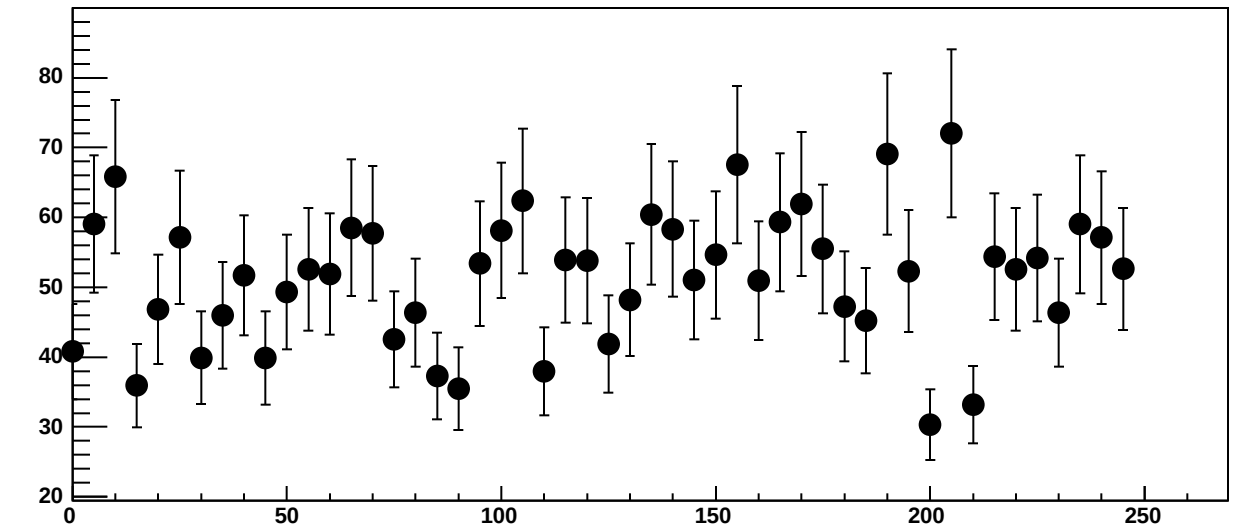

Chip 5, Channel 16, Enable 0, DAC=1600, ADC Noise vs Hold
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 5, Channel 16, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250

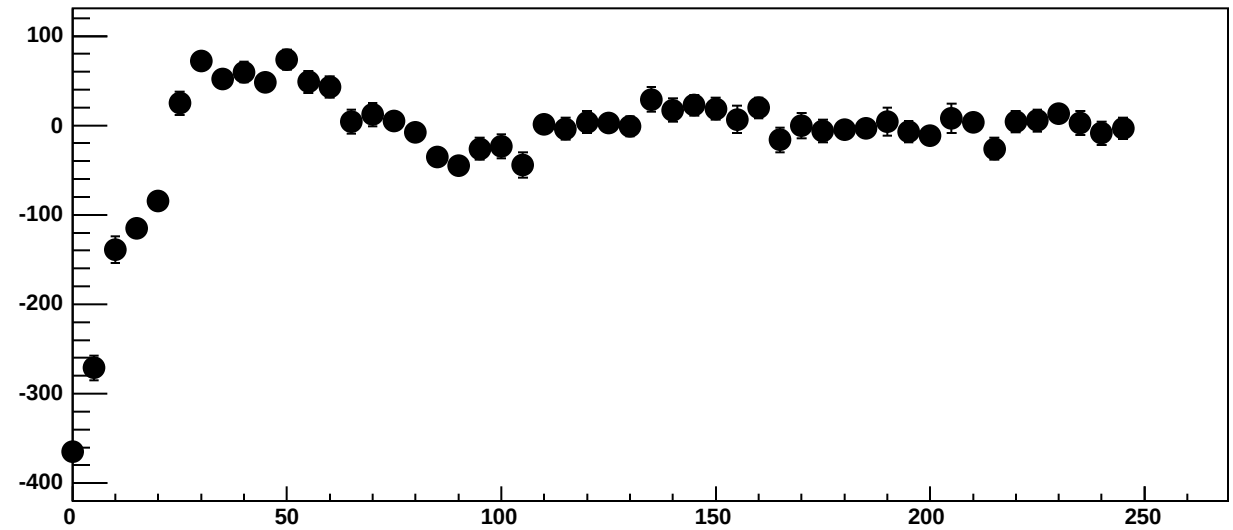
Chip 5, Channel 16, Enable 1, DAC=1600, ADC Mean vs Hold

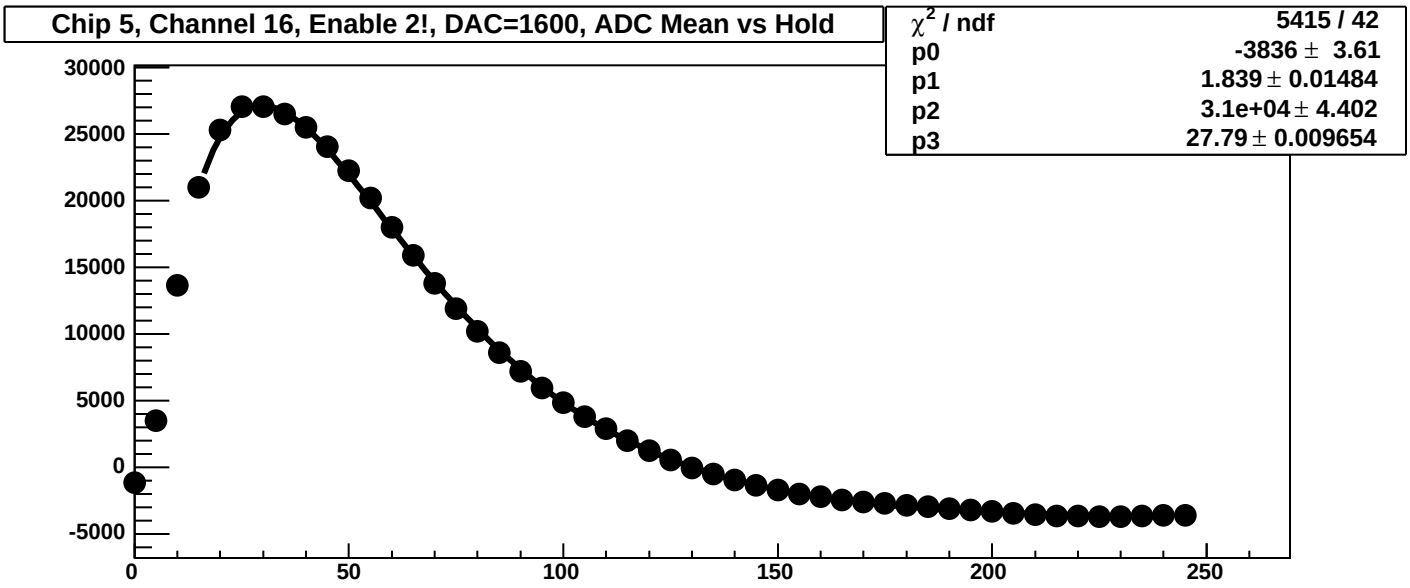


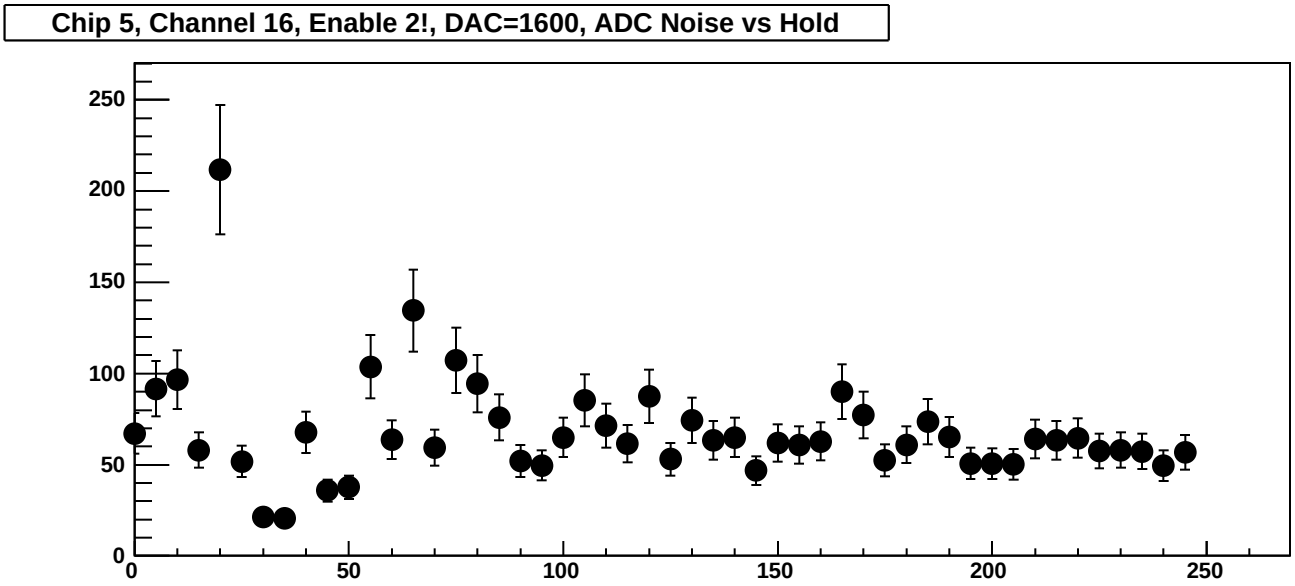
Chip 5, Channel 16, Enable 1, DAC=1600, ADC Noise vs Hold

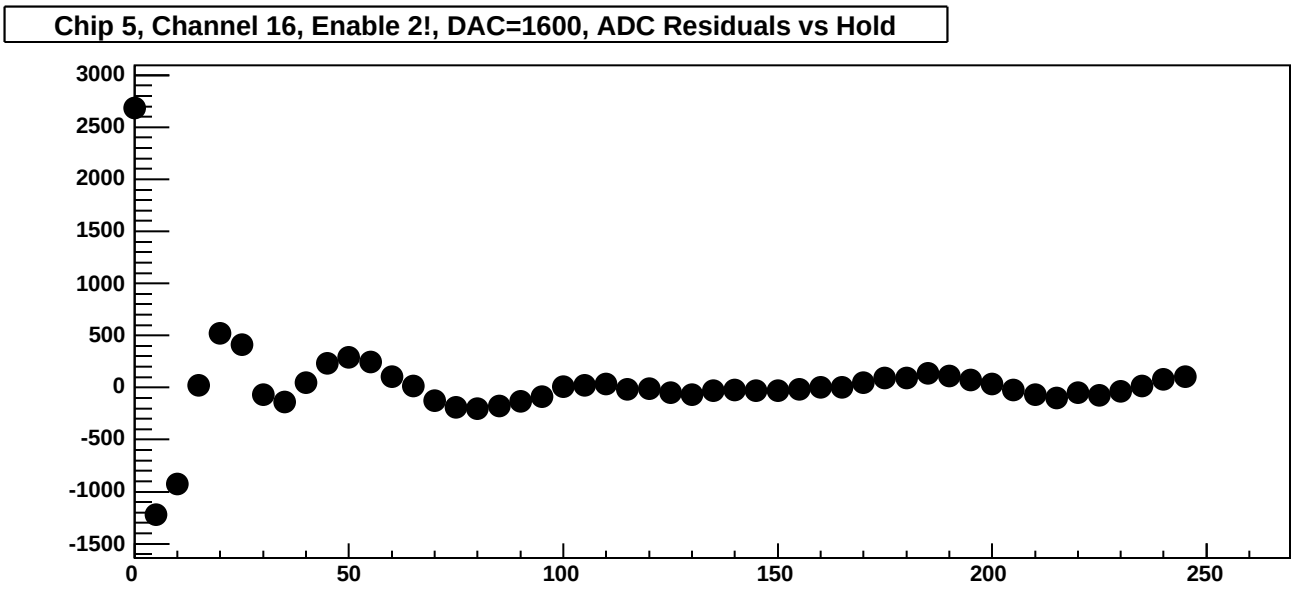


Chip 5, Channel 16, Enable 1, DAC=1600, ADC Residuals vs Hold

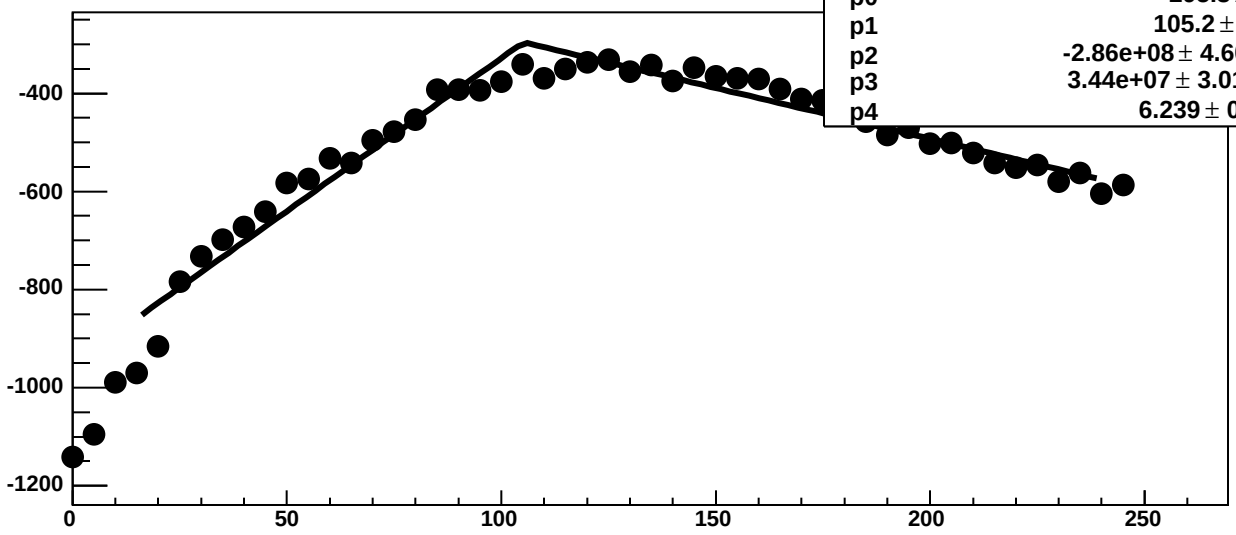



Chip 5, Channel 16, Enable 2!, DAC=1600, ADC Mean vs Hold
χ² / ndf 5415 / 42
p0 -3836 ± 3.61
p1 1.839 ± 0.01484
p2 3.1e+04 ± 4.402
p3 27.79 ± 0.009654
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

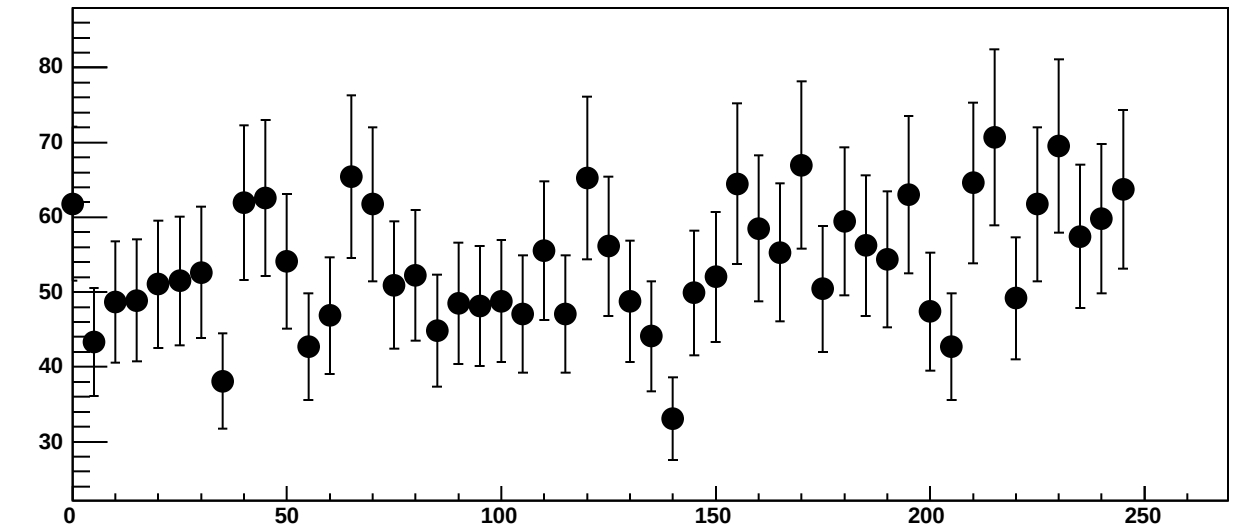

Chip 5, Channel 16, Enable 2!, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250


Chip 5, Channel 16, Enable 2!, DAC=1600, ADC Residuals vs Hold
3000
2500
2000
1500
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

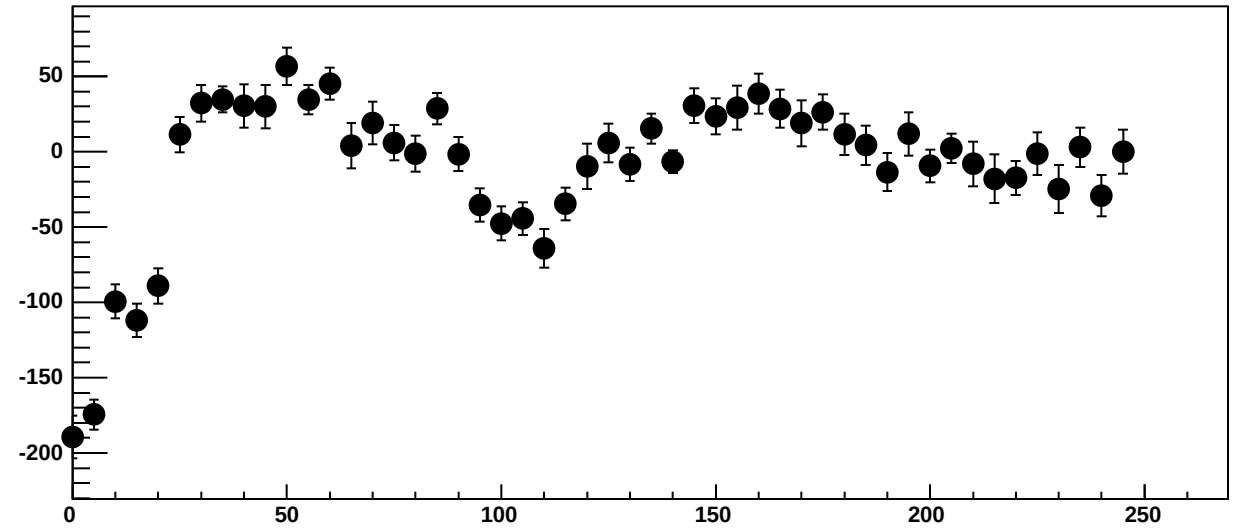
Chip 5, Channel 16, Enable 3, DAC=1600, ADC Mean vs Hold



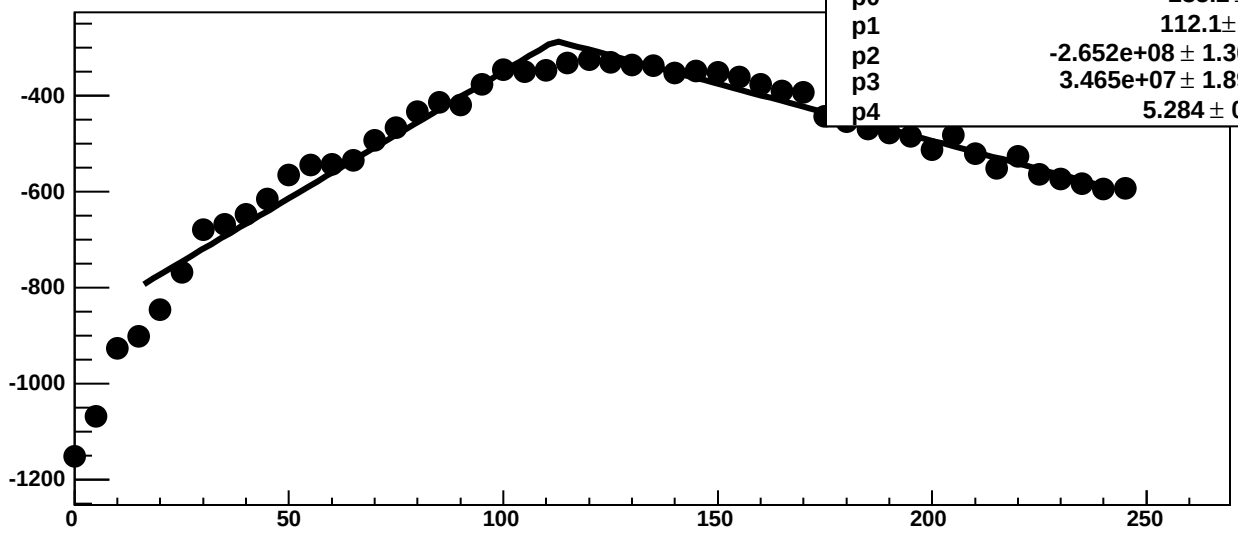
Chip 5, Channel 16, Enable 3, DAC=1600, ADC Noise vs Hold



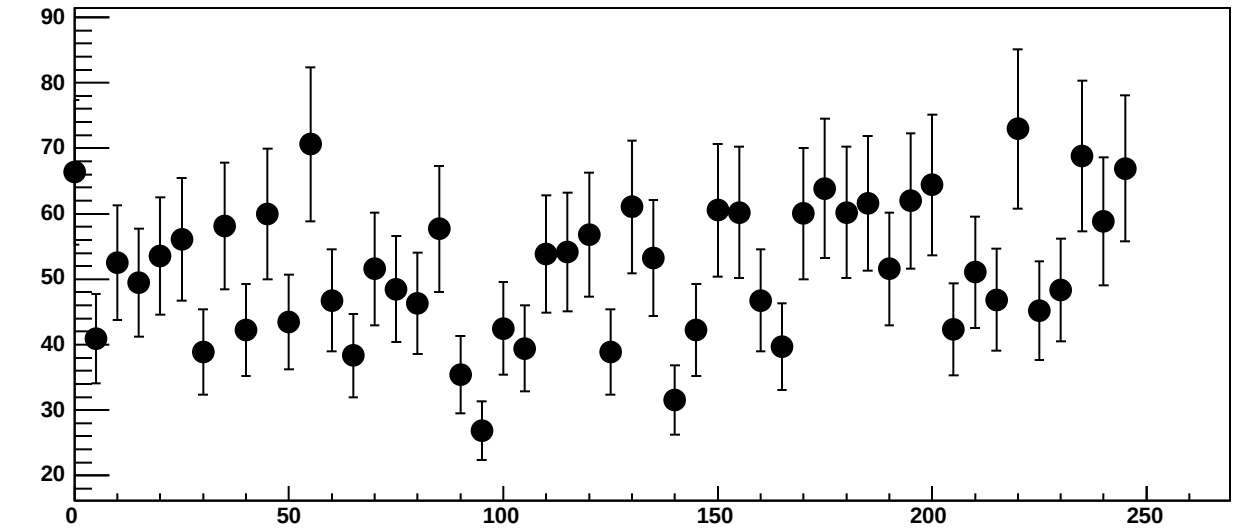
Chip 5, Channel 16, Enable 3, DAC=1600, ADC Residuals vs Hold



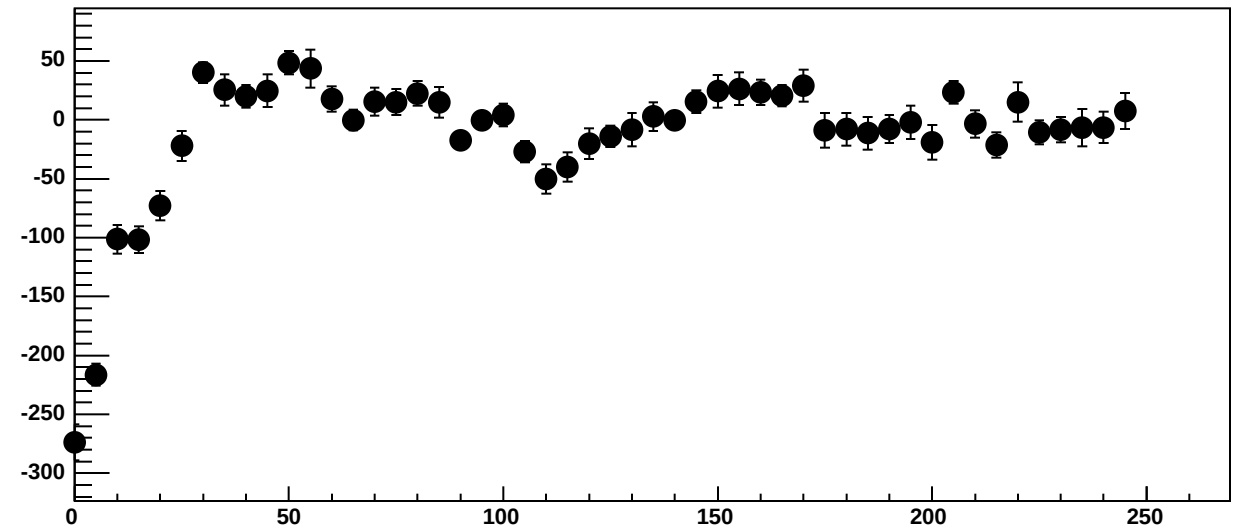
Chip 5, Channel 16, Enable 4, DAC=1600, ADC Mean vs Hold

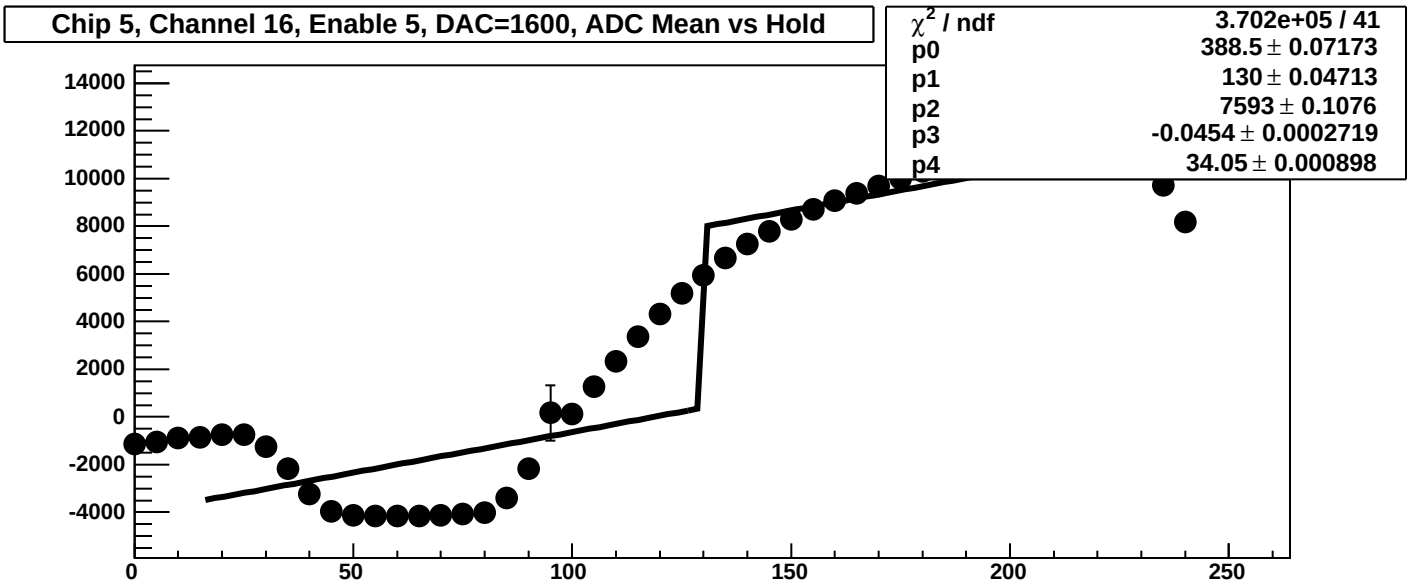


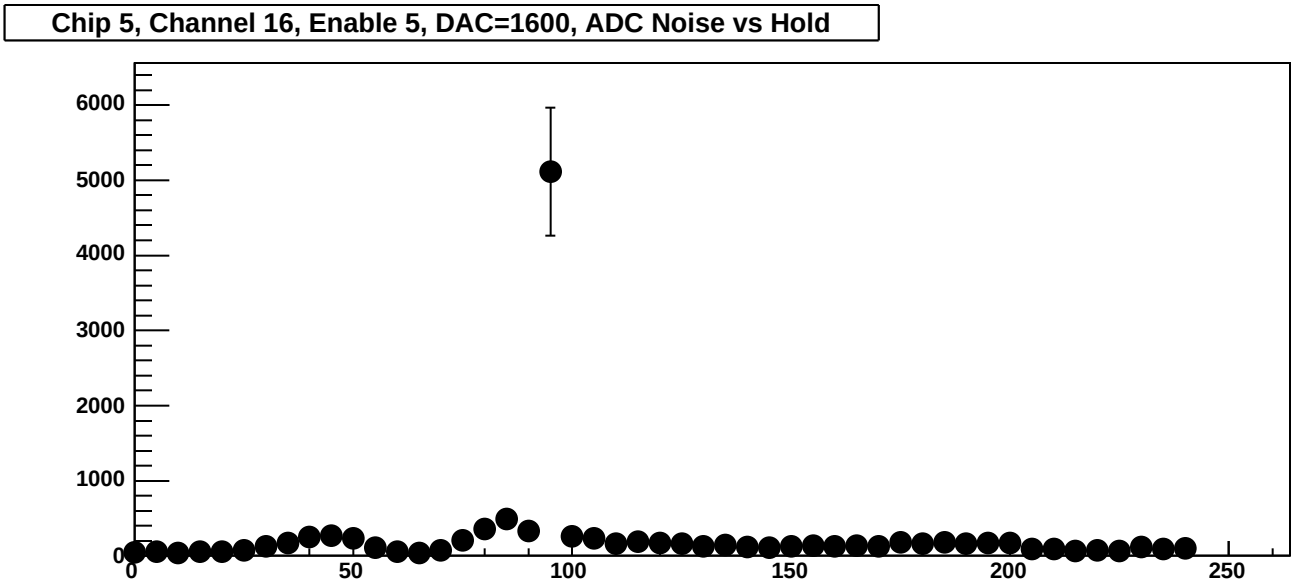
Chip 5, Channel 16, Enable 4, DAC=1600, ADC Noise vs Hold

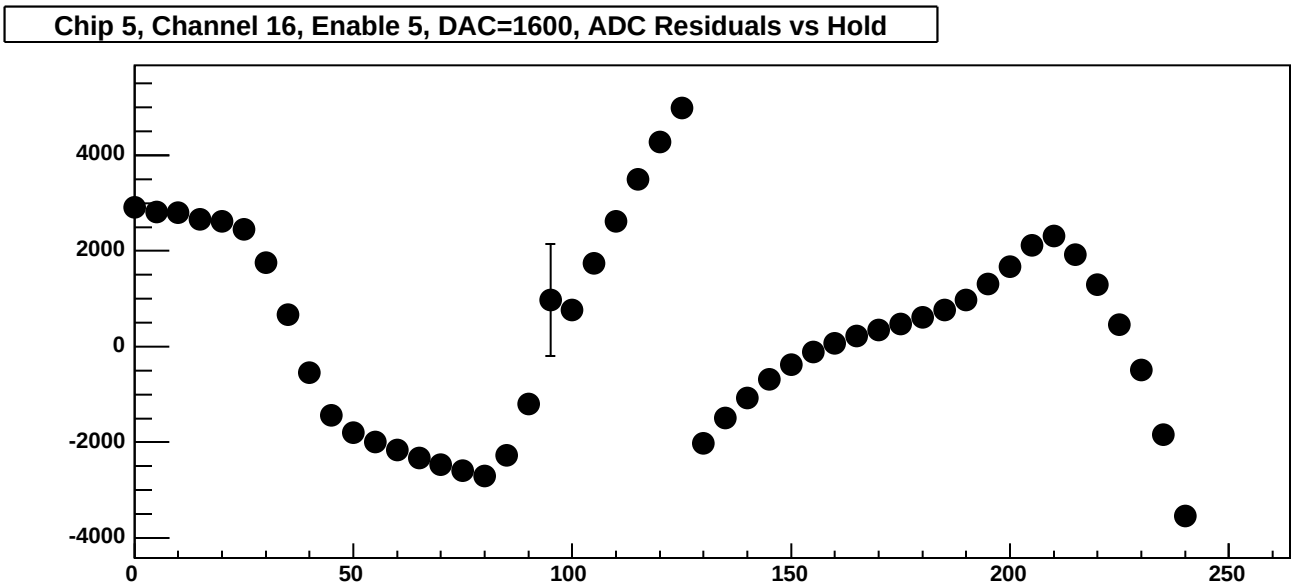


Chip 5, Channel 16, Enable 4, DAC=1600, ADC Residuals vs Hold

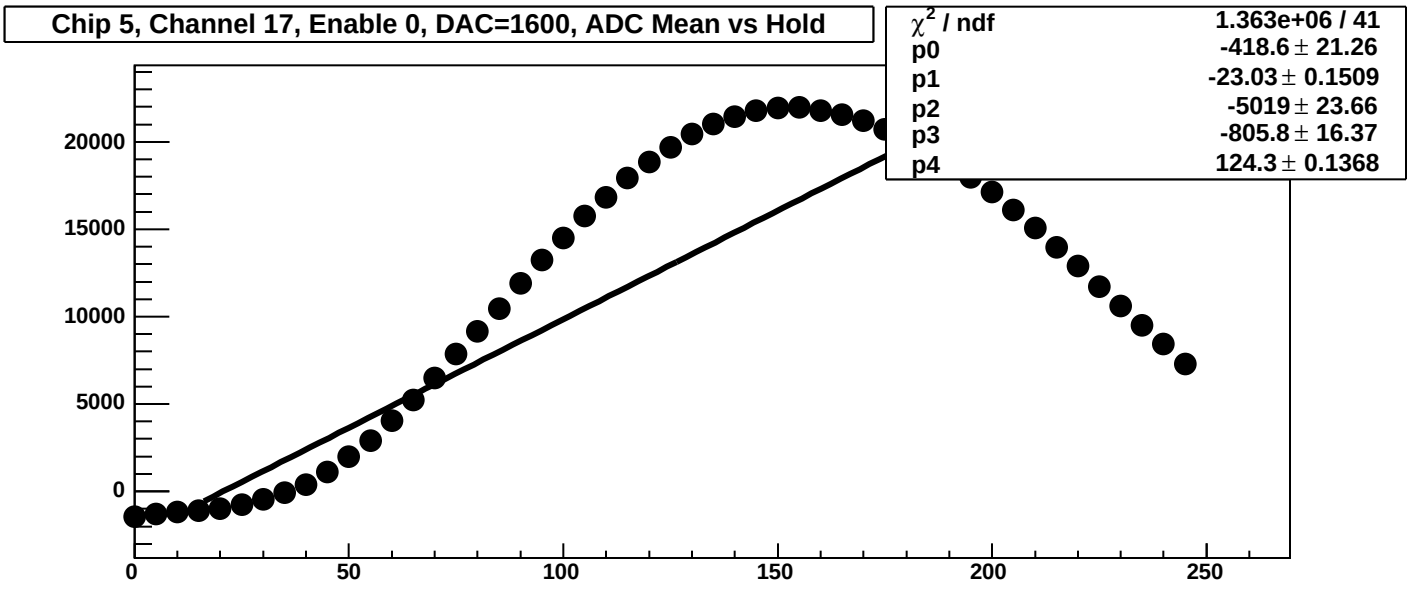



Chip 5, Channel 16, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.702e+05 / 41
p0 388.5 ± 0.07173
p1 130 ± 0.04713
p2 7593 ± 0.1076
p3 -0.0454 ± 0.0002719
p4 34.05 ± 0.000898
14000
12000
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

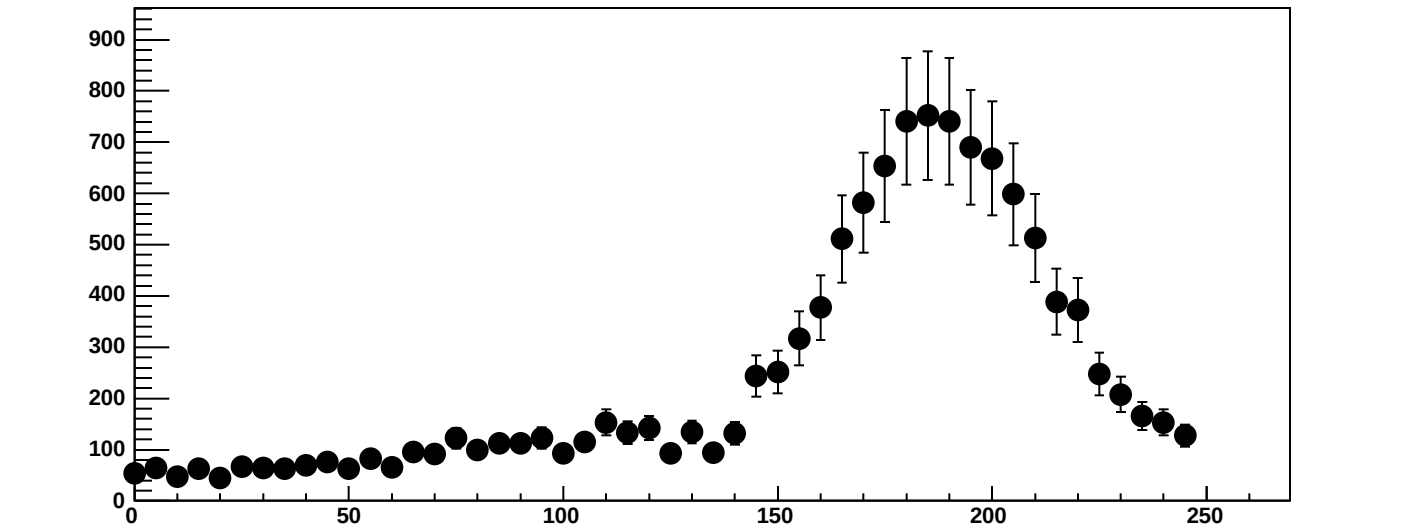

Chip 5, Channel 16, Enable 5, DAC=1600, ADC Noise vs Hold
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 5, Channel 16, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
0
50
100
150
200
250

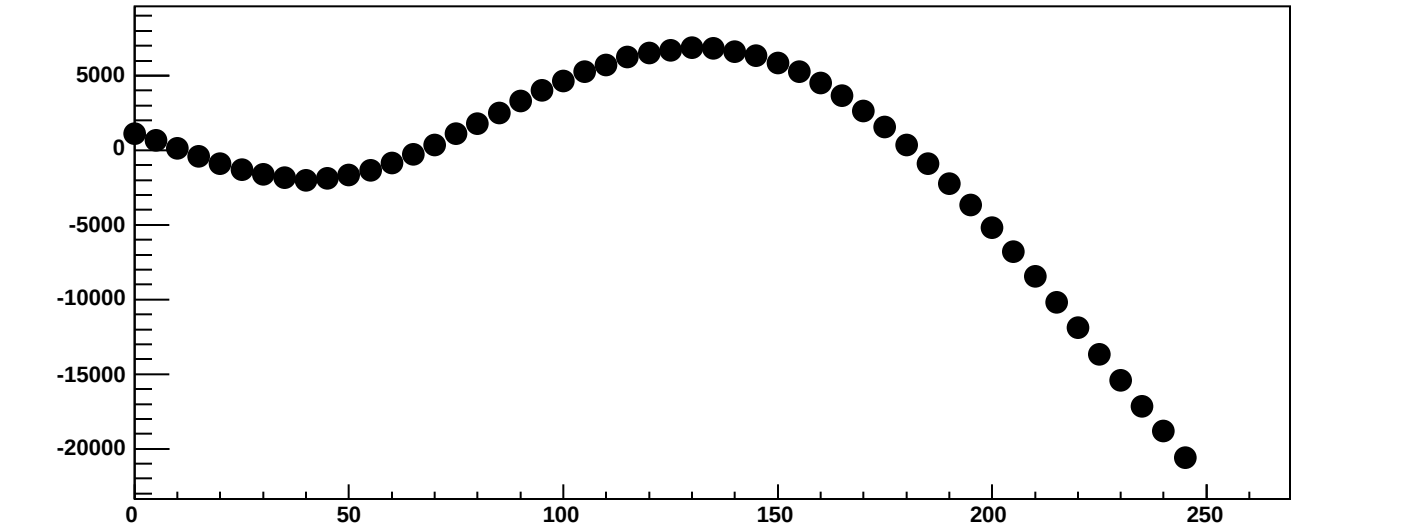
Chip 5, Channel 17, Enable 0, DAC=1600, ADC Mean vs Hold



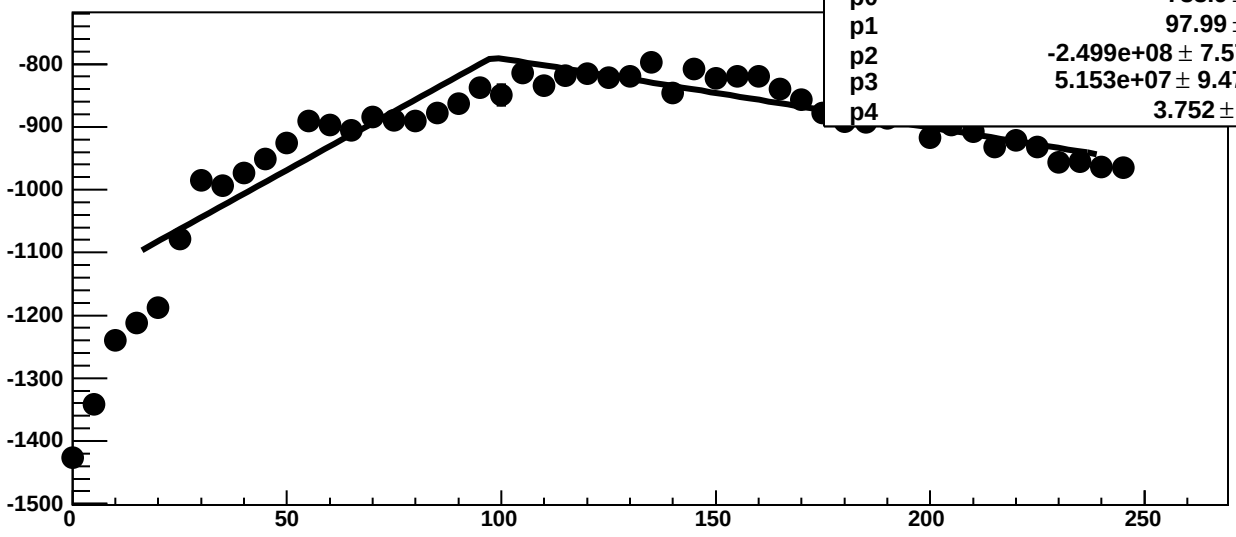
Chip 5, Channel 17, Enable 0, DAC=1600, ADC Noise vs Hold



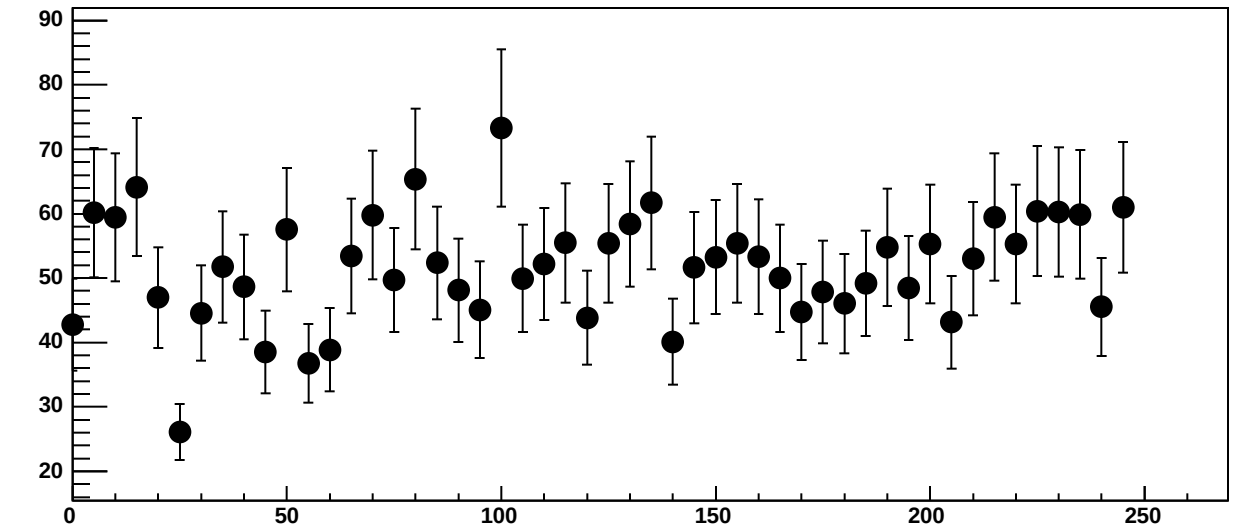
Chip 5, Channel 17, Enable 0, DAC=1600, ADC Residuals vs Hold



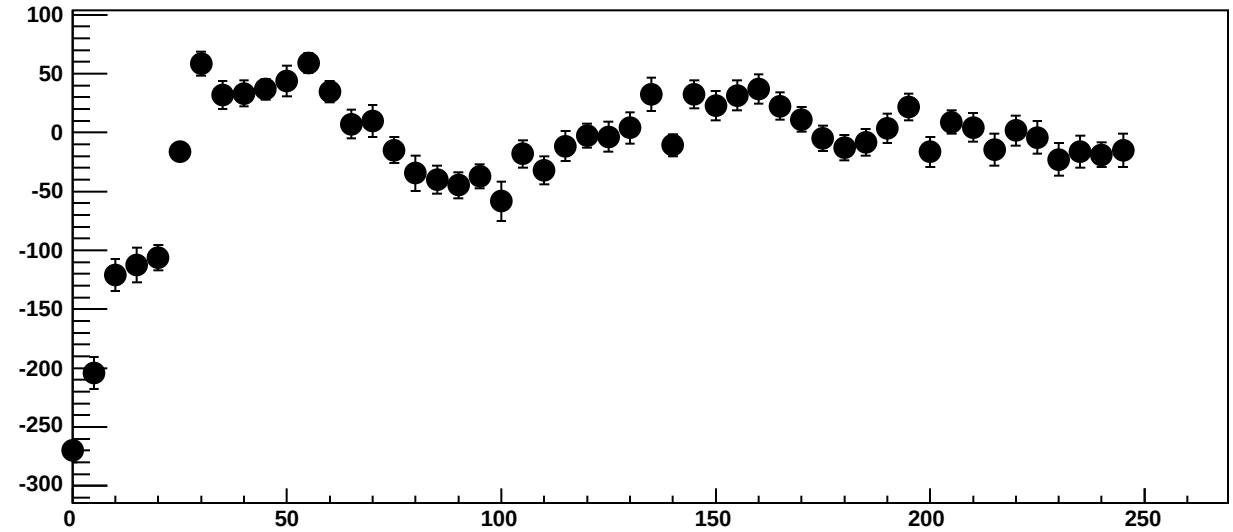
Chip 5, Channel 17, Enable 1, DAC=1600, ADC Mean vs Hold



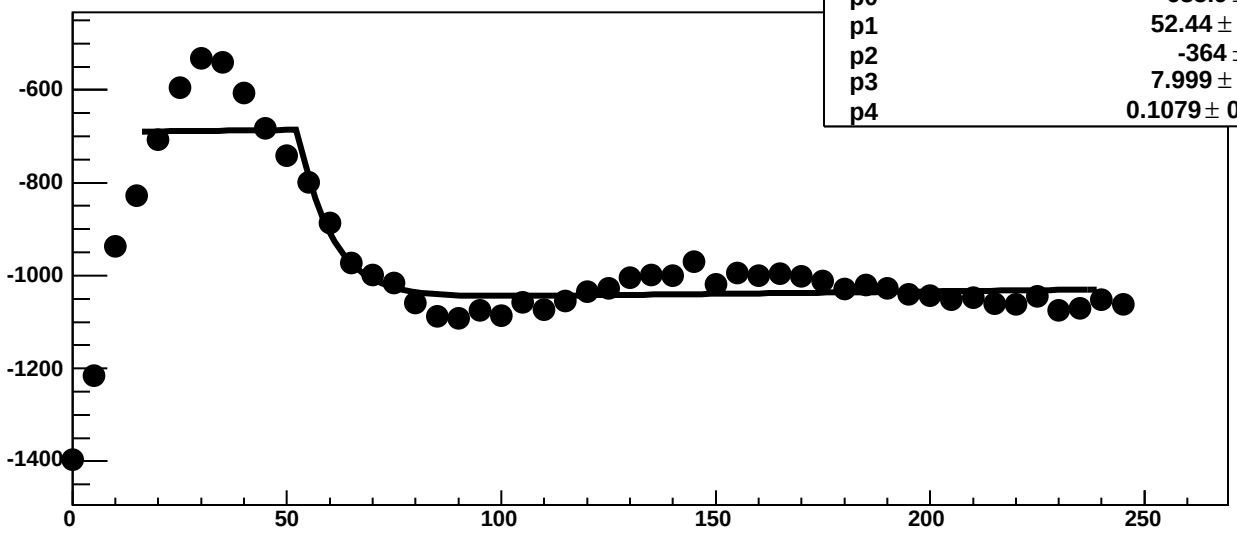
Chip 5, Channel 17, Enable 1, DAC=1600, ADC Noise vs Hold



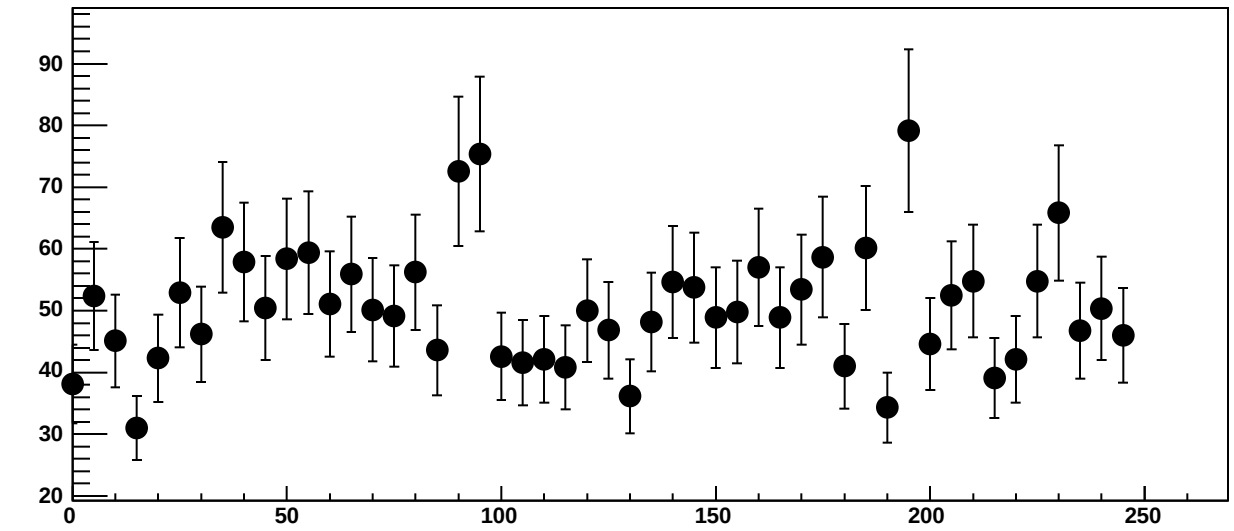
Chip 5, Channel 17, Enable 1, DAC=1600, ADC Residuals vs Hold



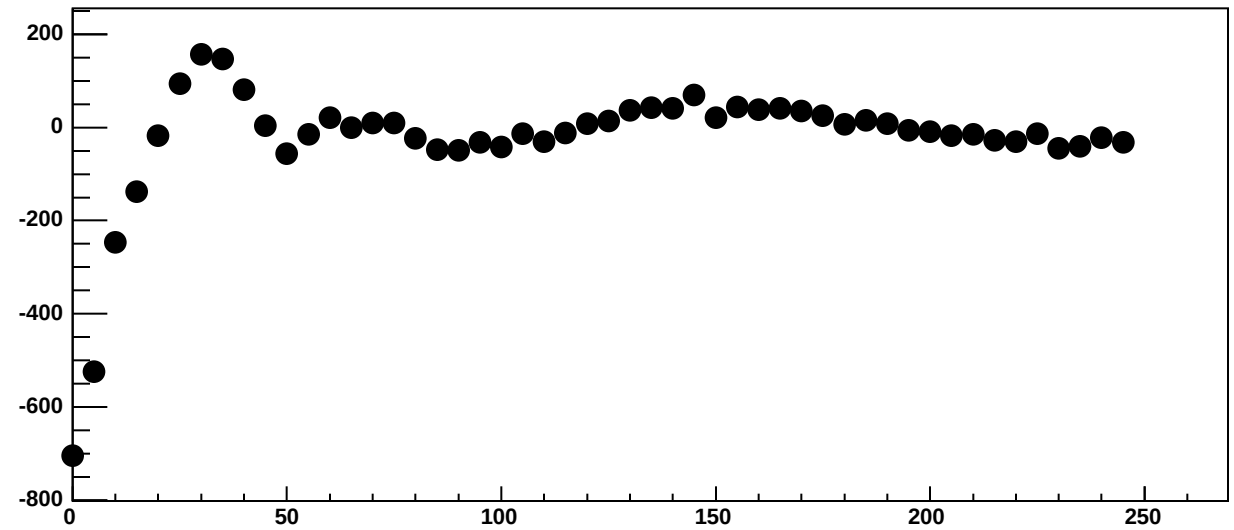
Chip 5, Channel 17, Enable 2, DAC=1600, ADC Mean vs Hold

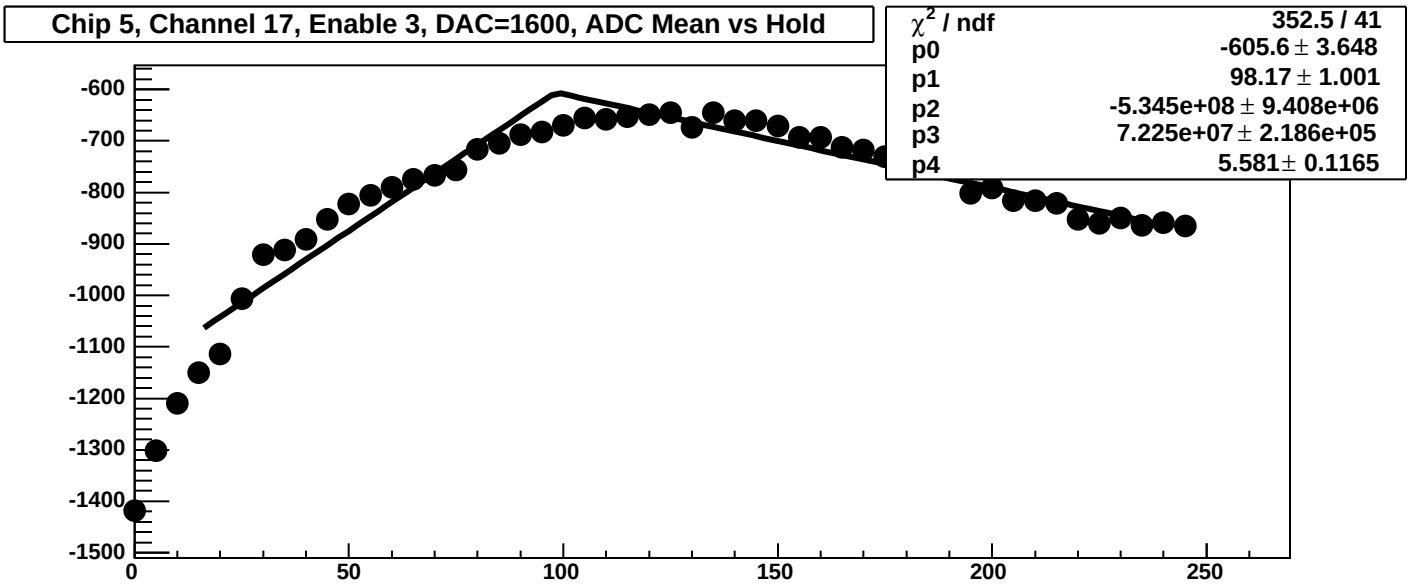


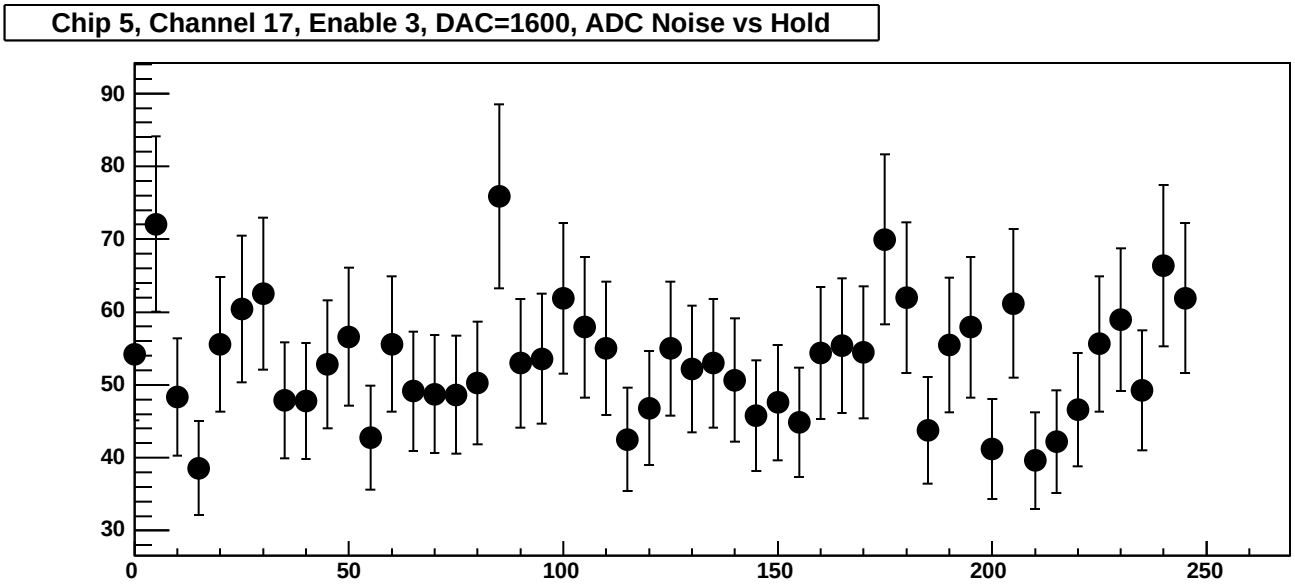
Chip 5, Channel 17, Enable 2, DAC=1600, ADC Noise vs Hold

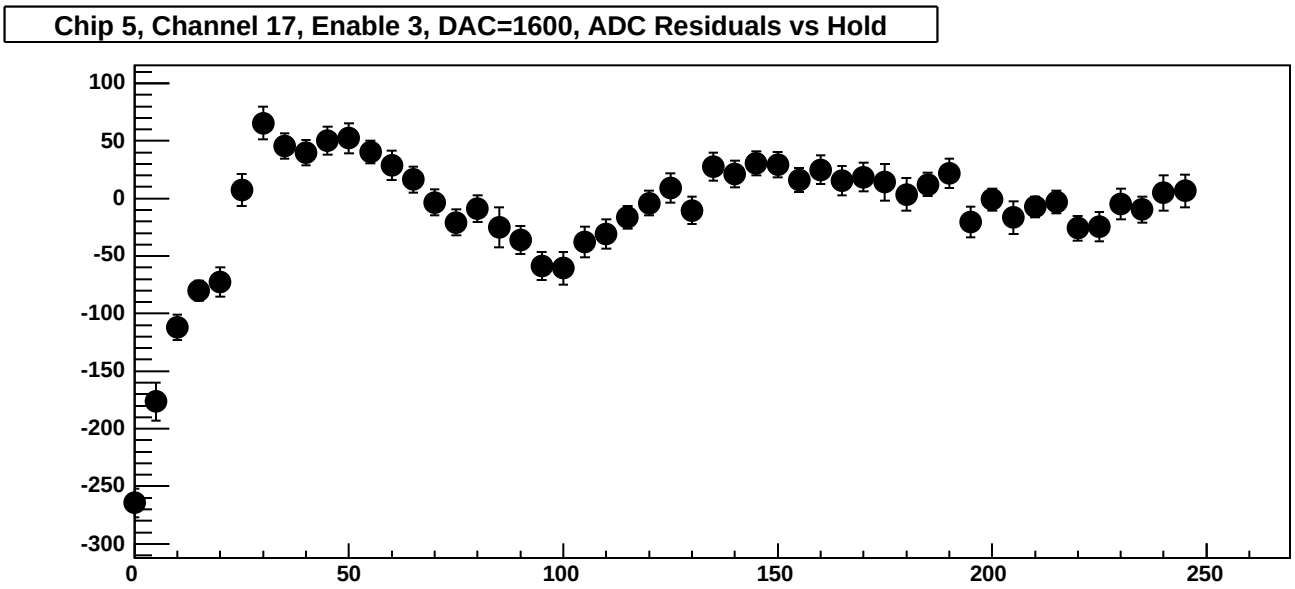


Chip 5, Channel 17, Enable 2, DAC=1600, ADC Residuals vs Hold

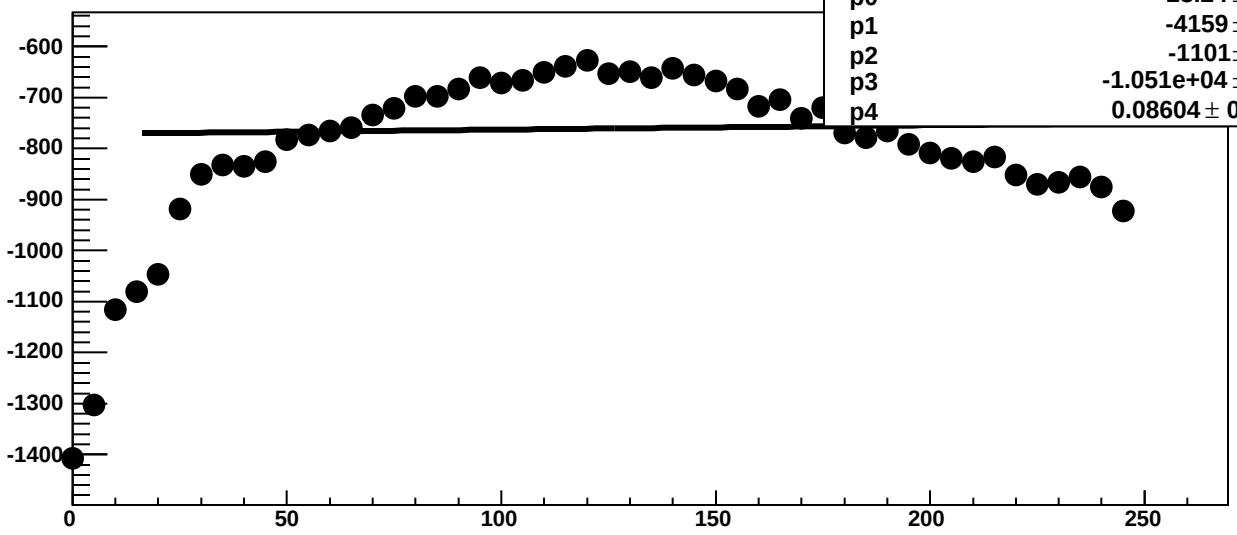



Chip 5, Channel 17, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 352.5 / 41
p0 -605.6 ± 3.648
p1 98.17 ± 1.001
p2 -5.345e+08 ± 9.408e+06
p3 7.225e+07 ± 2.186e+05
p4 5.581 ± 0.1165
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250

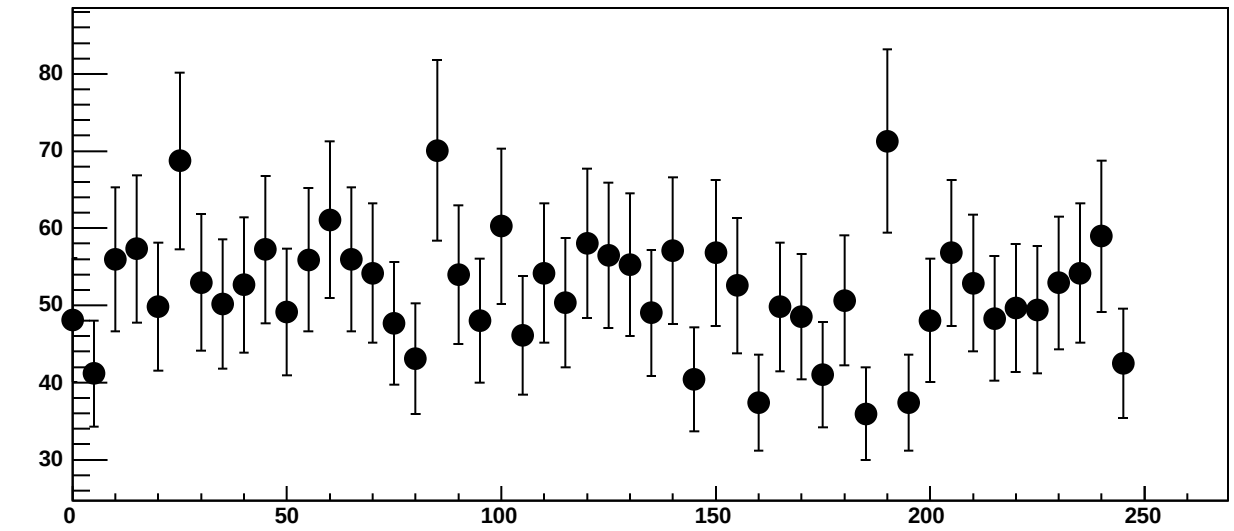

Chip 5, Channel 17, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 5, Channel 17, Enable 3, DAC=1600, ADC Residuals vs Hold
100
50
0
-50
-100
-150
-200
-250
-300
0
50
100
150
200
250

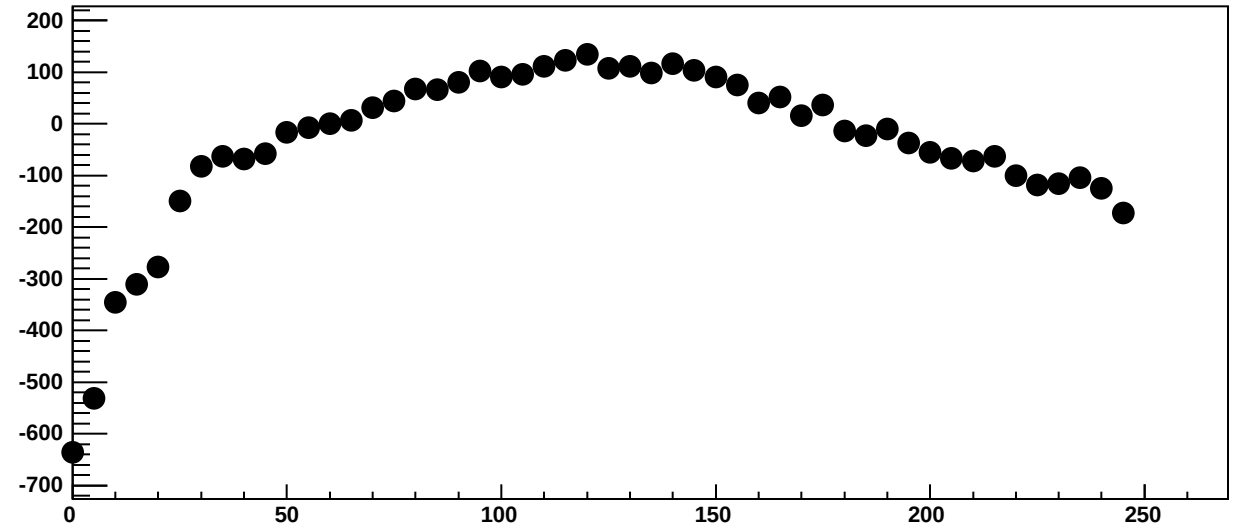
Chip 5, Channel 17, Enable 4, DAC=1600, ADC Mean vs Hold



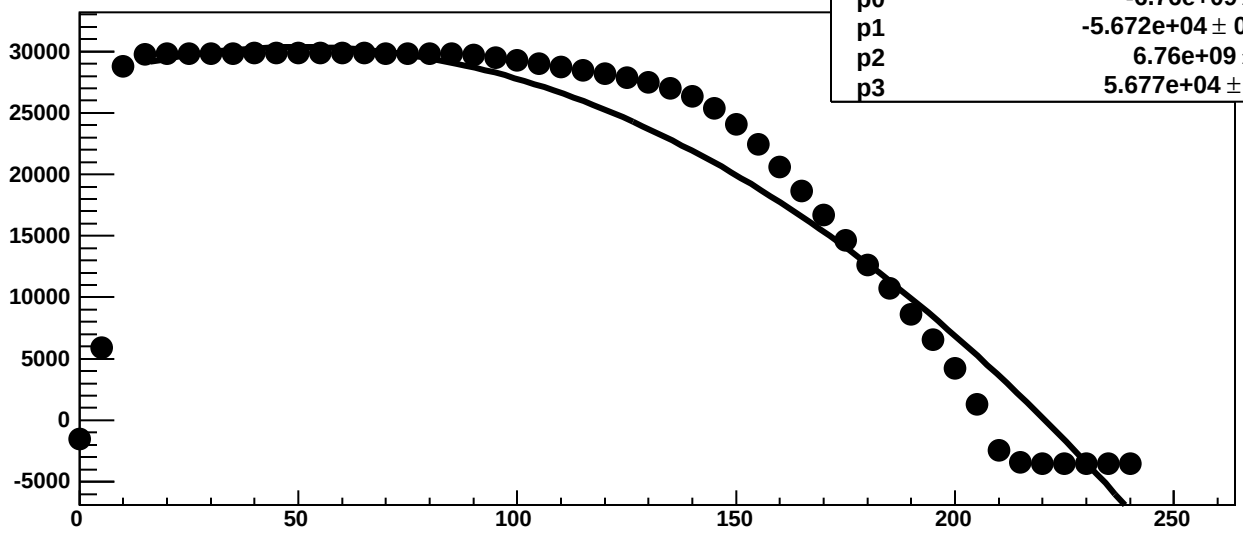
Chip 5, Channel 17, Enable 4, DAC=1600, ADC Noise vs Hold



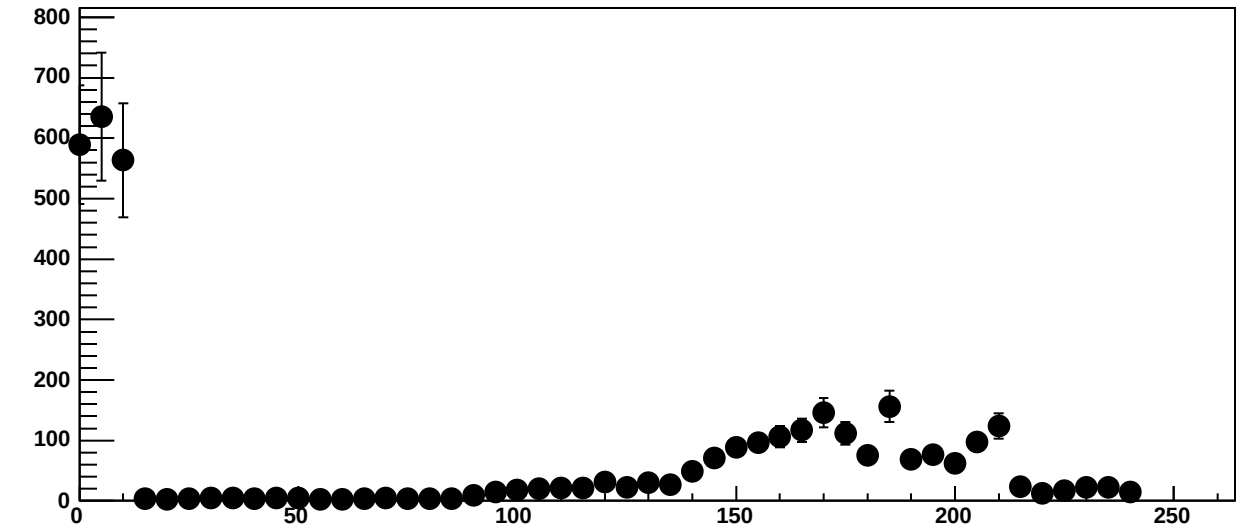
Chip 5, Channel 17, Enable 4, DAC=1600, ADC Residuals vs Hold



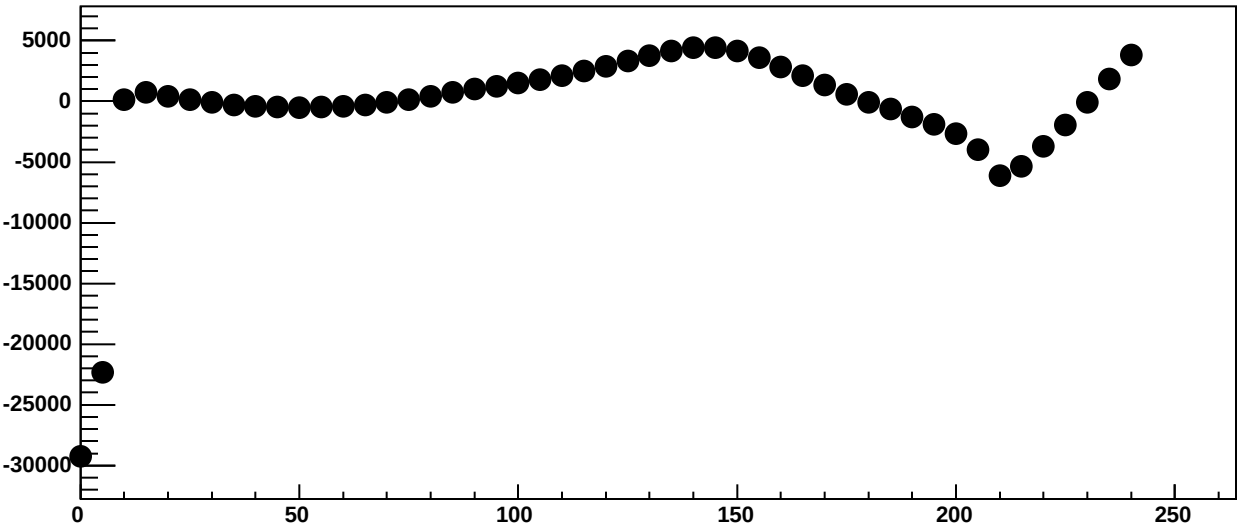
Chip 5, Channel 17, Enable 5!, DAC=1600, ADC Mean vs Hold



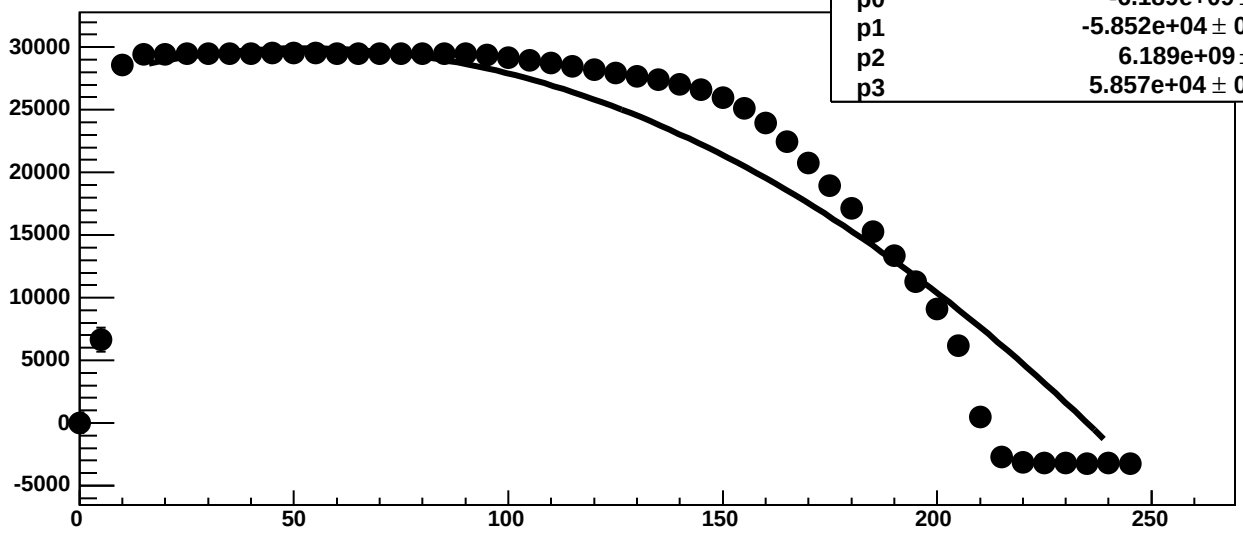
Chip 5, Channel 17, Enable 5!, DAC=1600, ADC Noise vs Hold



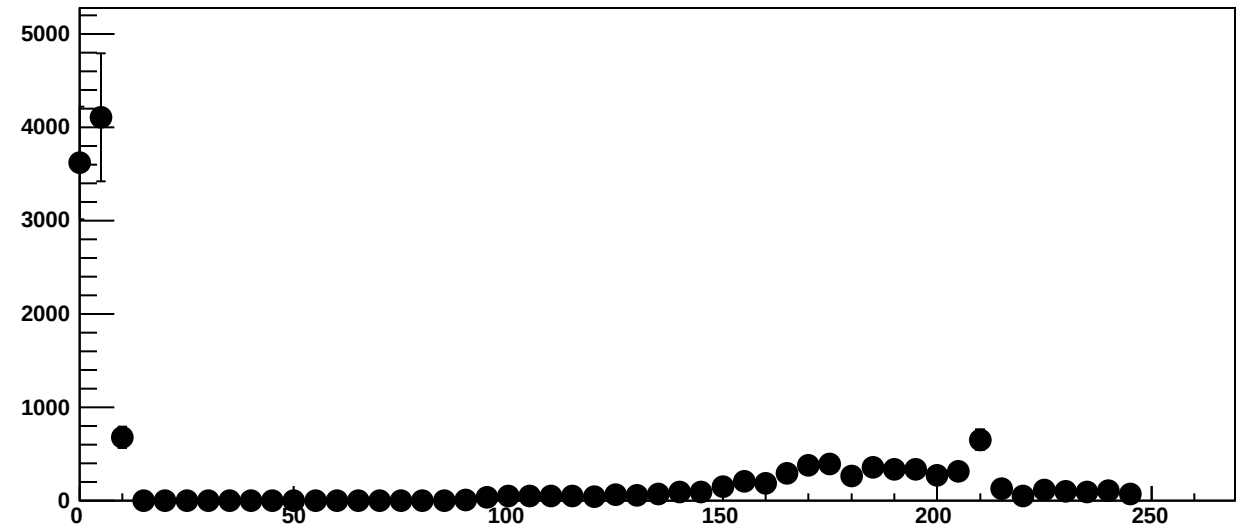
Chip 5, Channel 17, Enable 5!, DAC=1600, ADC Residuals vs Hold



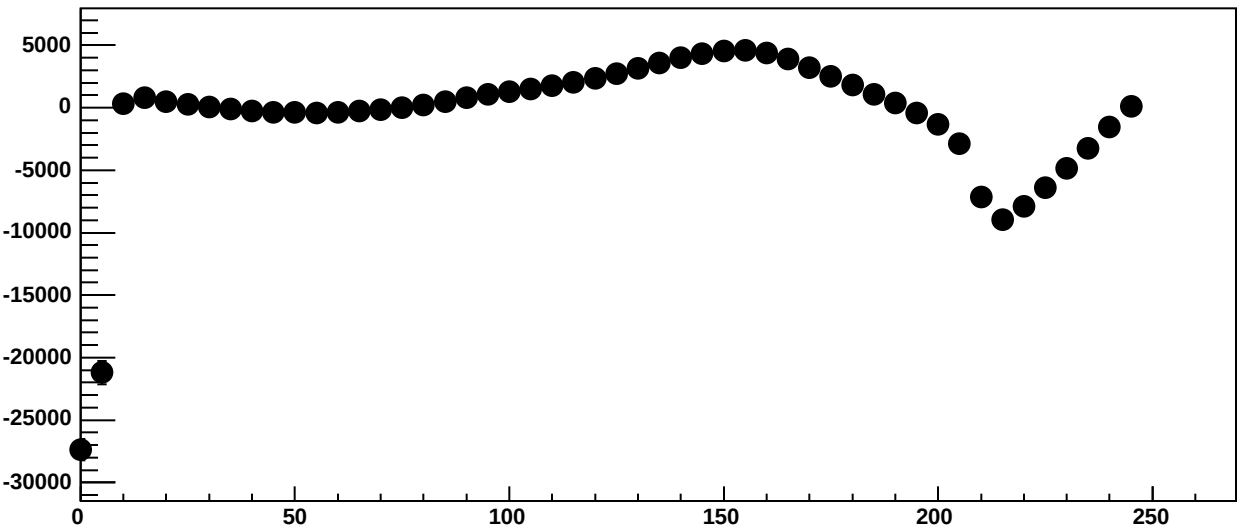
Chip 6, Channel 0, Enable 0!, DAC=1600, ADC Mean vs Hold



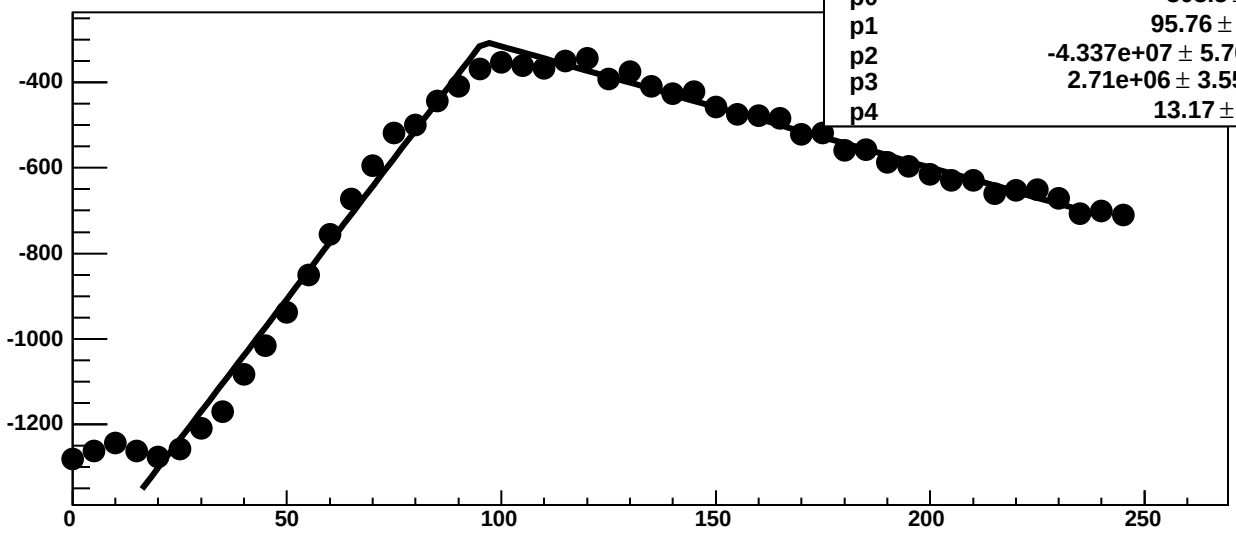
Chip 6, Channel 0, Enable 0!, DAC=1600, ADC Noise vs Hold



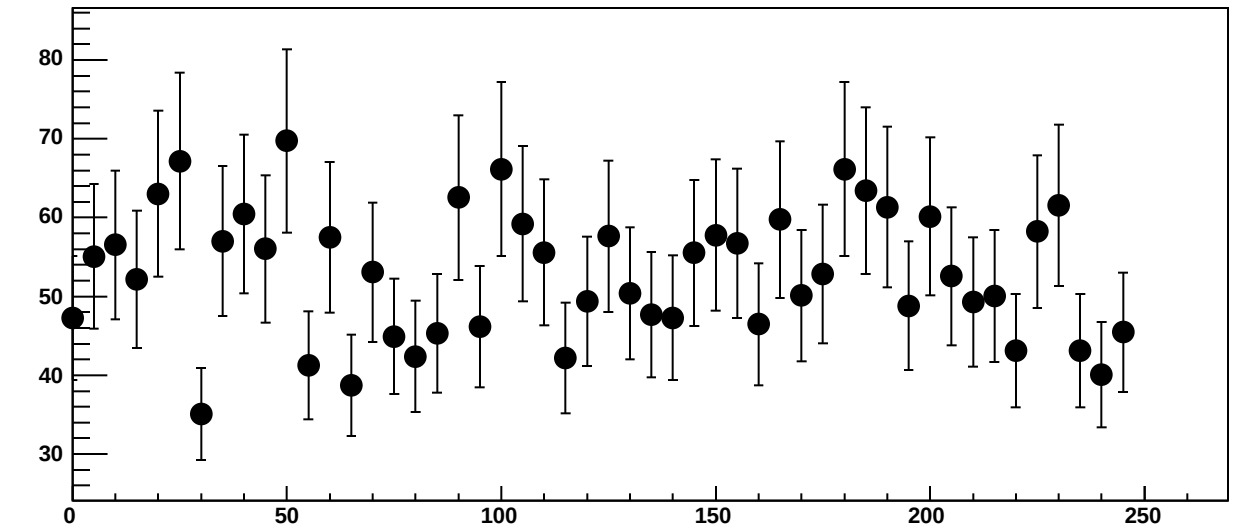
Chip 6, Channel 0, Enable 0!, DAC=1600, ADC Residuals vs Hold



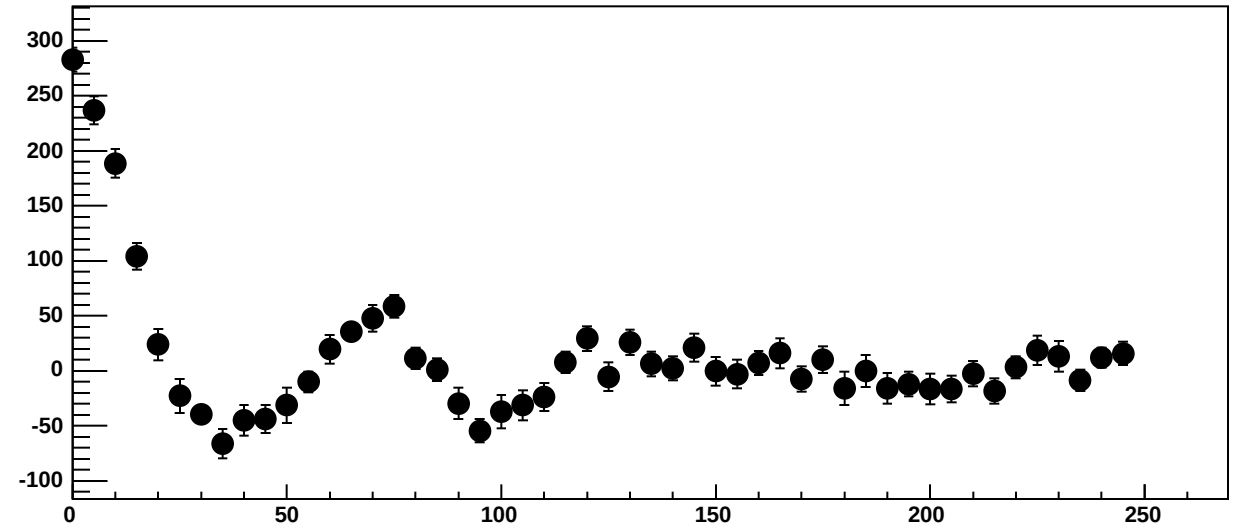
Chip 6, Channel 0, Enable 1, DAC=1600, ADC Mean vs Hold



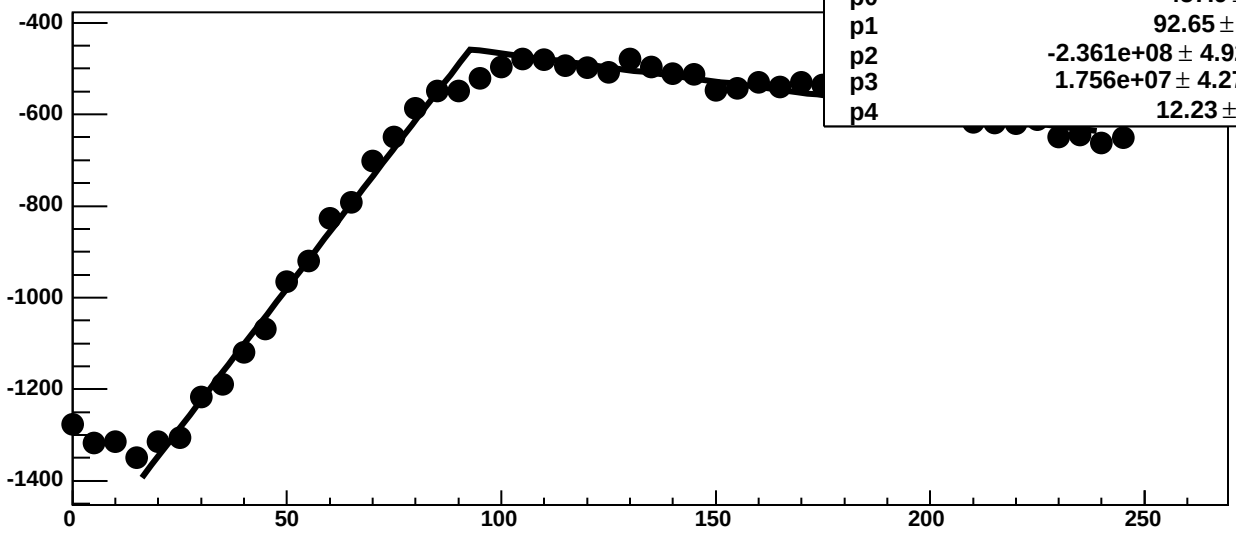
Chip 6, Channel 0, Enable 1, DAC=1600, ADC Noise vs Hold



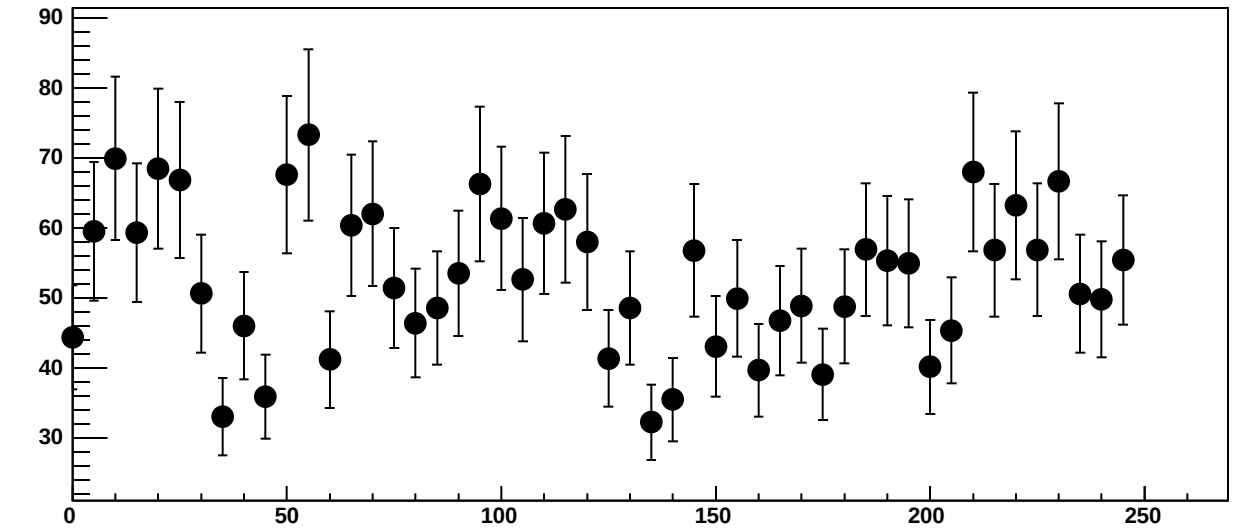
Chip 6, Channel 0, Enable 1, DAC=1600, ADC Residuals vs Hold



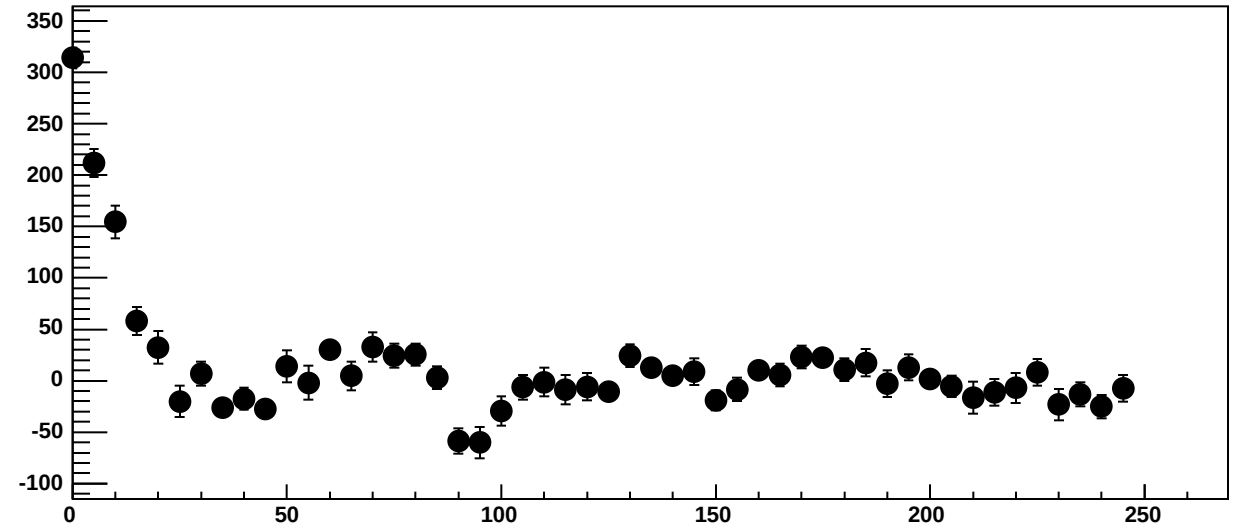
Chip 6, Channel 0, Enable 2, DAC=1600, ADC Mean vs Hold



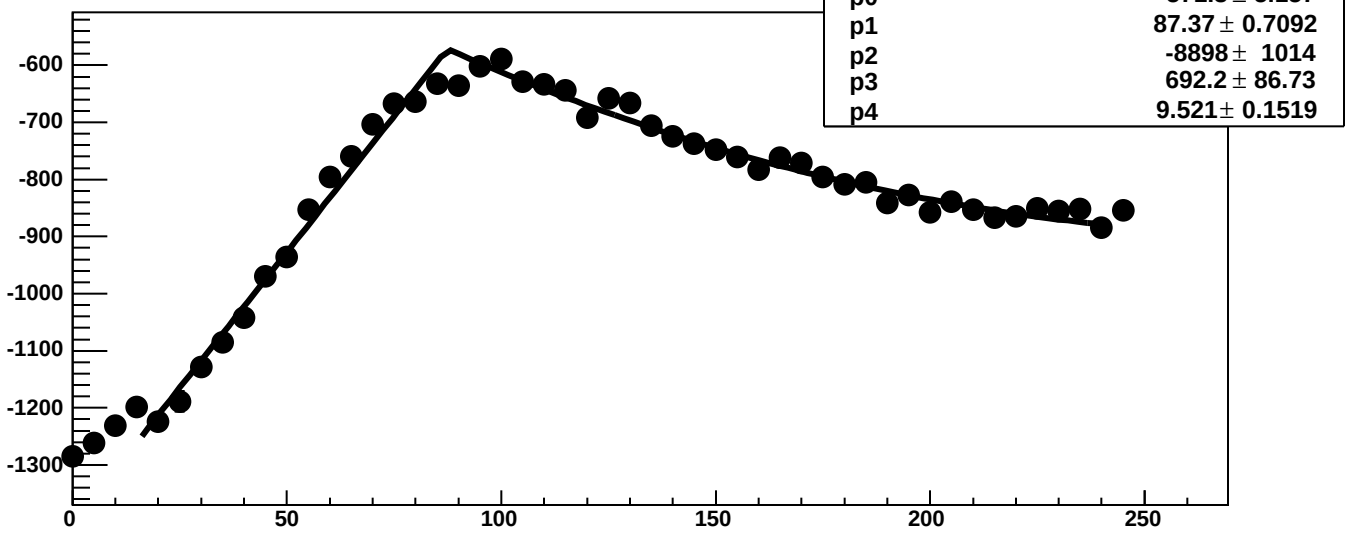
Chip 6, Channel 0, Enable 2, DAC=1600, ADC Noise vs Hold



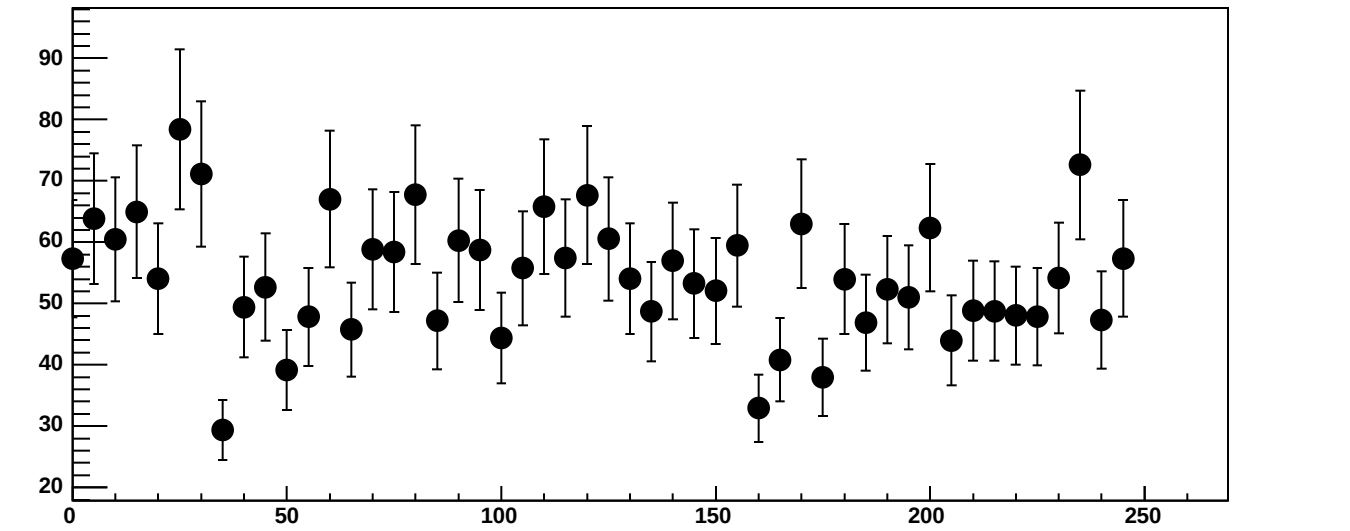
Chip 6, Channel 0, Enable 2, DAC=1600, ADC Residuals vs Hold



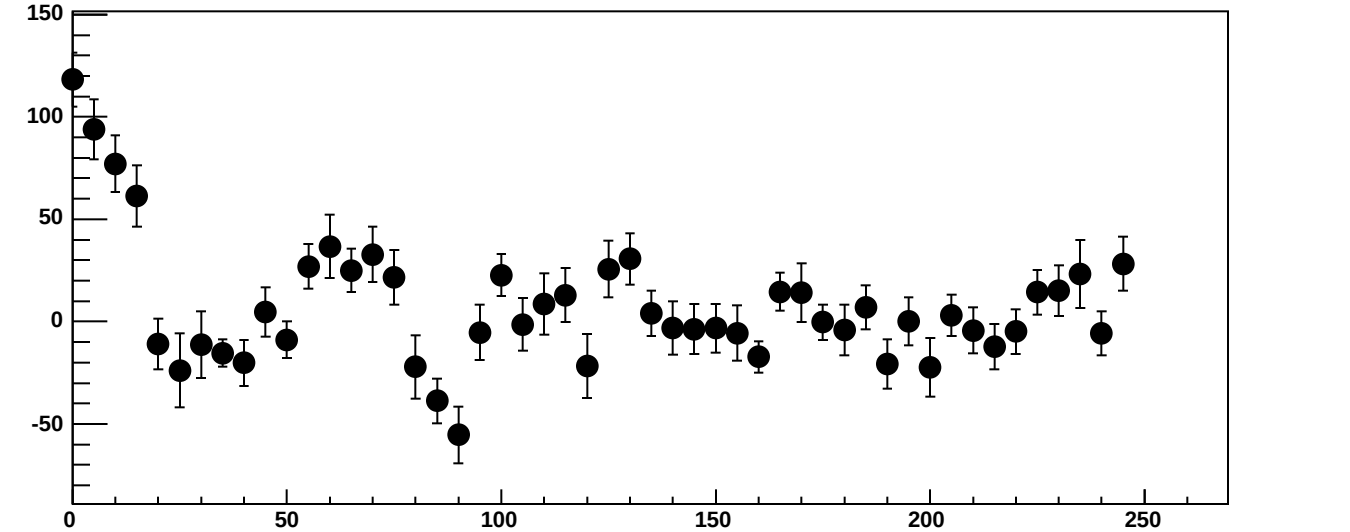
Chip 6, Channel 0, Enable 3, DAC=1600, ADC Mean vs Hold



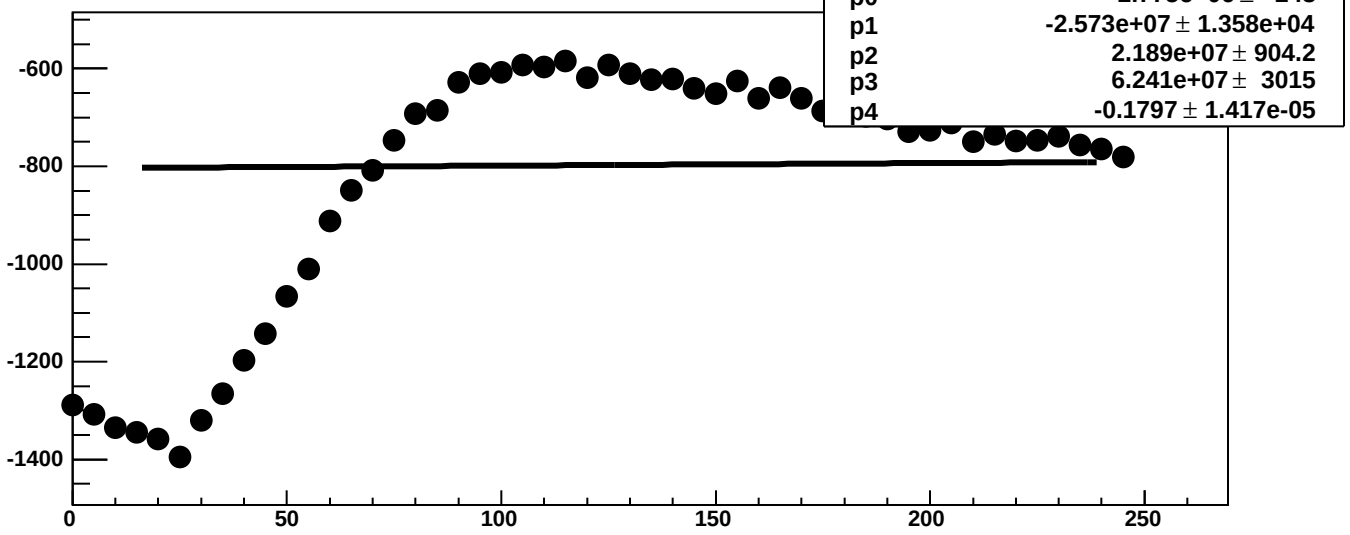
Chip 6, Channel 0, Enable 3, DAC=1600, ADC Noise vs Hold



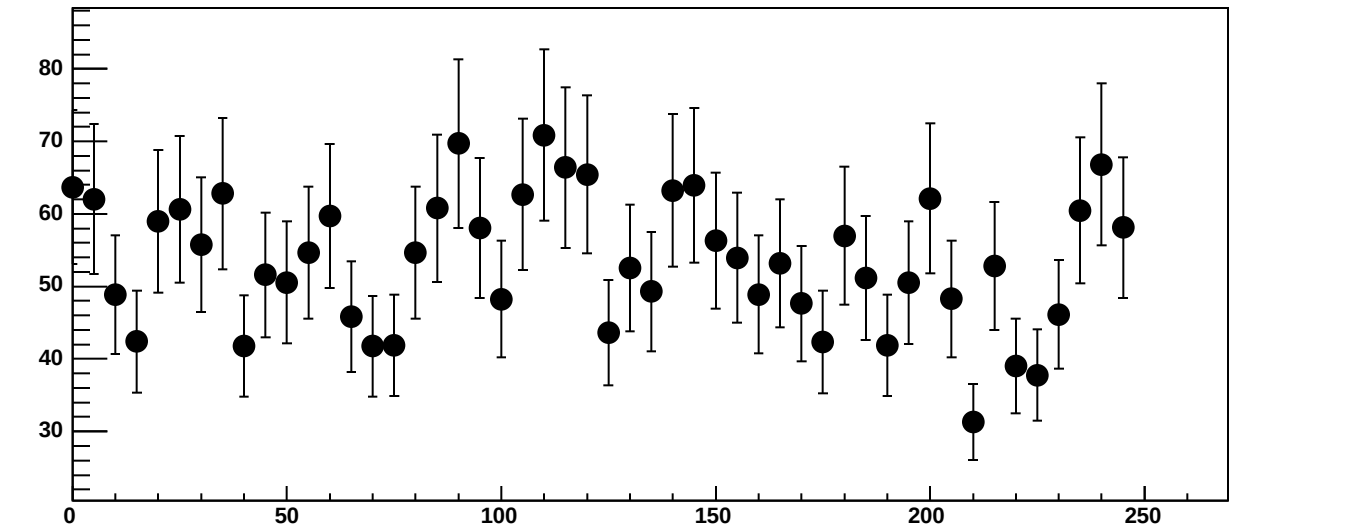
Chip 6, Channel 0, Enable 3, DAC=1600, ADC Residuals vs Hold



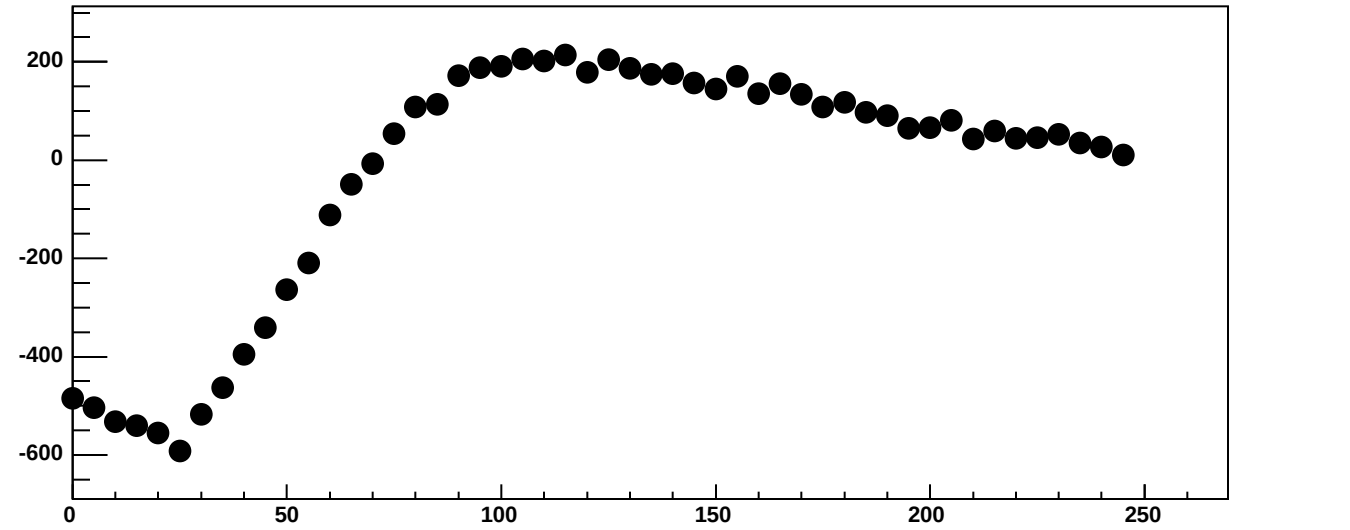
Chip 6, Channel 0, Enable 4, DAC=1600, ADC Mean vs Hold



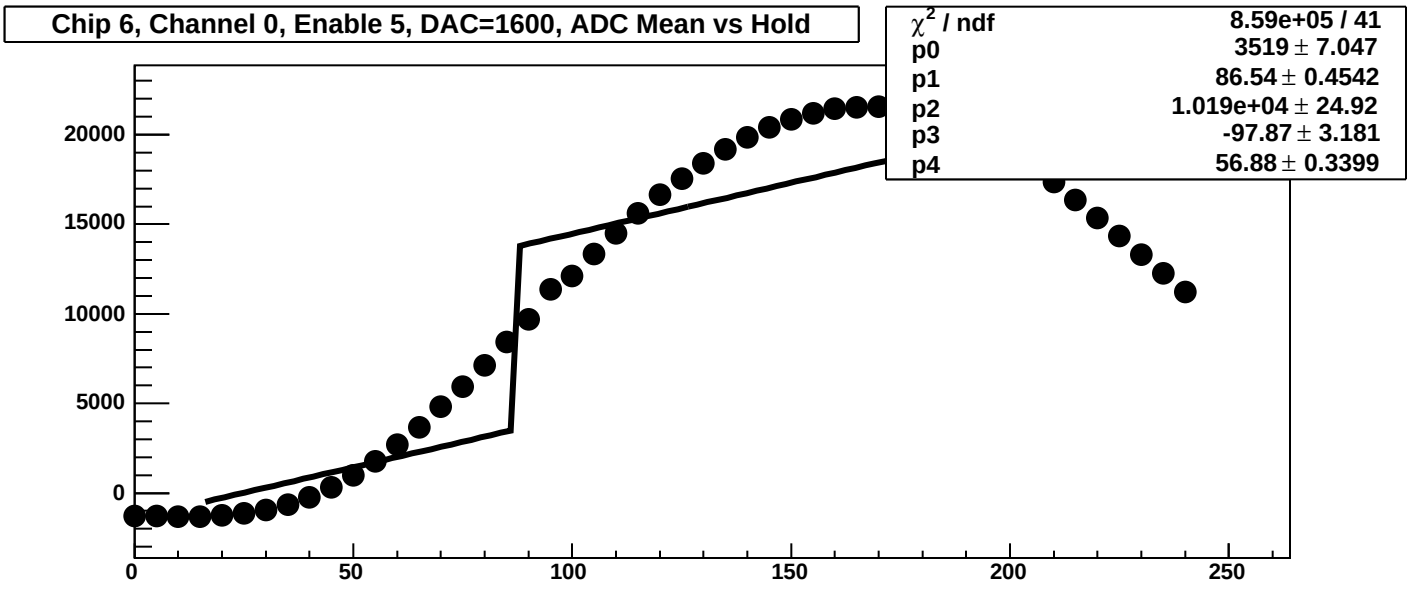
Chip 6, Channel 0, Enable 4, DAC=1600, ADC Noise vs Hold



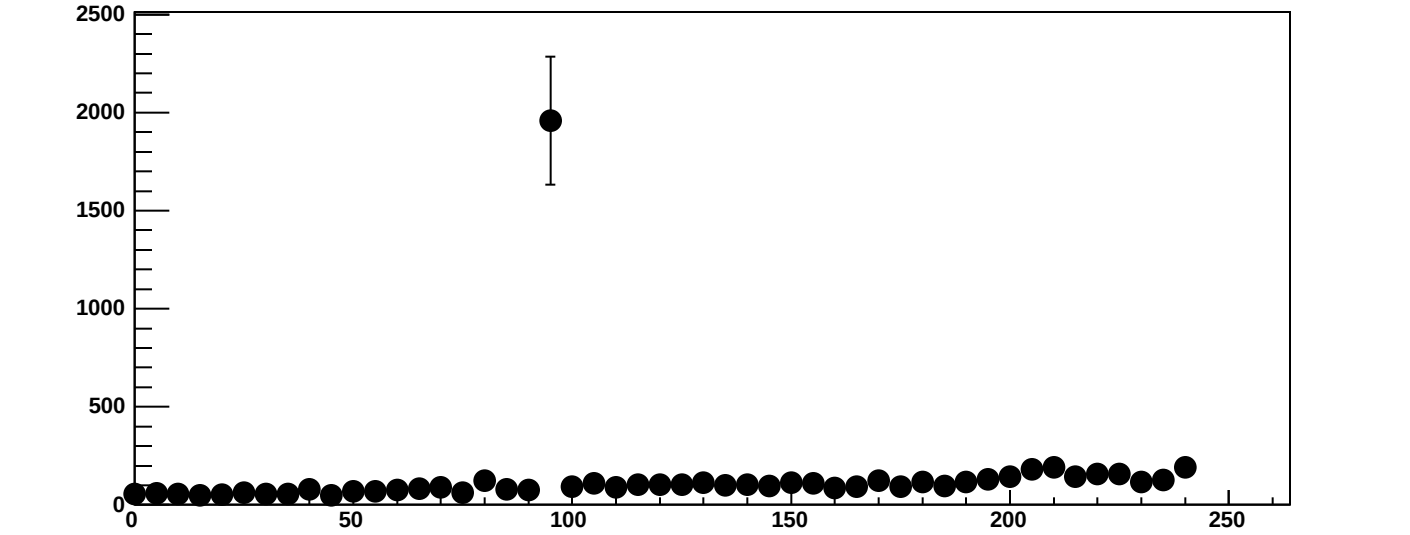
Chip 6, Channel 0, Enable 4, DAC=1600, ADC Residuals vs Hold



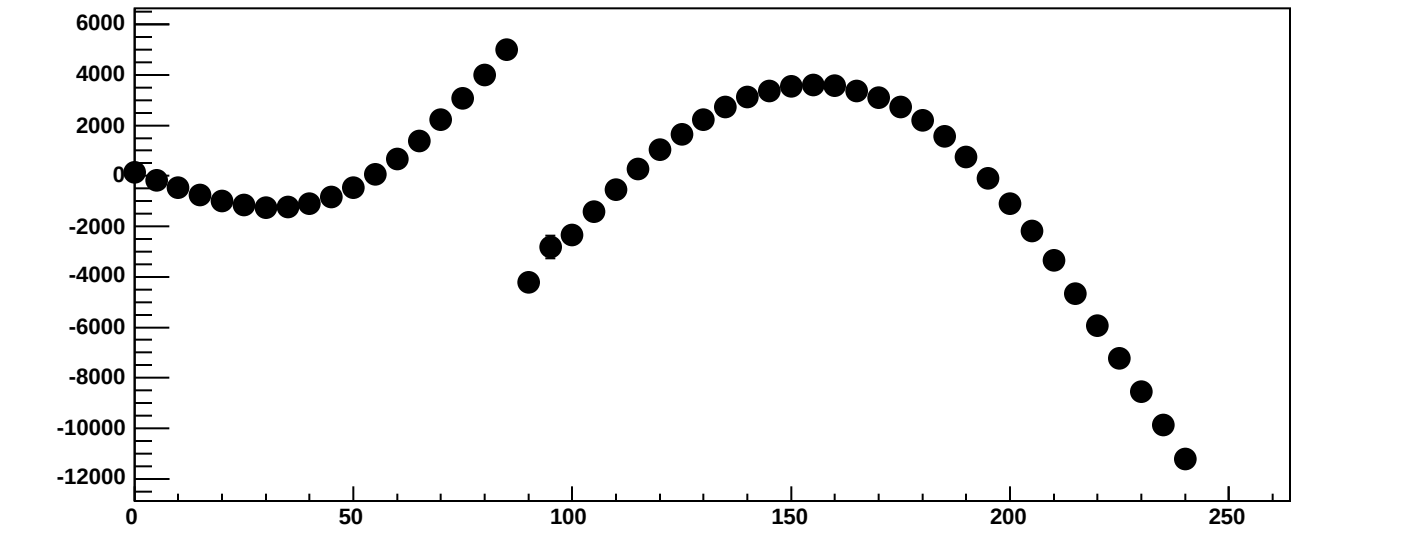
Chip 6, Channel 0, Enable 5, DAC=1600, ADC Mean vs Hold

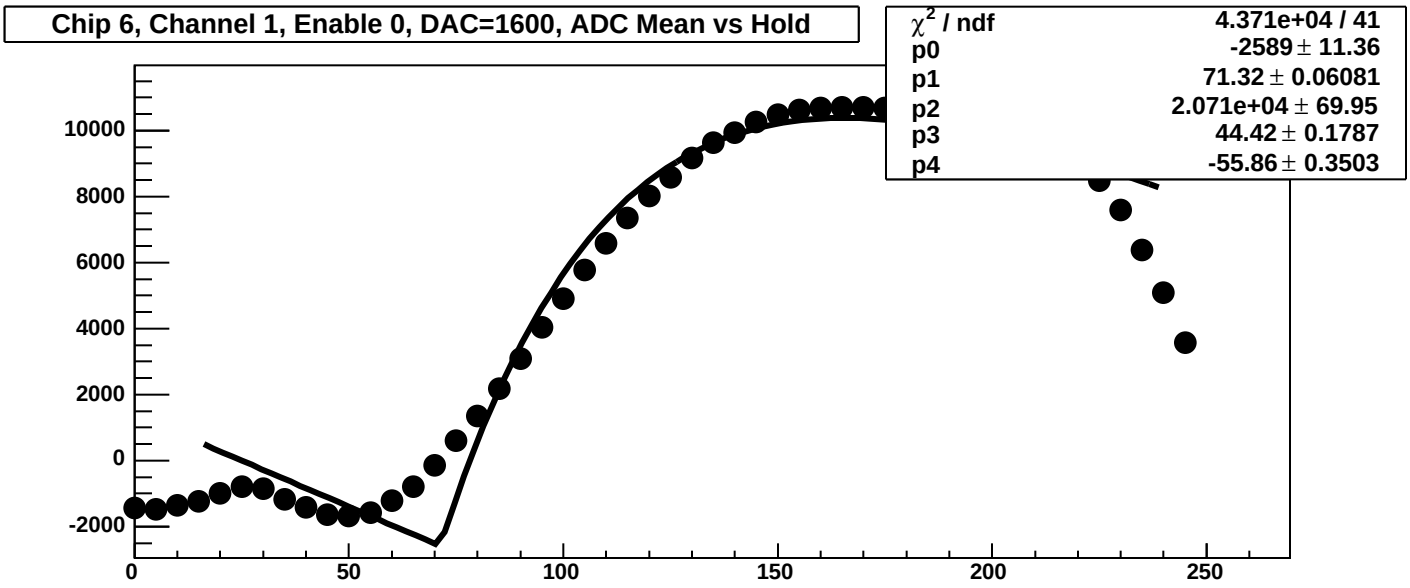


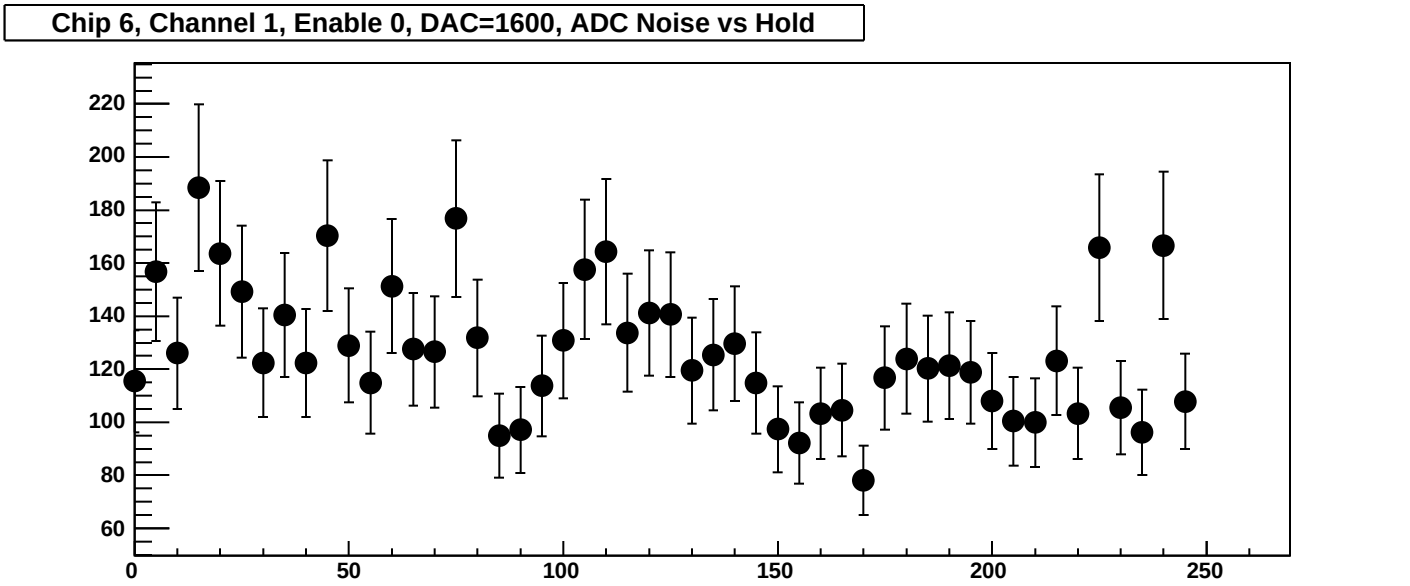
Chip 6, Channel 0, Enable 5, DAC=1600, ADC Noise vs Hold

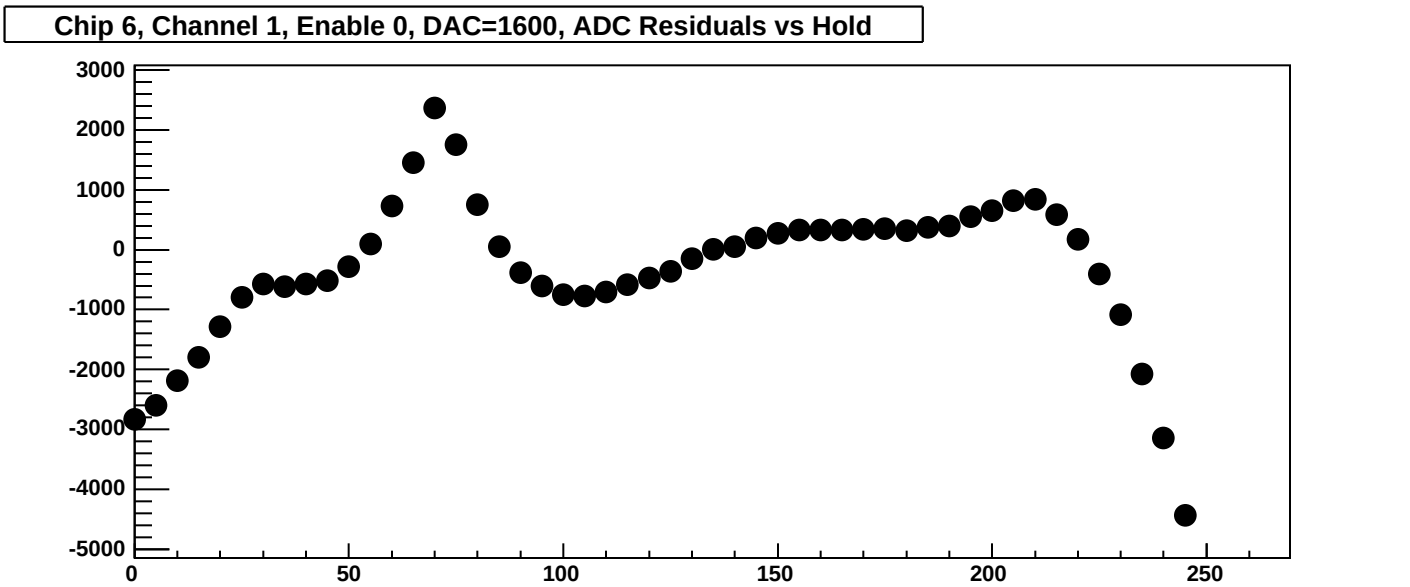


Chip 6, Channel 0, Enable 5, DAC=1600, ADC Residuals vs Hold

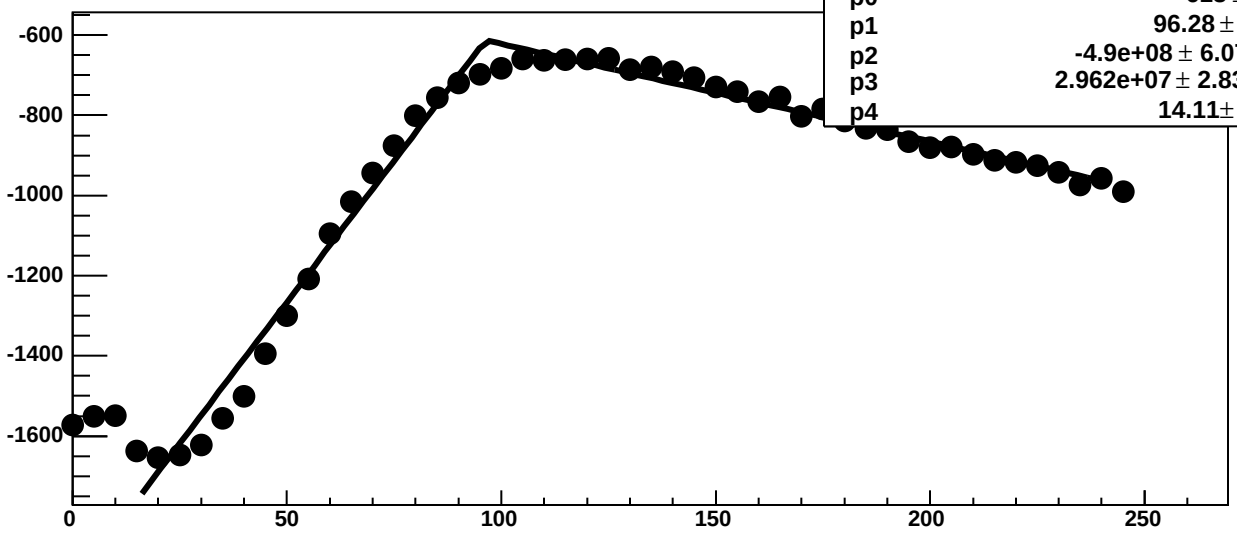



Chip 6, Channel 1, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.371e+04 / 41
p0 -2589 ± 11.36
p1 71.32 ± 0.06081
p2 2.071e+04 ± 69.95
p3 44.42 ± 0.1787
p4 -55.86 ± 0.3503
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

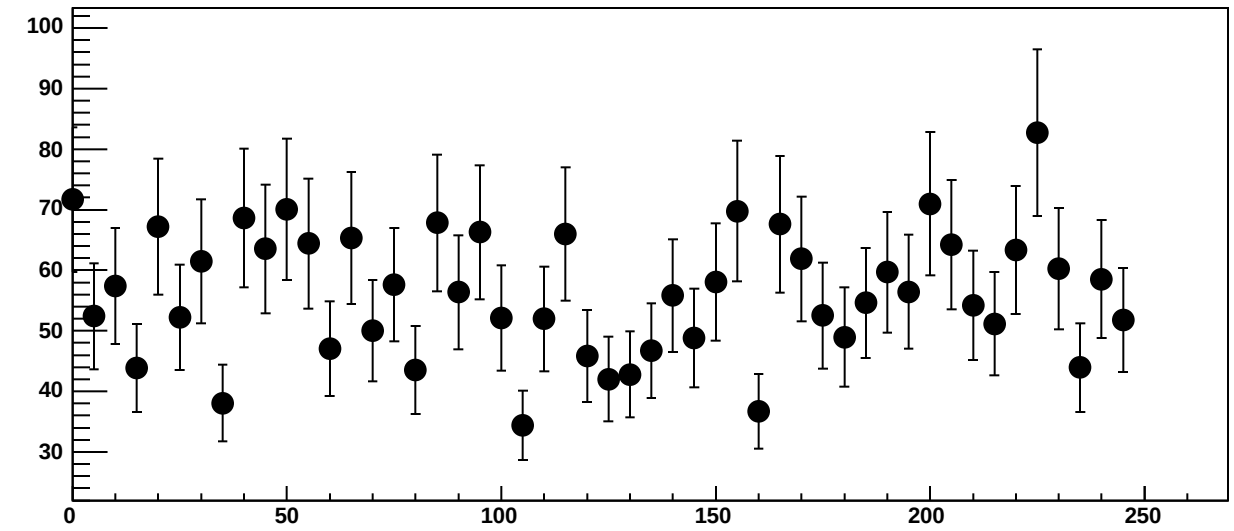

Chip 6, Channel 1, Enable 0, DAC=1600, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
0
50
100
150
200
250


Chip 6, Channel 1, Enable 0, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

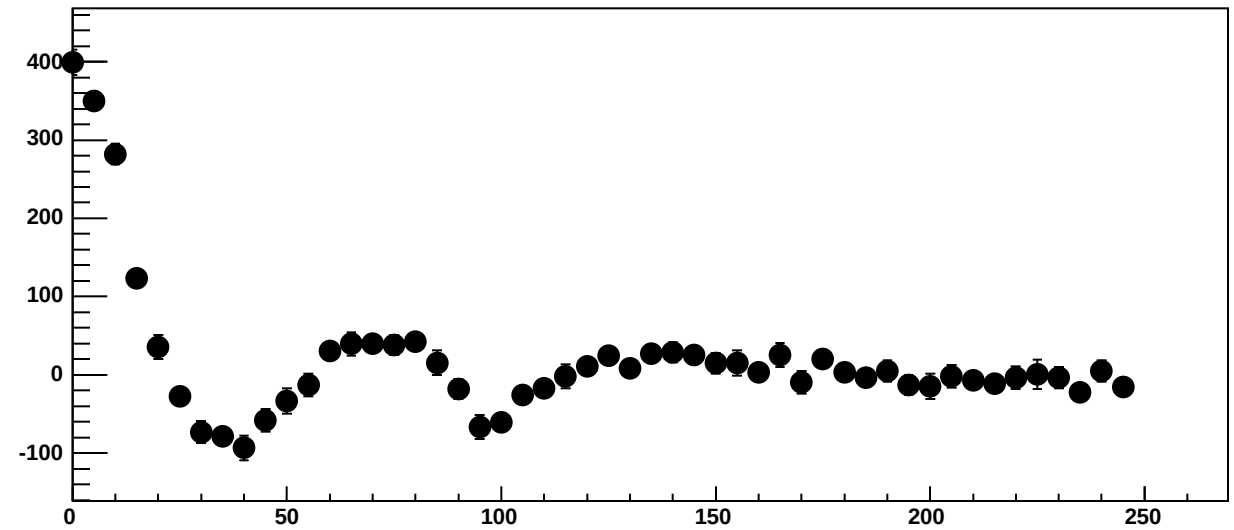
Chip 6, Channel 1, Enable 1, DAC=1600, ADC Mean vs Hold



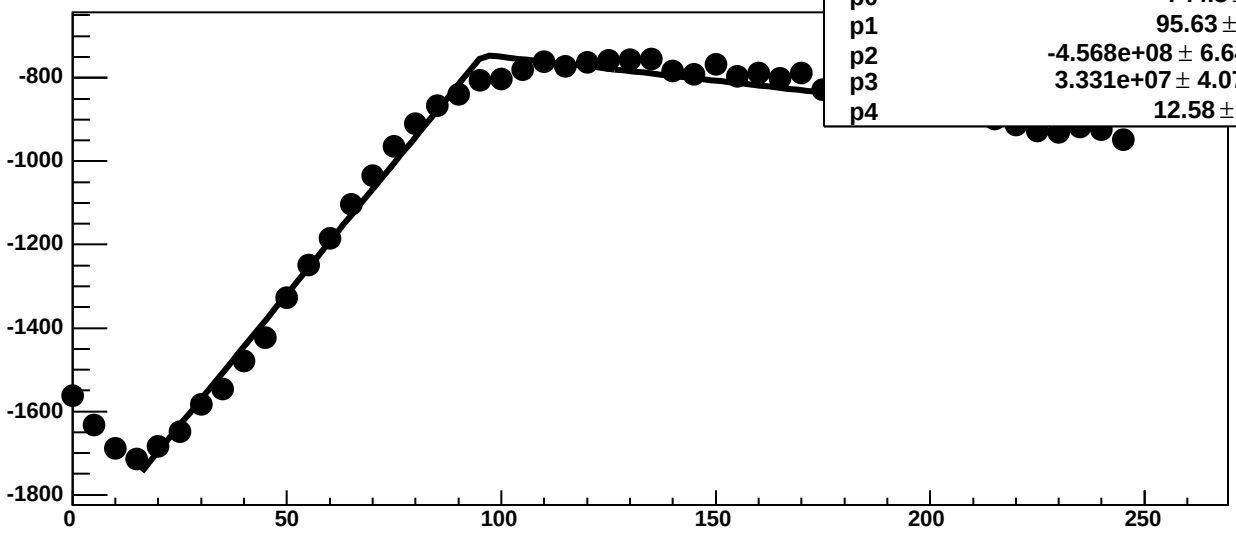
Chip 6, Channel 1, Enable 1, DAC=1600, ADC Noise vs Hold



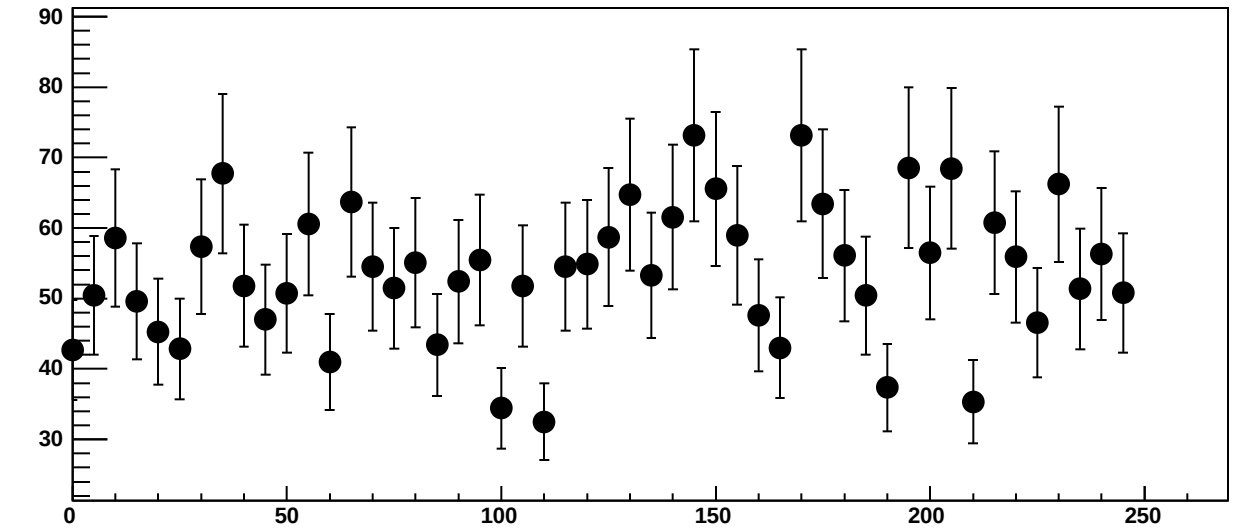
Chip 6, Channel 1, Enable 1, DAC=1600, ADC Residuals vs Hold



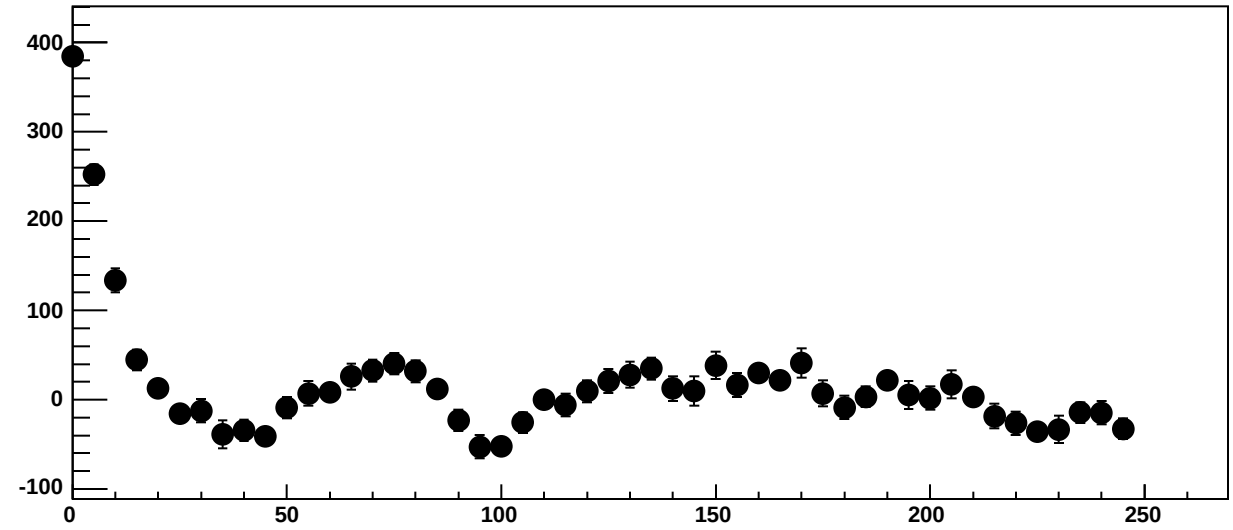
Chip 6, Channel 1, Enable 2, DAC=1600, ADC Mean vs Hold



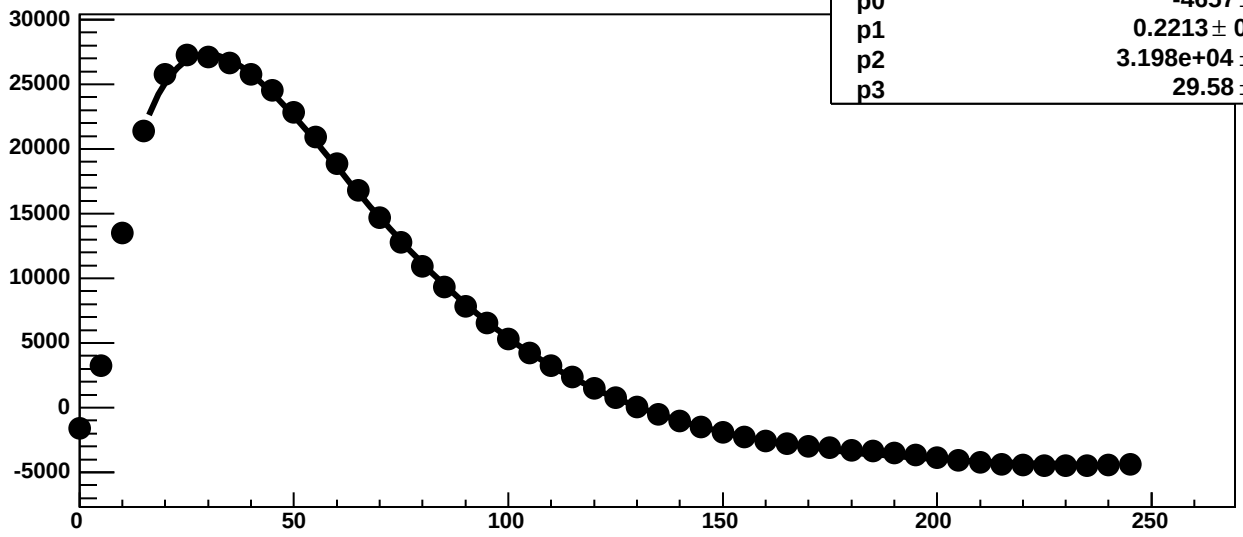
Chip 6, Channel 1, Enable 2, DAC=1600, ADC Noise vs Hold



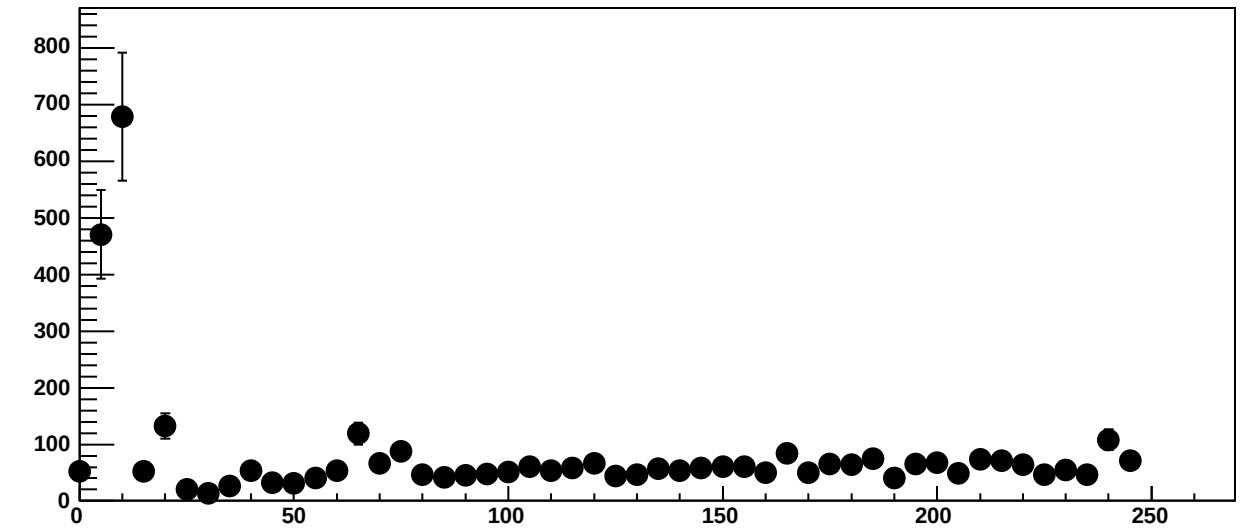
Chip 6, Channel 1, Enable 2, DAC=1600, ADC Residuals vs Hold



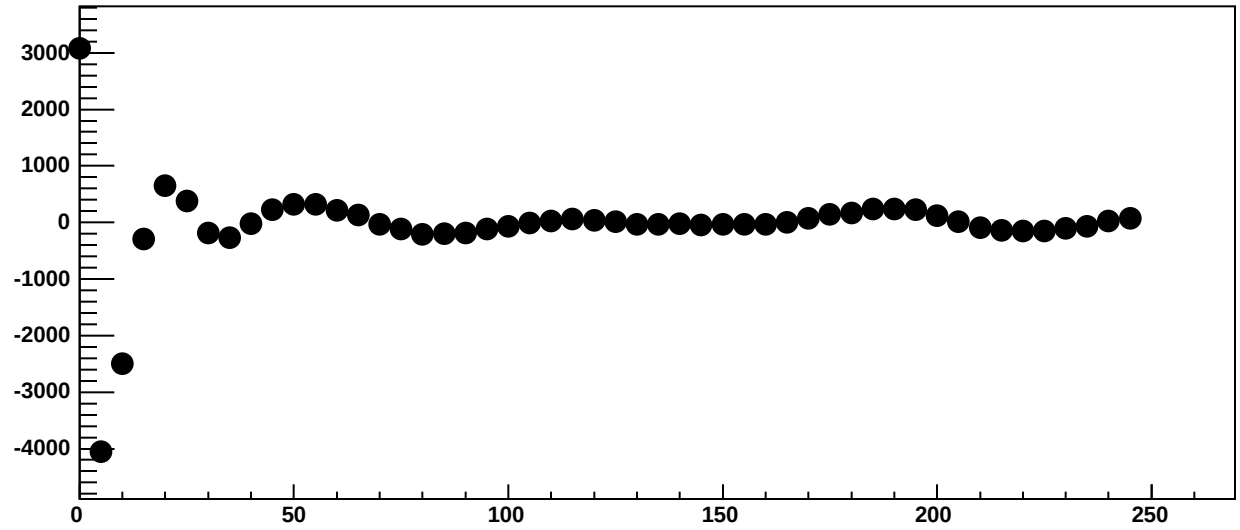
Chip 6, Channel 1, Enable 3!, DAC=1600, ADC Mean vs Hold

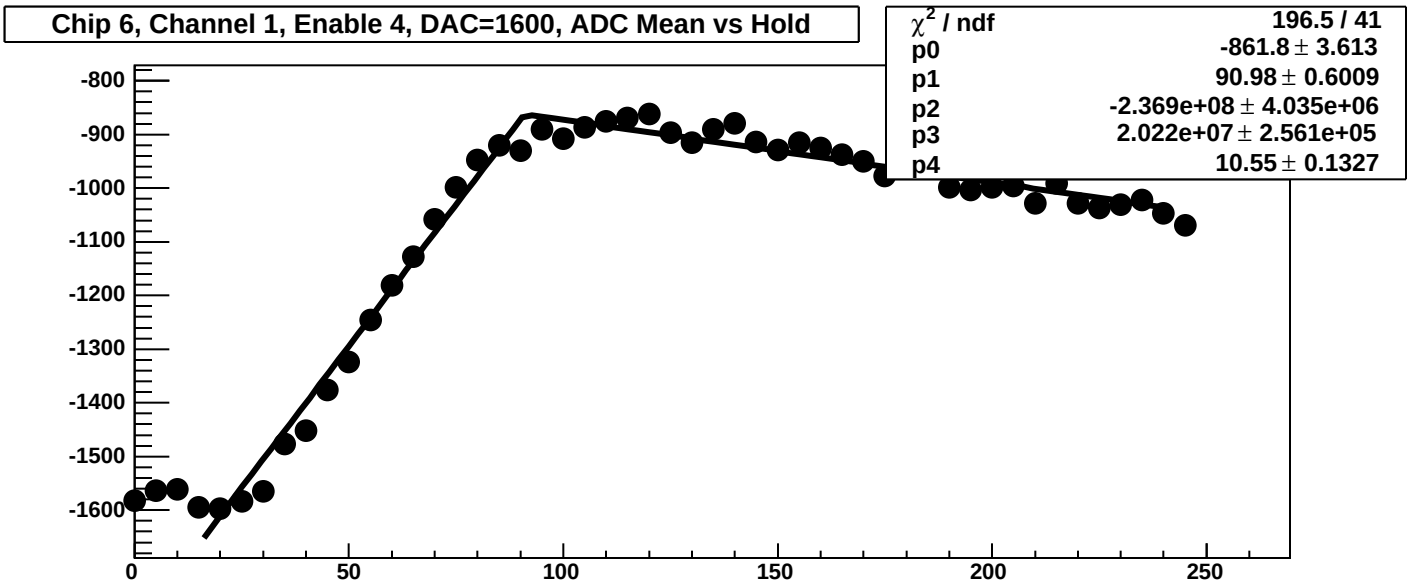


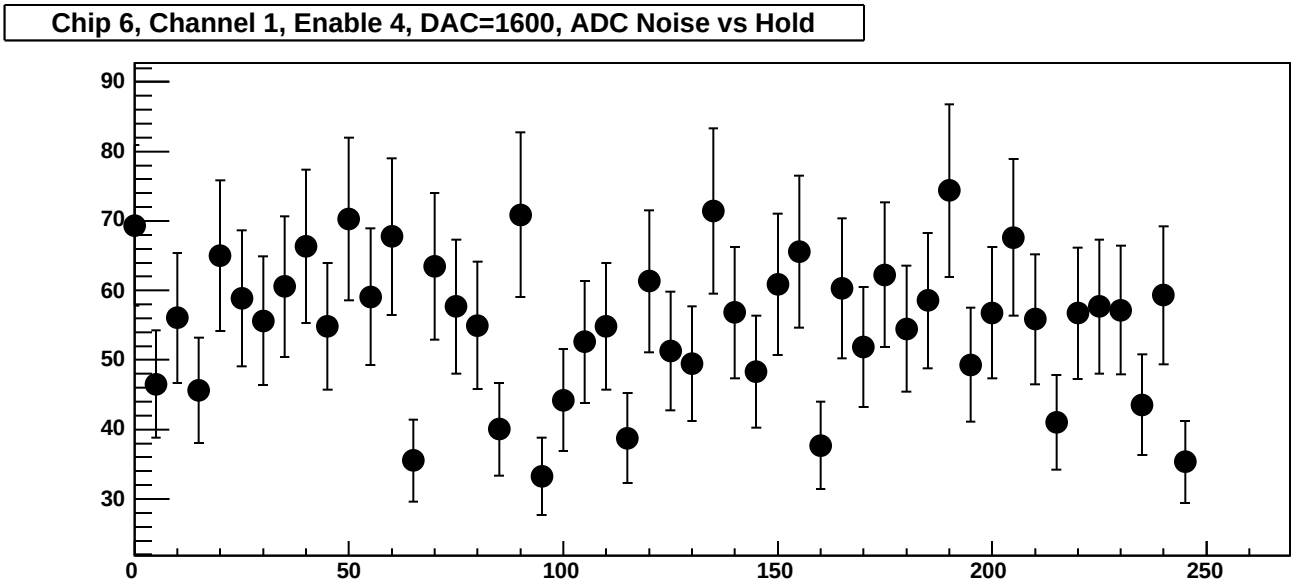
Chip 6, Channel 1, Enable 3!, DAC=1600, ADC Noise vs Hold

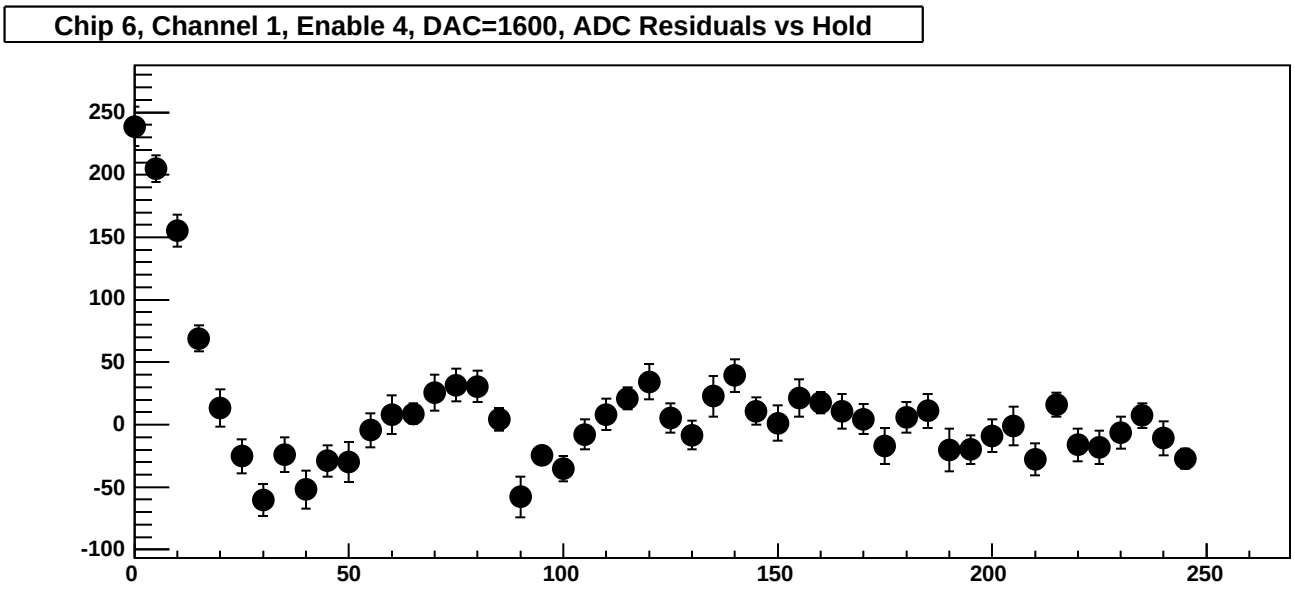


Chip 6, Channel 1, Enable 3!, DAC=1600, ADC Residuals vs Hold

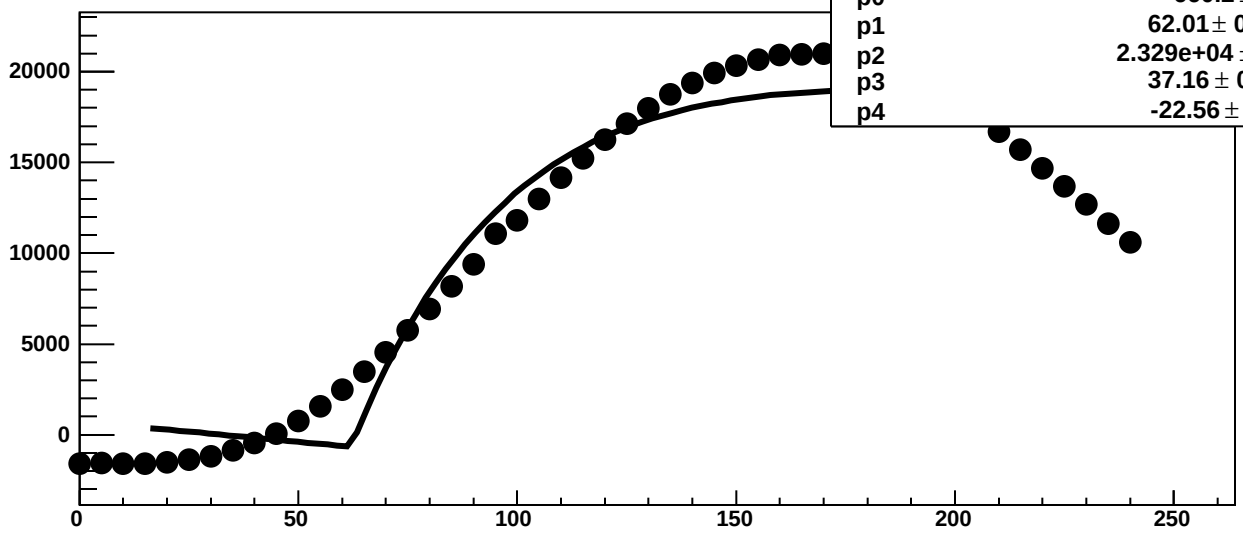



Chip 6, Channel 1, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 196.5 / 41
p0 -861.8 ± 3.613
p1 90.98 ± 0.6009
p2 -2.369e+08 ± 4.035e+06
p3 2.022e+07 ± 2.561e+05
p4 10.55 ± 0.1327
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
50
100
150
200
250

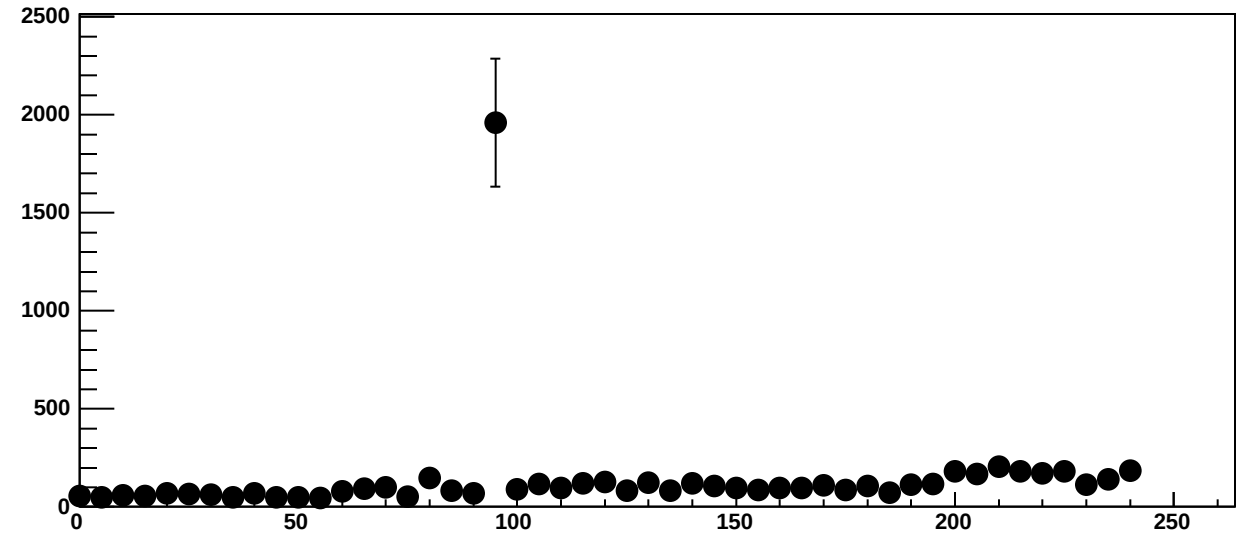

Chip 6, Channel 1, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 6, Channel 1, Enable 4, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

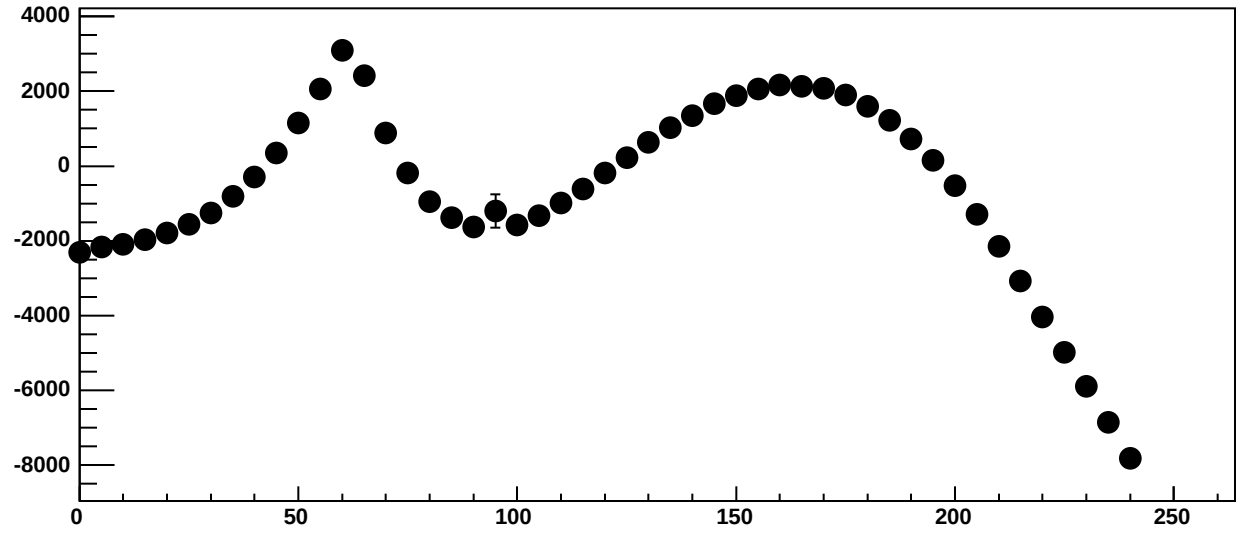
Chip 6, Channel 1, Enable 5, DAC=1600, ADC Mean vs Hold



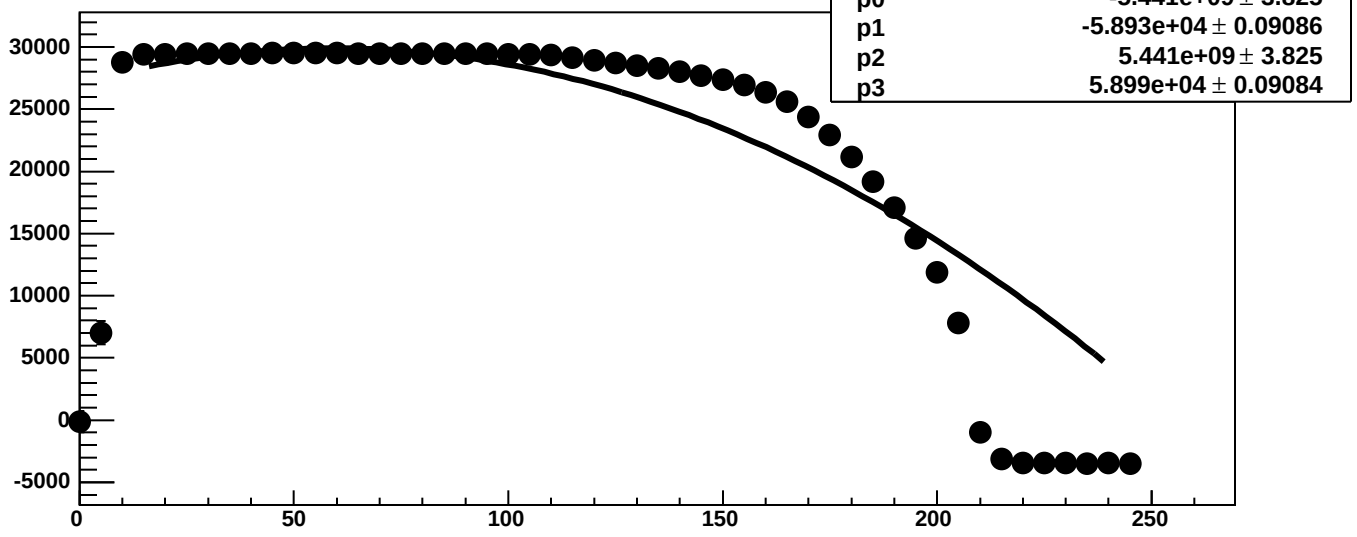
Chip 6, Channel 1, Enable 5, DAC=1600, ADC Noise vs Hold



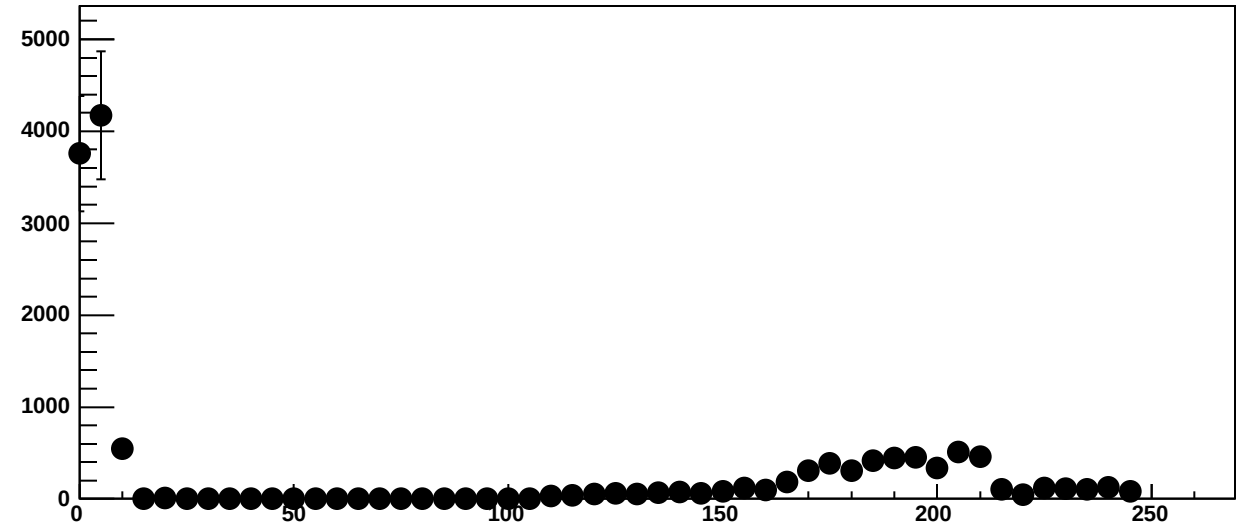
Chip 6, Channel 1, Enable 5, DAC=1600, ADC Residuals vs Hold



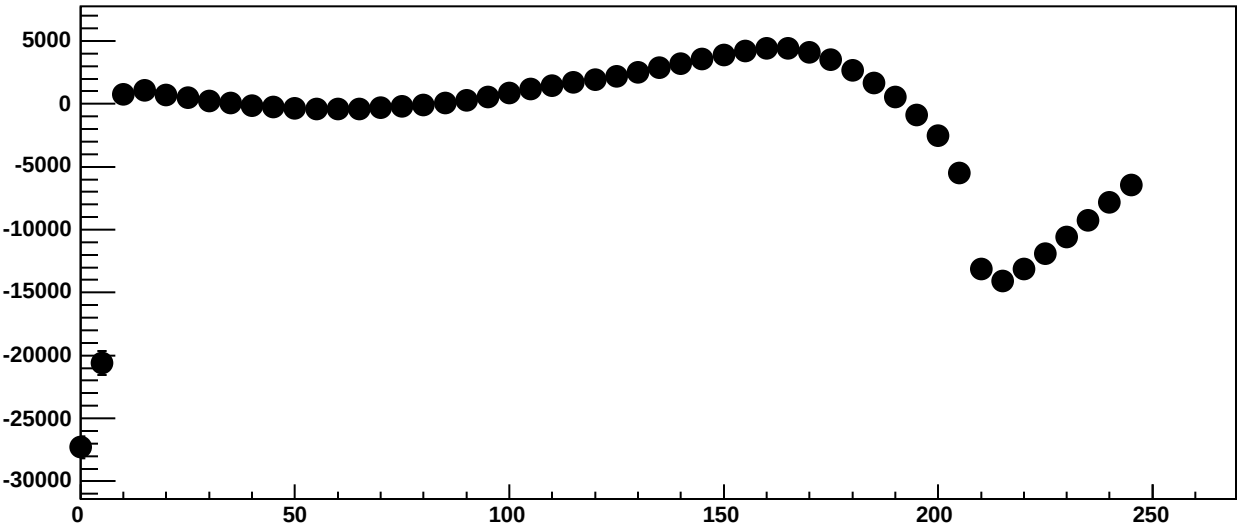
Chip 6, Channel 2, Enable 0!, DAC=1600, ADC Mean vs Hold



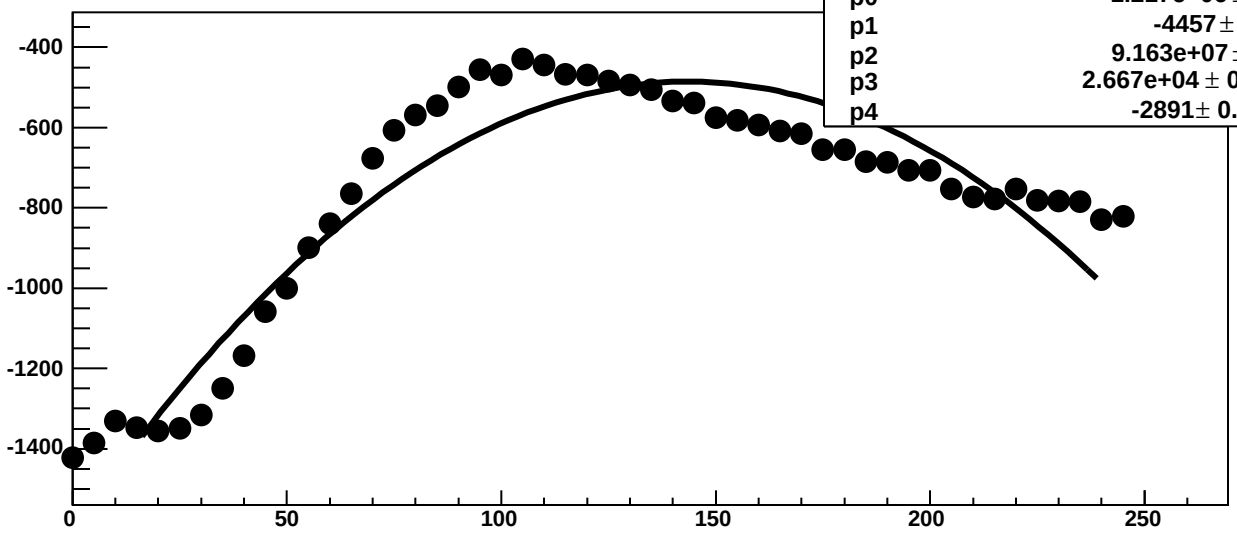
Chip 6, Channel 2, Enable 0!, DAC=1600, ADC Noise vs Hold



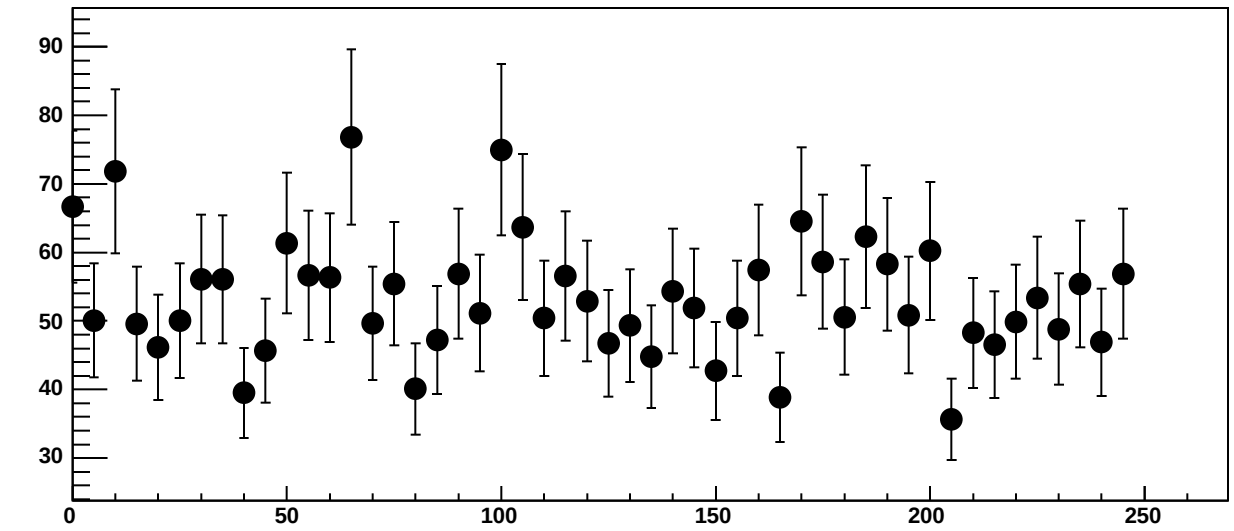
Chip 6, Channel 2, Enable 0!, DAC=1600, ADC Residuals vs Hold



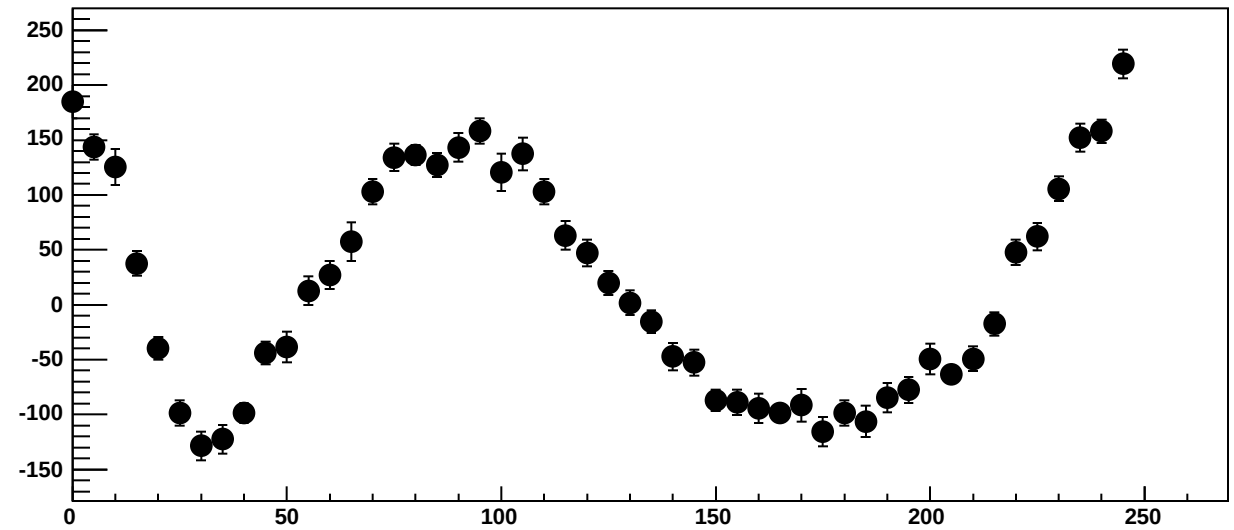
Chip 6, Channel 2, Enable 1, DAC=1600, ADC Mean vs Hold



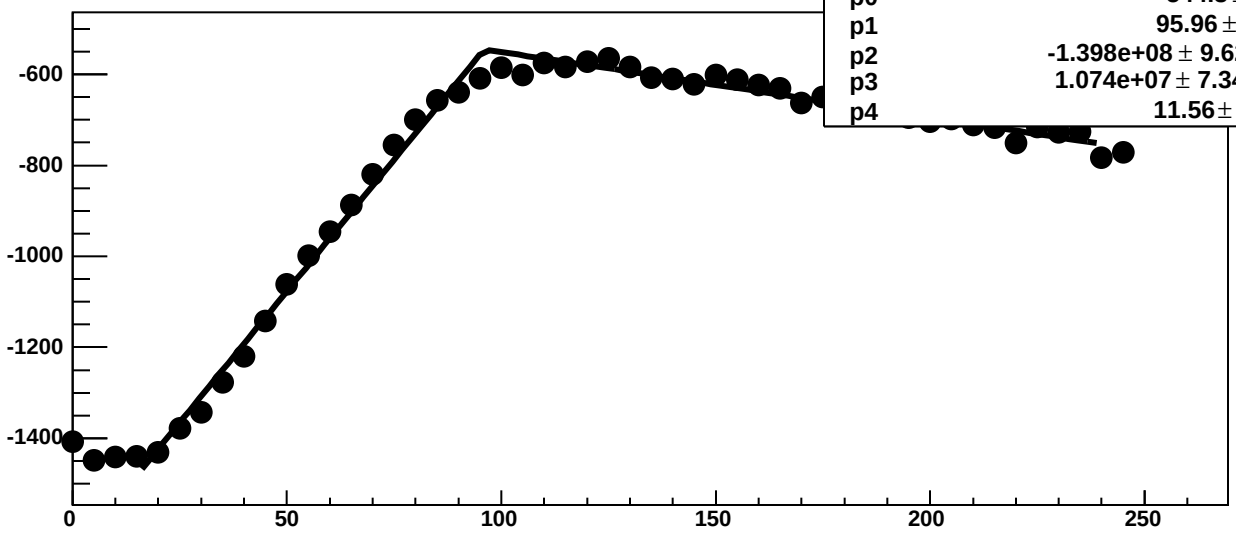
Chip 6, Channel 2, Enable 1, DAC=1600, ADC Noise vs Hold



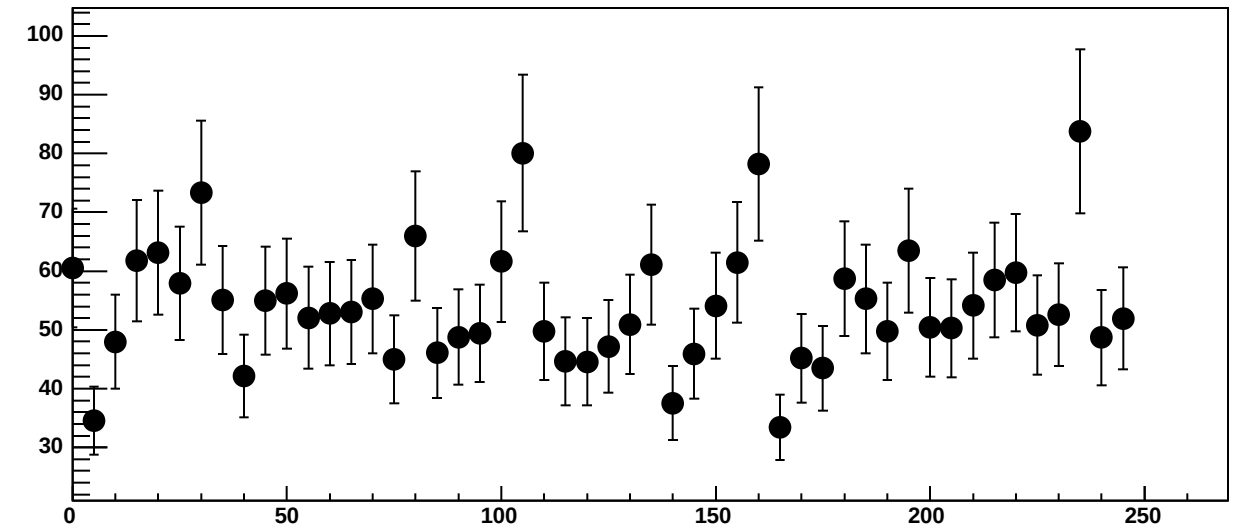
Chip 6, Channel 2, Enable 1, DAC=1600, ADC Residuals vs Hold



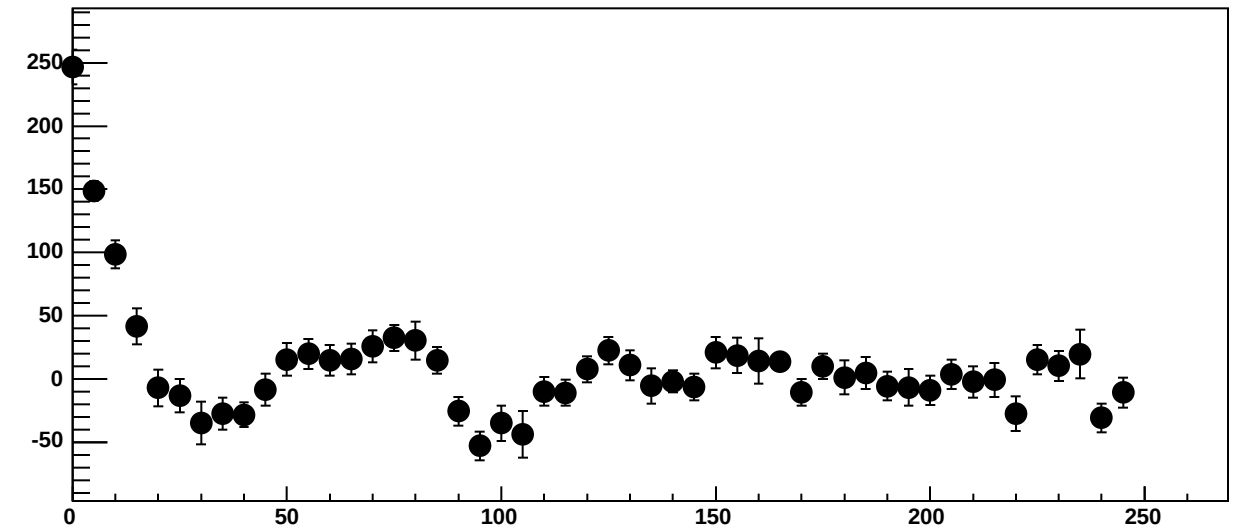
Chip 6, Channel 2, Enable 2, DAC=1600, ADC Mean vs Hold



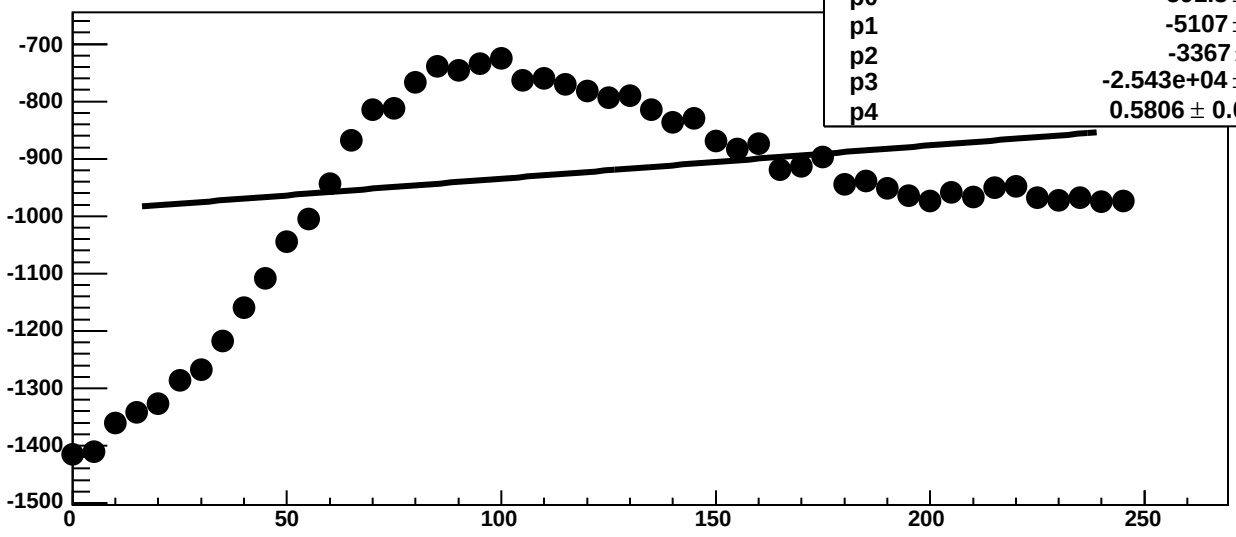
Chip 6, Channel 2, Enable 2, DAC=1600, ADC Noise vs Hold



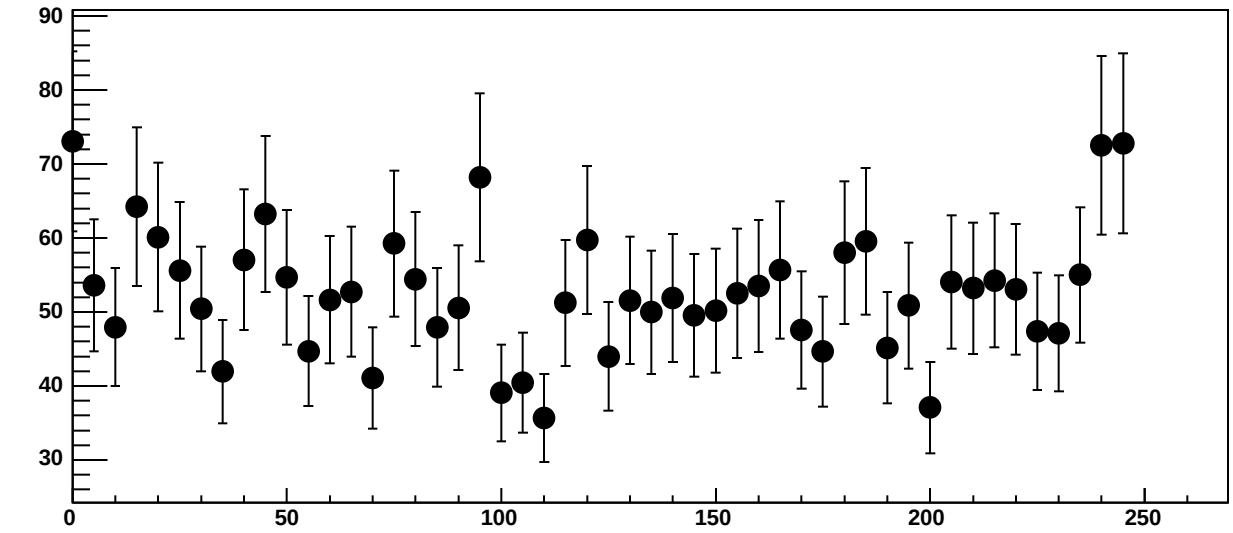
Chip 6, Channel 2, Enable 2, DAC=1600, ADC Residuals vs Hold



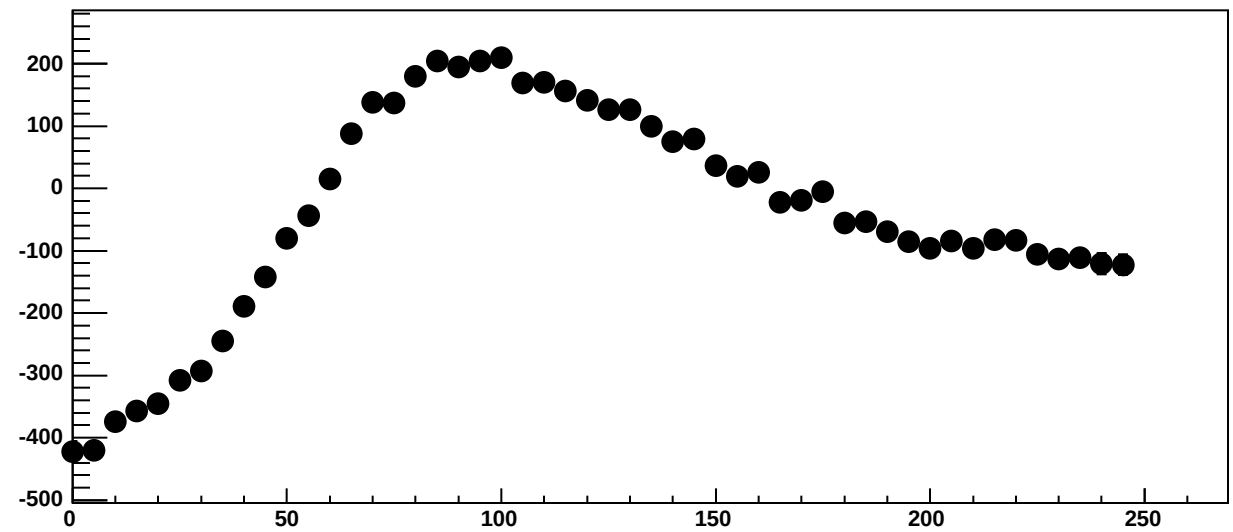
Chip 6, Channel 2, Enable 3, DAC=1600, ADC Mean vs Hold

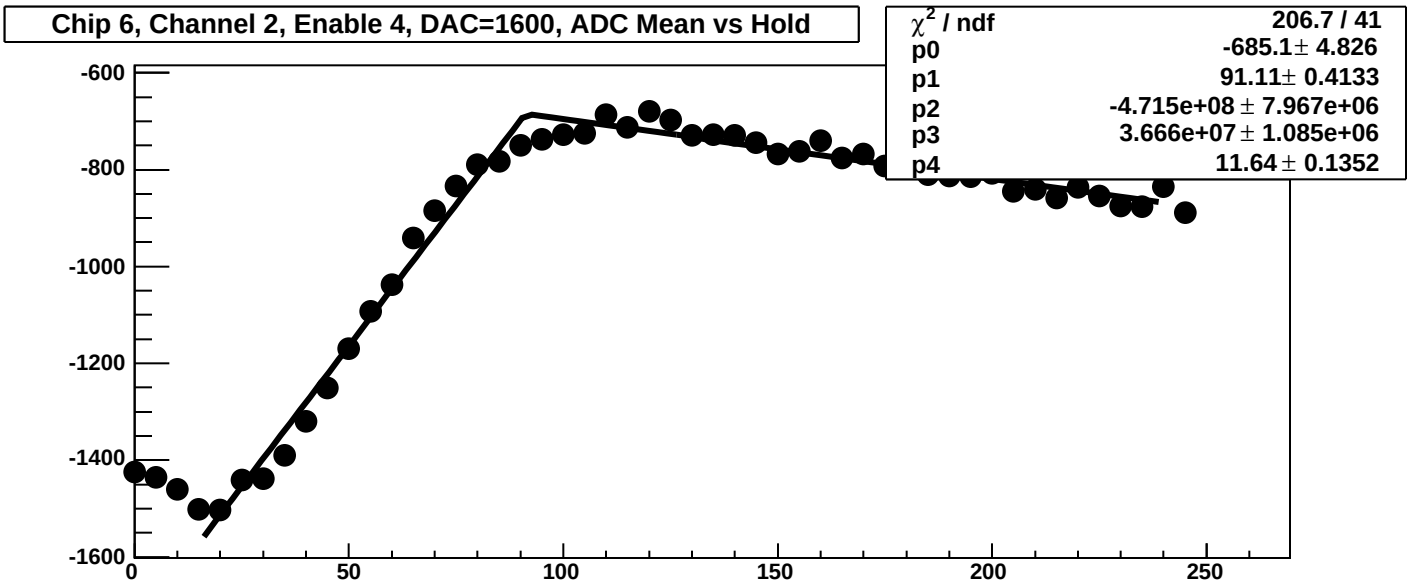


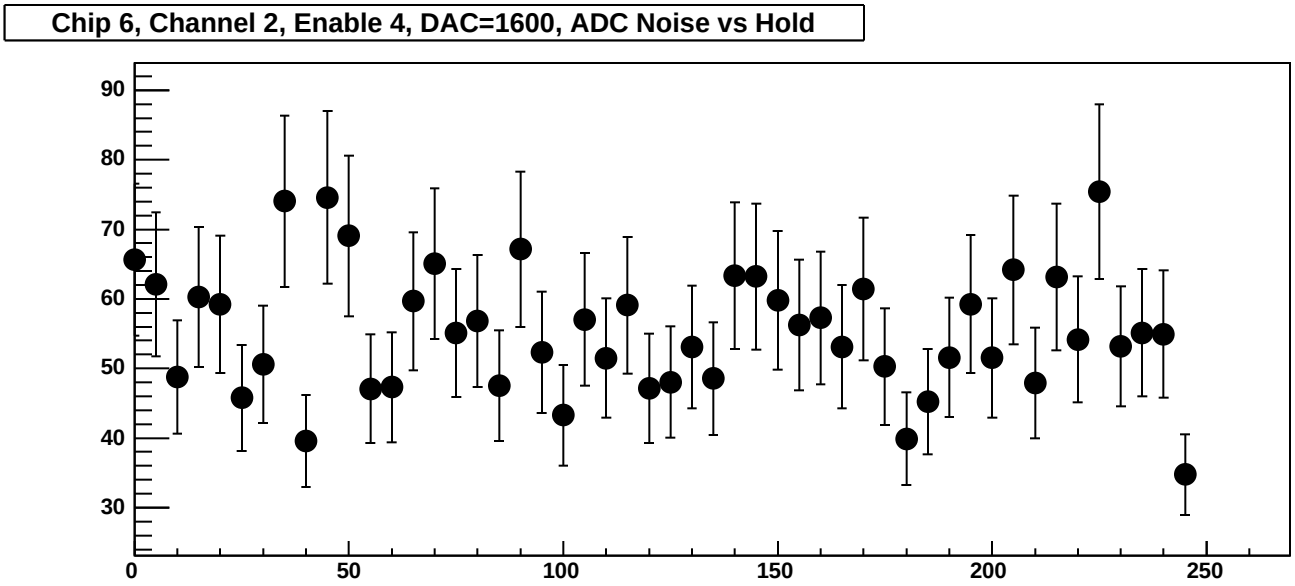
Chip 6, Channel 2, Enable 3, DAC=1600, ADC Noise vs Hold

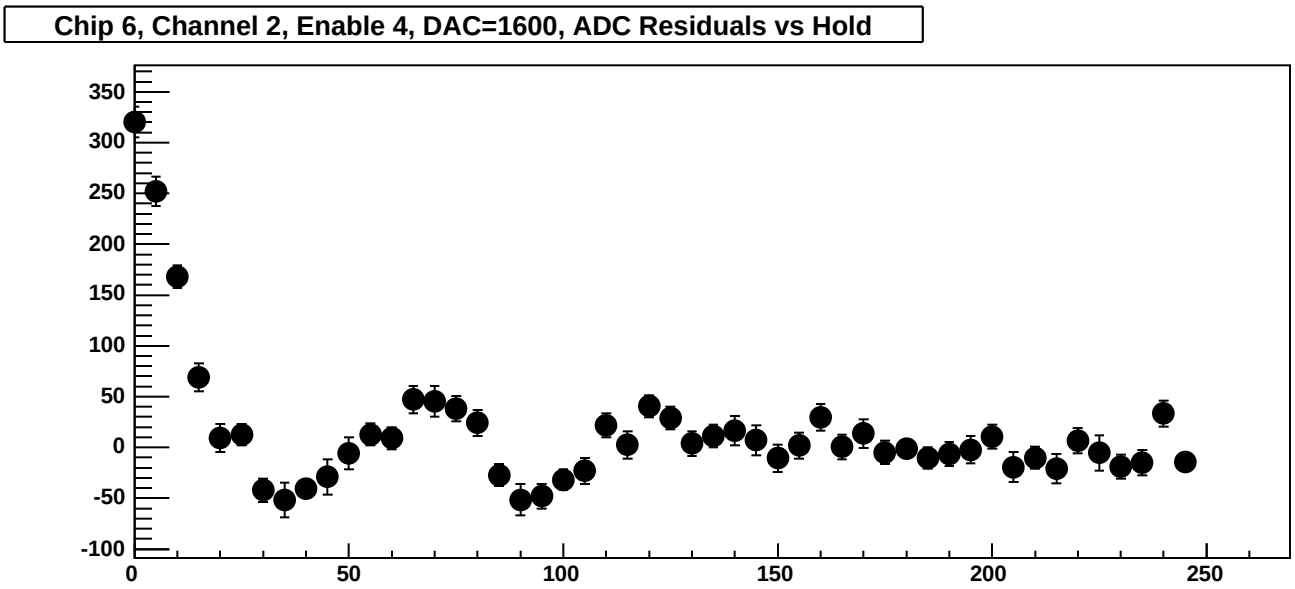


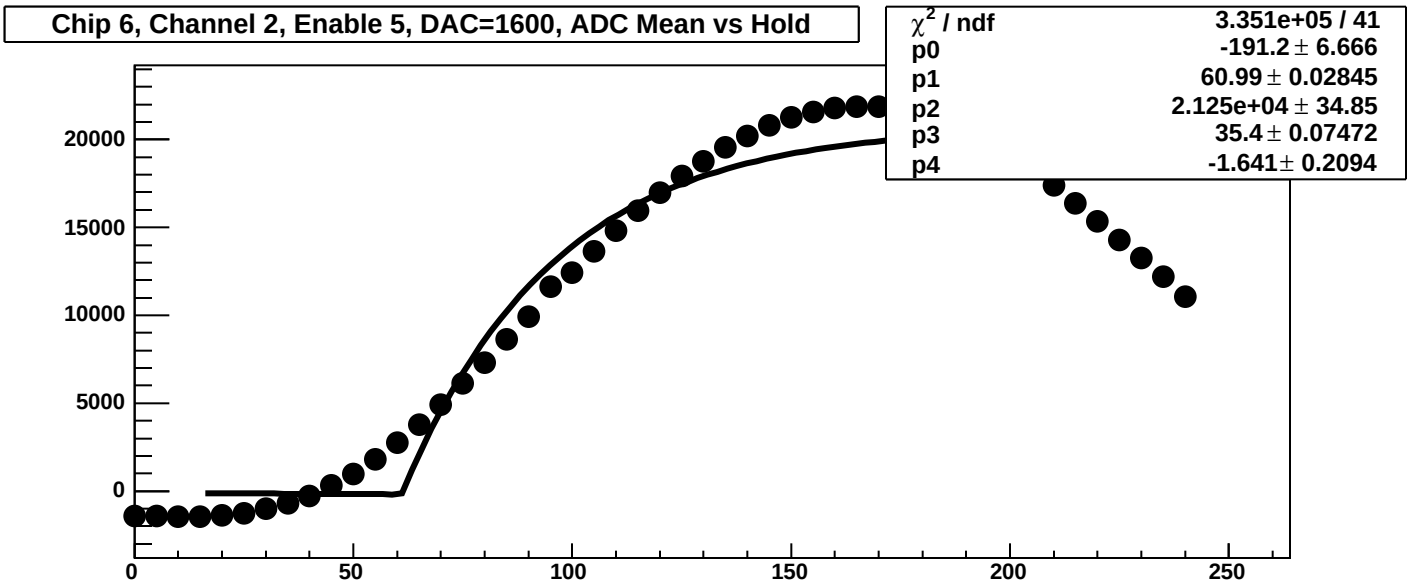
Chip 6, Channel 2, Enable 3, DAC=1600, ADC Residuals vs Hold

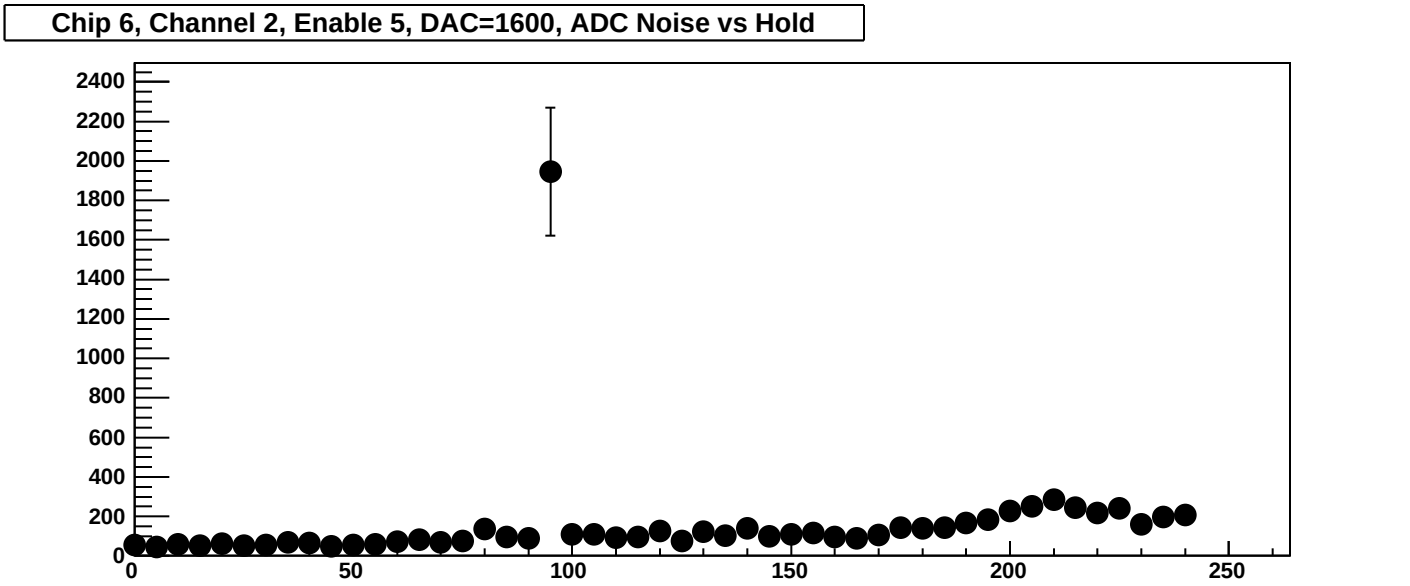


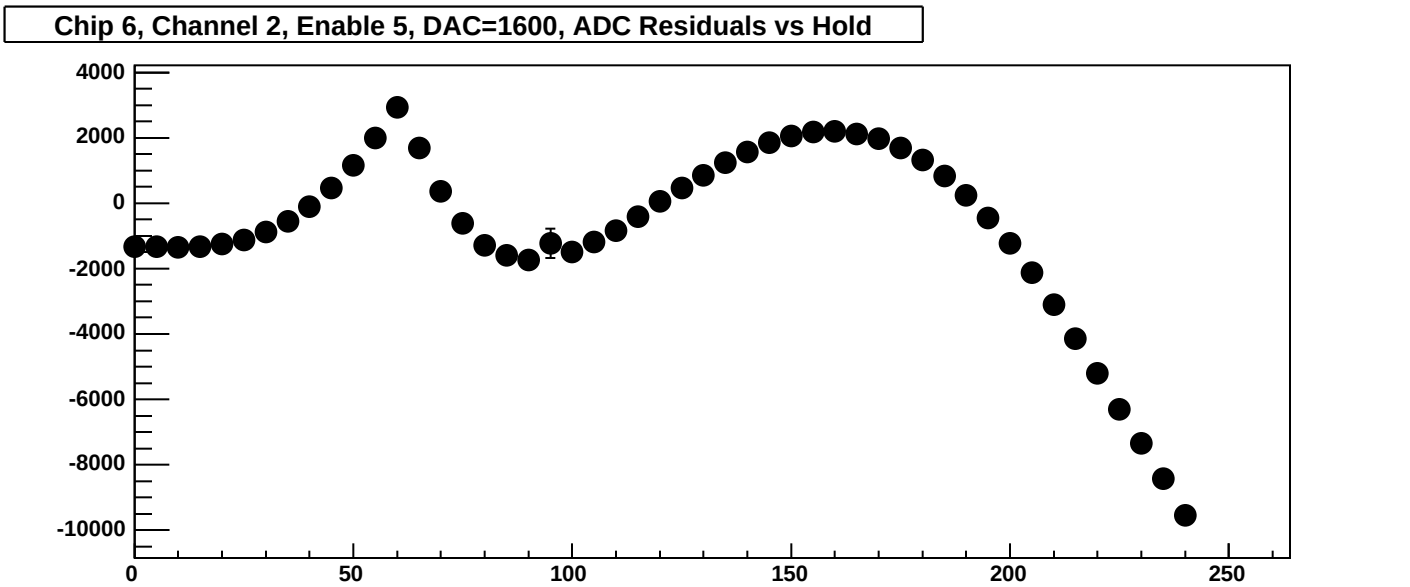

Chip 6, Channel 2, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 206.7 / 41
p0 -685.1 ± 4.826
p1 91.11 ± 0.4133
p2 -4.715e+08 ± 7.967e+06
p3 3.666e+07 ± 1.085e+06
p4 11.64 ± 0.1352
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250


Chip 6, Channel 2, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

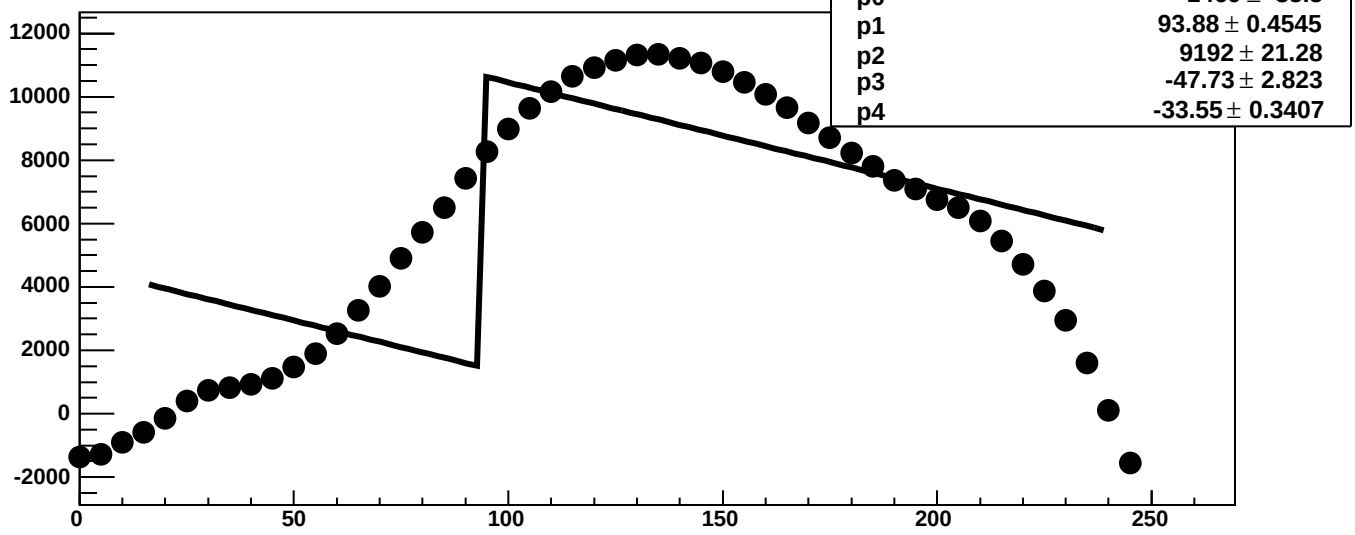

Chip 6, Channel 2, Enable 4, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250


Chip 6, Channel 2, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.351e+05 / 41
p0 -191.2 ± 6.666
p1 60.99 ± 0.02845
p2 2.125e+04 ± 34.85
p3 35.4 ± 0.07472
p4 -1.641 ± 0.2094
20000
15000
10000
5000
0
0
50
100
150
200
250

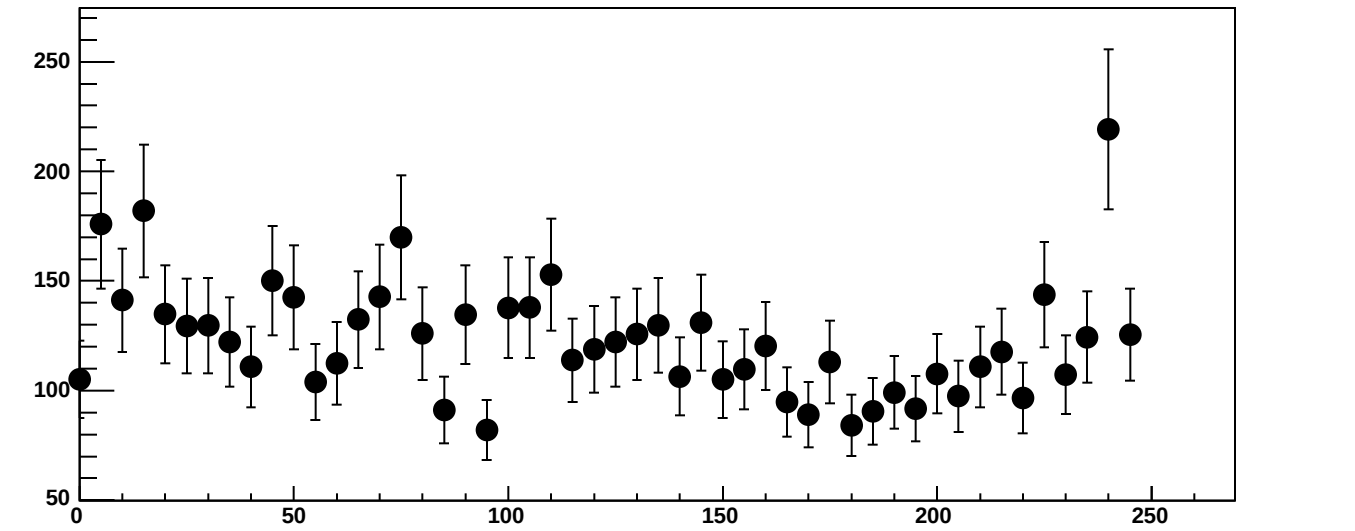

Chip 6, Channel 2, Enable 5, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 6, Channel 2, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

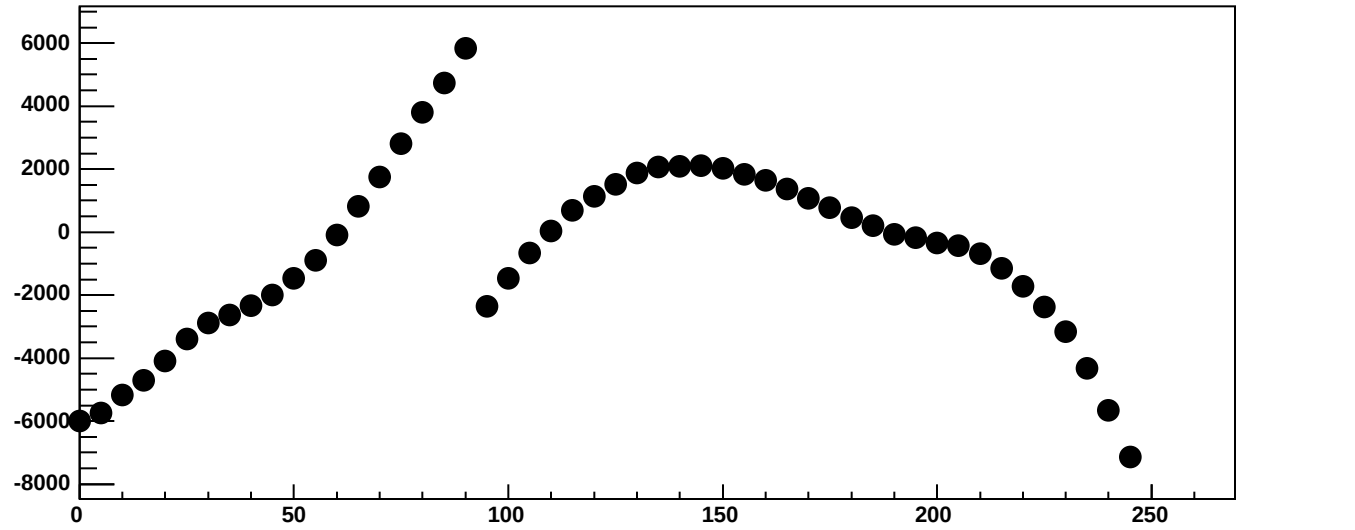
Chip 6, Channel 3, Enable 0, DAC=1600, ADC Mean vs Hold



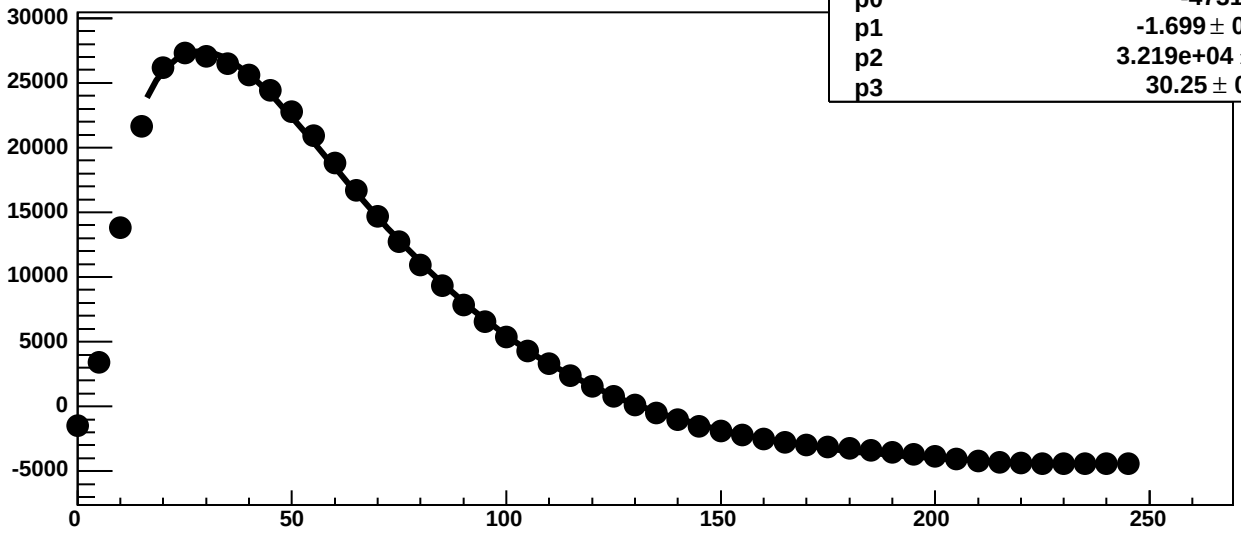
Chip 6, Channel 3, Enable 0, DAC=1600, ADC Noise vs Hold



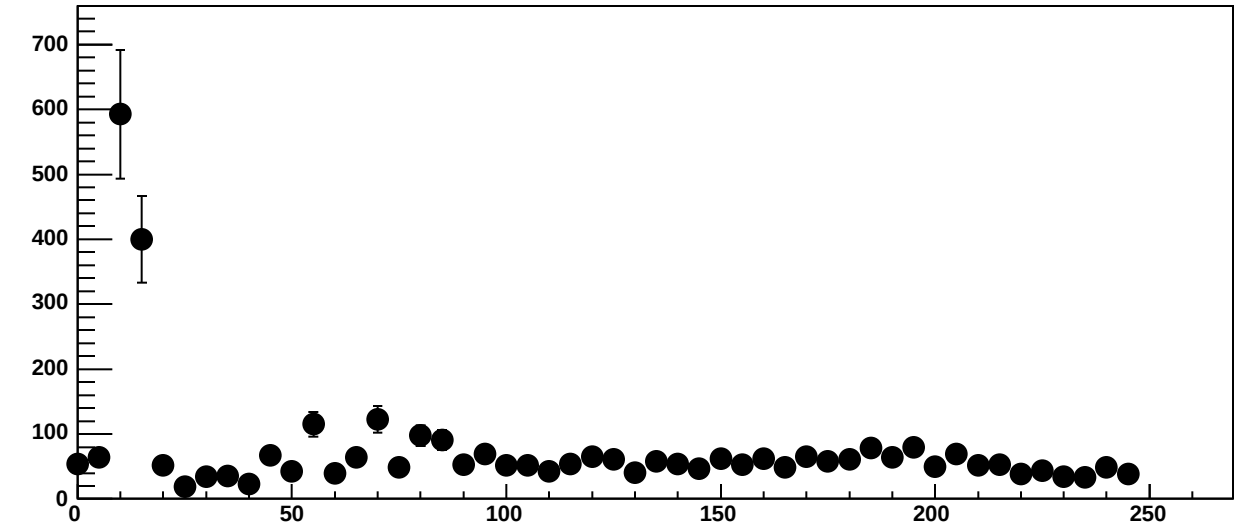
Chip 6, Channel 3, Enable 0, DAC=1600, ADC Residuals vs Hold



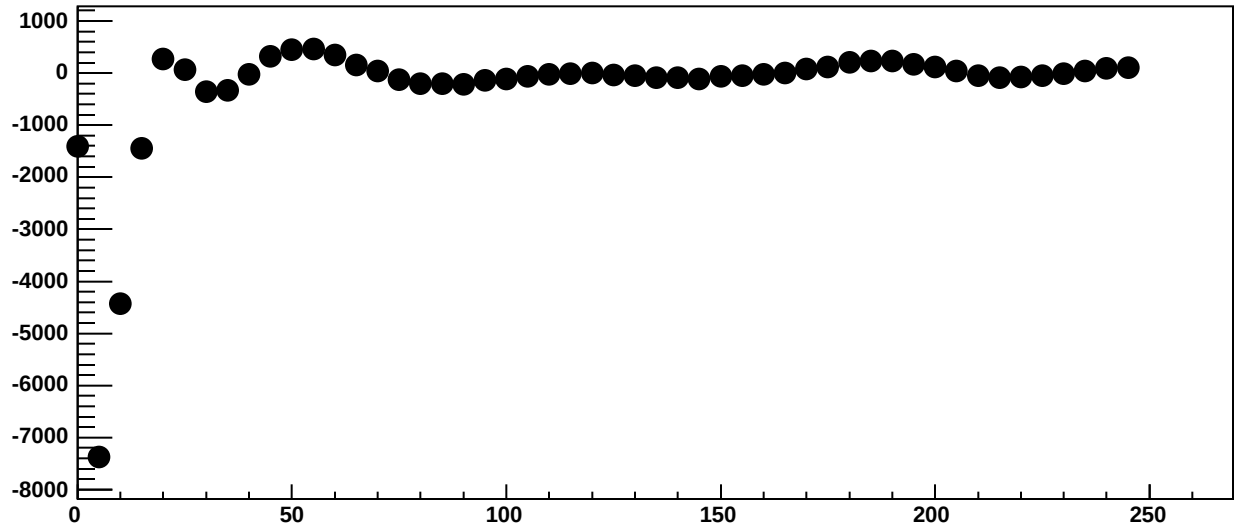
Chip 6, Channel 3, Enable 1!, DAC=1600, ADC Mean vs Hold



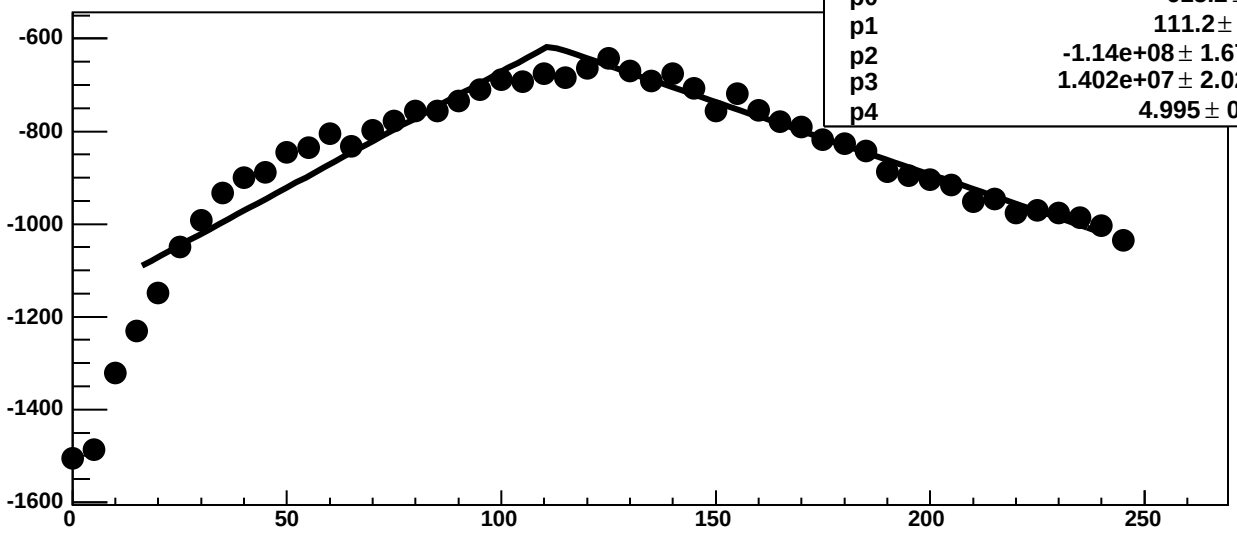
Chip 6, Channel 3, Enable 1!, DAC=1600, ADC Noise vs Hold



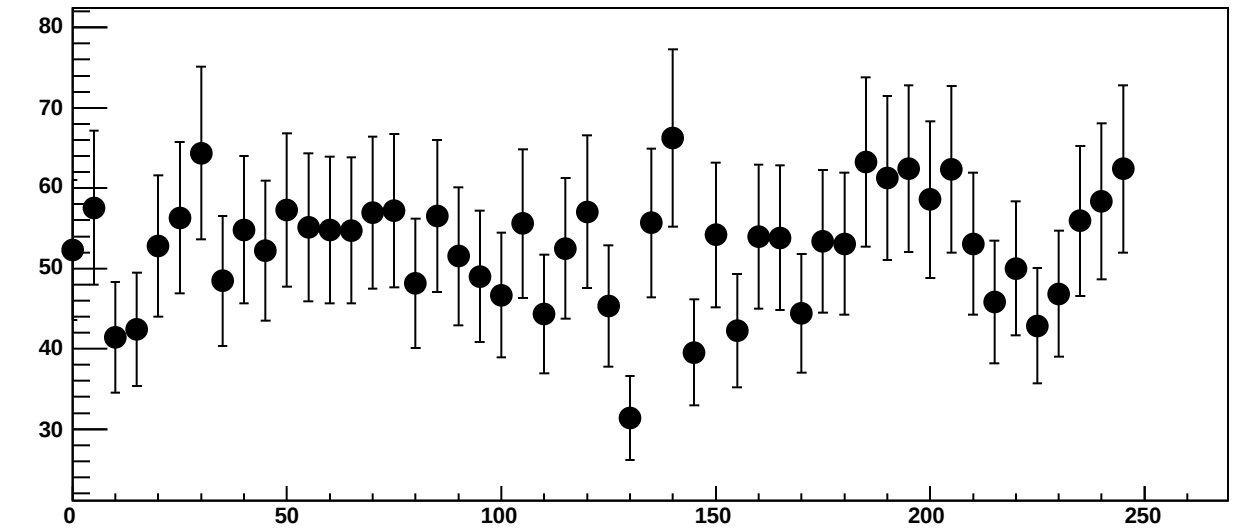
Chip 6, Channel 3, Enable 1!, DAC=1600, ADC Residuals vs Hold



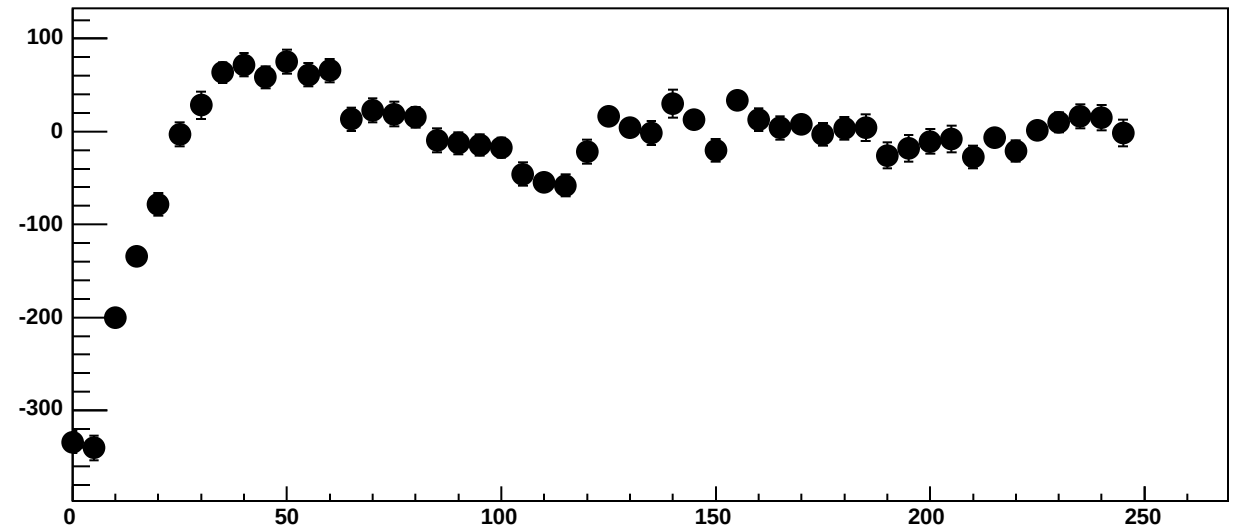
Chip 6, Channel 3, Enable 2, DAC=1600, ADC Mean vs Hold



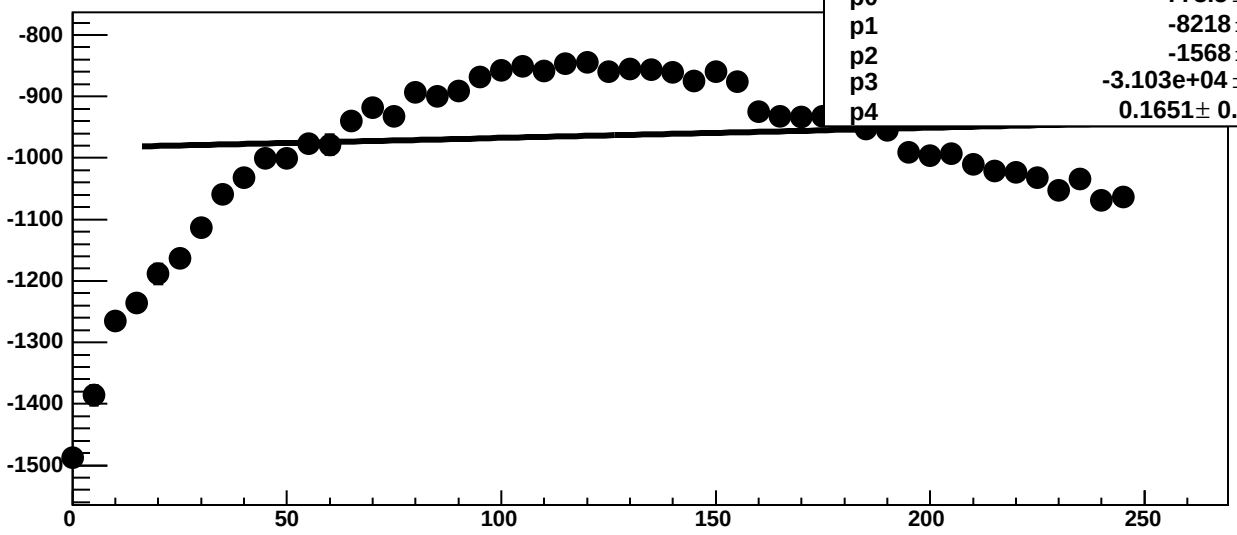
Chip 6, Channel 3, Enable 2, DAC=1600, ADC Noise vs Hold



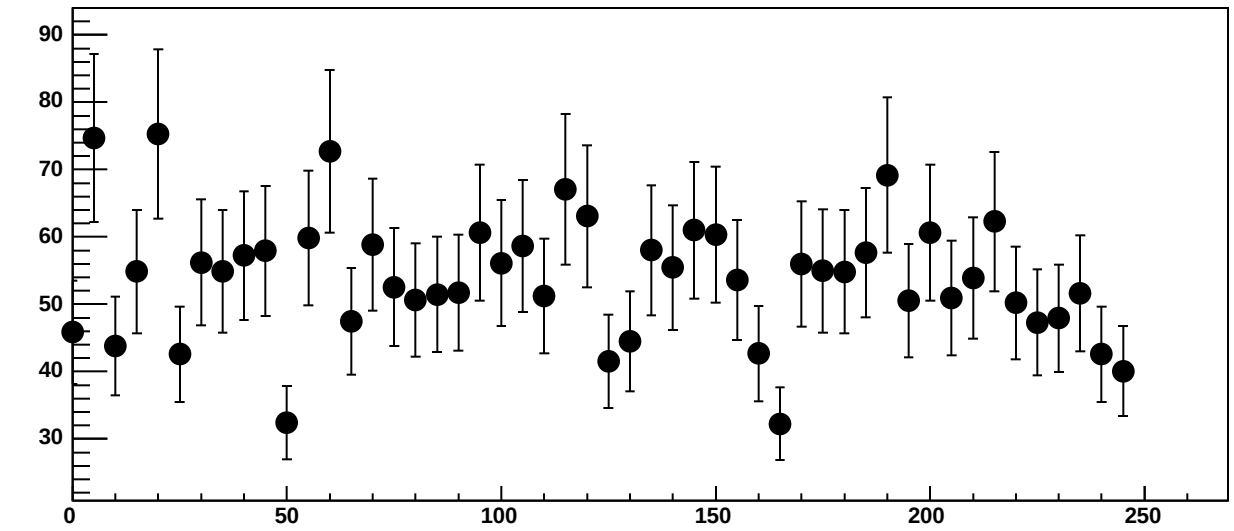
Chip 6, Channel 3, Enable 2, DAC=1600, ADC Residuals vs Hold



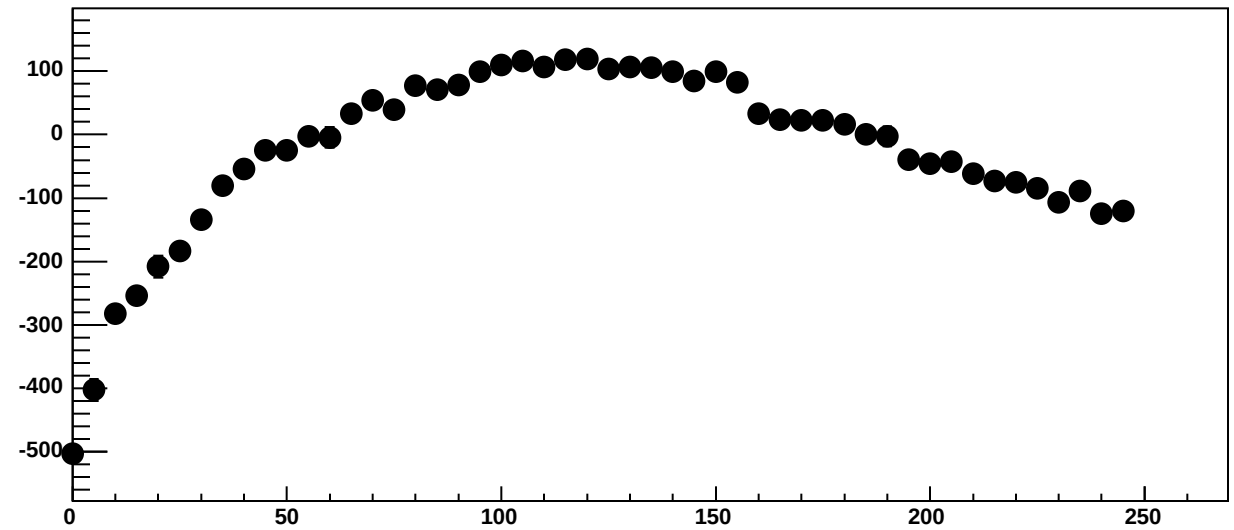
Chip 6, Channel 3, Enable 3, DAC=1600, ADC Mean vs Hold



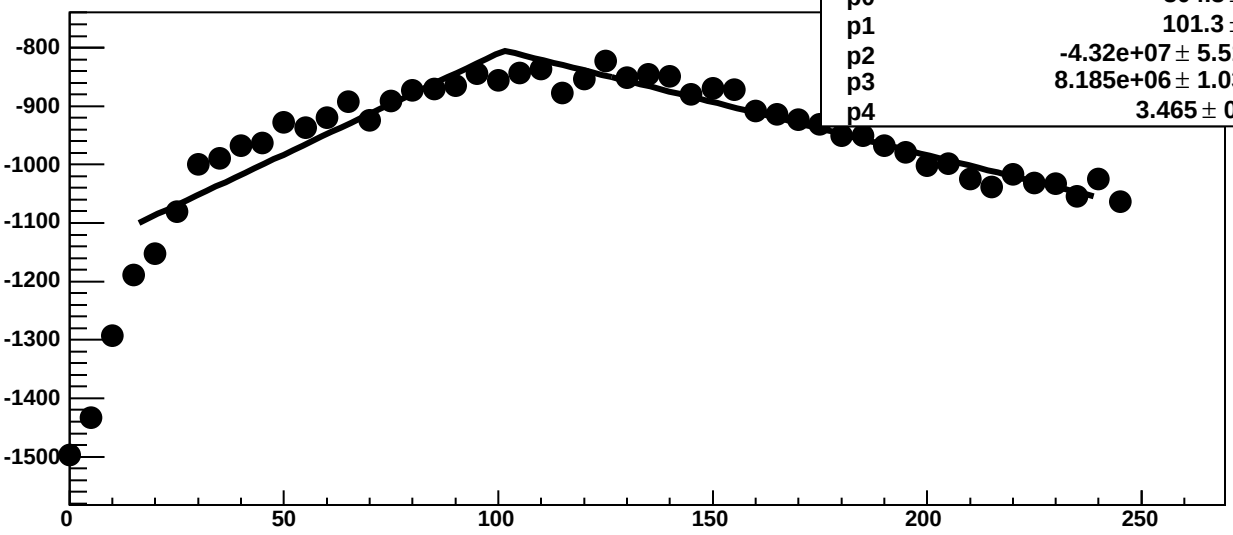
Chip 6, Channel 3, Enable 3, DAC=1600, ADC Noise vs Hold



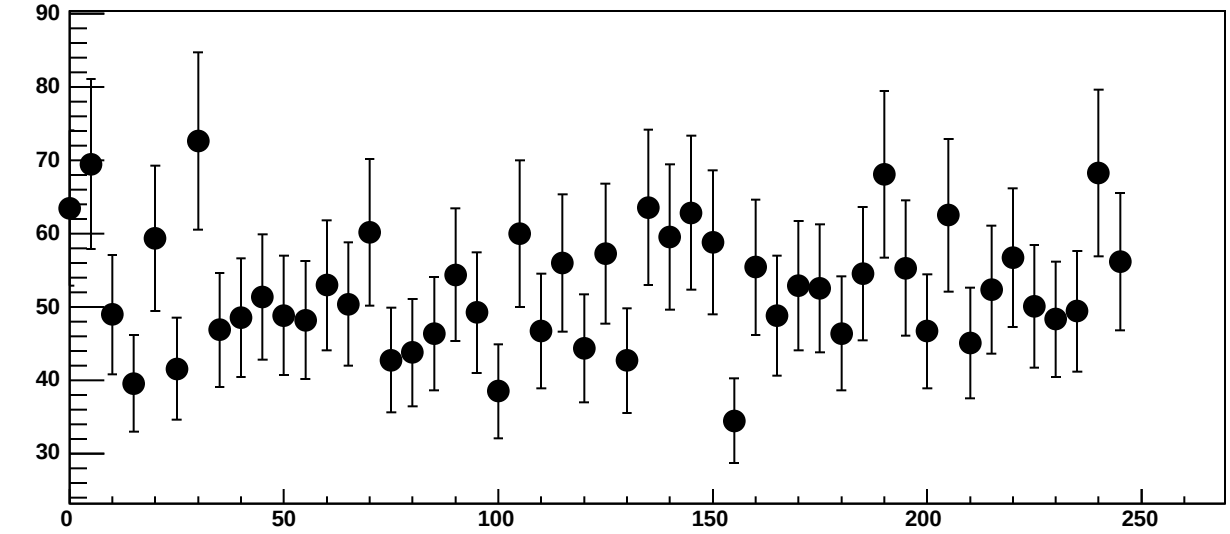
Chip 6, Channel 3, Enable 3, DAC=1600, ADC Residuals vs Hold



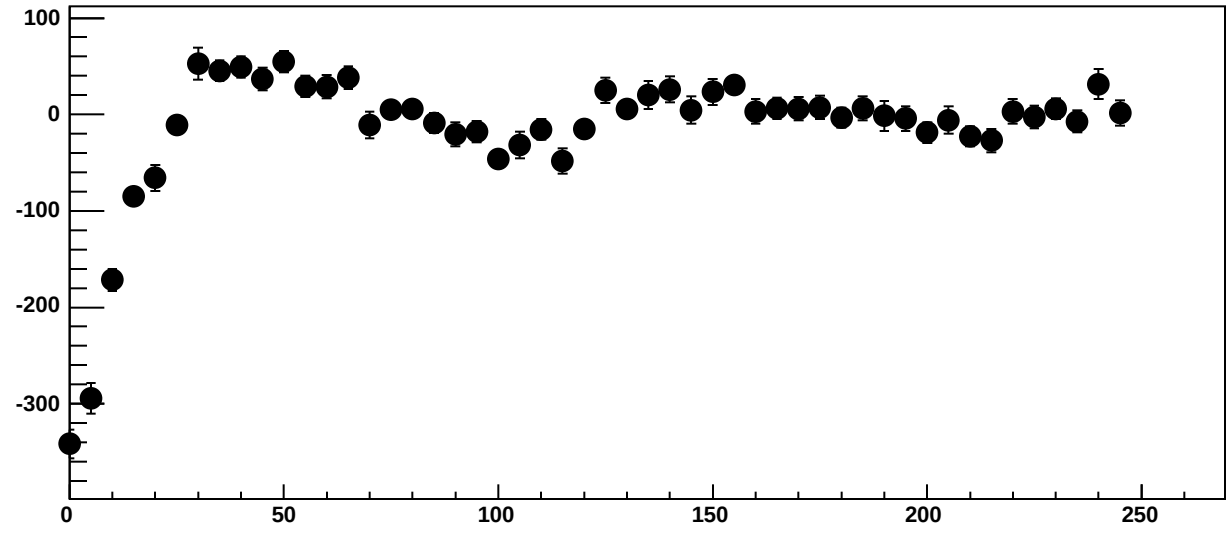
Chip 6, Channel 3, Enable 4, DAC=1600, ADC Mean vs Hold



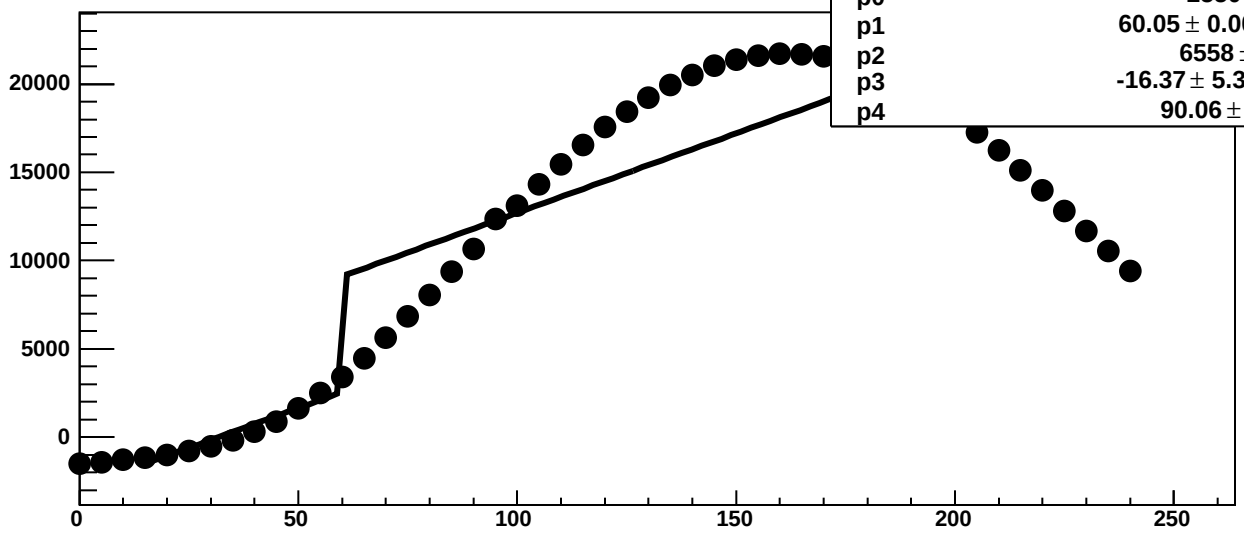
Chip 6, Channel 3, Enable 4, DAC=1600, ADC Noise vs Hold



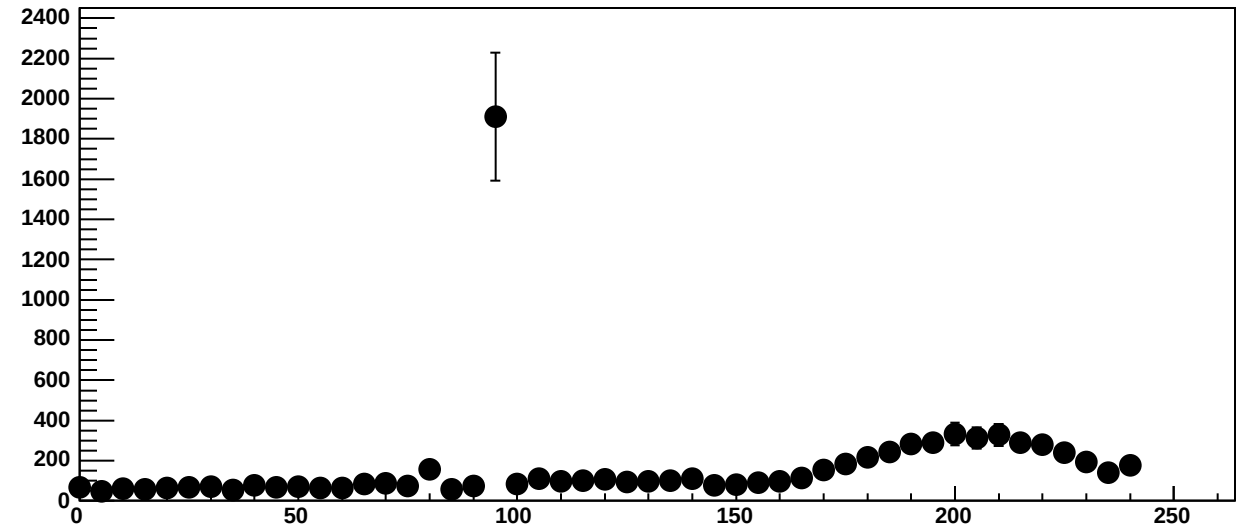
Chip 6, Channel 3, Enable 4, DAC=1600, ADC Residuals vs Hold



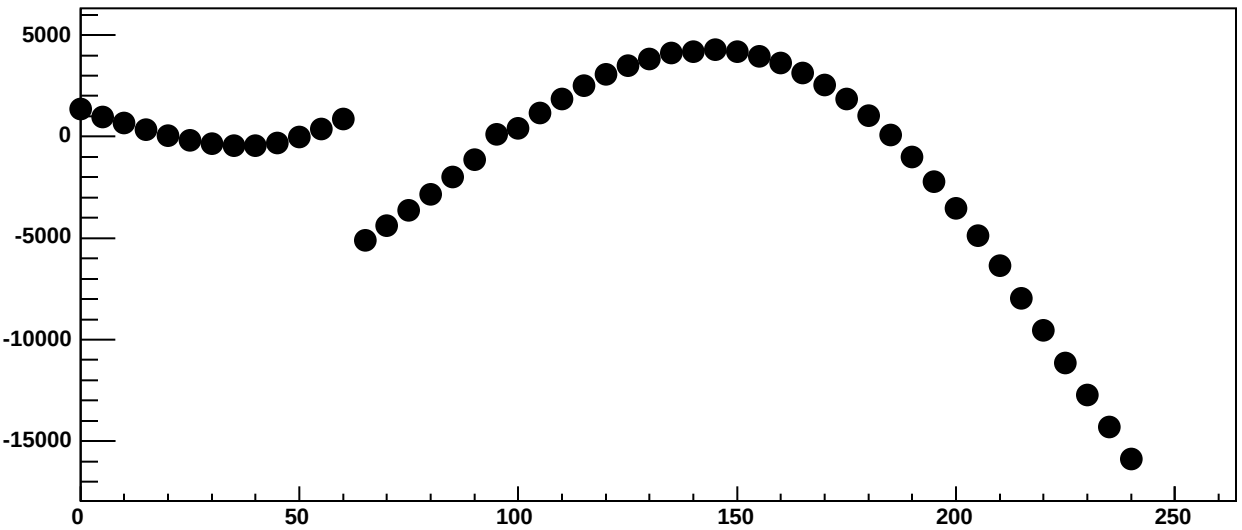
Chip 6, Channel 3, Enable 5, DAC=1600, ADC Mean vs Hold



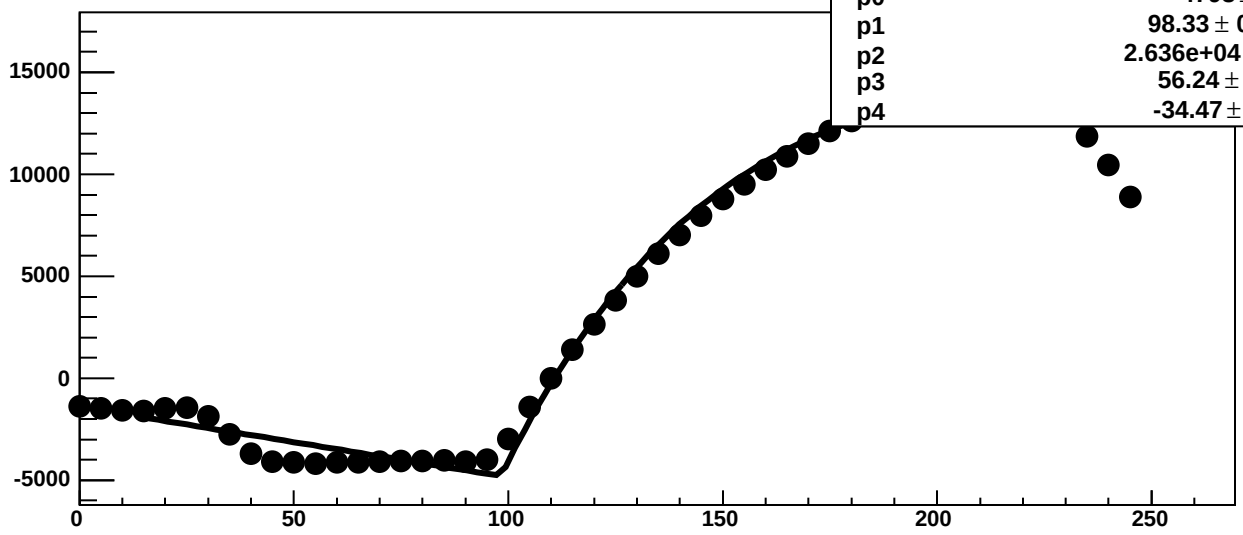
Chip 6, Channel 3, Enable 5, DAC=1600, ADC Noise vs Hold



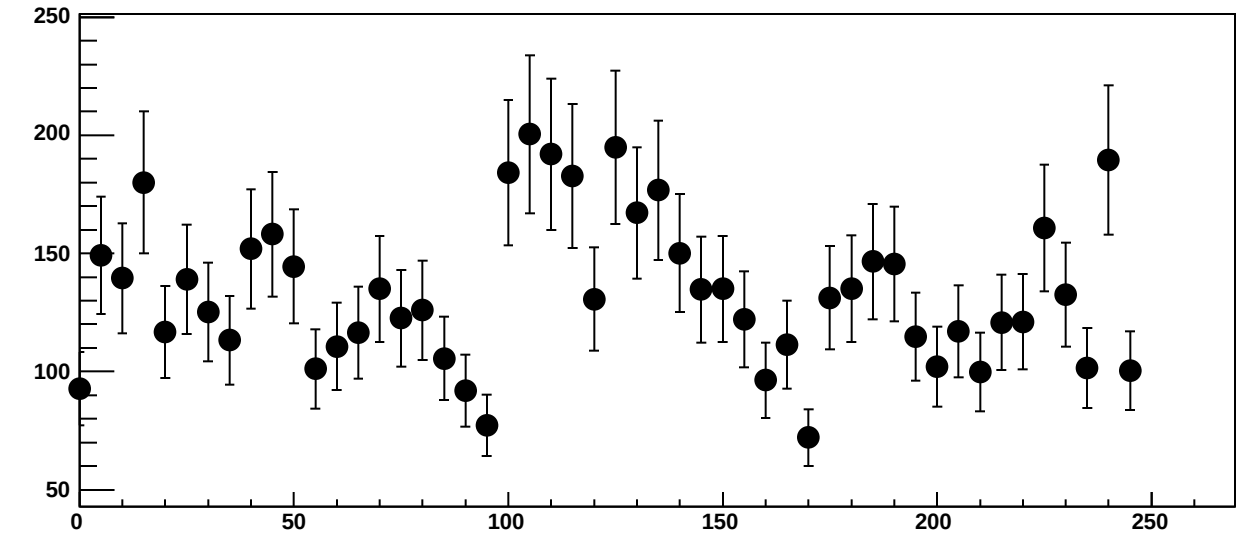
Chip 6, Channel 3, Enable 5, DAC=1600, ADC Residuals vs Hold



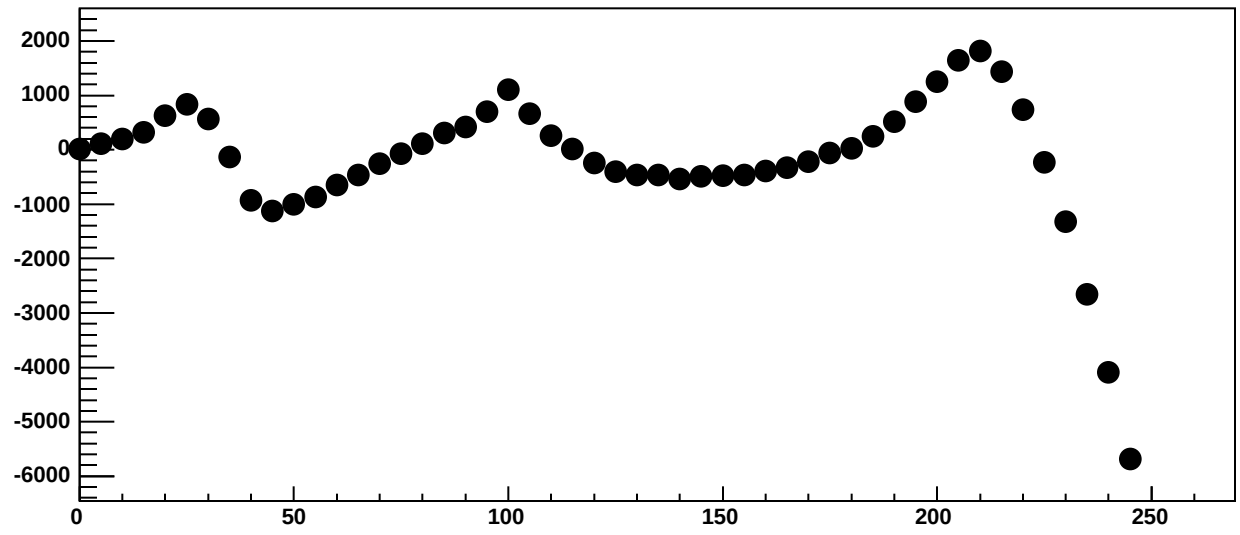
Chip 6, Channel 4, Enable 0, DAC=1600, ADC Mean vs Hold



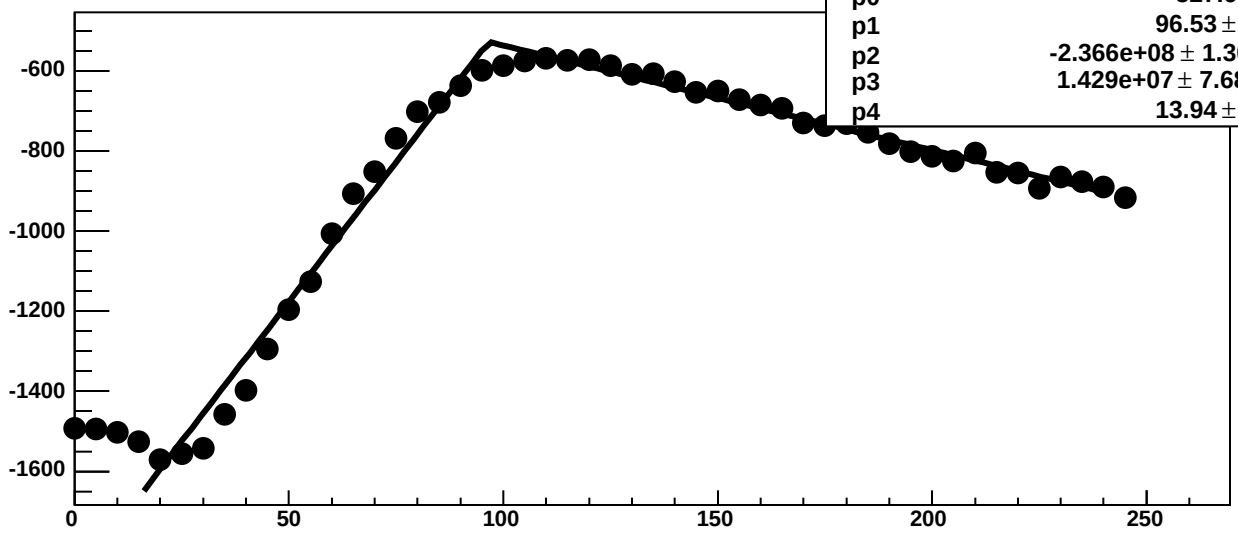
Chip 6, Channel 4, Enable 0, DAC=1600, ADC Noise vs Hold



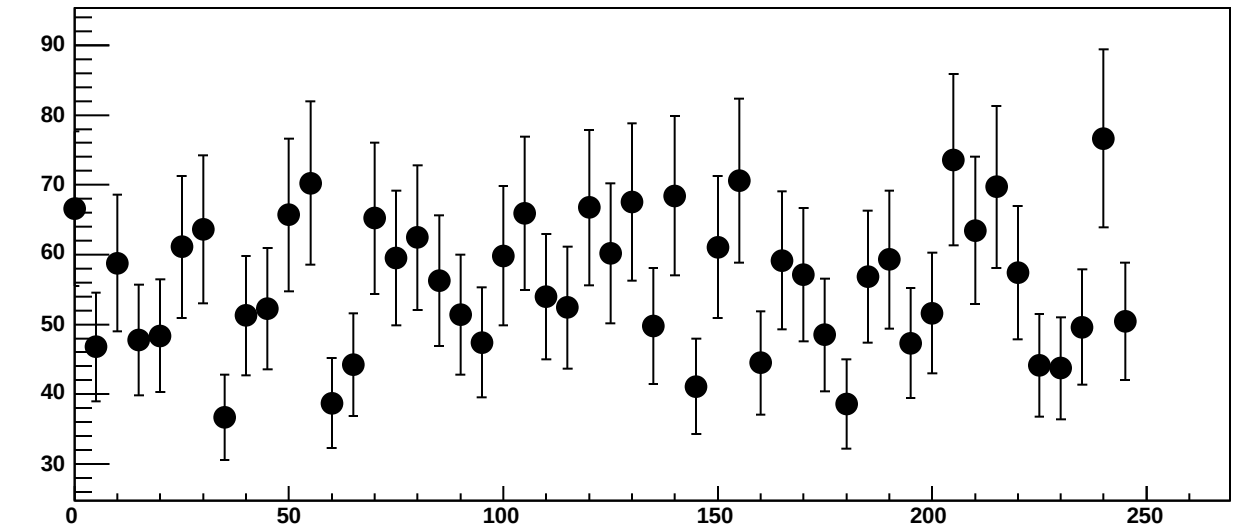
Chip 6, Channel 4, Enable 0, DAC=1600, ADC Residuals vs Hold



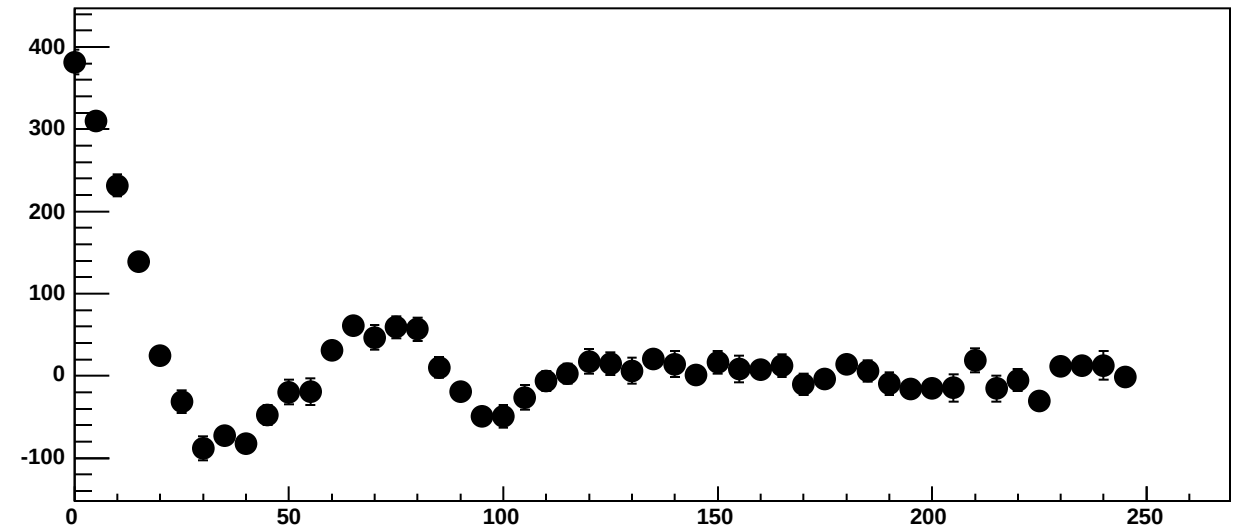
Chip 6, Channel 4, Enable 1, DAC=1600, ADC Mean vs Hold



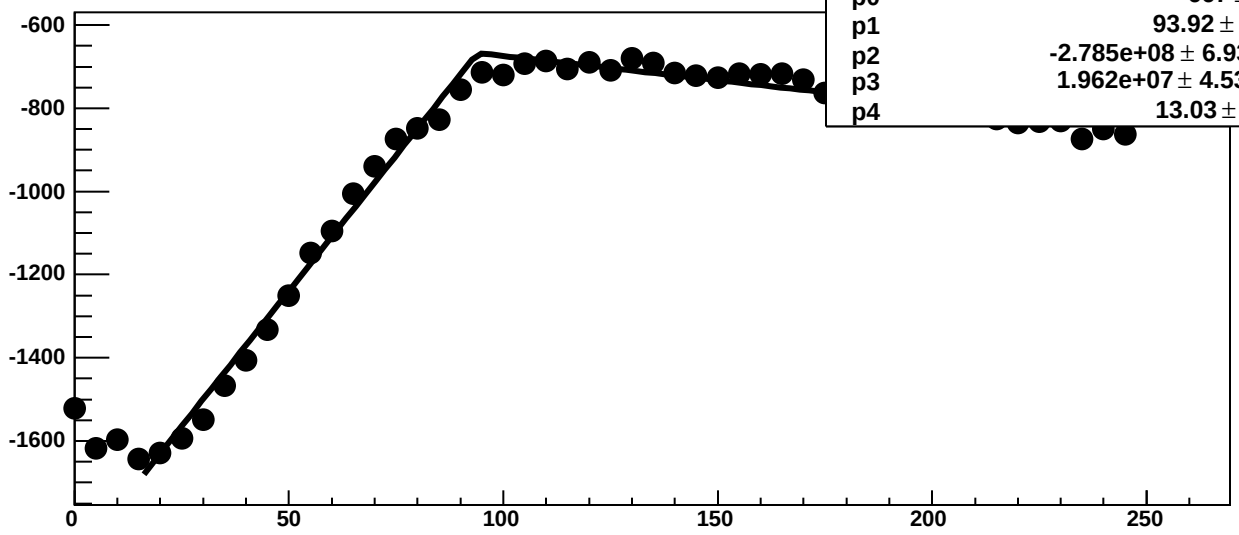
Chip 6, Channel 4, Enable 1, DAC=1600, ADC Noise vs Hold



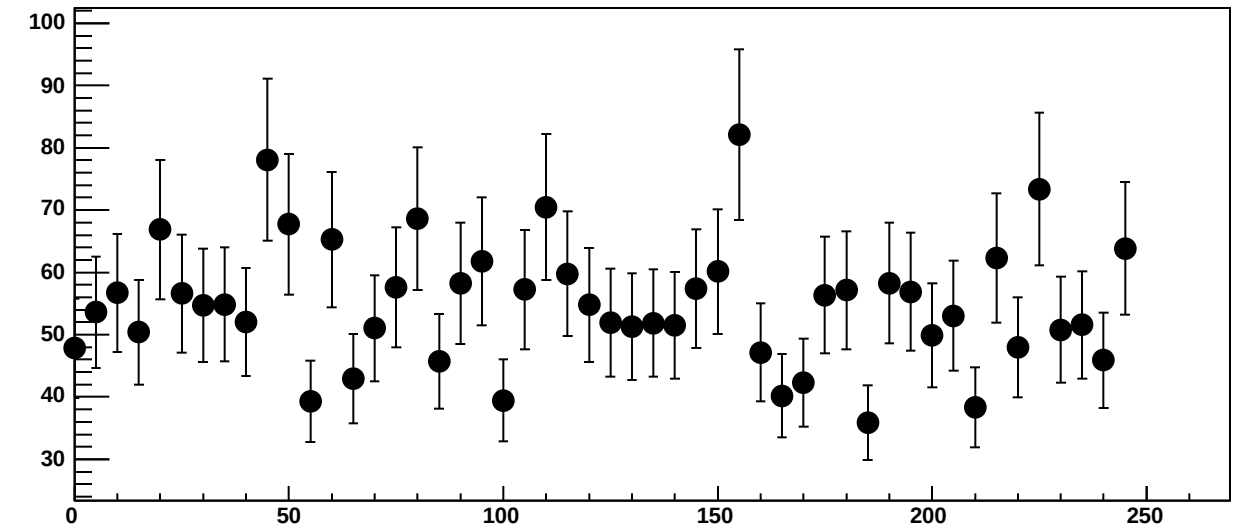
Chip 6, Channel 4, Enable 1, DAC=1600, ADC Residuals vs Hold



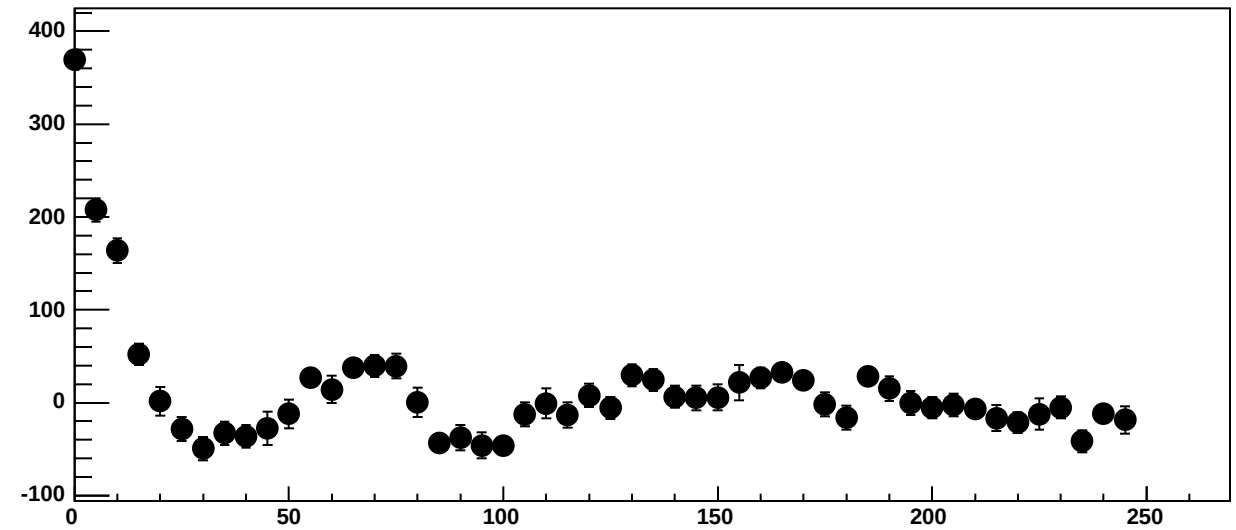
Chip 6, Channel 4, Enable 2, DAC=1600, ADC Mean vs Hold



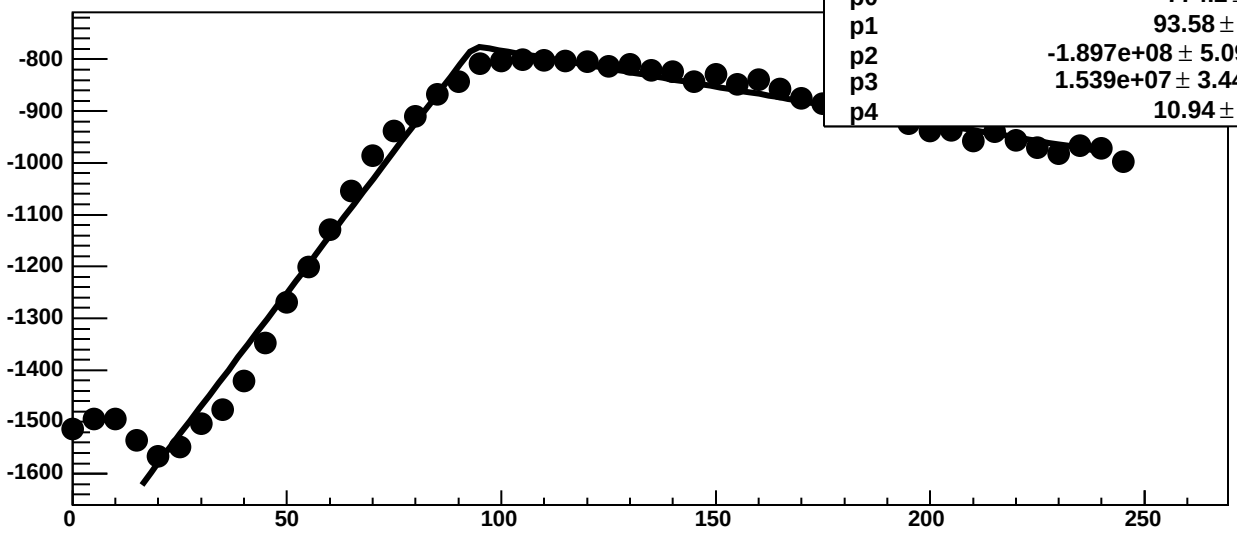
Chip 6, Channel 4, Enable 2, DAC=1600, ADC Noise vs Hold



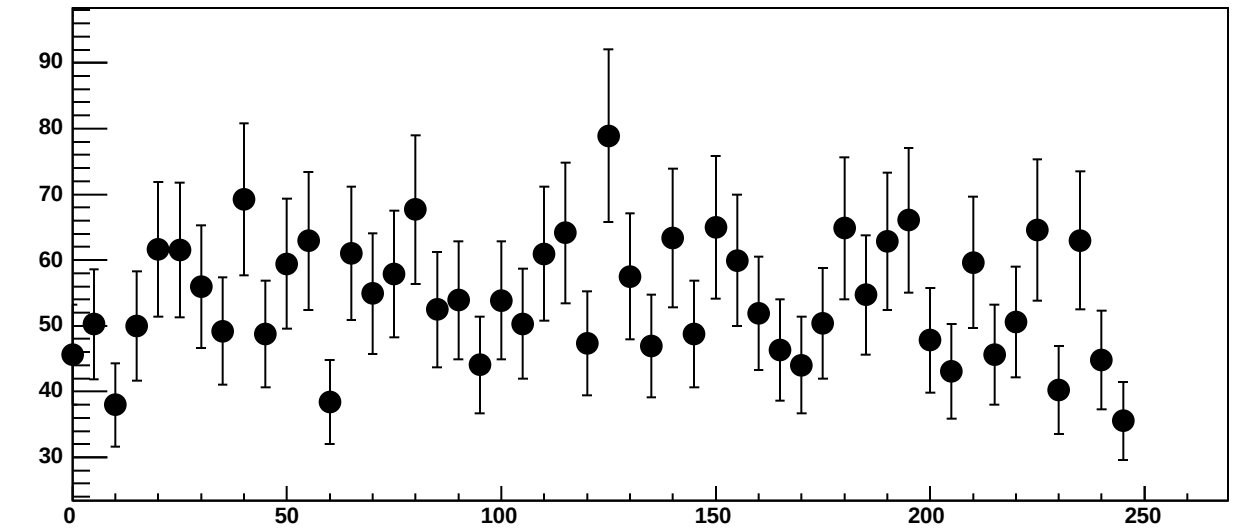
Chip 6, Channel 4, Enable 2, DAC=1600, ADC Residuals vs Hold



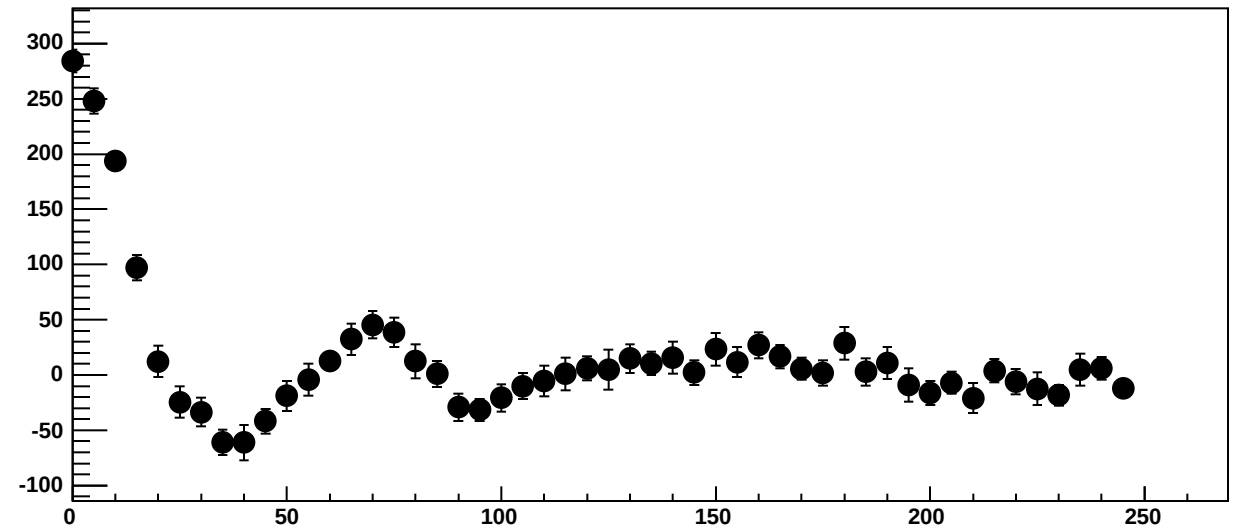
Chip 6, Channel 4, Enable 3, DAC=1600, ADC Mean vs Hold



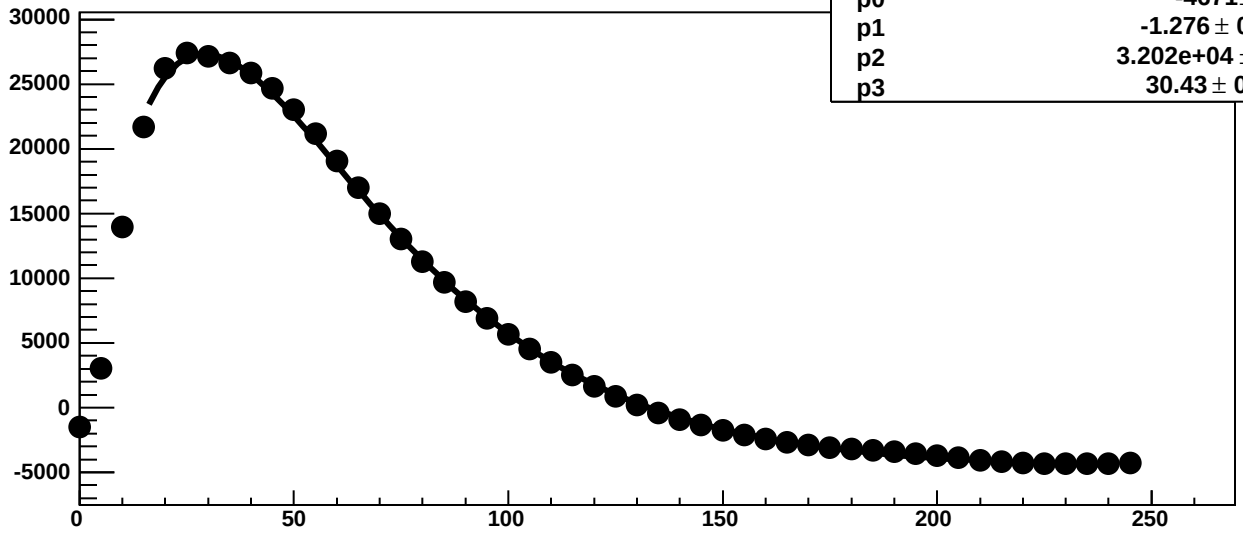
Chip 6, Channel 4, Enable 3, DAC=1600, ADC Noise vs Hold



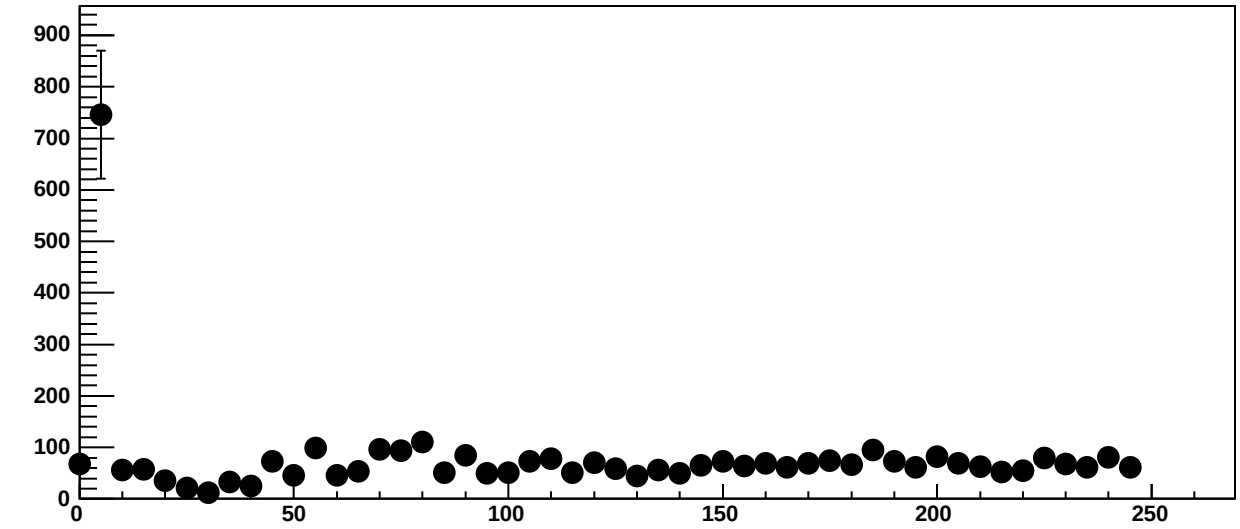
Chip 6, Channel 4, Enable 3, DAC=1600, ADC Residuals vs Hold



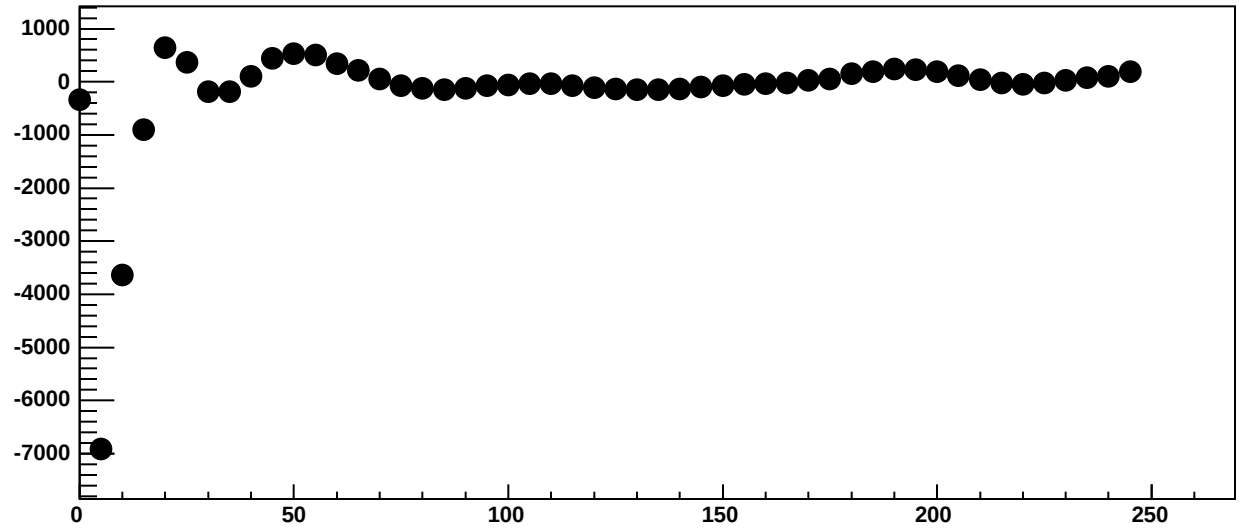
Chip 6, Channel 4, Enable 4!, DAC=1600, ADC Mean vs Hold



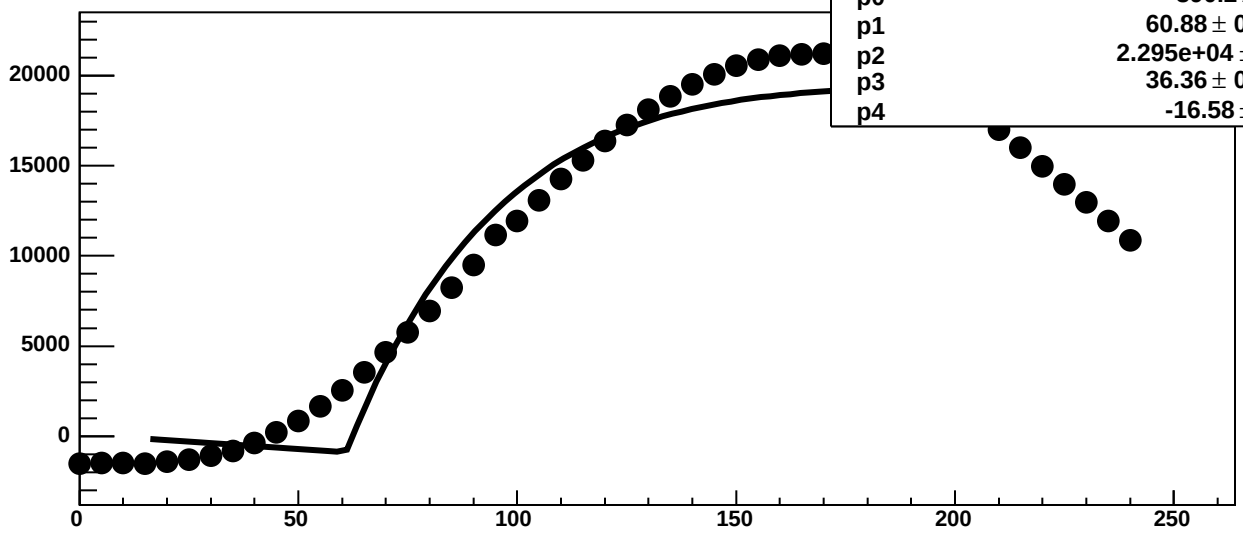
Chip 6, Channel 4, Enable 4!, DAC=1600, ADC Noise vs Hold



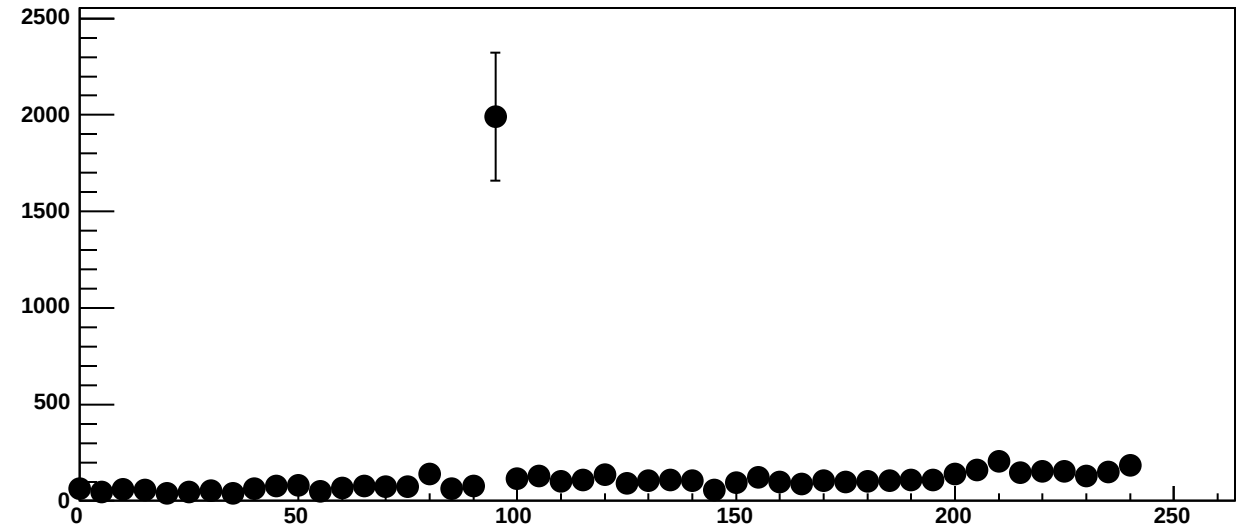
Chip 6, Channel 4, Enable 4!, DAC=1600, ADC Residuals vs Hold



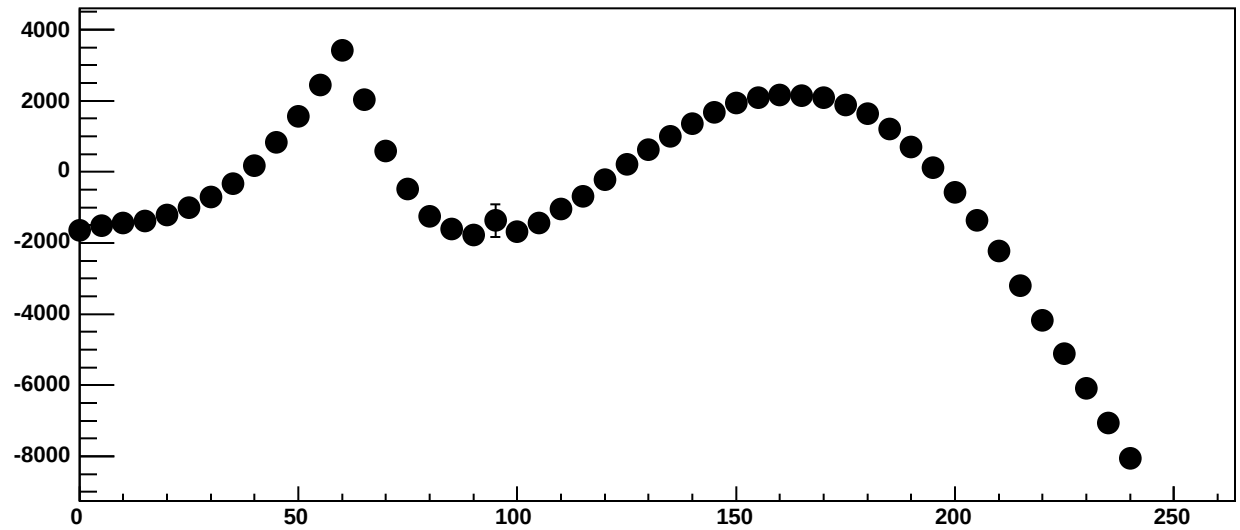
Chip 6, Channel 4, Enable 5, DAC=1600, ADC Mean vs Hold



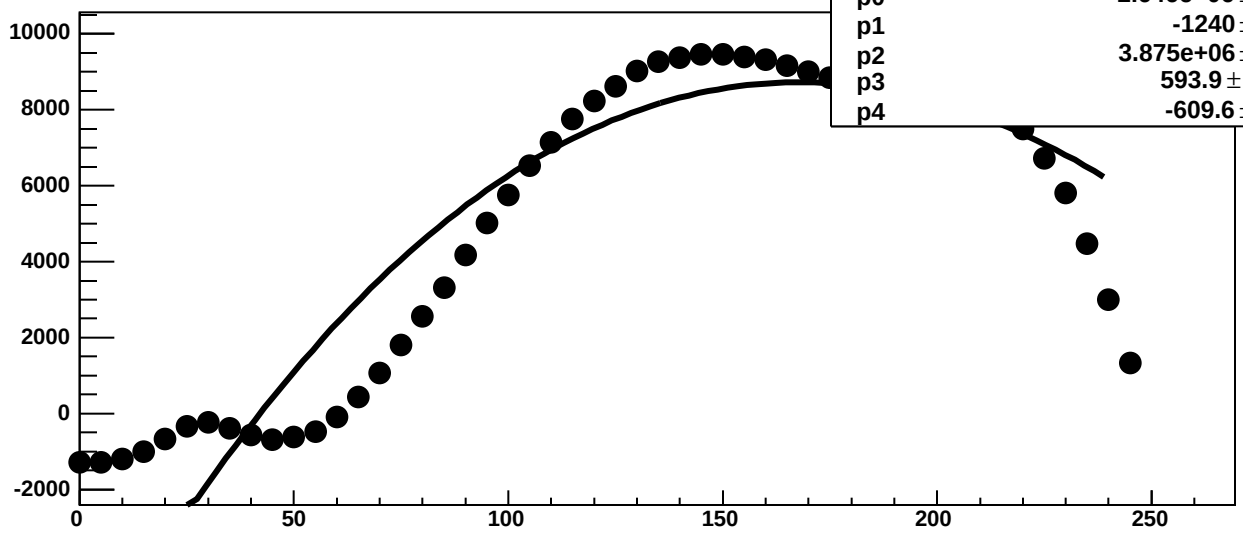
Chip 6, Channel 4, Enable 5, DAC=1600, ADC Noise vs Hold



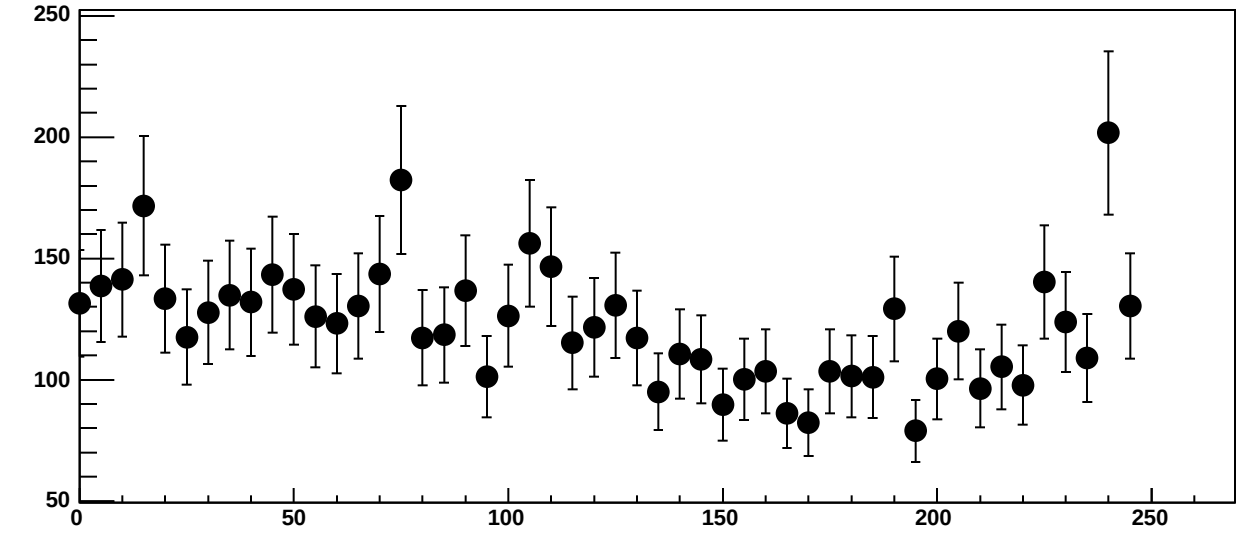
Chip 6, Channel 4, Enable 5, DAC=1600, ADC Residuals vs Hold



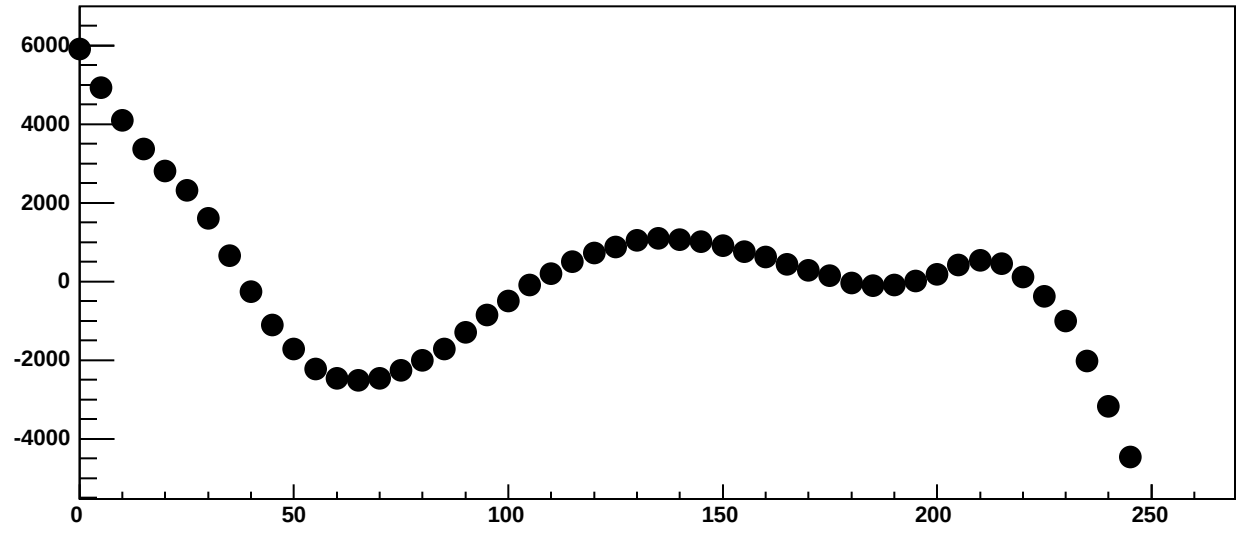
Chip 6, Channel 5, Enable 0, DAC=1600, ADC Mean vs Hold



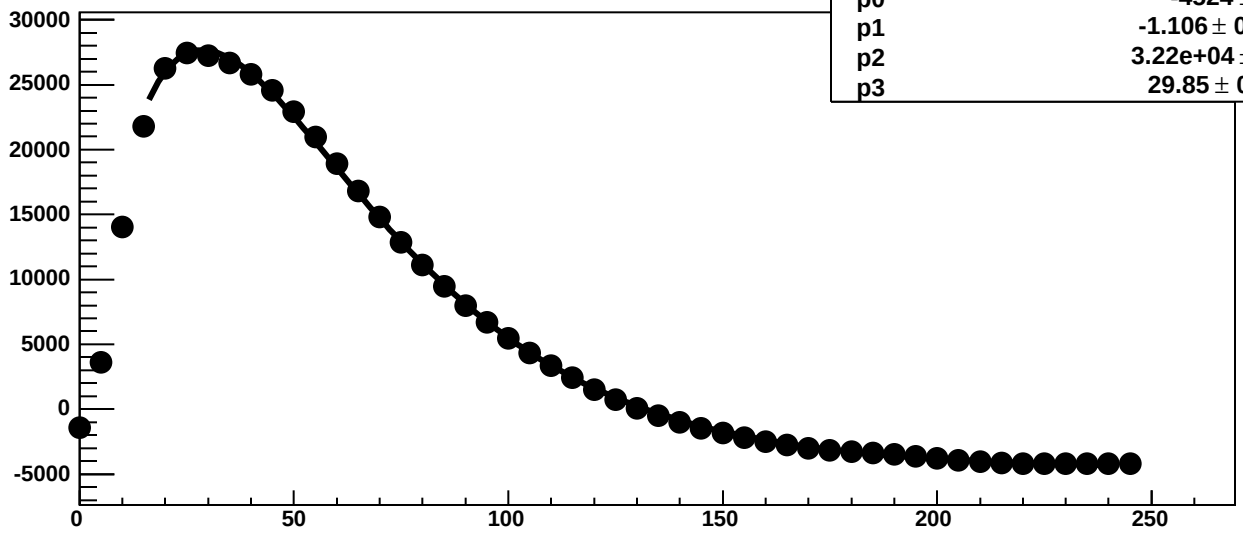
Chip 6, Channel 5, Enable 0, DAC=1600, ADC Noise vs Hold



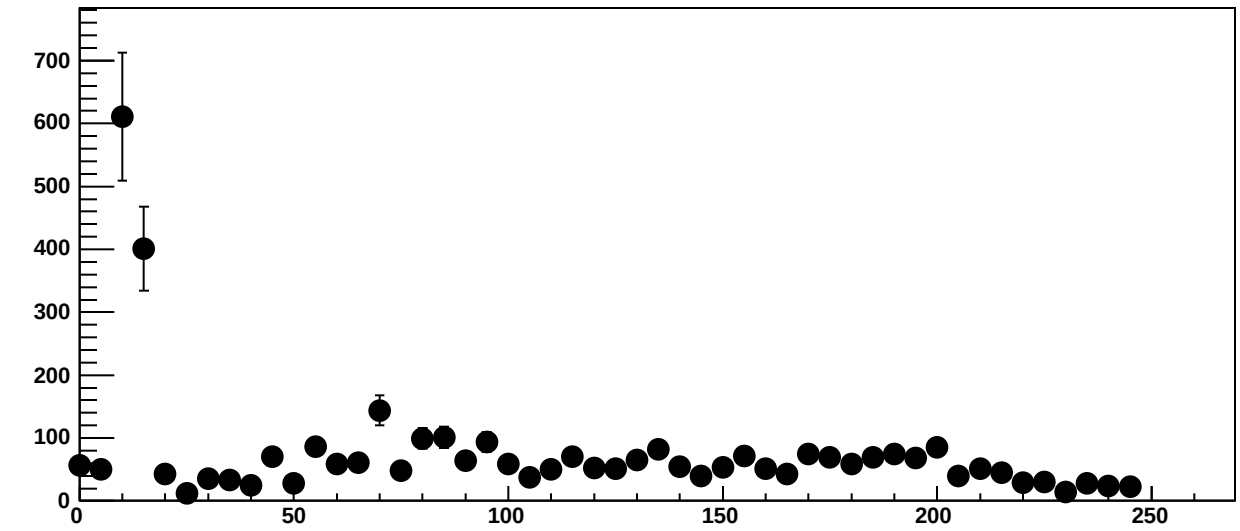
Chip 6, Channel 5, Enable 0, DAC=1600, ADC Residuals vs Hold



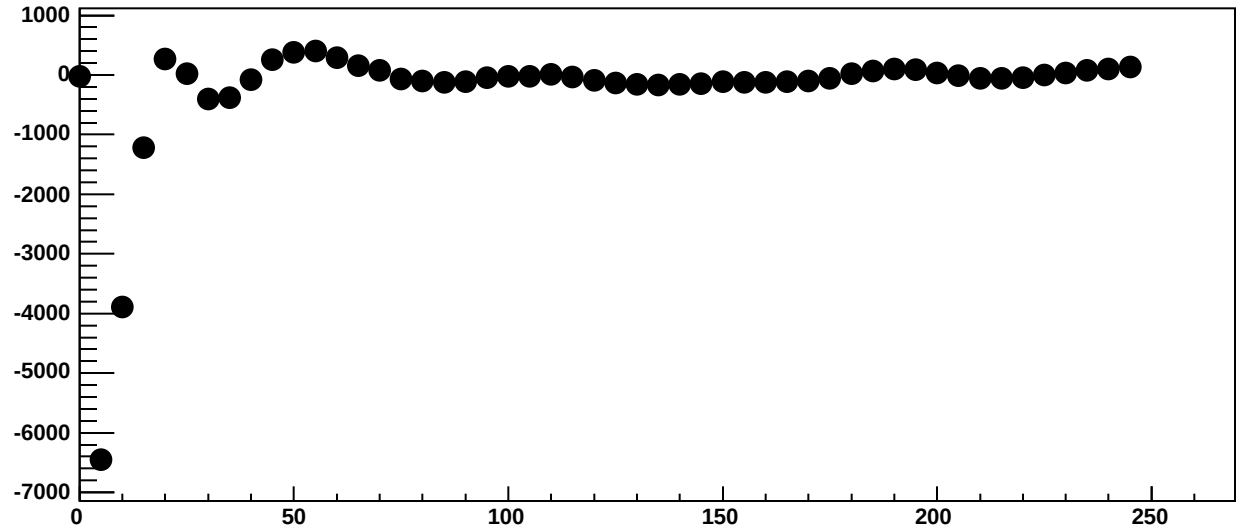
Chip 6, Channel 5, Enable 1!, DAC=1600, ADC Mean vs Hold



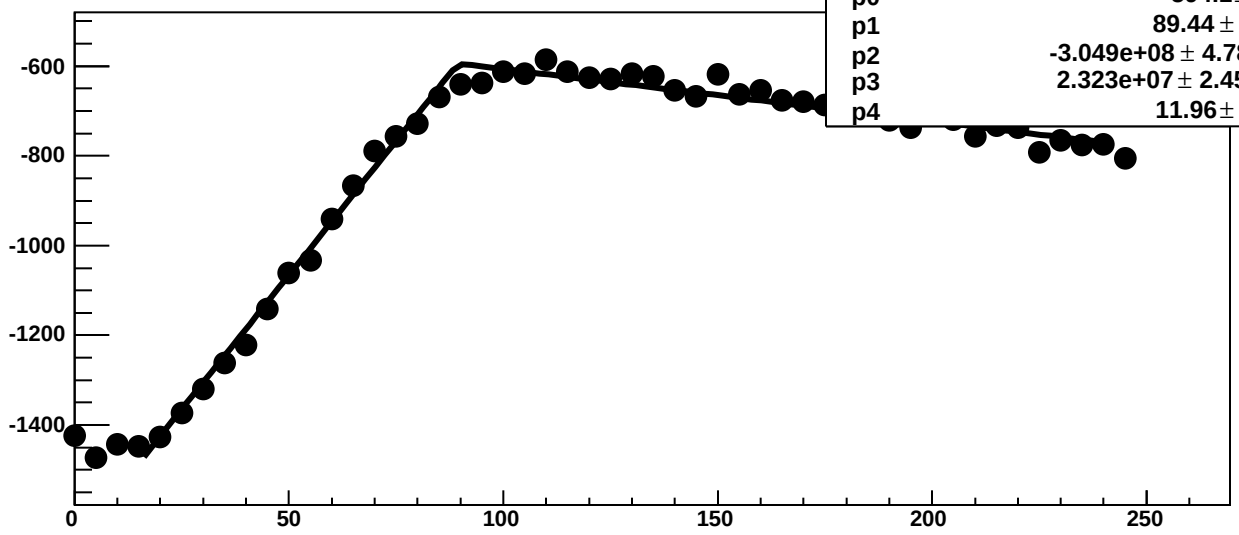
Chip 6, Channel 5, Enable 1!, DAC=1600, ADC Noise vs Hold



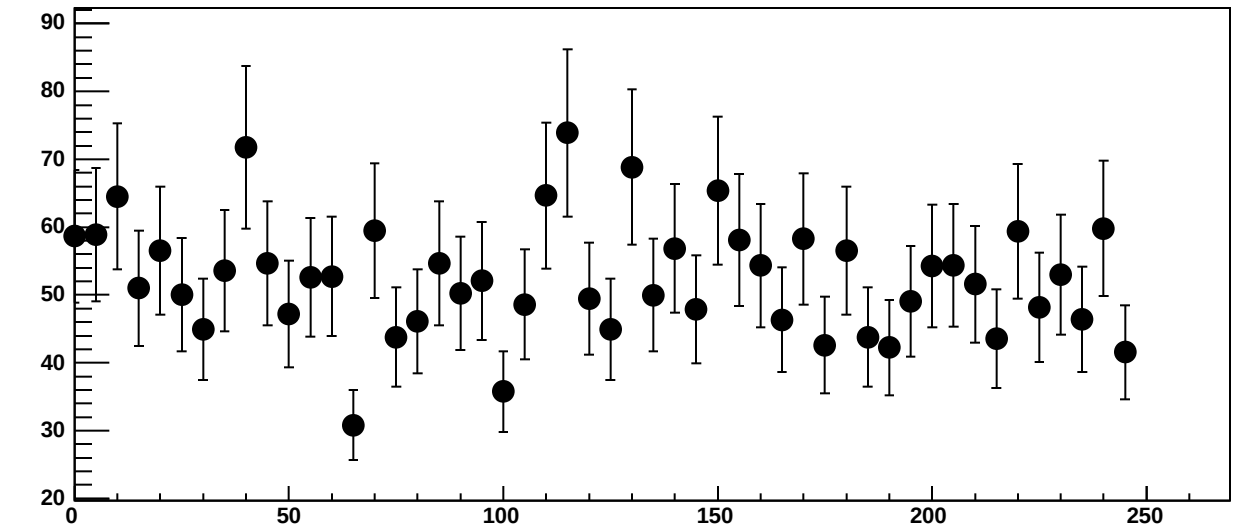
Chip 6, Channel 5, Enable 1!, DAC=1600, ADC Residuals vs Hold



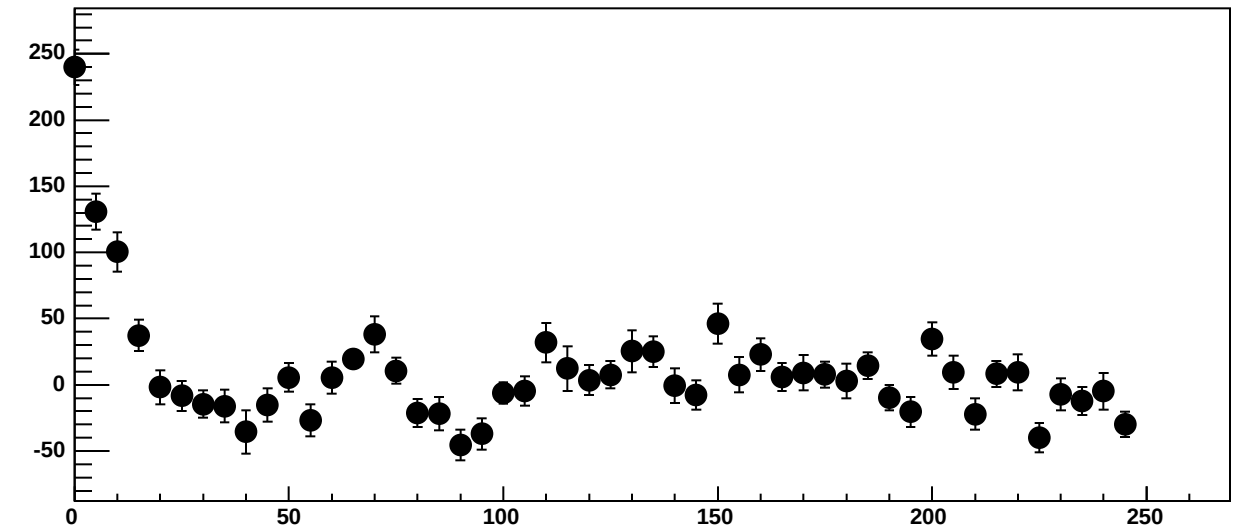
Chip 6, Channel 5, Enable 2, DAC=1600, ADC Mean vs Hold



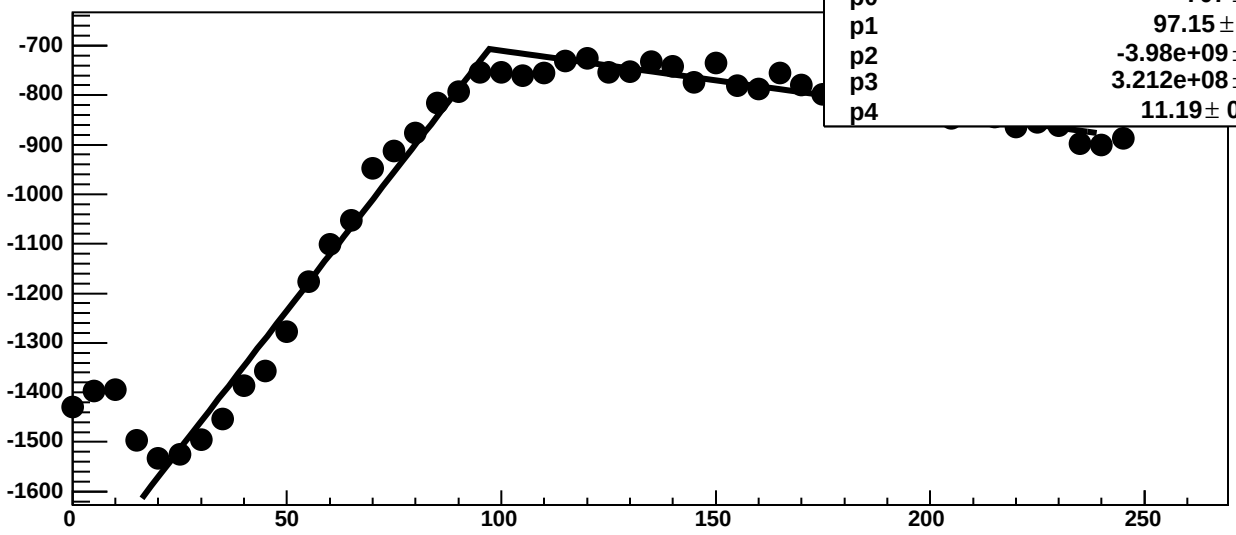
Chip 6, Channel 5, Enable 2, DAC=1600, ADC Noise vs Hold



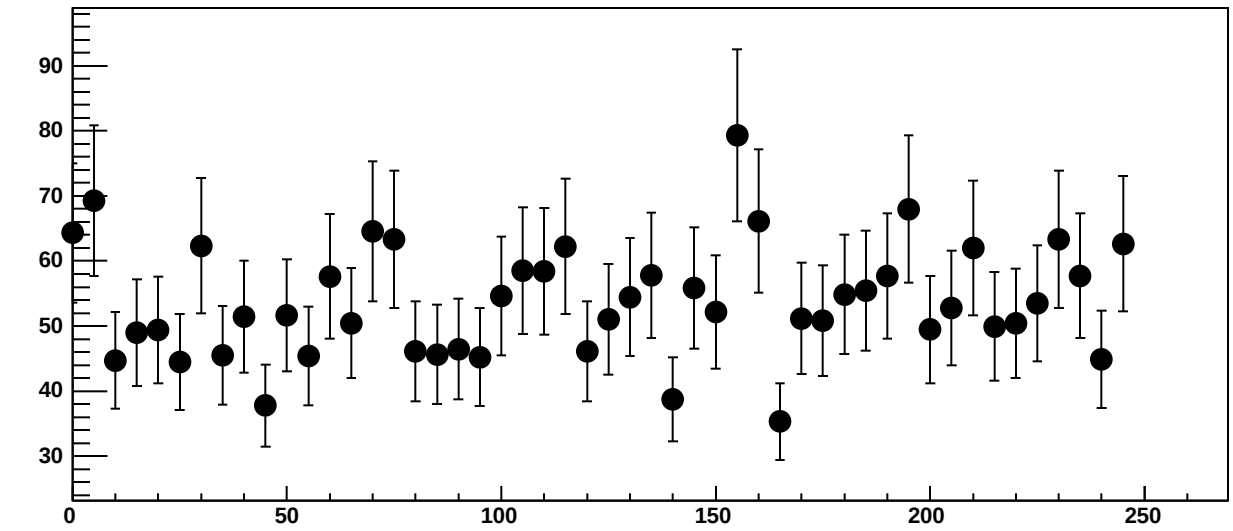
Chip 6, Channel 5, Enable 2, DAC=1600, ADC Residuals vs Hold



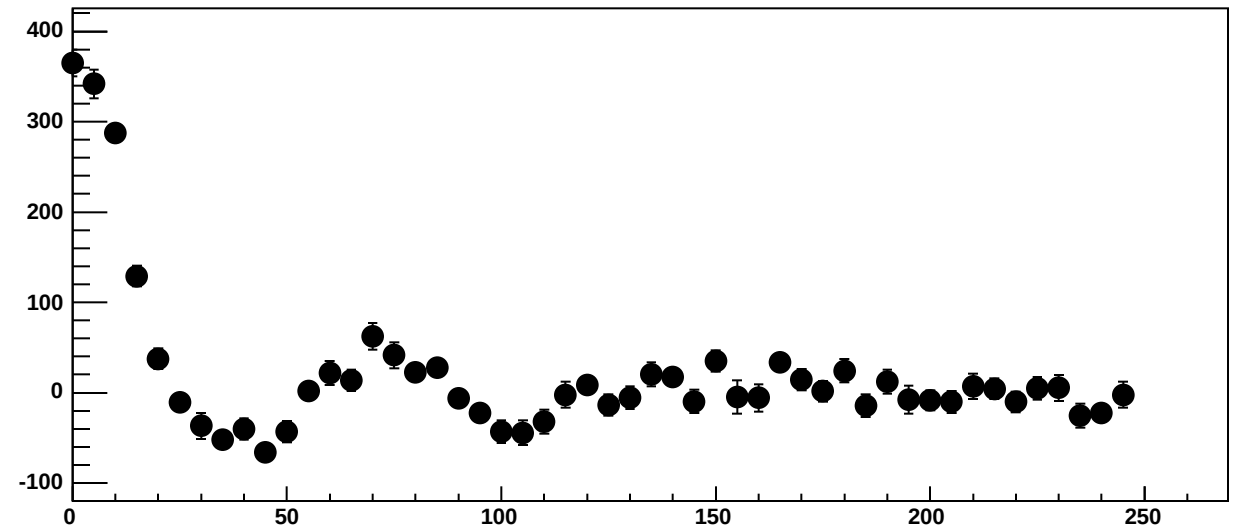
Chip 6, Channel 5, Enable 3, DAC=1600, ADC Mean vs Hold



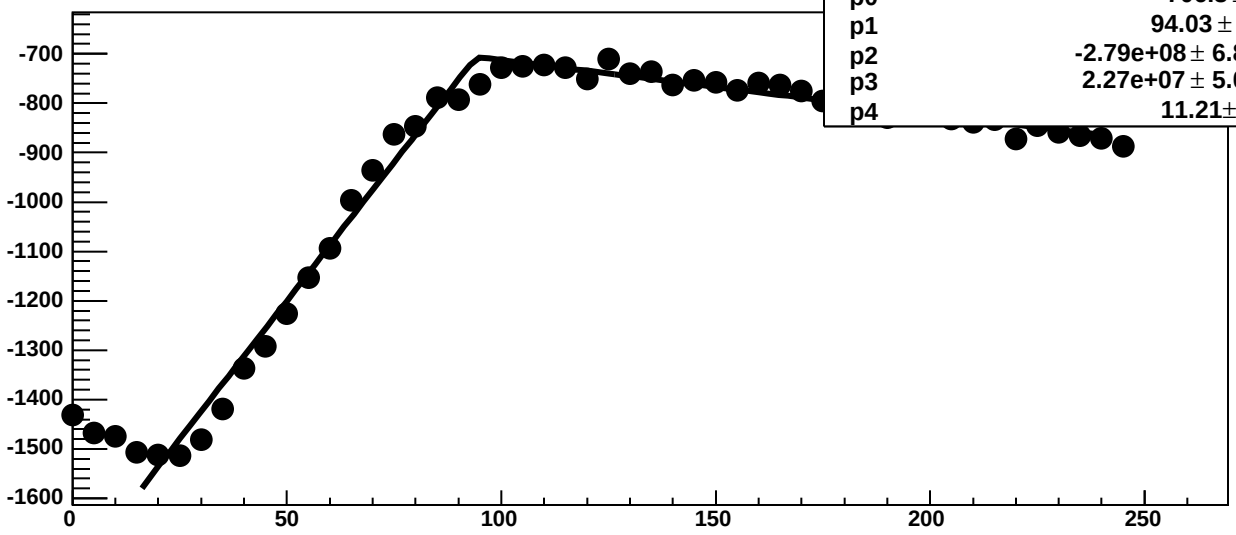
Chip 6, Channel 5, Enable 3, DAC=1600, ADC Noise vs Hold



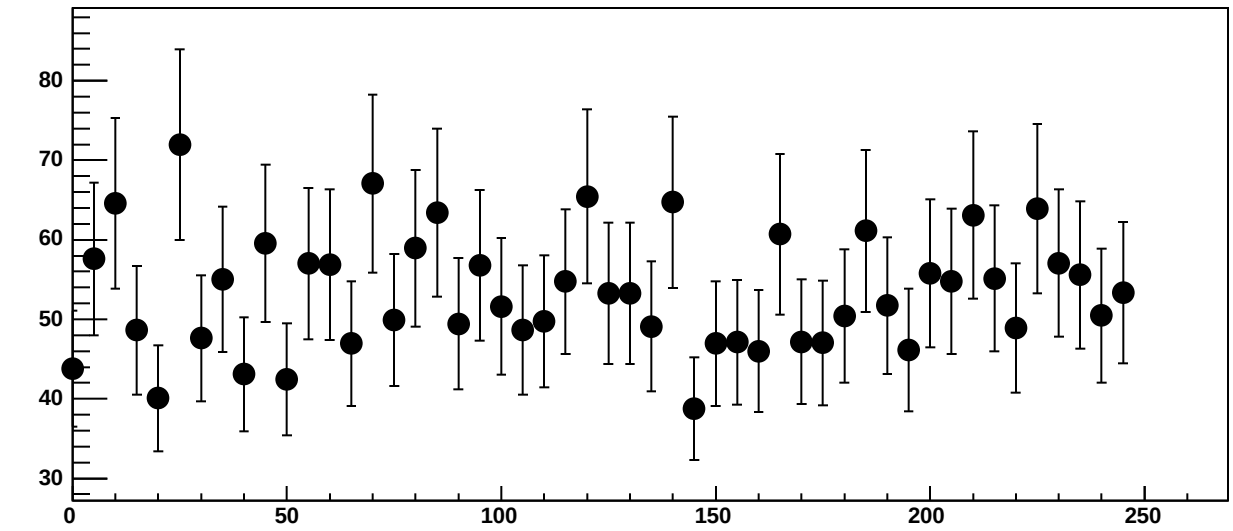
Chip 6, Channel 5, Enable 3, DAC=1600, ADC Residuals vs Hold



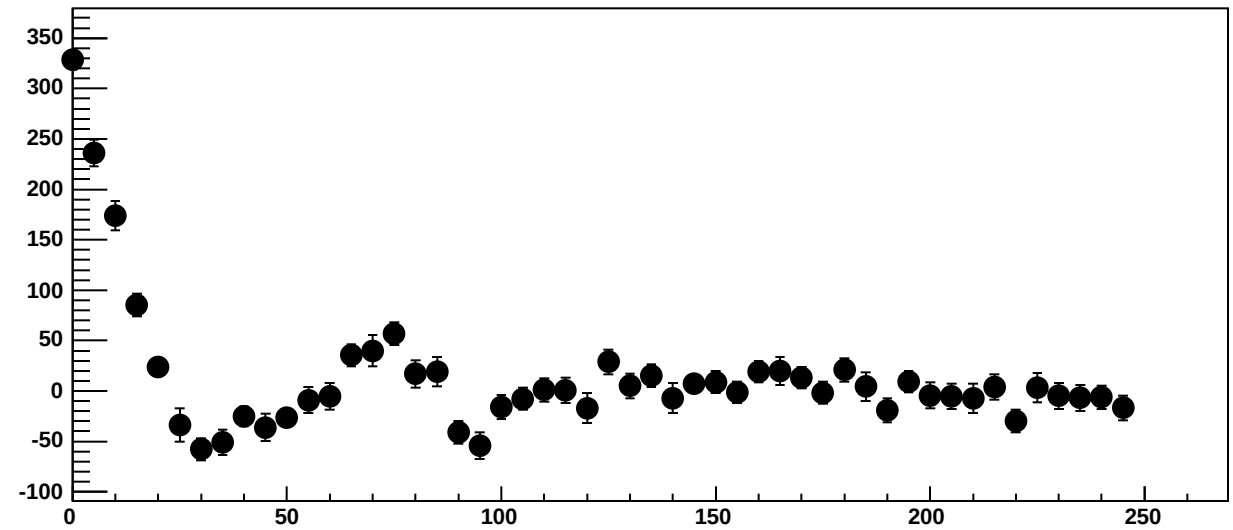
Chip 6, Channel 5, Enable 4, DAC=1600, ADC Mean vs Hold



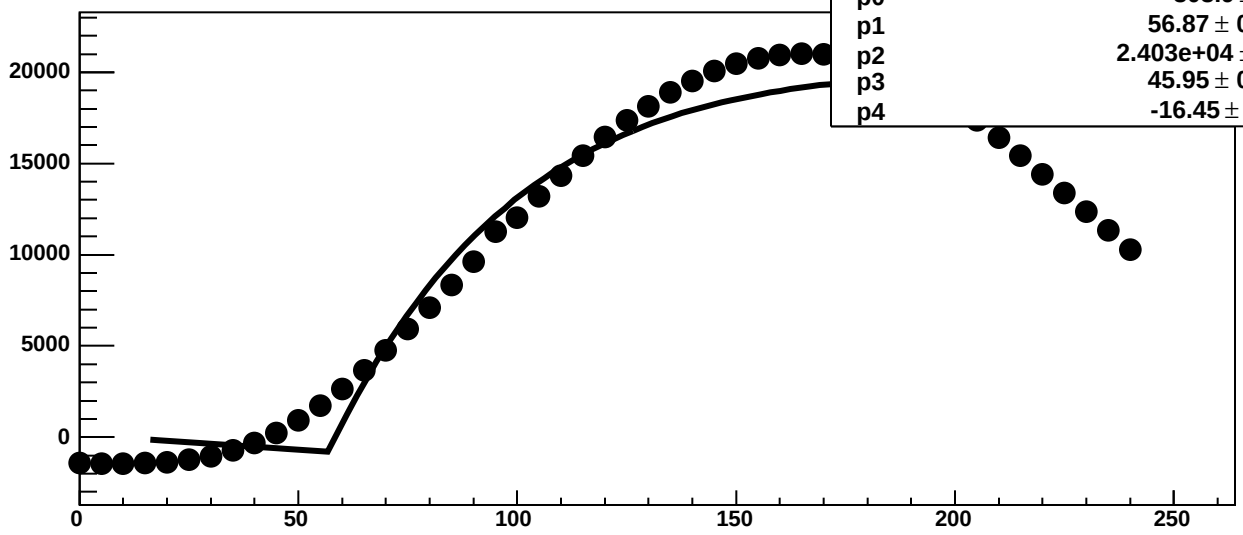
Chip 6, Channel 5, Enable 4, DAC=1600, ADC Noise vs Hold



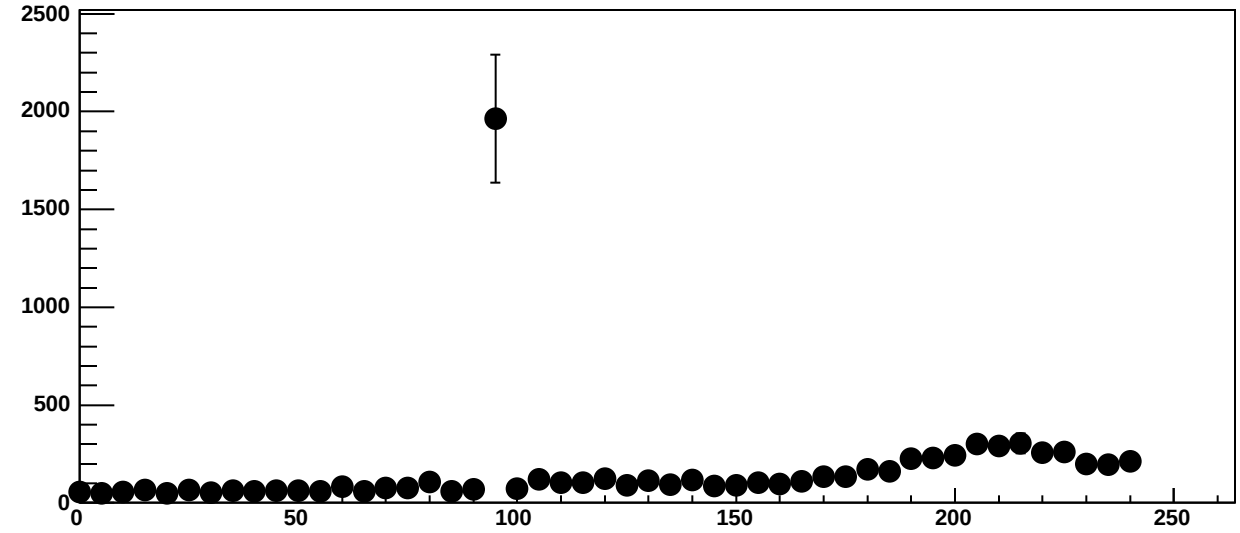
Chip 6, Channel 5, Enable 4, DAC=1600, ADC Residuals vs Hold



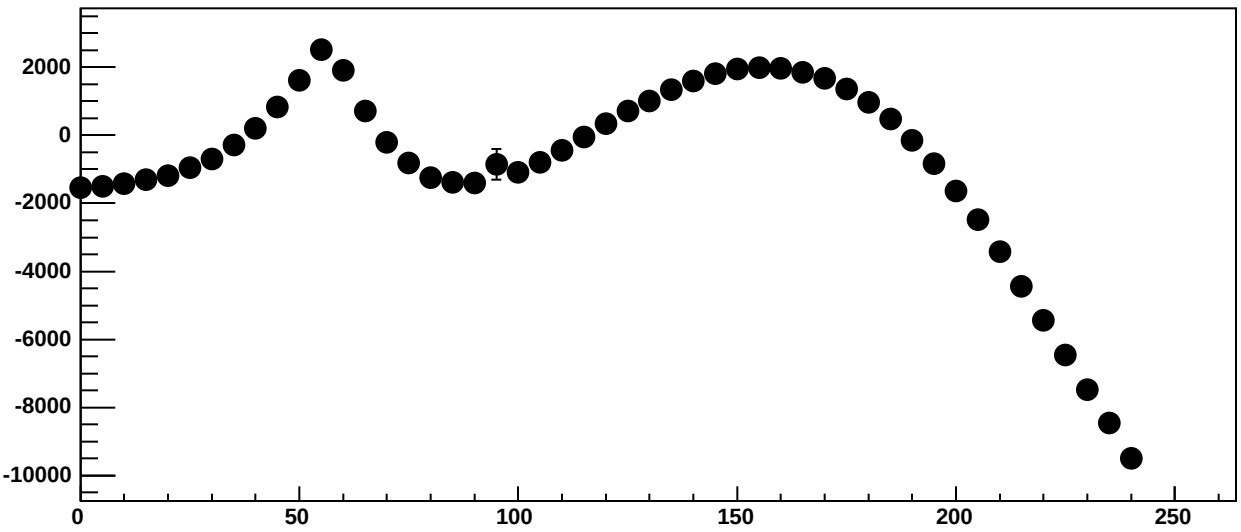
Chip 6, Channel 5, Enable 5, DAC=1600, ADC Mean vs Hold

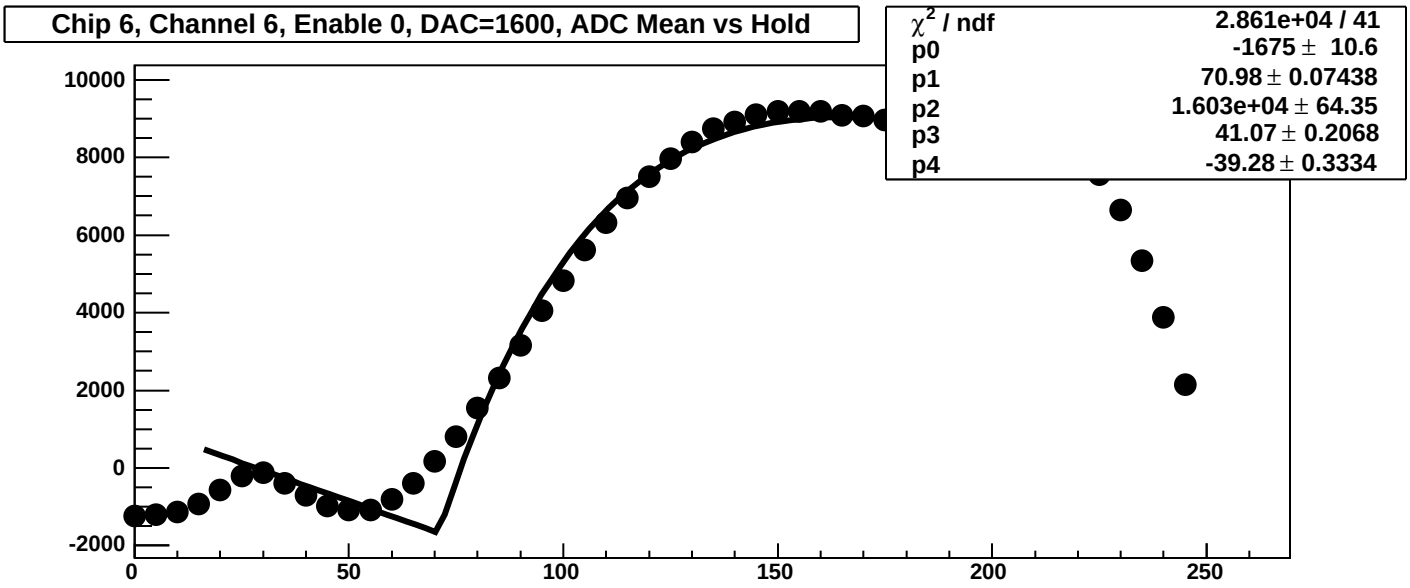


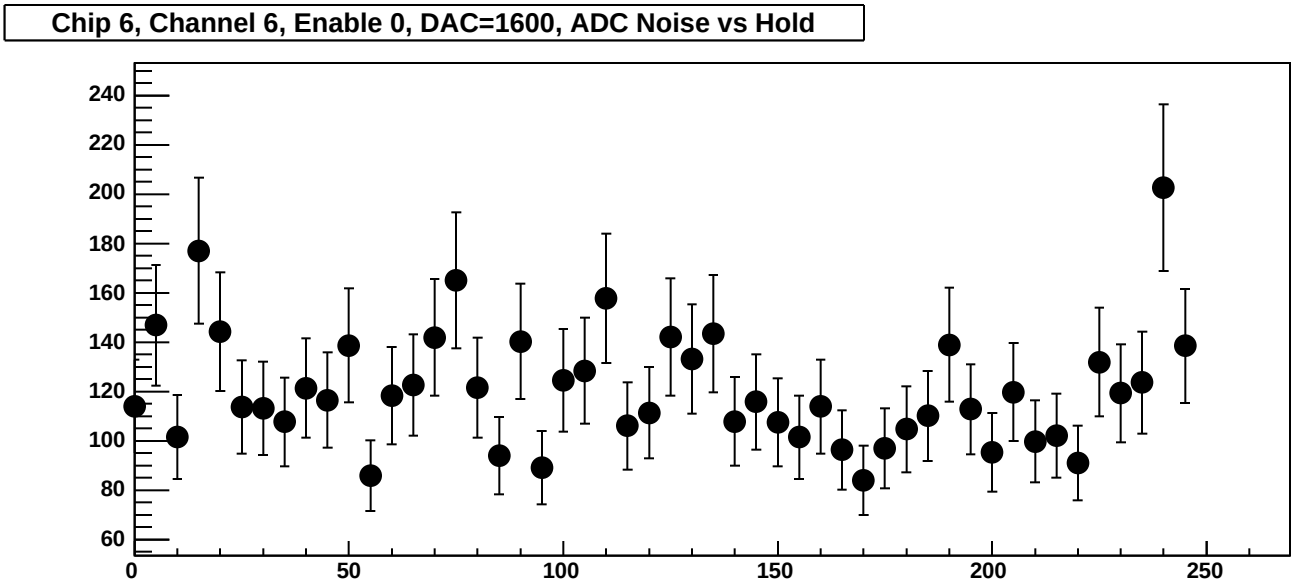
Chip 6, Channel 5, Enable 5, DAC=1600, ADC Noise vs Hold

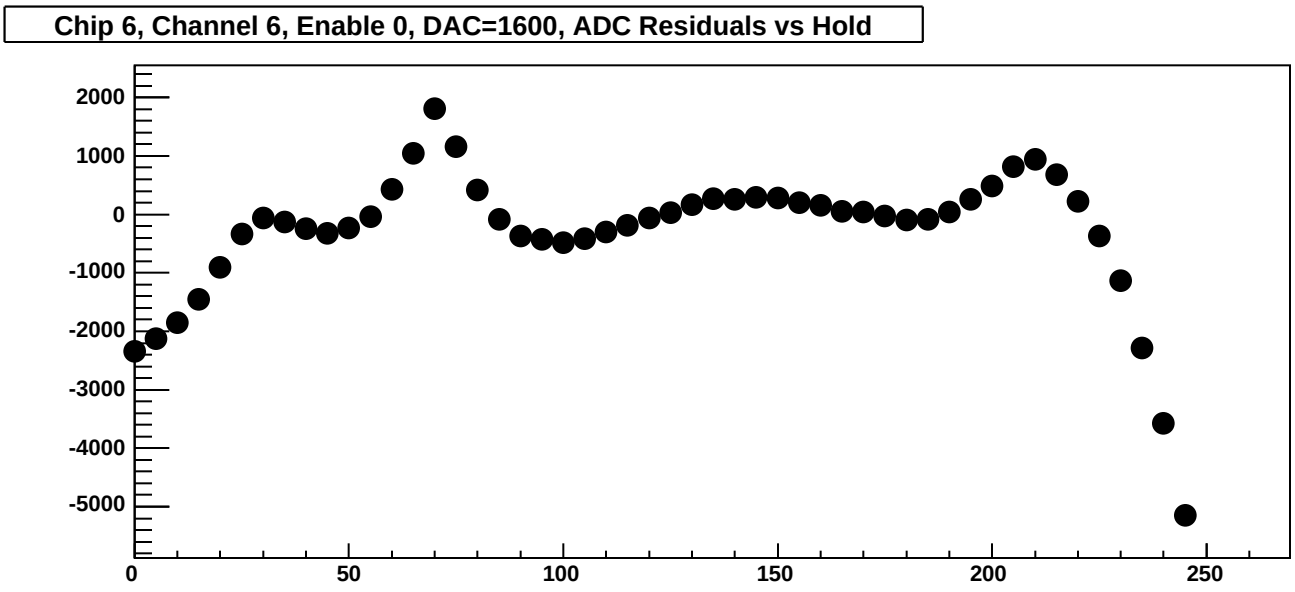


Chip 6, Channel 5, Enable 5, DAC=1600, ADC Residuals vs Hold

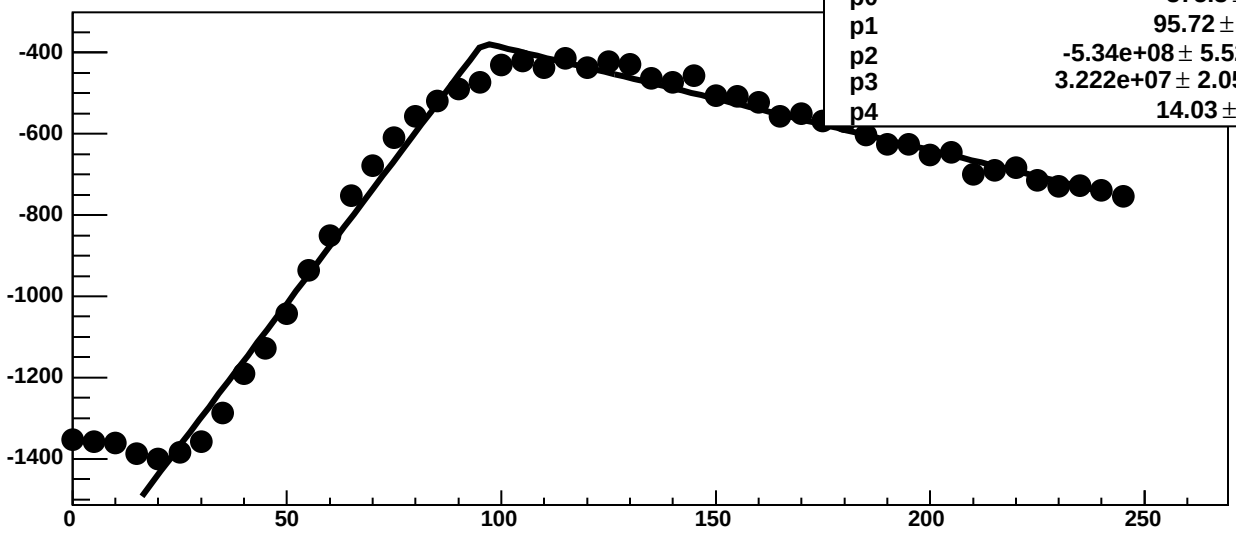



Chip 6, Channel 6, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.861e+04 / 41
p0 -1675 ± 10.6
p1 70.98 ± 0.07438
p2 1.603e+04 ± 64.35
p3 41.07 ± 0.2068
p4 -39.28 ± 0.3334
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

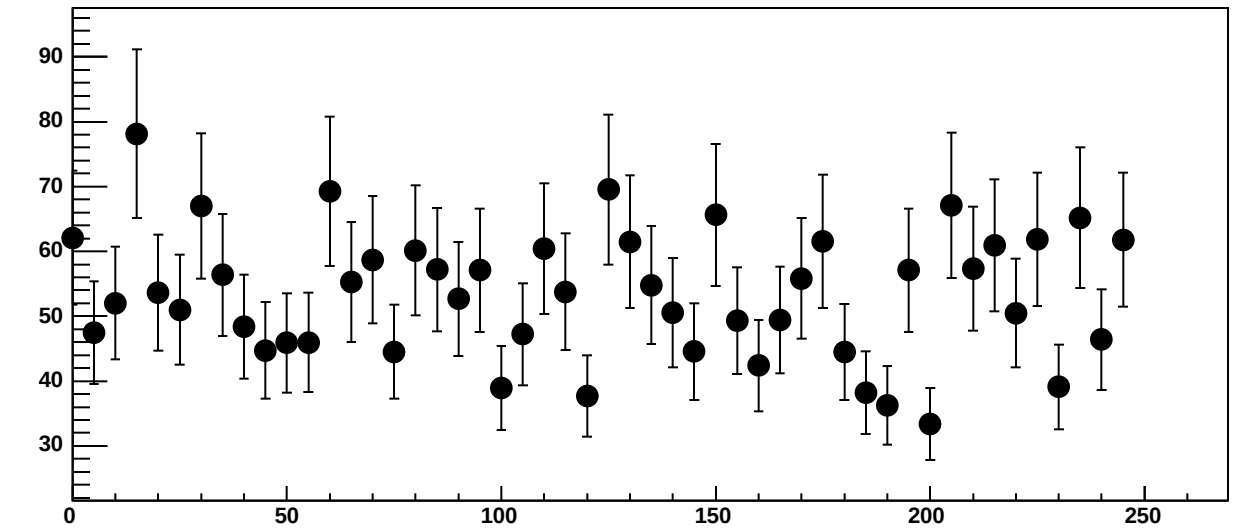

Chip 6, Channel 6, Enable 0, DAC=1600, ADC Noise vs Hold
240
220
200
180
160
140
120
100
80
60
0
50
100
150
200
250


Chip 6, Channel 6, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

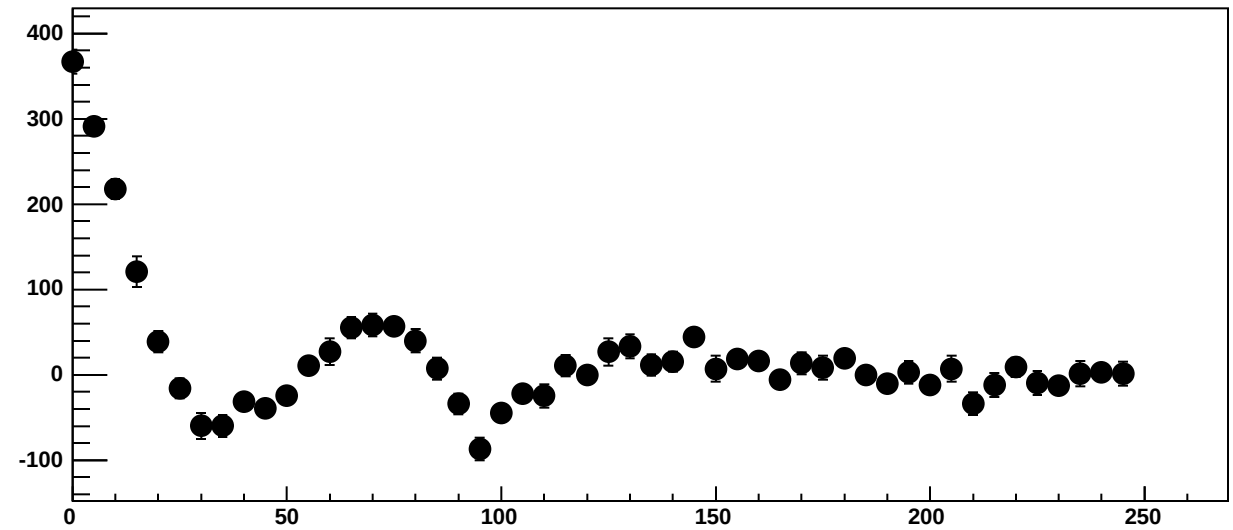
Chip 6, Channel 6, Enable 1, DAC=1600, ADC Mean vs Hold



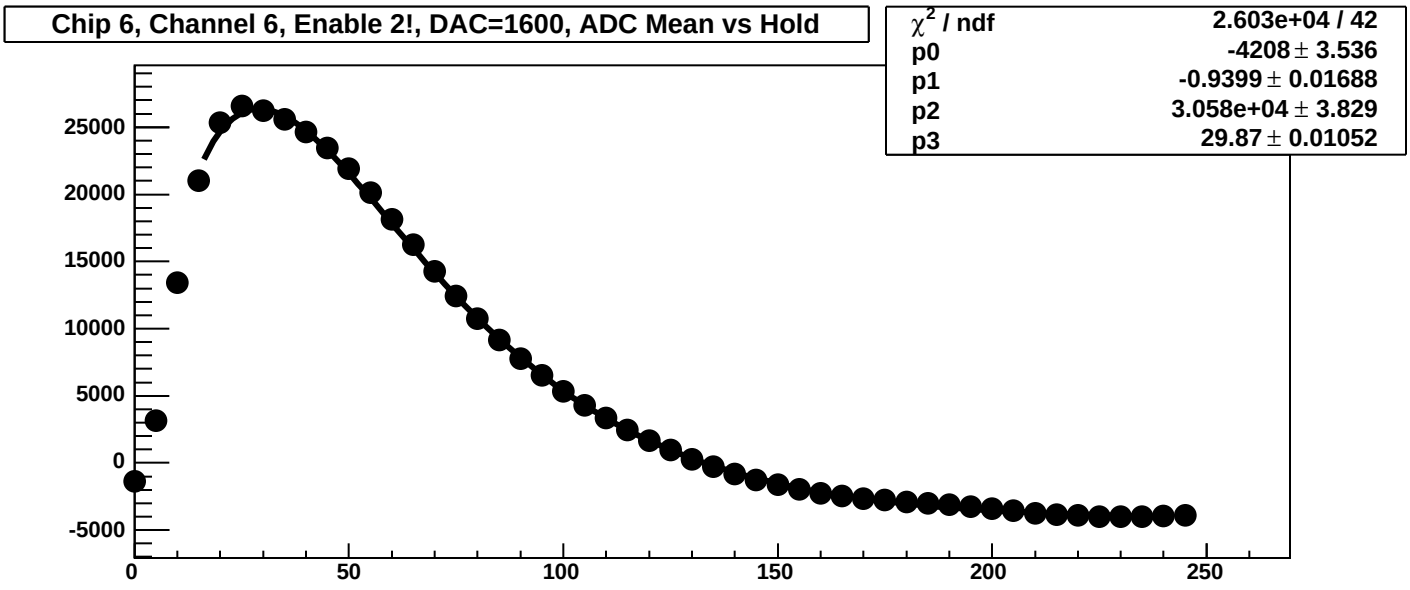
Chip 6, Channel 6, Enable 1, DAC=1600, ADC Noise vs Hold



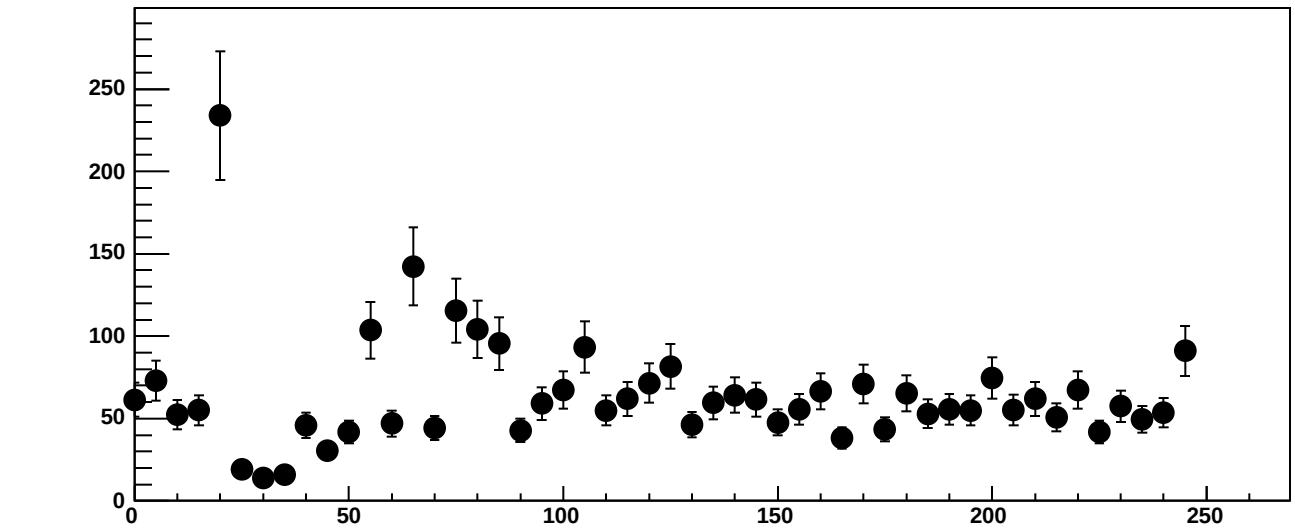
Chip 6, Channel 6, Enable 1, DAC=1600, ADC Residuals vs Hold



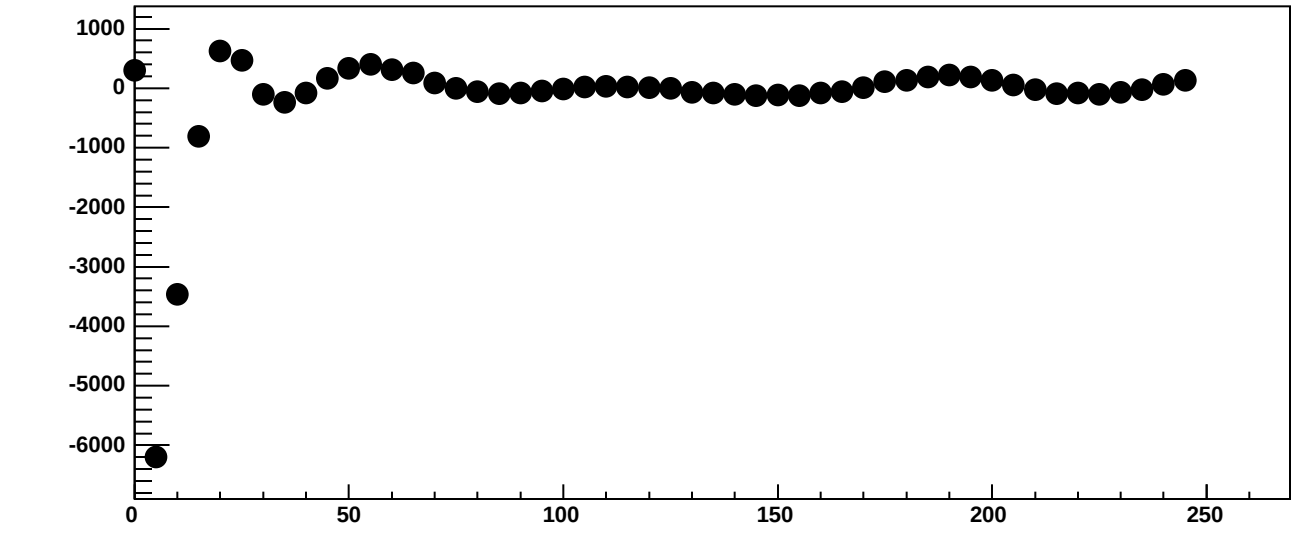
Chip 6, Channel 6, Enable 2!, DAC=1600, ADC Mean vs Hold



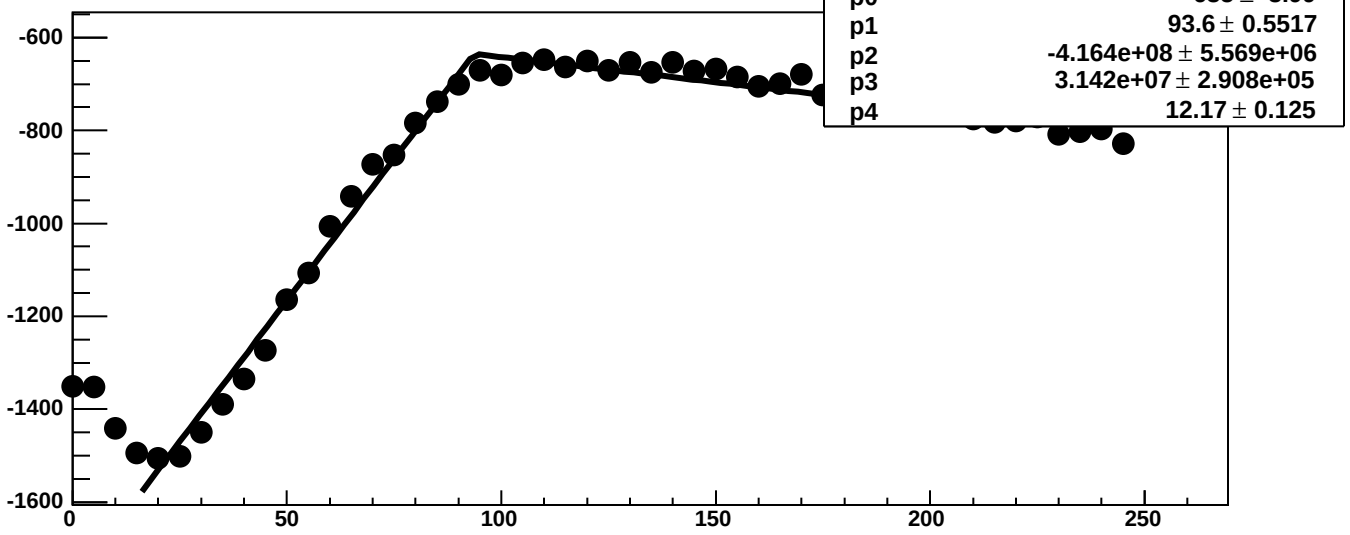
Chip 6, Channel 6, Enable 2!, DAC=1600, ADC Noise vs Hold



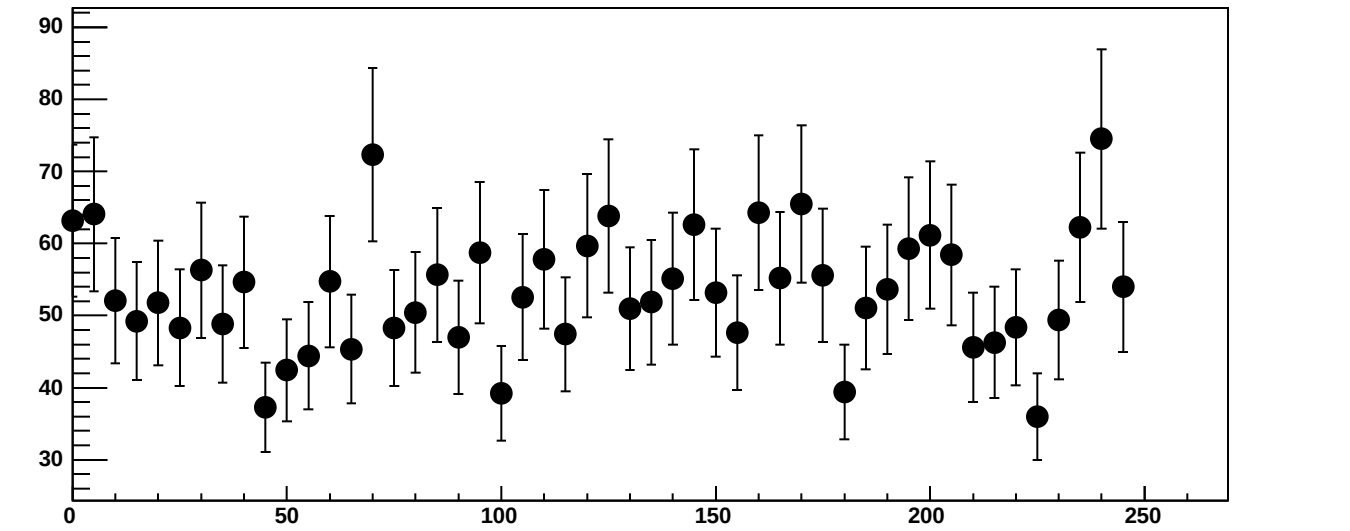
Chip 6, Channel 6, Enable 2!, DAC=1600, ADC Residuals vs Hold



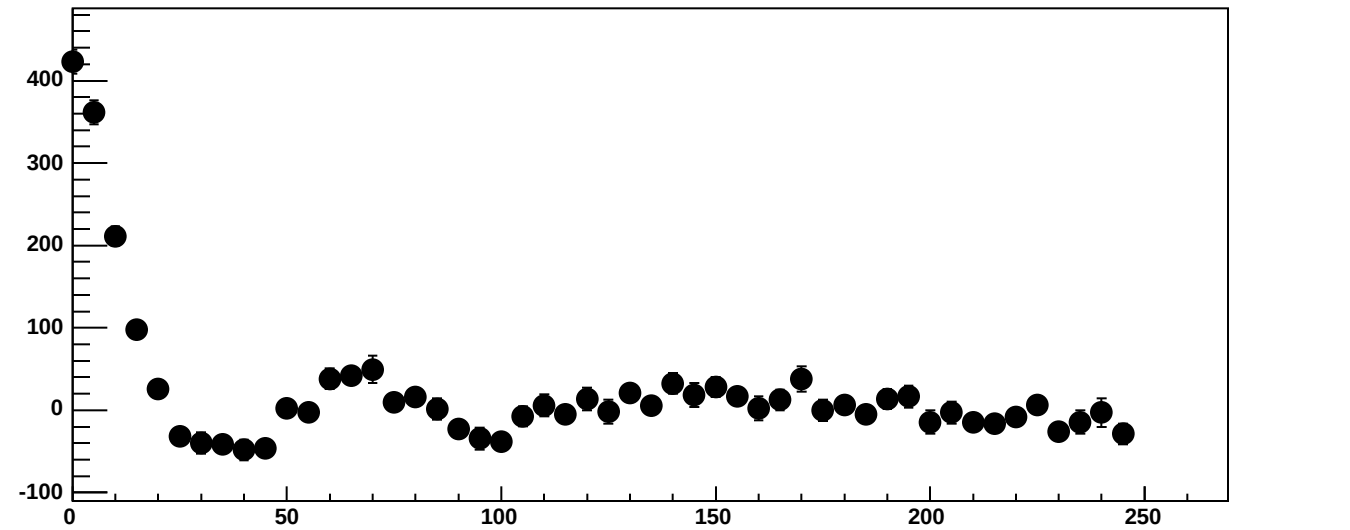
Chip 6, Channel 6, Enable 3, DAC=1600, ADC Mean vs Hold



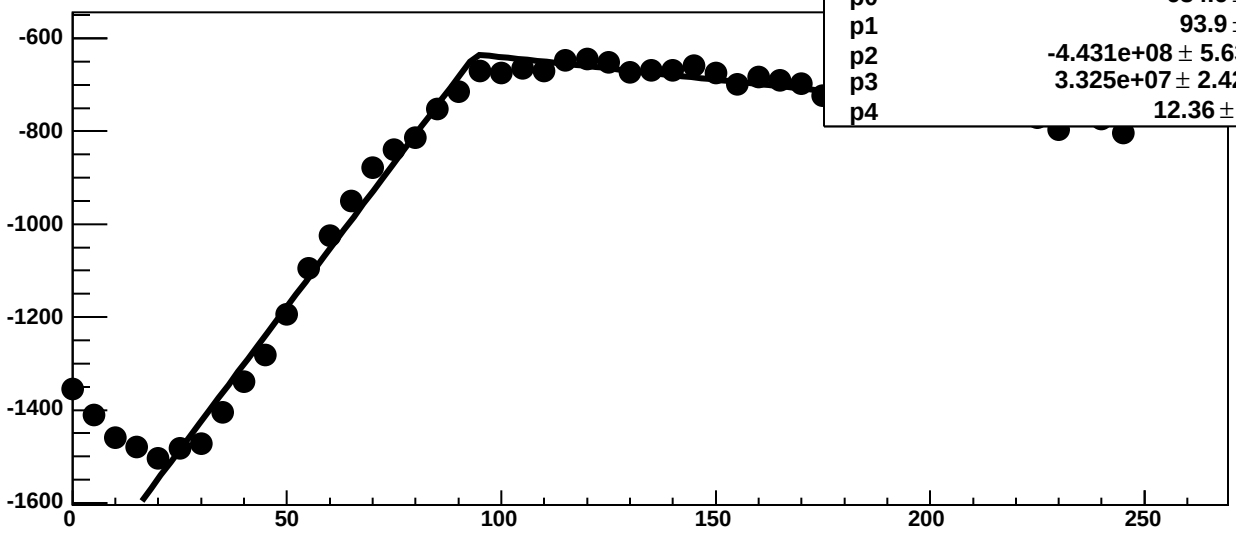
Chip 6, Channel 6, Enable 3, DAC=1600, ADC Noise vs Hold



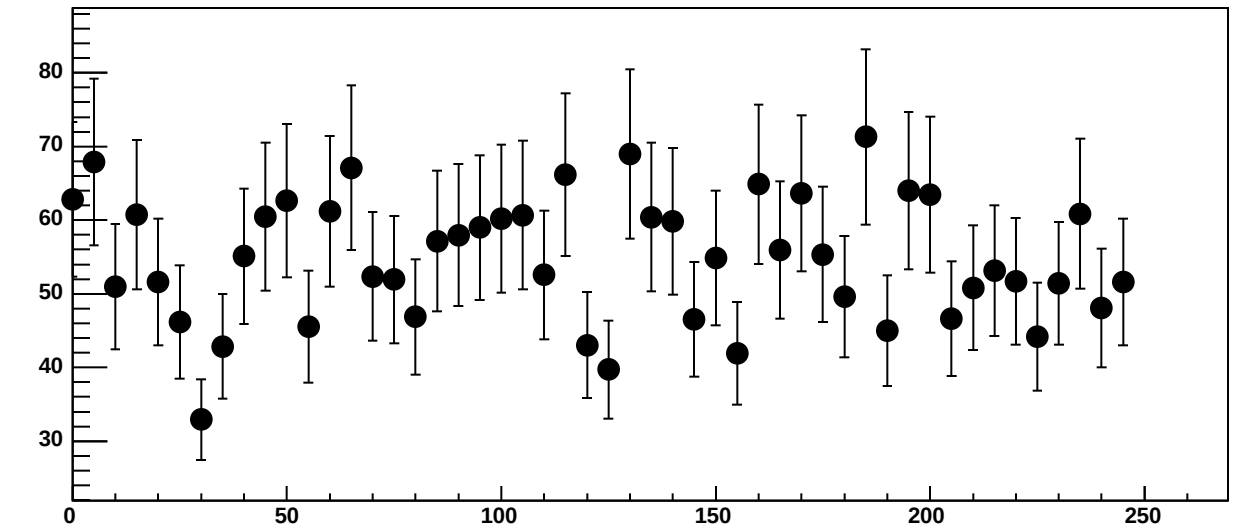
Chip 6, Channel 6, Enable 3, DAC=1600, ADC Residuals vs Hold



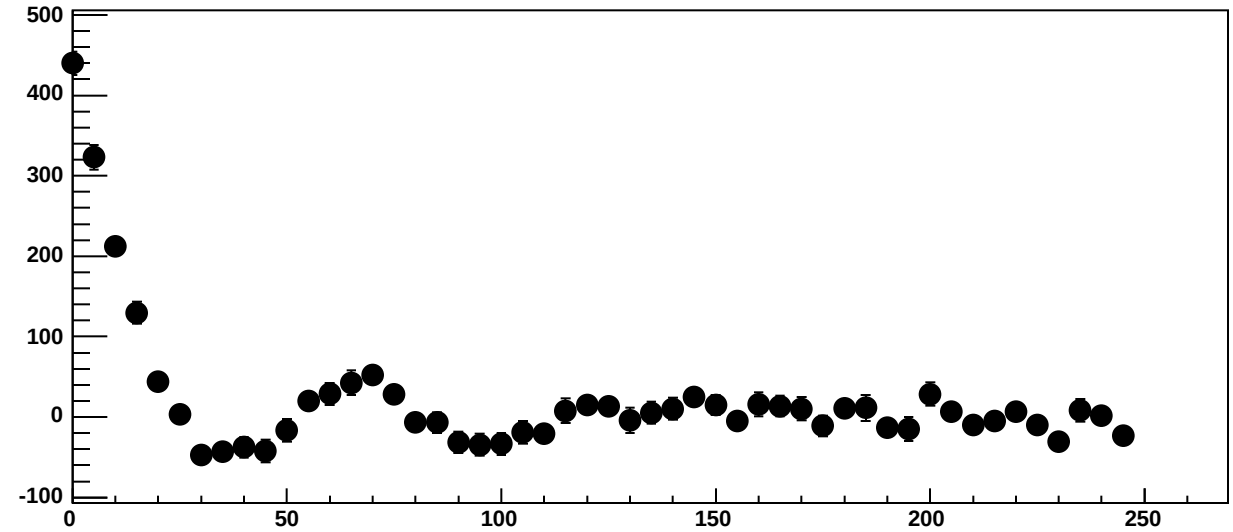
Chip 6, Channel 6, Enable 4, DAC=1600, ADC Mean vs Hold

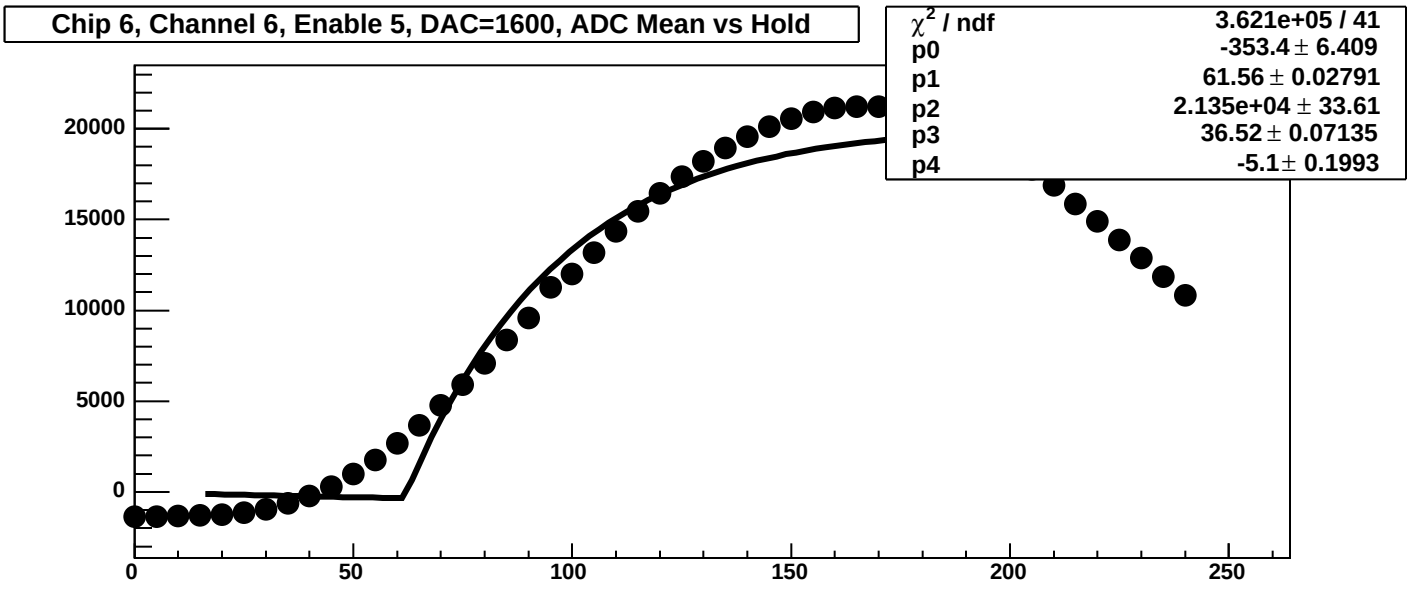


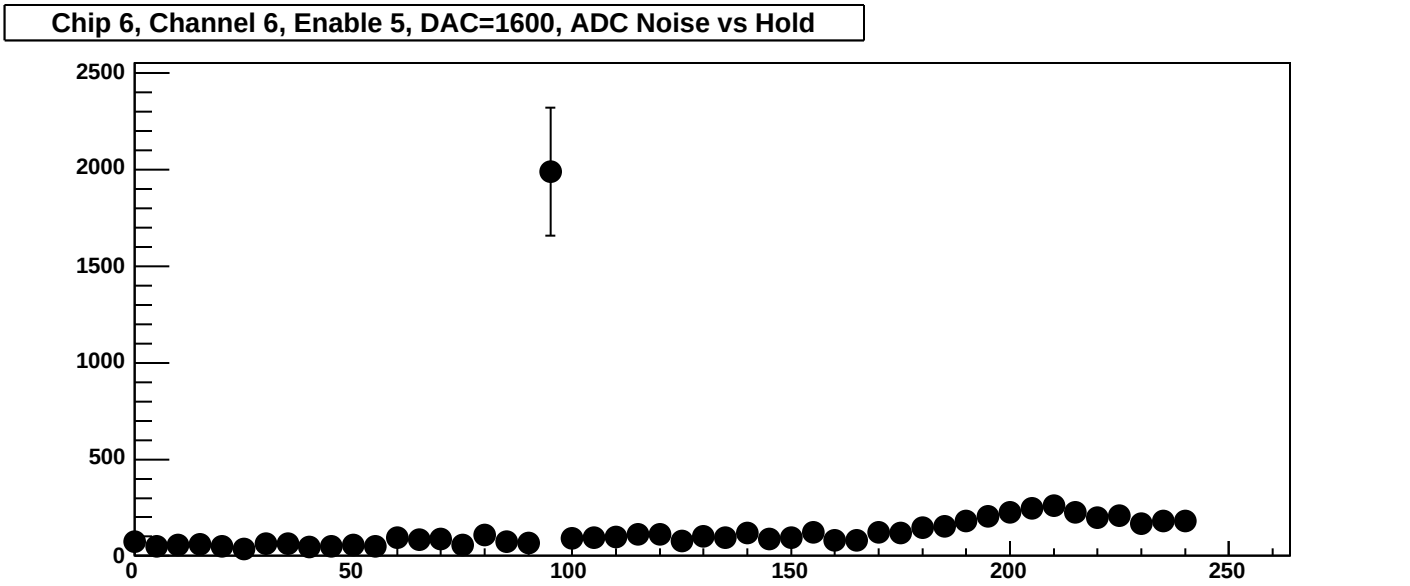
Chip 6, Channel 6, Enable 4, DAC=1600, ADC Noise vs Hold

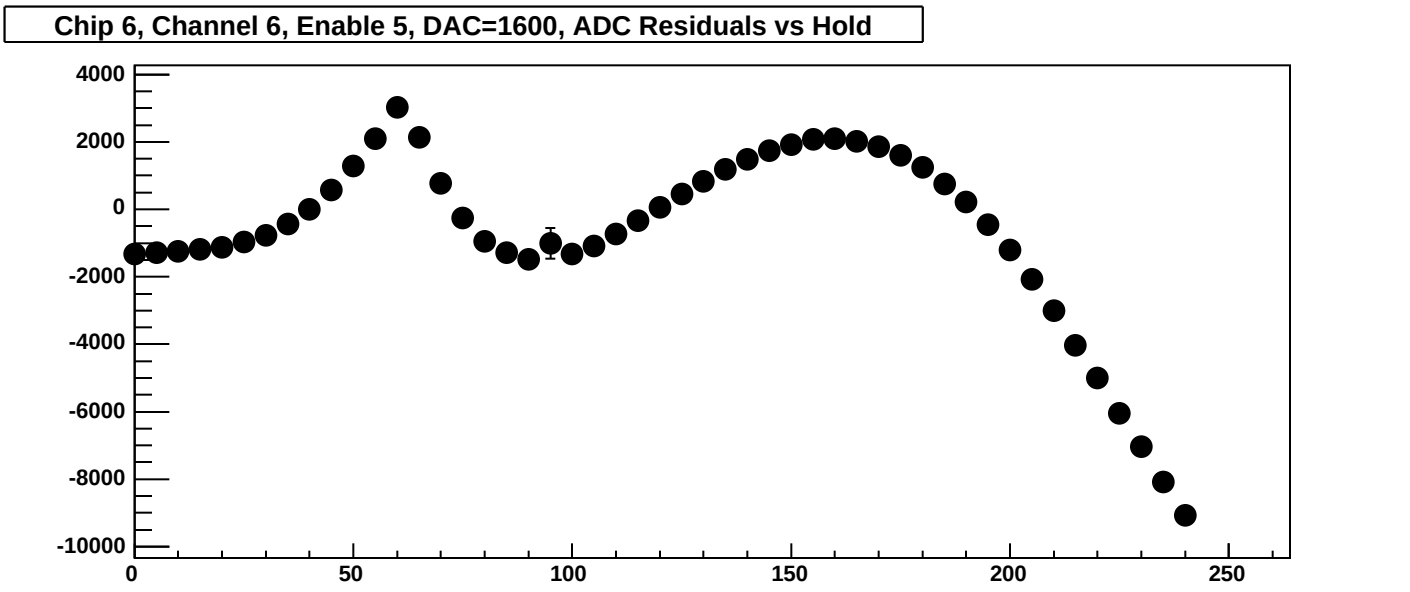


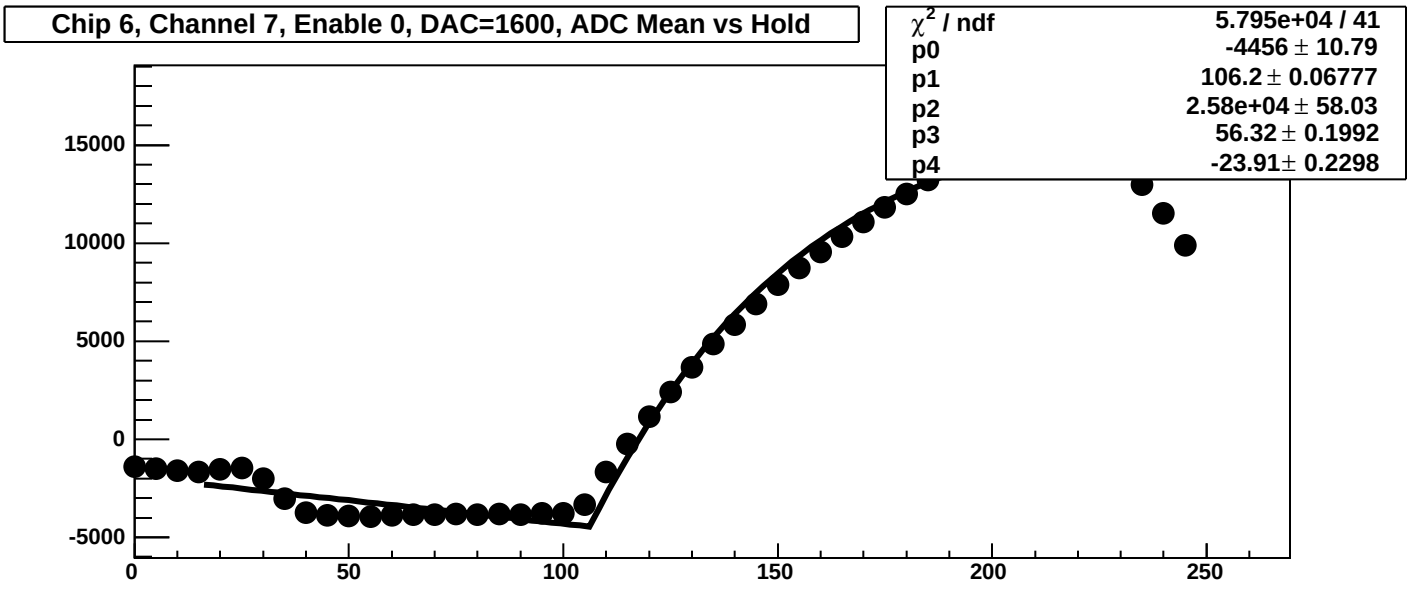
Chip 6, Channel 6, Enable 4, DAC=1600, ADC Residuals vs Hold

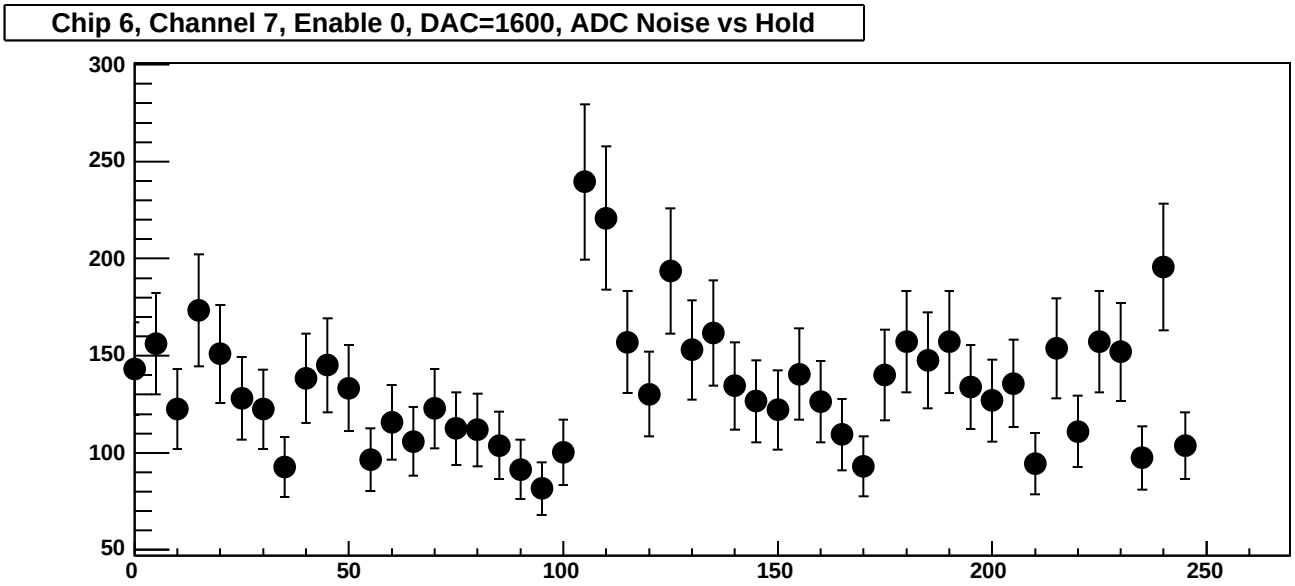


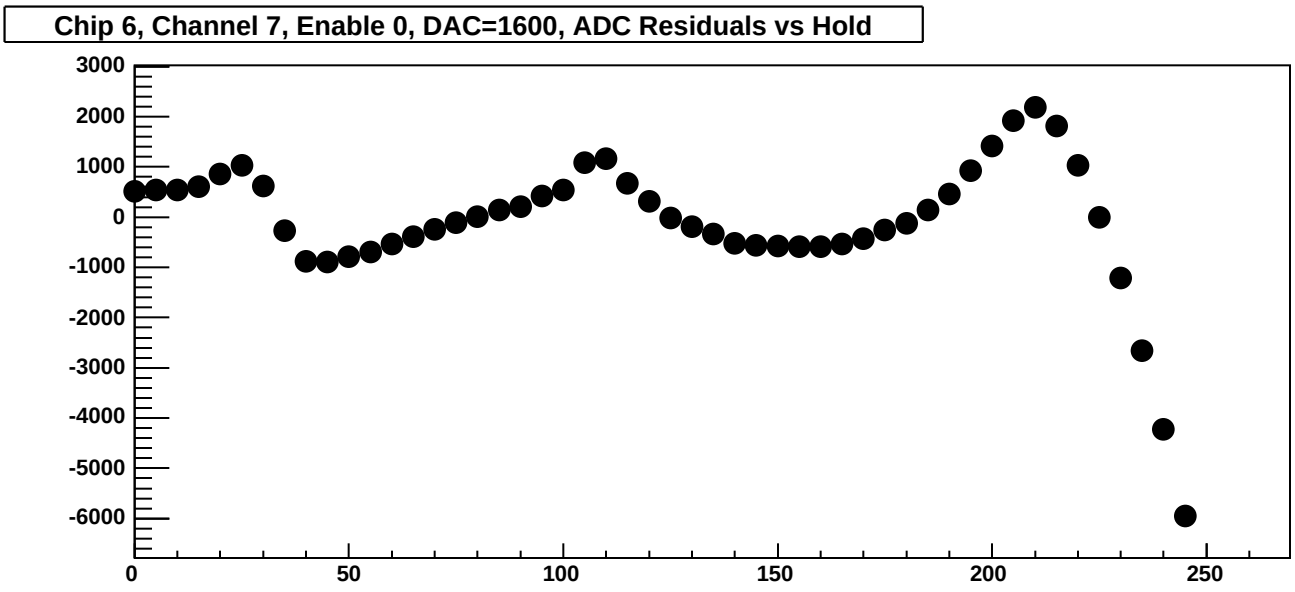

Chip 6, Channel 6, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.621e+05 / 41
p0 -353.4 ± 6.409
p1 61.56 ± 0.02791
p2 2.135e+04 ± 33.61
p3 36.52 ± 0.07135
p4 -5.1 ± 0.1993
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 6, Channel 6, Enable 5, DAC=1600, ADC Noise vs Hold
2500
2000
1500
1000
500
0
0
50
100
150
200
250

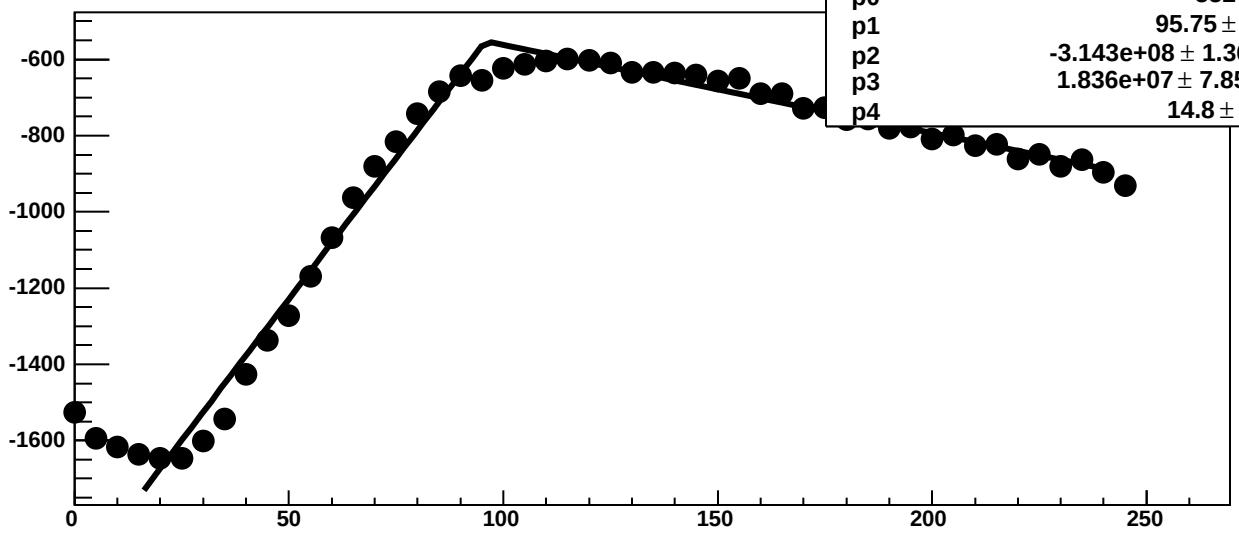

Chip 6, Channel 6, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250


Chip 6, Channel 7, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.795e+04 / 41
p0 -4456 ± 10.79
p1 106.2 ± 0.06777
p2 2.58e+04 ± 58.03
p3 56.32 ± 0.1992
p4 -23.91 ± 0.2298
15000
10000
5000
0
-5000
0
50
100
150
200
250

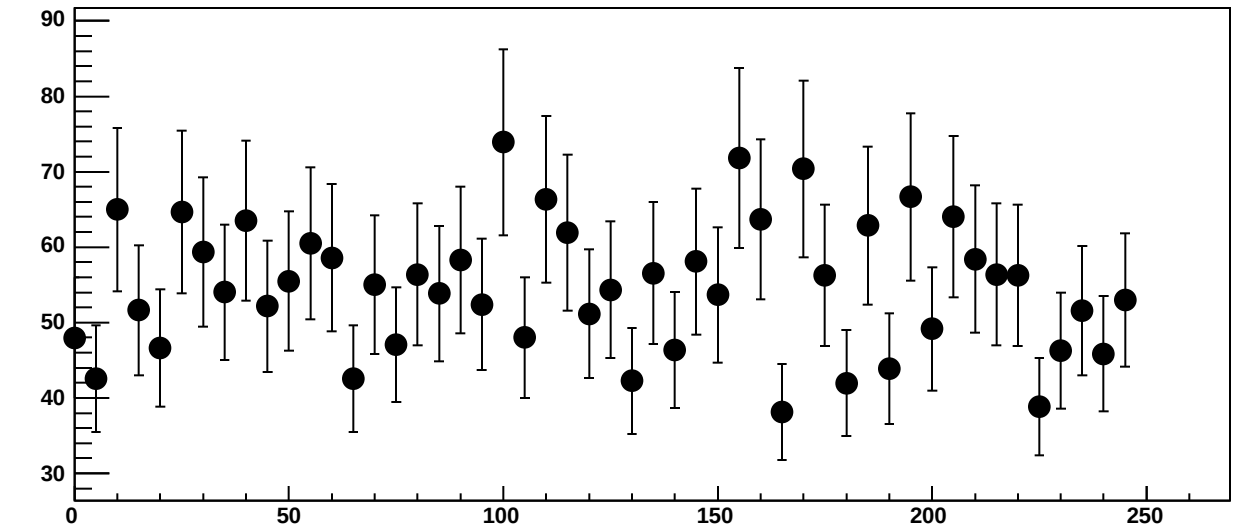

Chip 6, Channel 7, Enable 0, DAC=1600, ADC Noise vs Hold
300
250
200
150
100
50
0
50
100
150
200
250


Chip 6, Channel 7, Enable 0, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
50
100
150
200
250

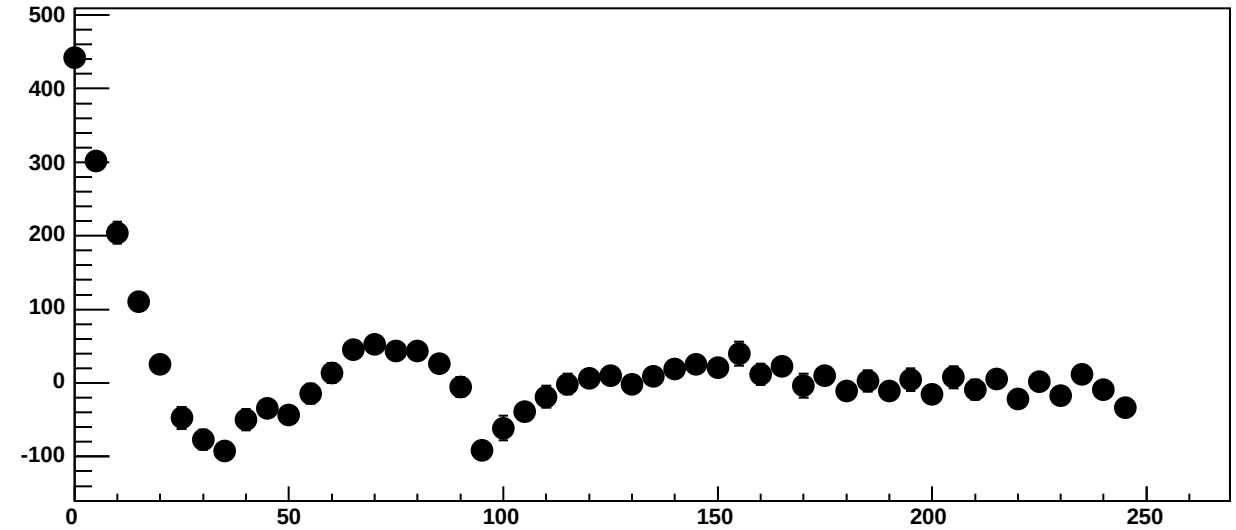
Chip 6, Channel 7, Enable 1, DAC=1600, ADC Mean vs Hold

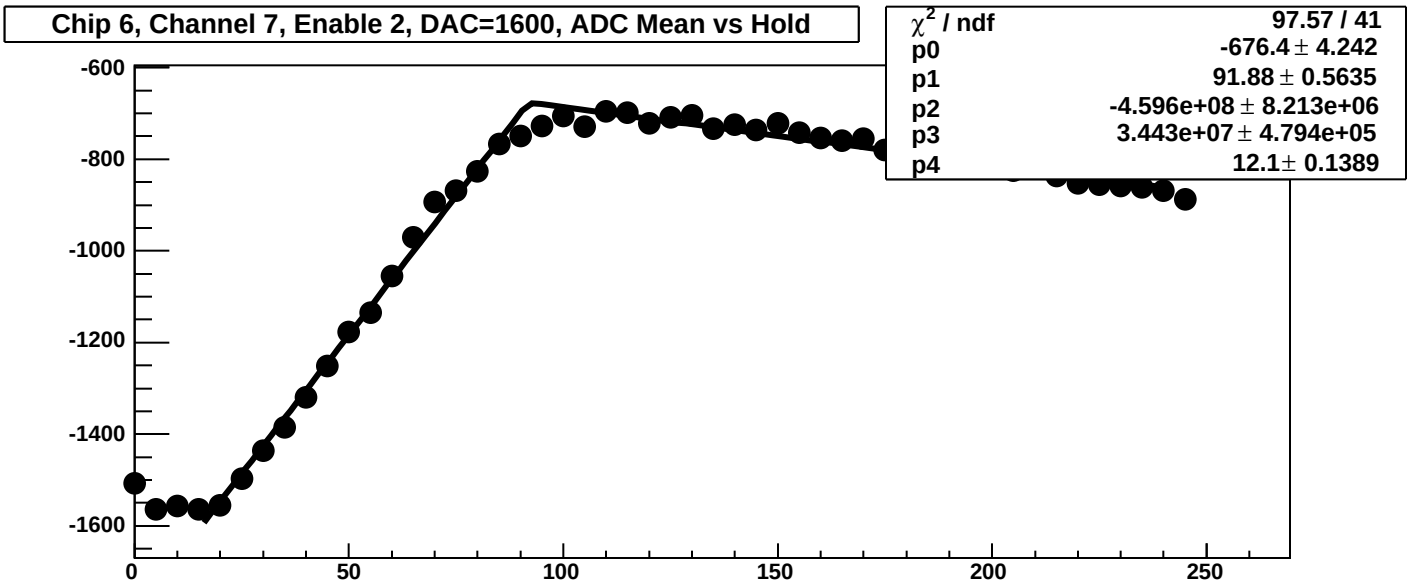


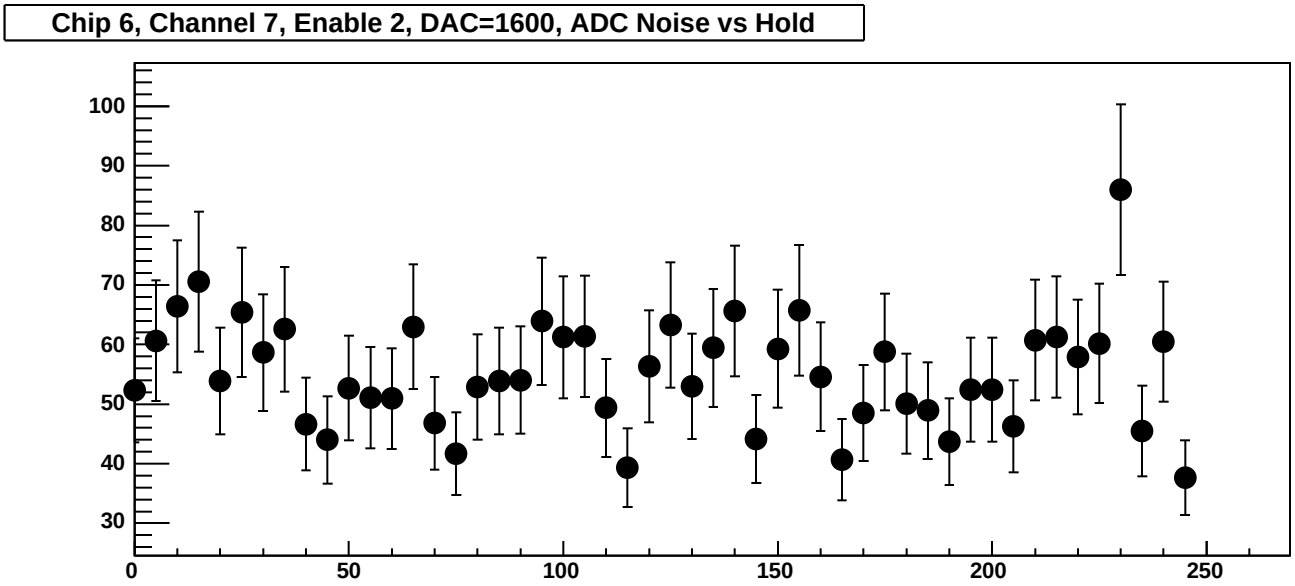
Chip 6, Channel 7, Enable 1, DAC=1600, ADC Noise vs Hold

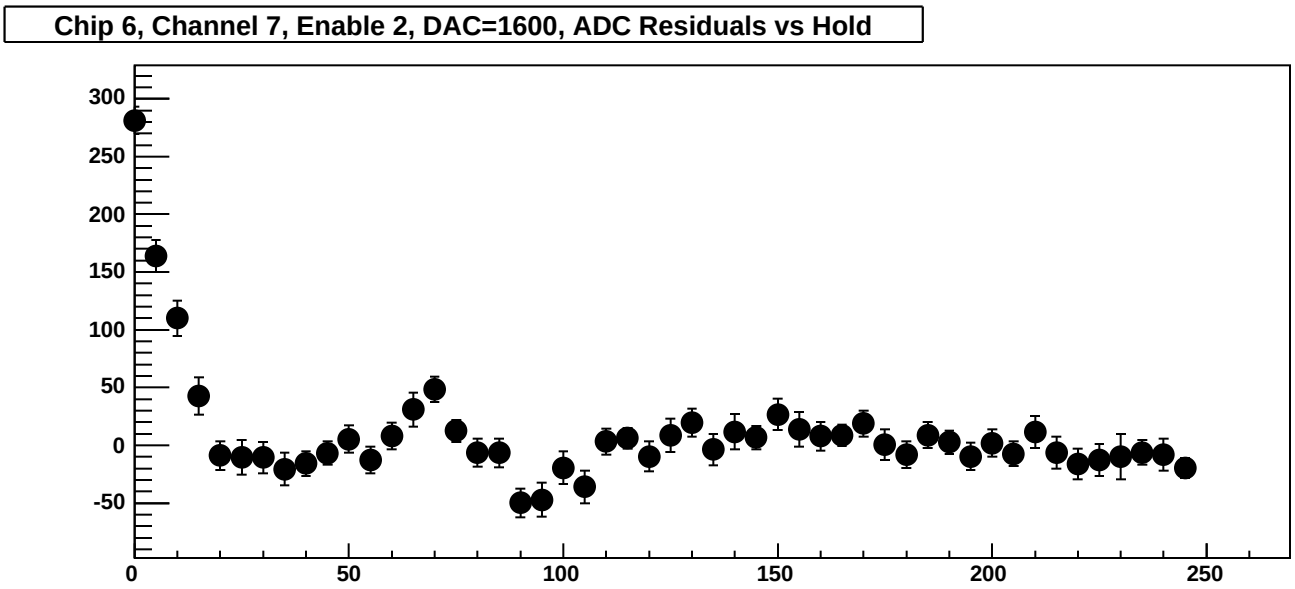


Chip 6, Channel 7, Enable 1, DAC=1600, ADC Residuals vs Hold

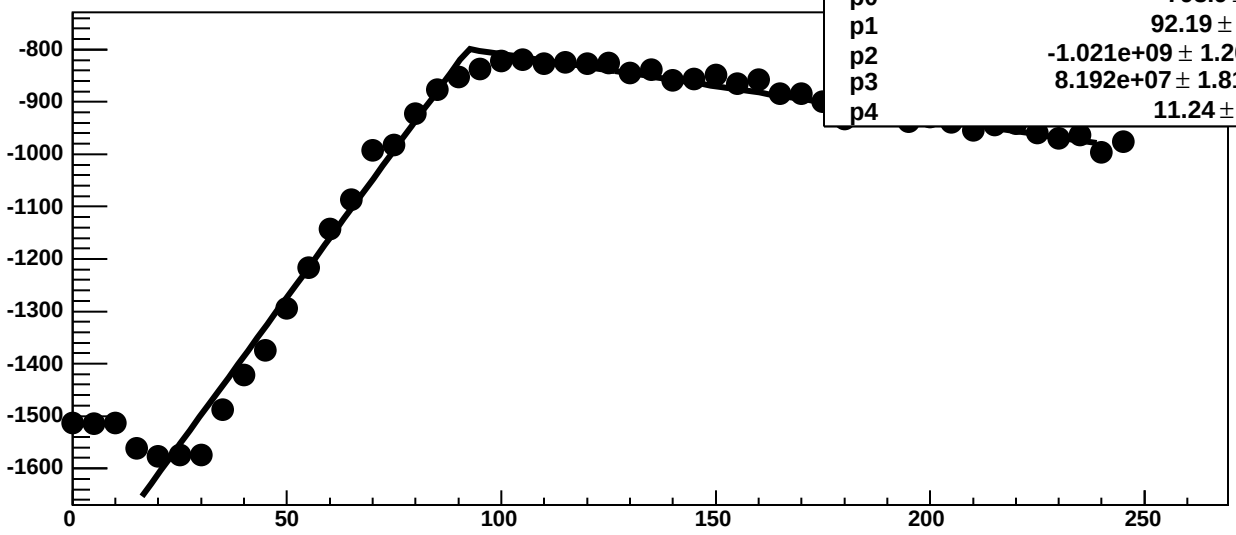



Chip 6, Channel 7, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 97.57 / 41
p0 -676.4 ± 4.242
p1 91.88 ± 0.5635
p2 -4.596e+08 ± 8.213e+06
p3 3.443e+07 ± 4.794e+05
p4 12.1 ± 0.1389
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250

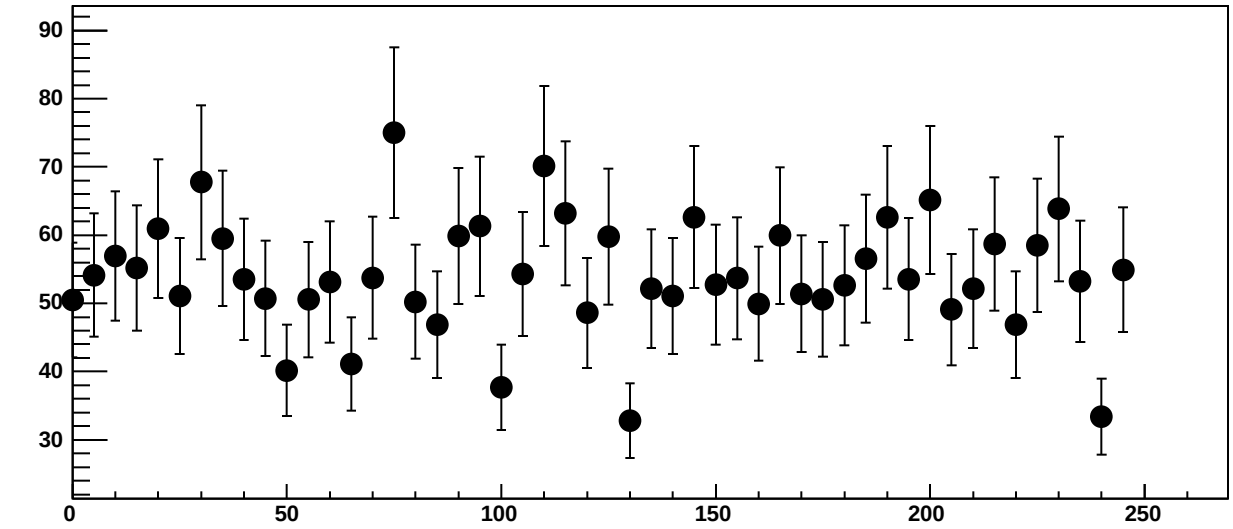

Chip 6, Channel 7, Enable 2, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 6, Channel 7, Enable 2, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
0
50
100
150
200
250

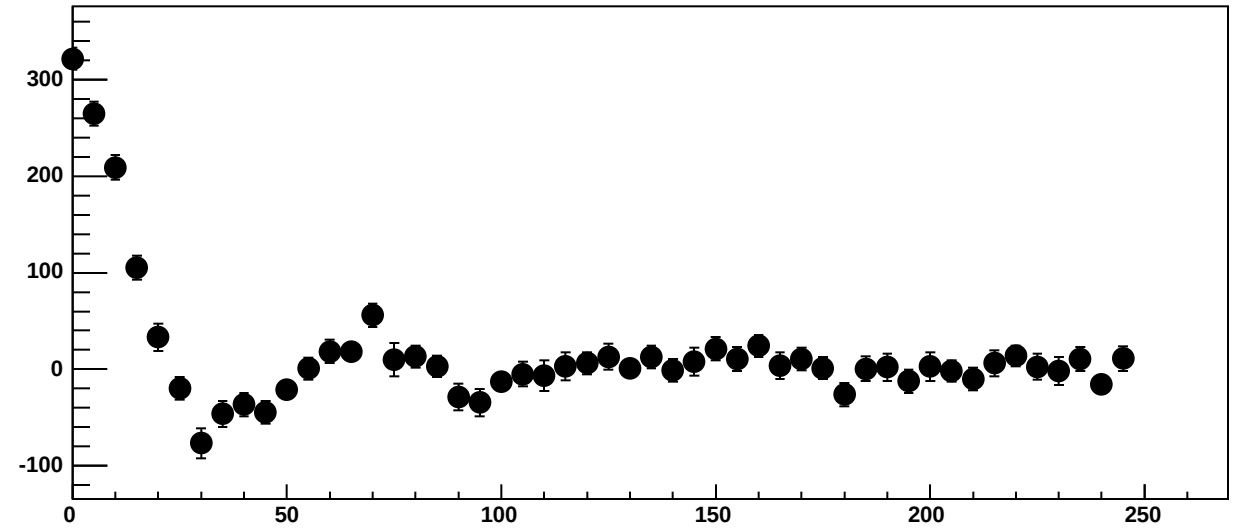
Chip 6, Channel 7, Enable 3, DAC=1600, ADC Mean vs Hold



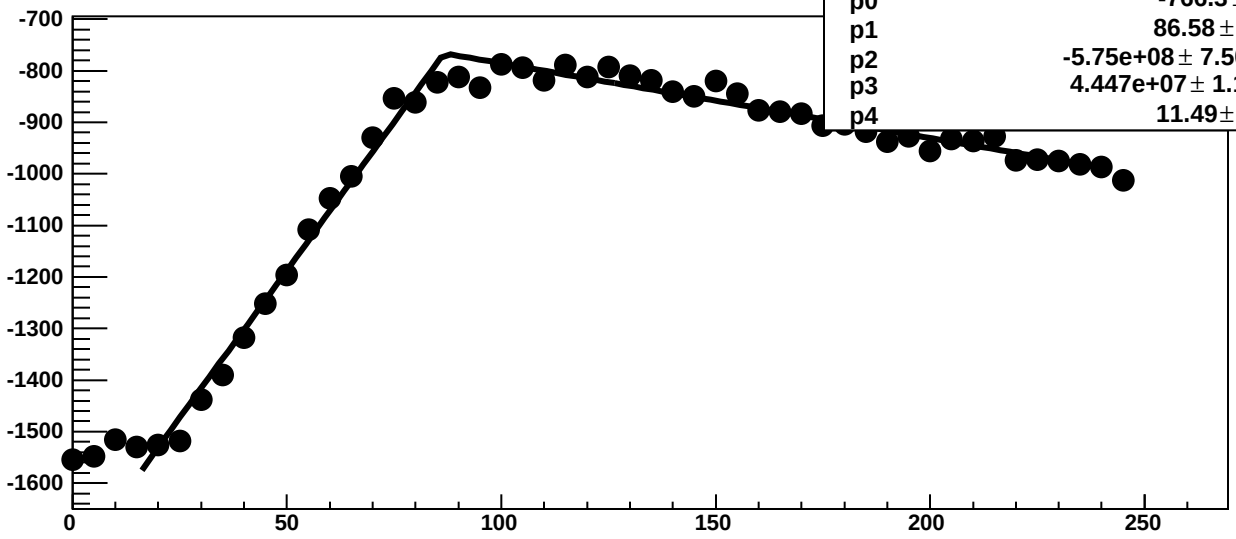
Chip 6, Channel 7, Enable 3, DAC=1600, ADC Noise vs Hold



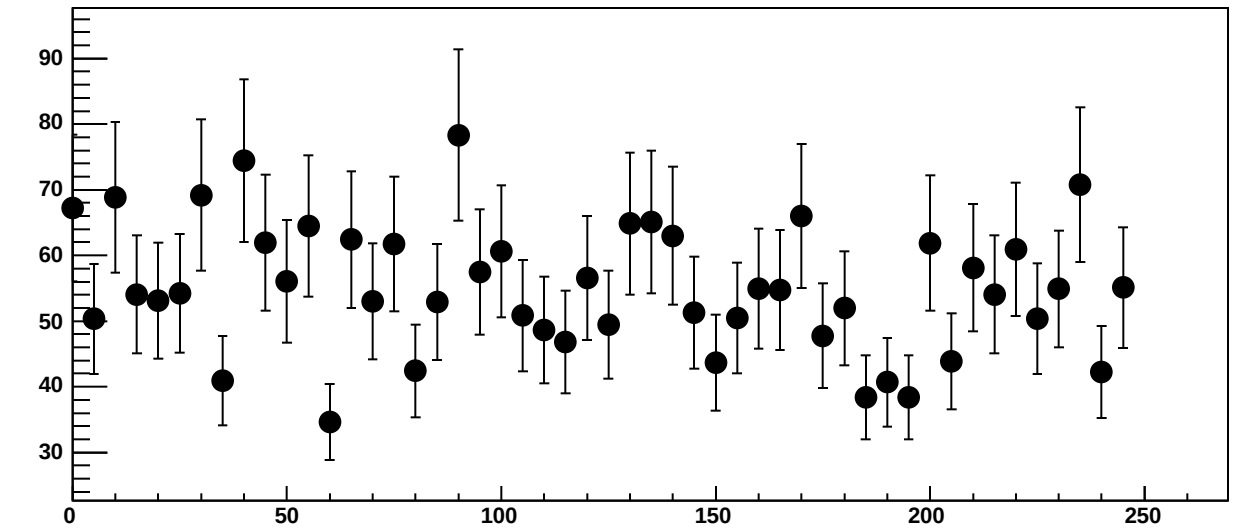
Chip 6, Channel 7, Enable 3, DAC=1600, ADC Residuals vs Hold



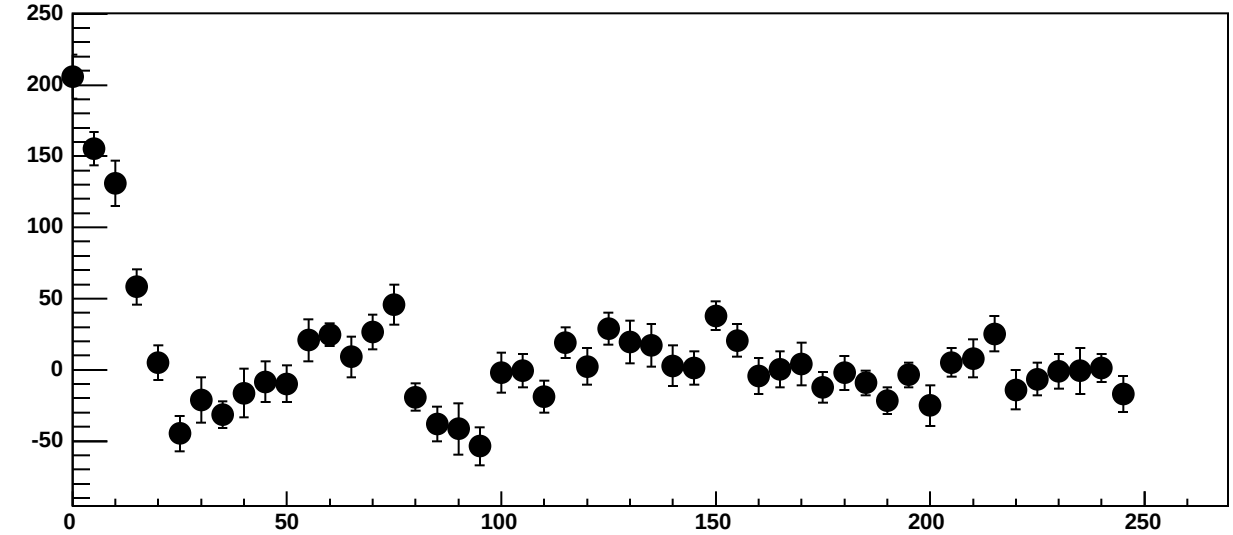
Chip 6, Channel 7, Enable 4, DAC=1600, ADC Mean vs Hold

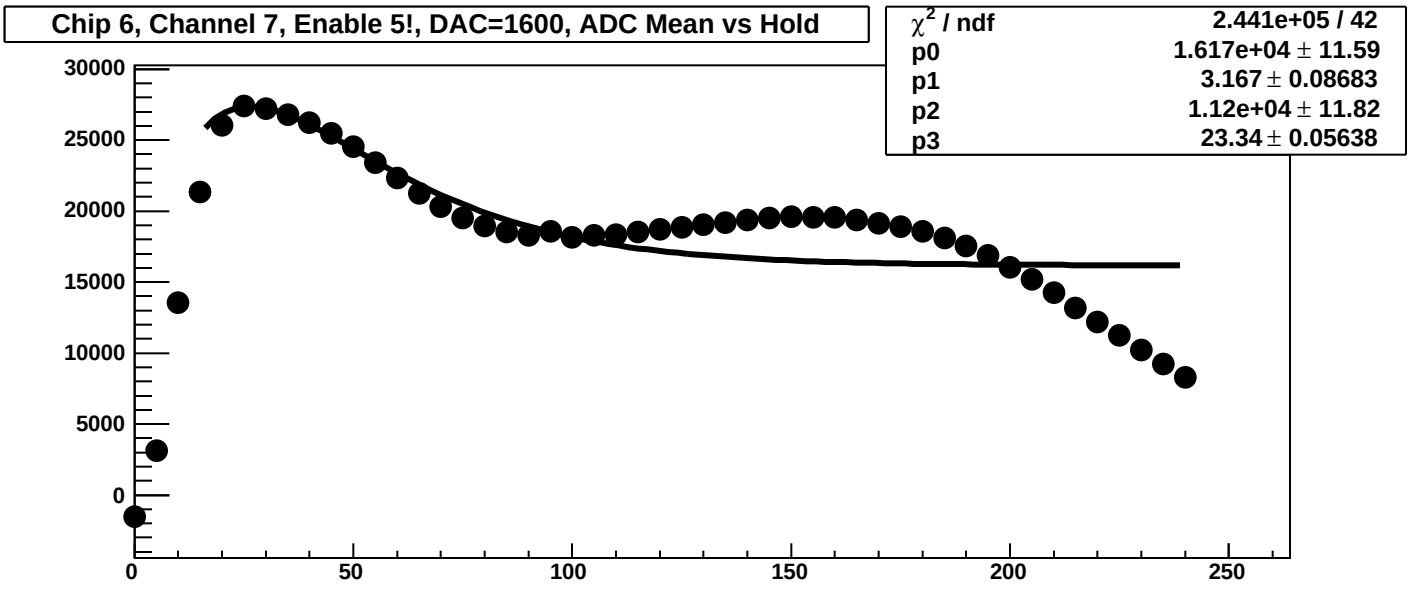


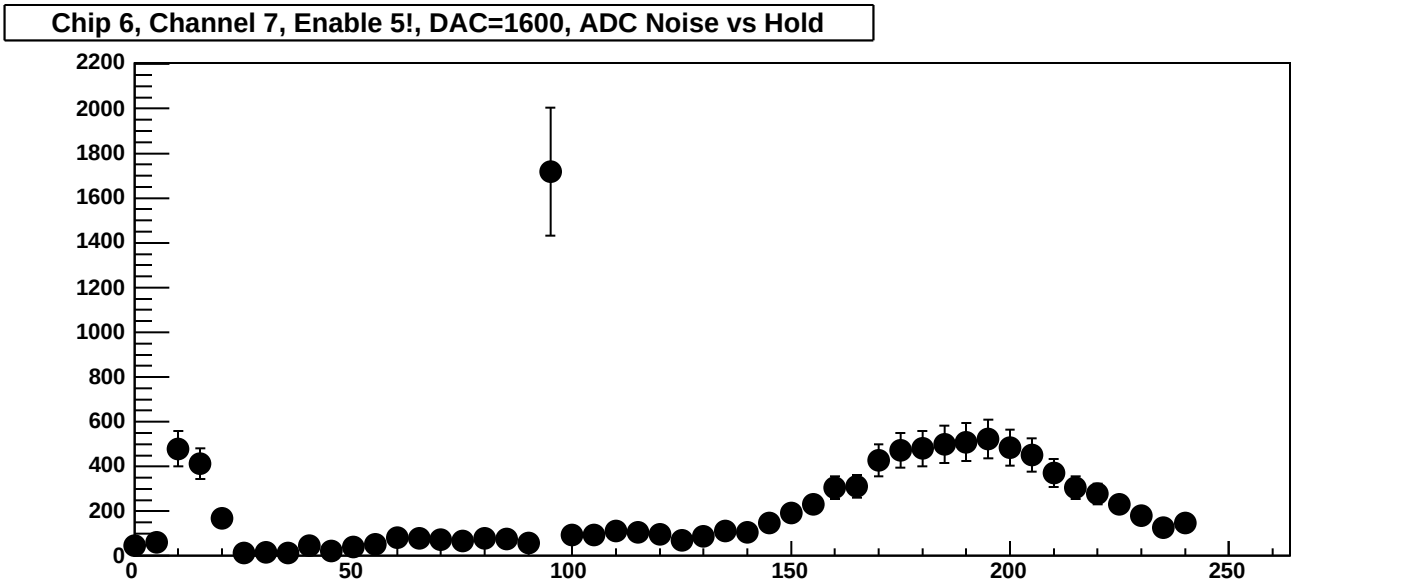
Chip 6, Channel 7, Enable 4, DAC=1600, ADC Noise vs Hold

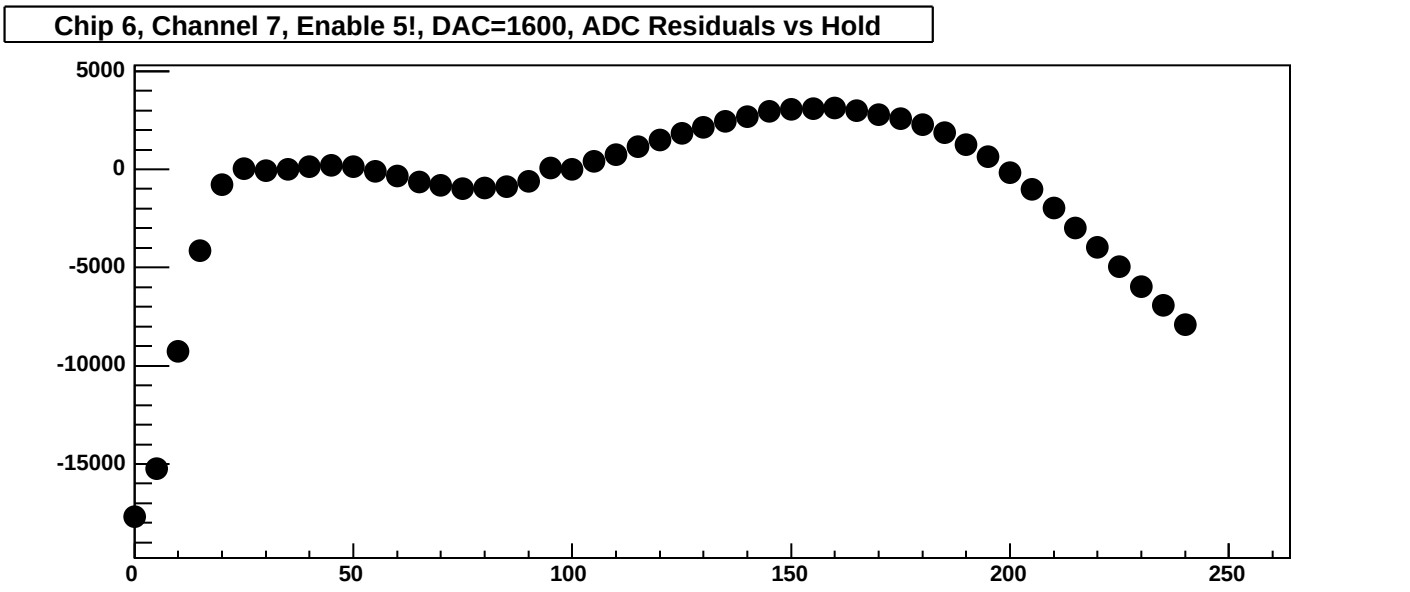


Chip 6, Channel 7, Enable 4, DAC=1600, ADC Residuals vs Hold

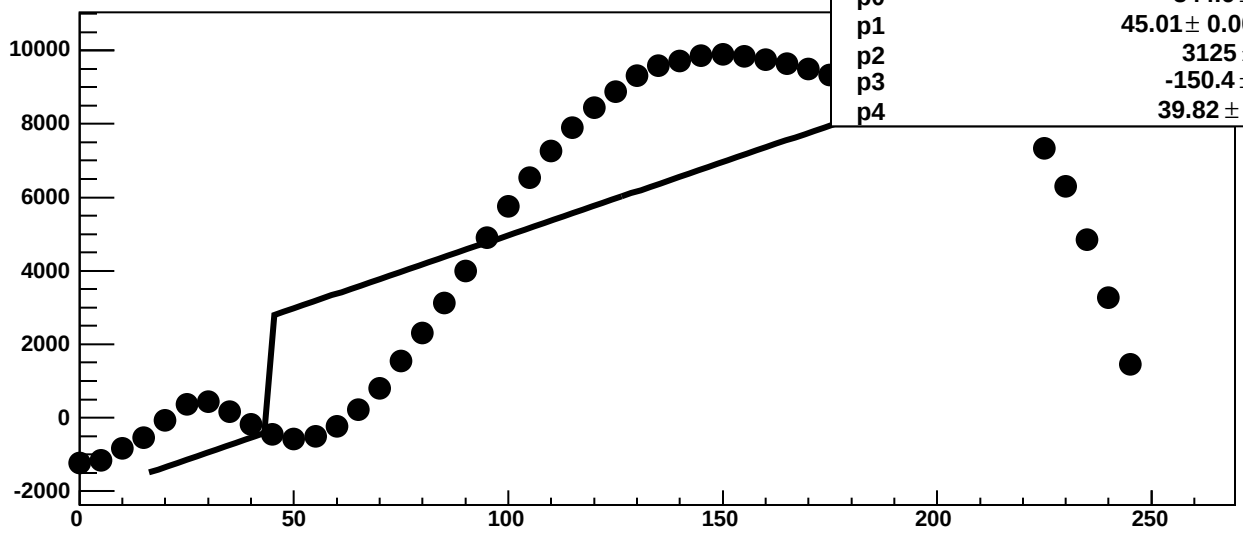



Chip 6, Channel 7, Enable 5!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.441e+05 / 42
p0 1.617e+04 ± 11.59
p1 3.167 ± 0.08683
p2 1.12e+04 ± 11.82
p3 23.34 ± 0.05638
30000
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

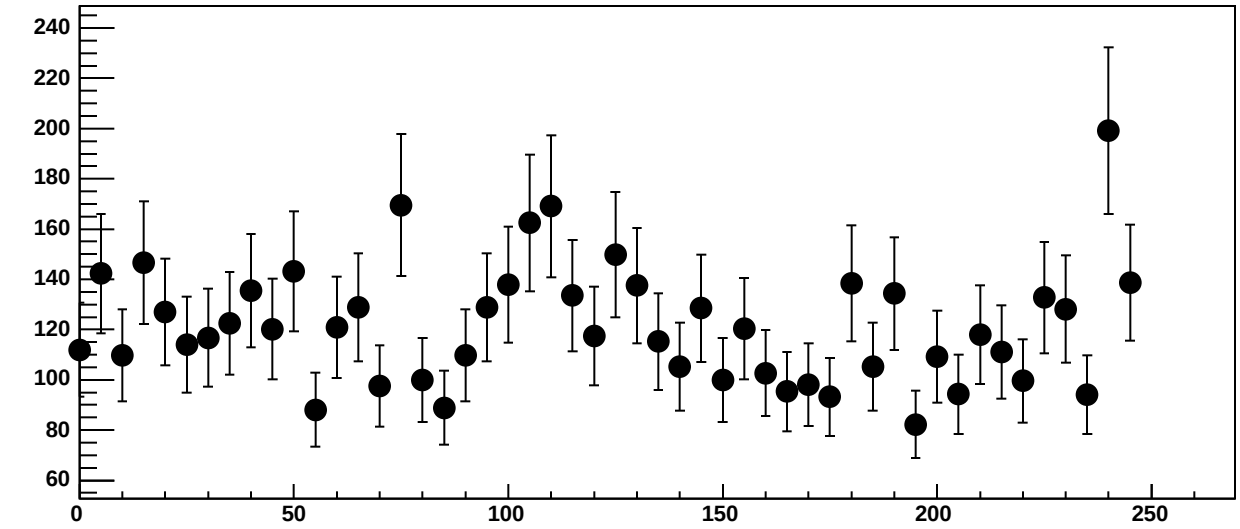

Chip 6, Channel 7, Enable 5!, DAC=1600, ADC Noise vs Hold
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 6, Channel 7, Enable 5!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
0
50
100
150
200
250

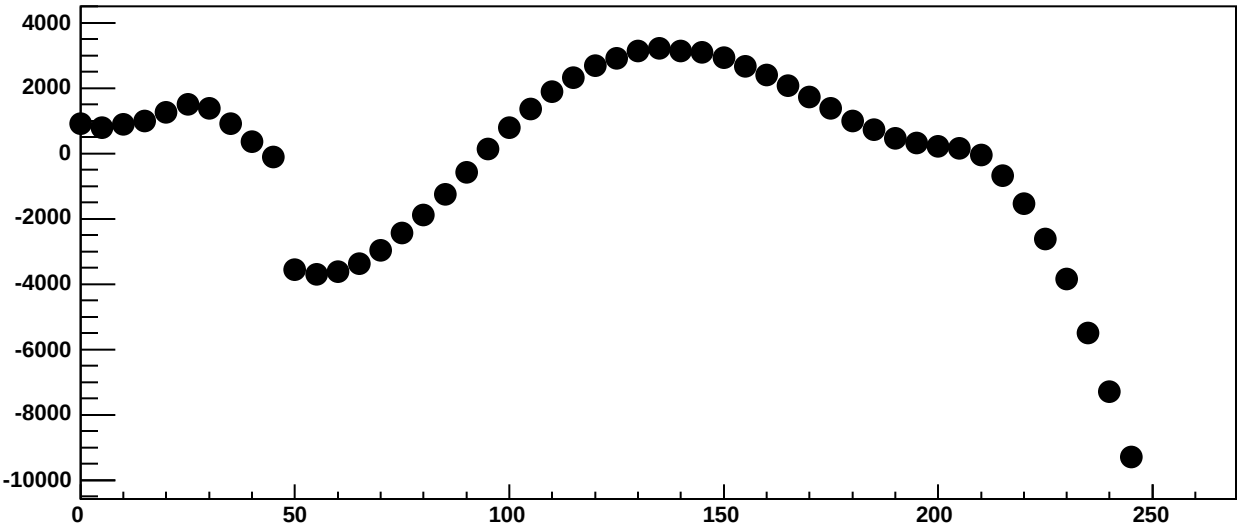
Chip 6, Channel 8, Enable 0, DAC=1600, ADC Mean vs Hold



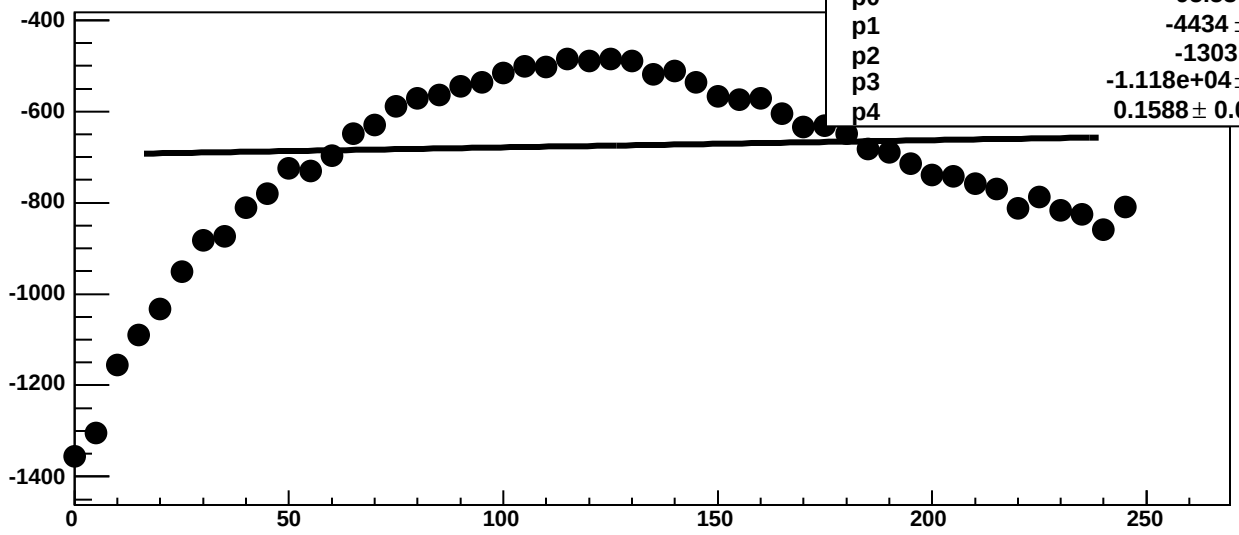
Chip 6, Channel 8, Enable 0, DAC=1600, ADC Noise vs Hold



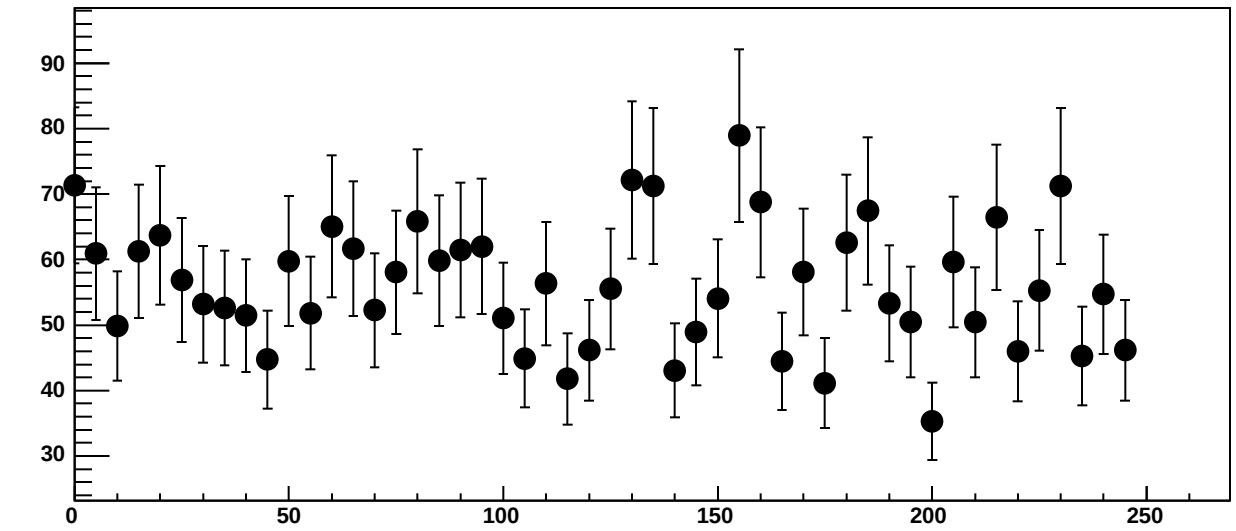
Chip 6, Channel 8, Enable 0, DAC=1600, ADC Residuals vs Hold



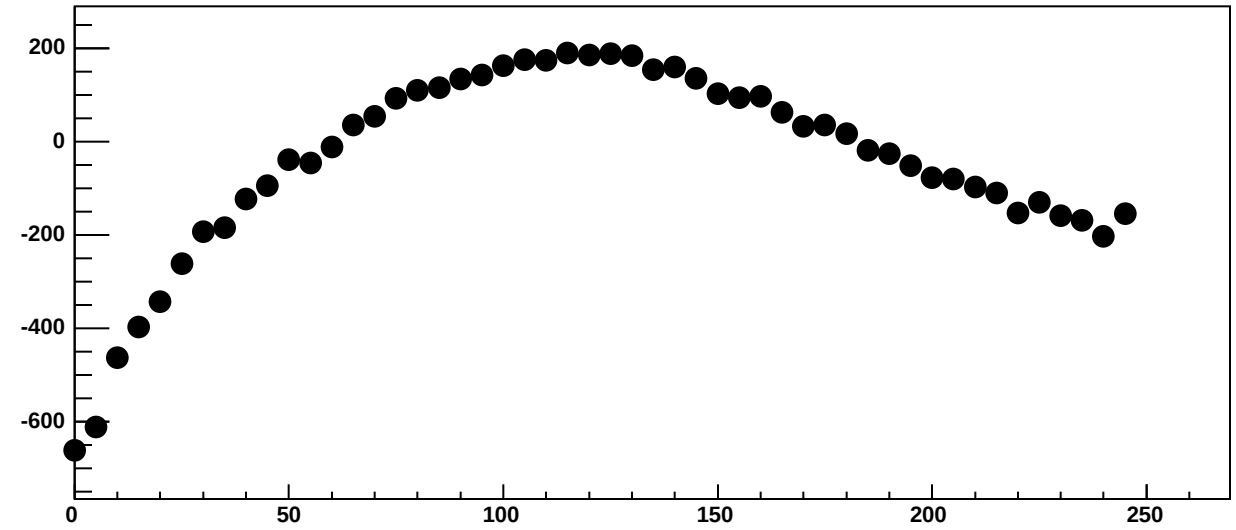
Chip 6, Channel 8, Enable 1, DAC=1600, ADC Mean vs Hold



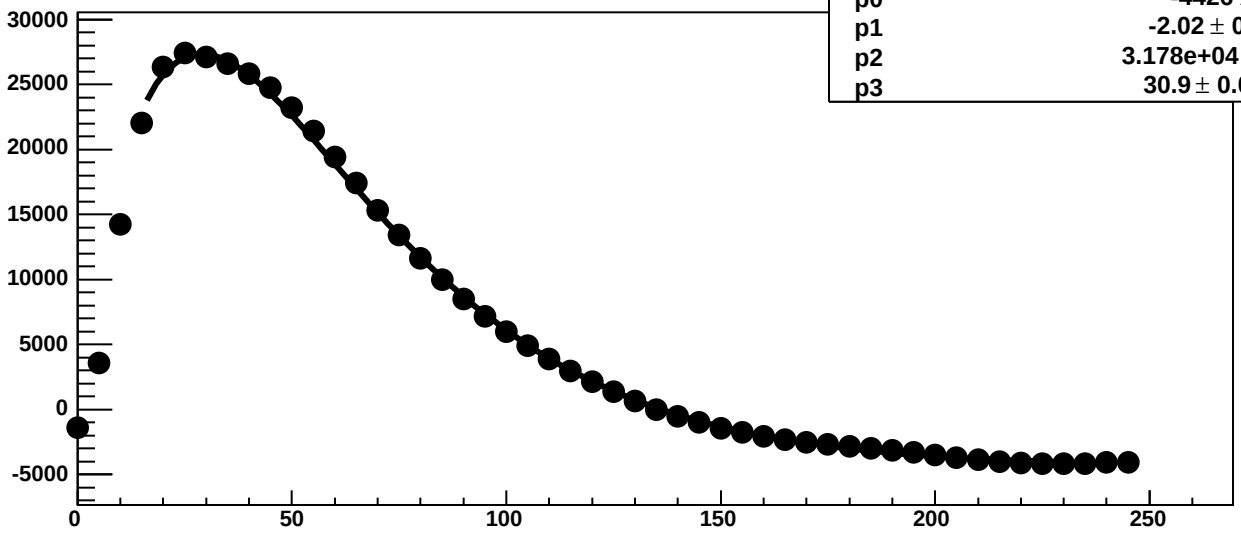
Chip 6, Channel 8, Enable 1, DAC=1600, ADC Noise vs Hold



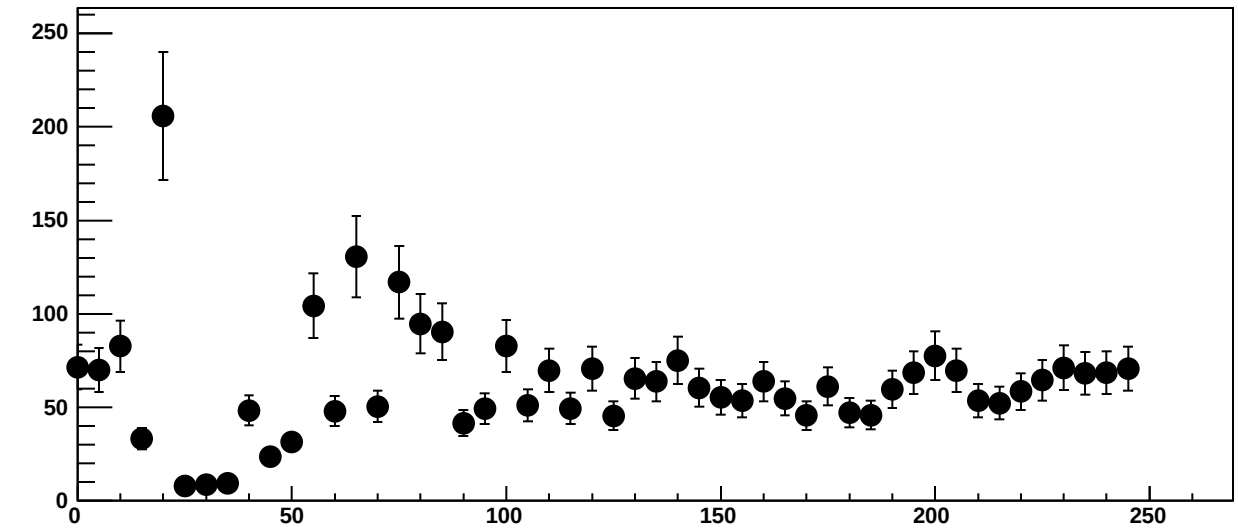
Chip 6, Channel 8, Enable 1, DAC=1600, ADC Residuals vs Hold



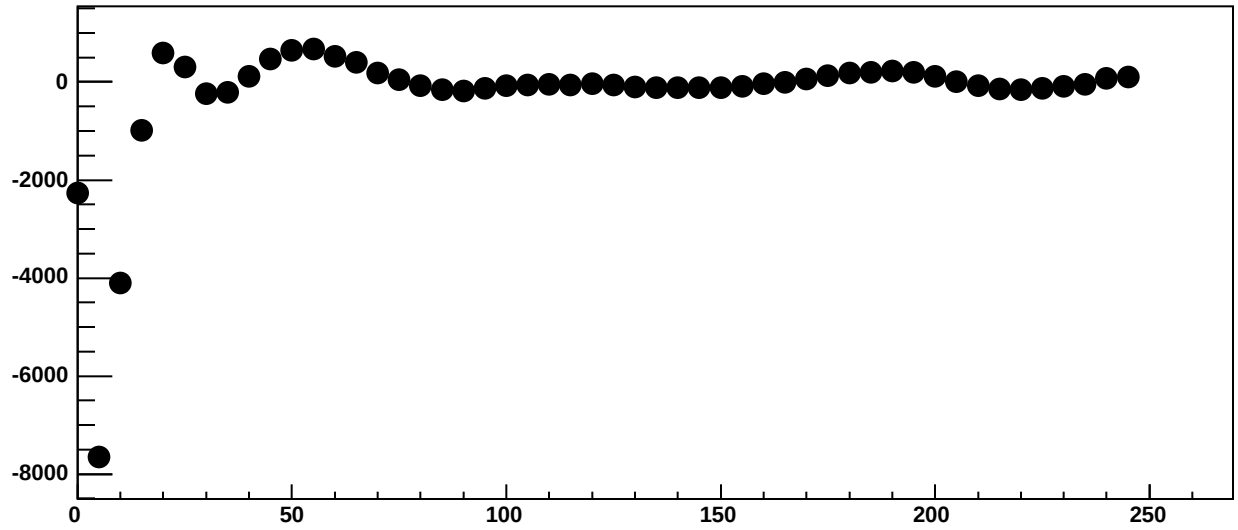
Chip 6, Channel 8, Enable 2!, DAC=1600, ADC Mean vs Hold



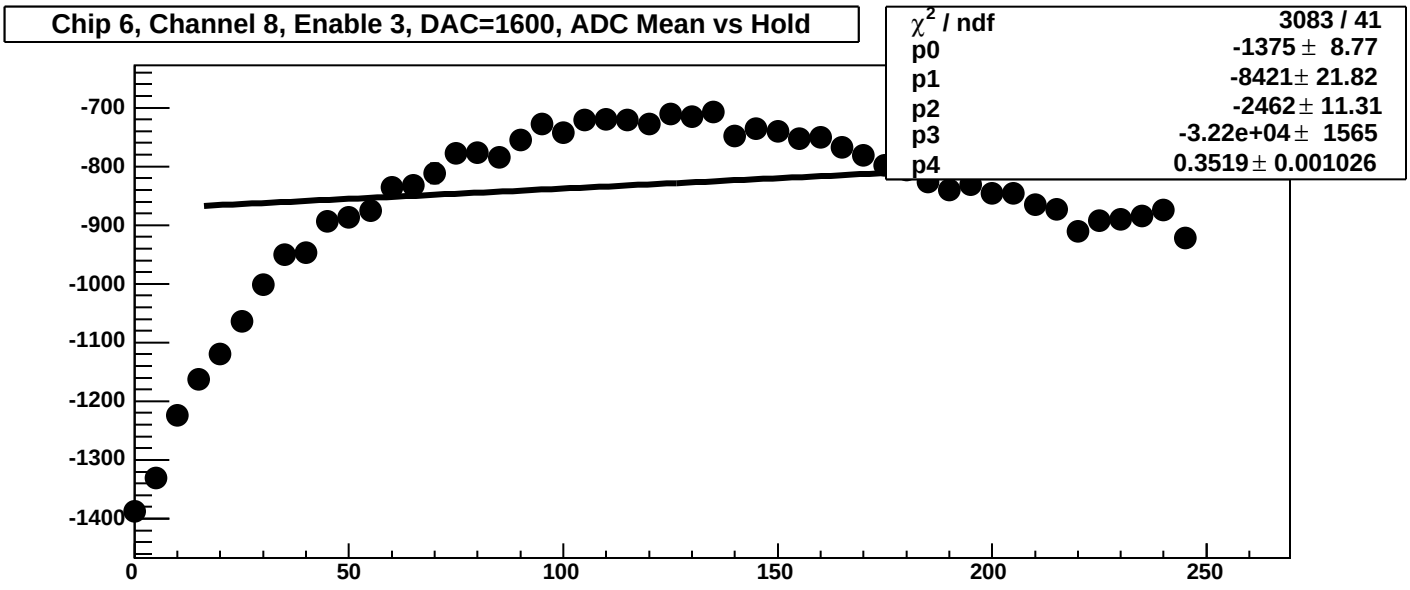
Chip 6, Channel 8, Enable 2!, DAC=1600, ADC Noise vs Hold



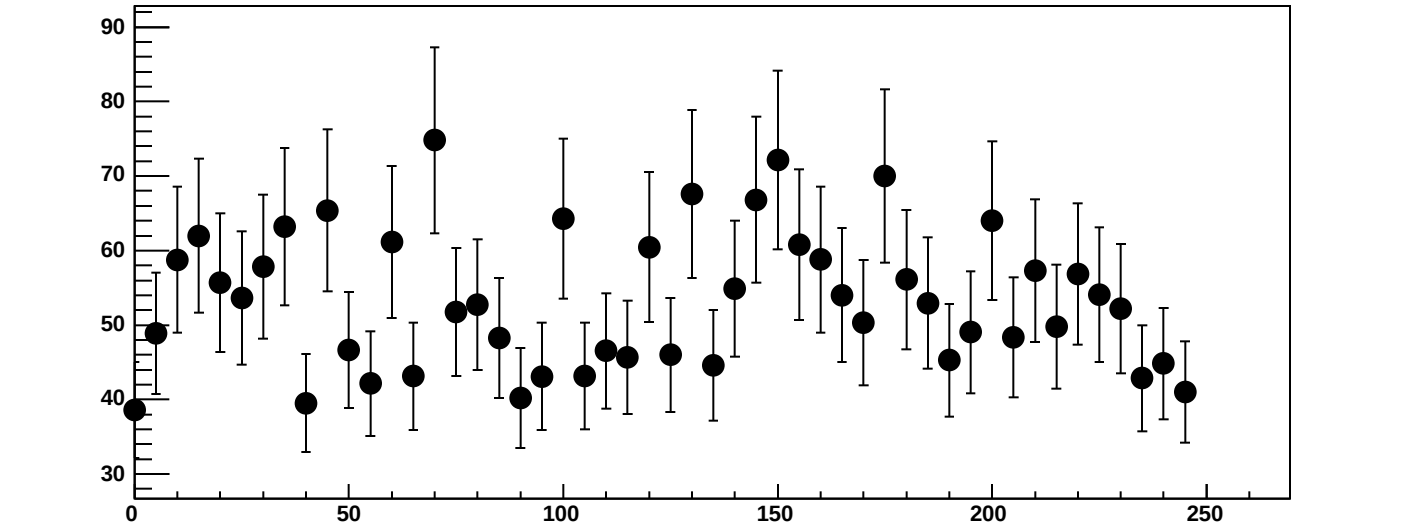
Chip 6, Channel 8, Enable 2!, DAC=1600, ADC Residuals vs Hold



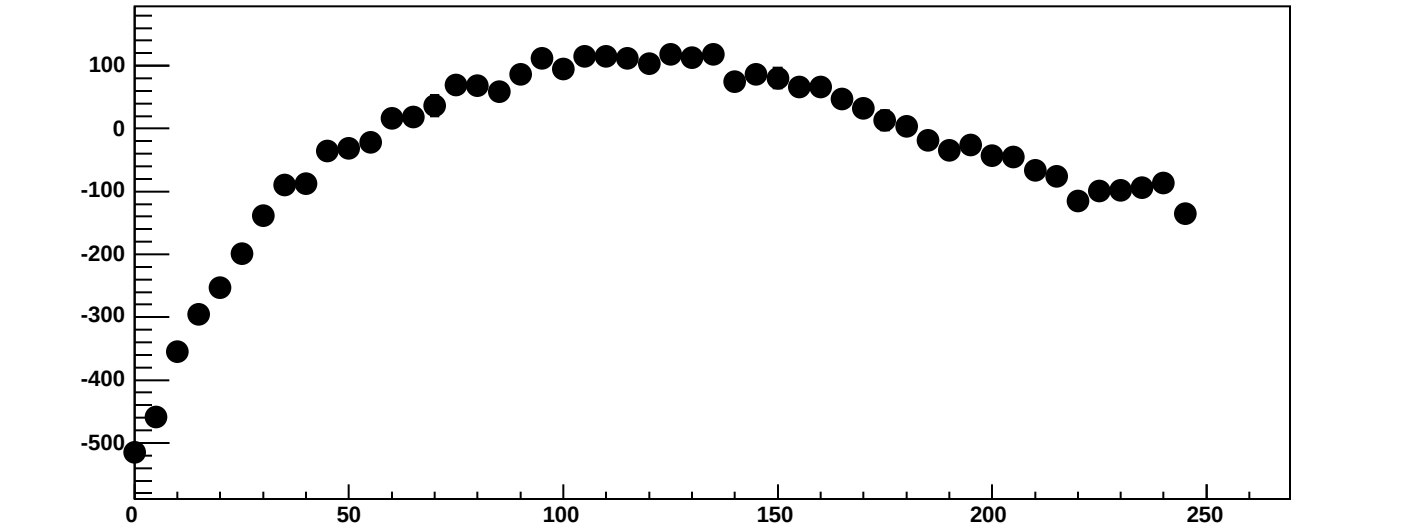
Chip 6, Channel 8, Enable 3, DAC=1600, ADC Mean vs Hold



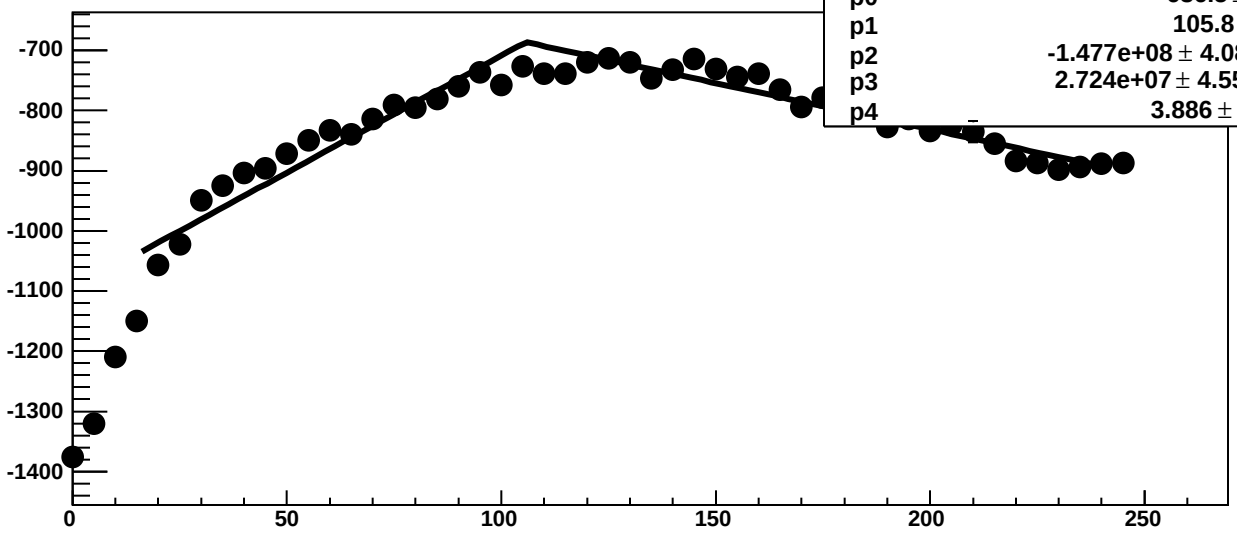
Chip 6, Channel 8, Enable 3, DAC=1600, ADC Noise vs Hold



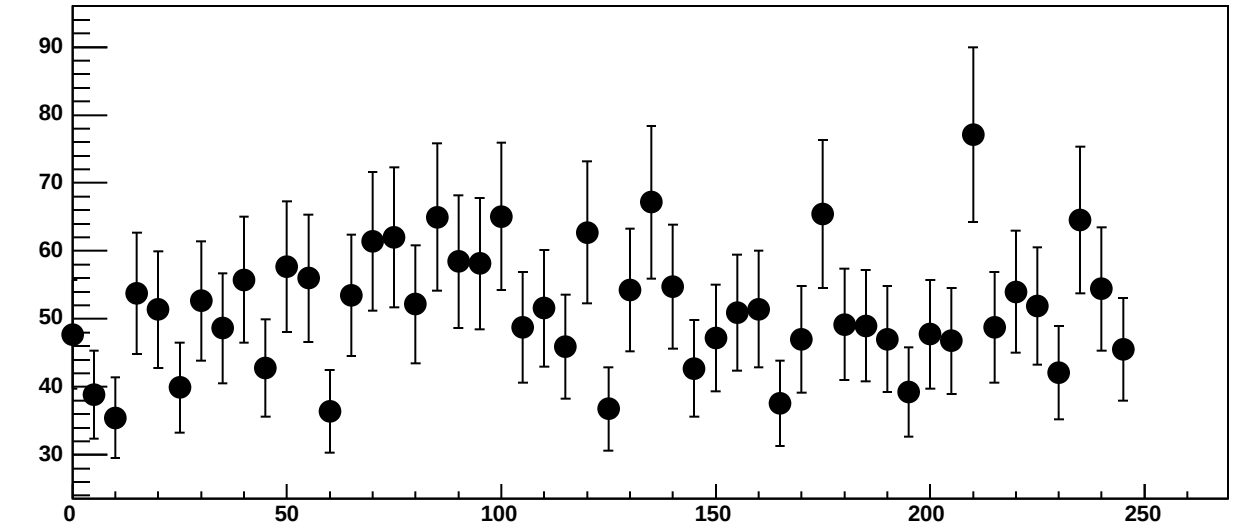
Chip 6, Channel 8, Enable 3, DAC=1600, ADC Residuals vs Hold



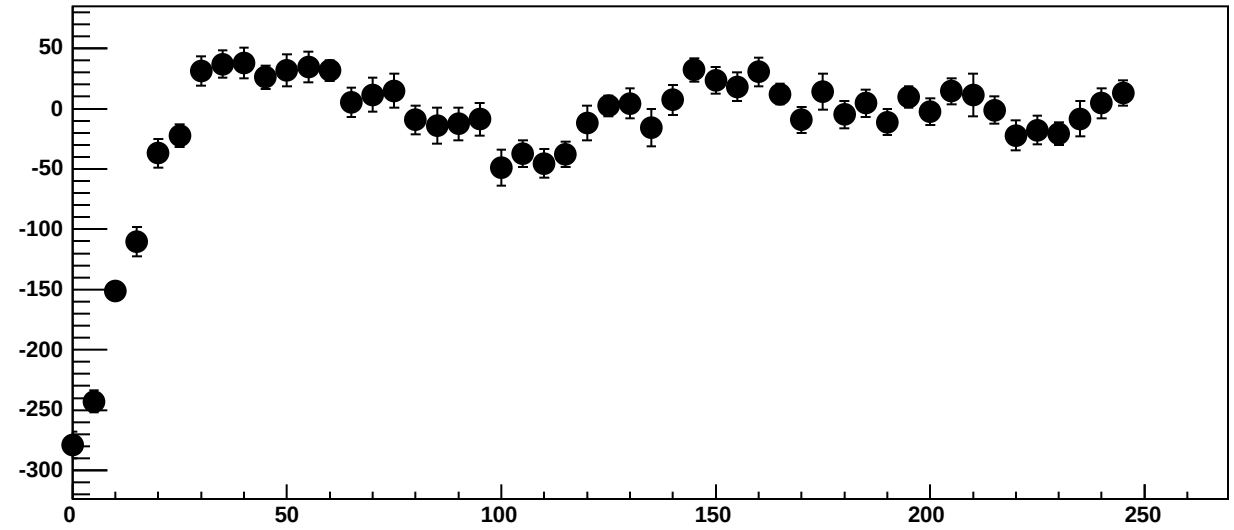
Chip 6, Channel 8, Enable 4, DAC=1600, ADC Mean vs Hold



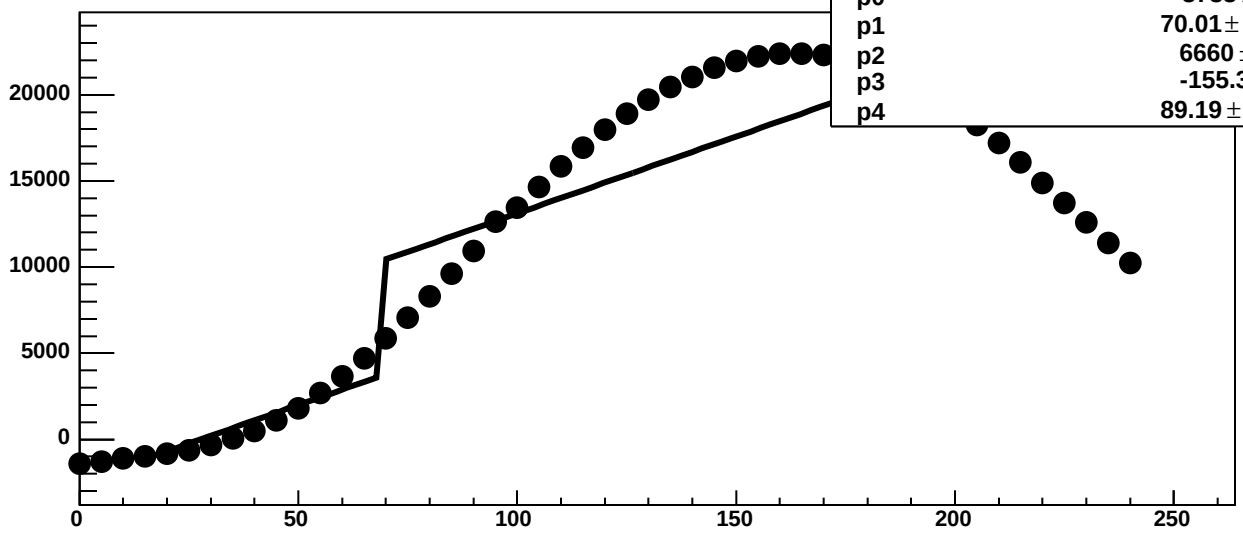
Chip 6, Channel 8, Enable 4, DAC=1600, ADC Noise vs Hold



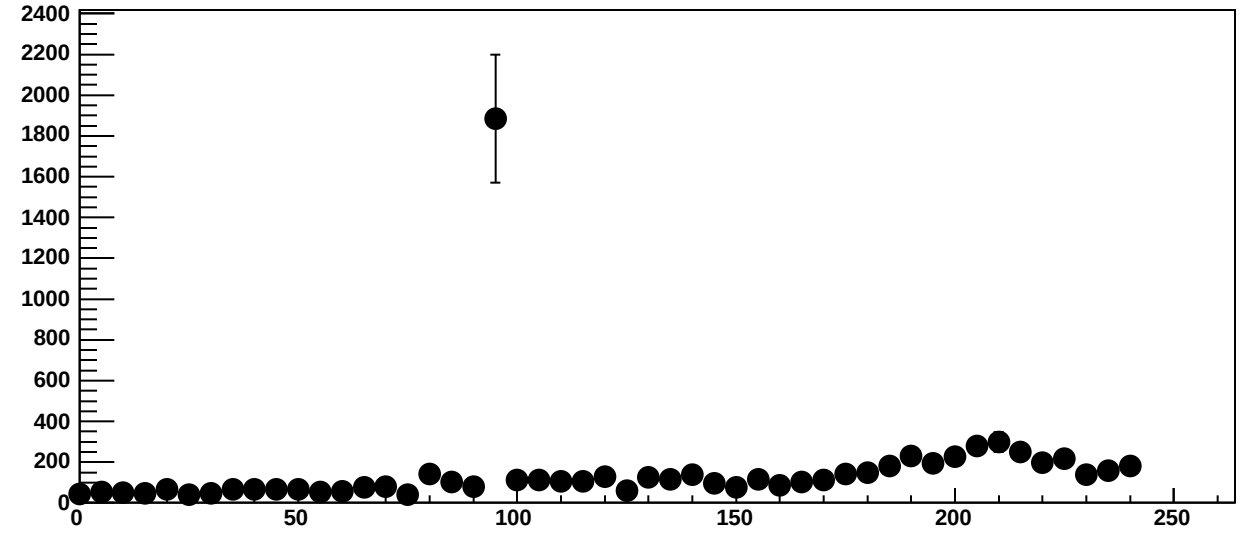
Chip 6, Channel 8, Enable 4, DAC=1600, ADC Residuals vs Hold



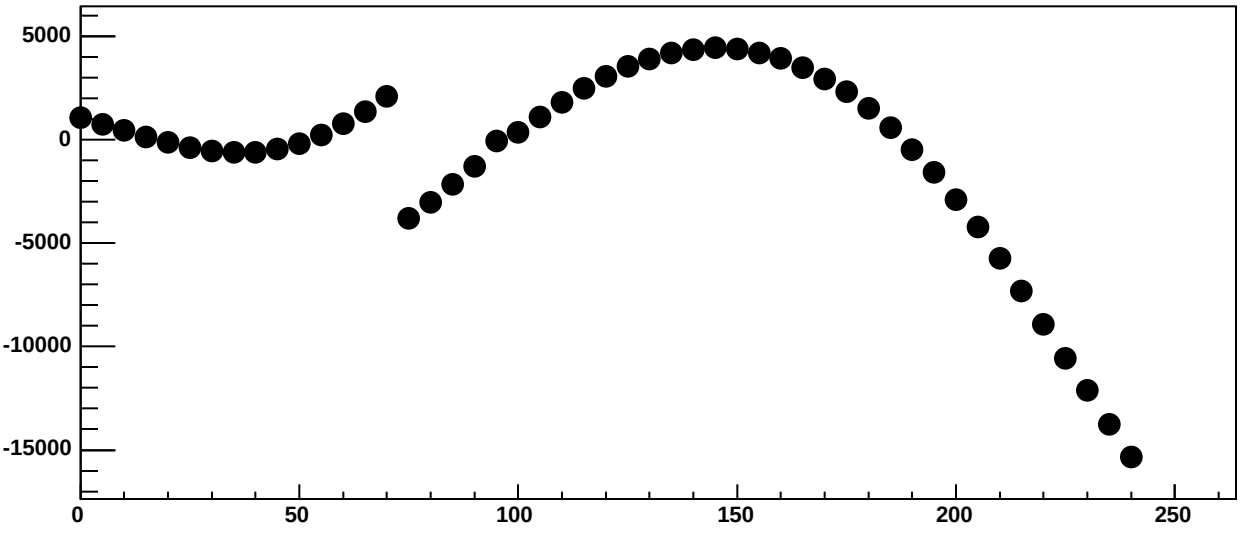
Chip 6, Channel 8, Enable 5, DAC=1600, ADC Mean vs Hold



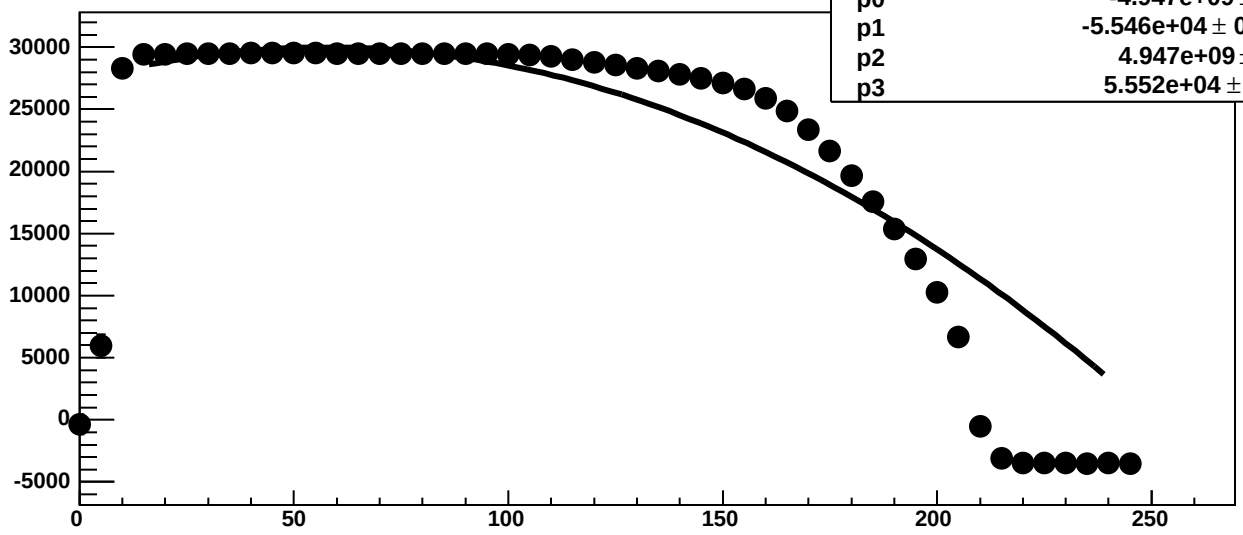
Chip 6, Channel 8, Enable 5, DAC=1600, ADC Noise vs Hold



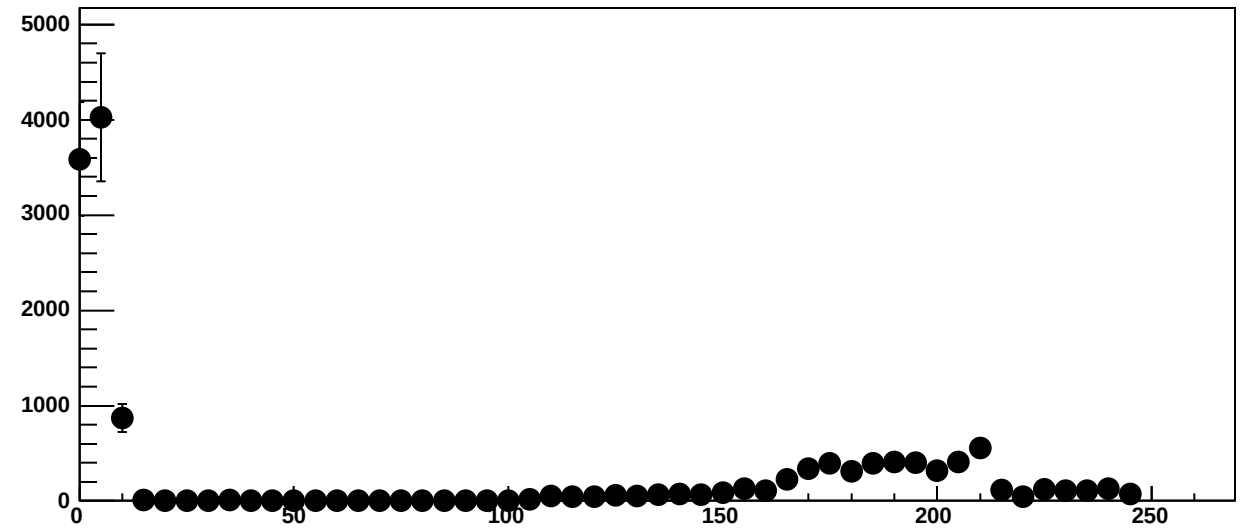
Chip 6, Channel 8, Enable 5, DAC=1600, ADC Residuals vs Hold



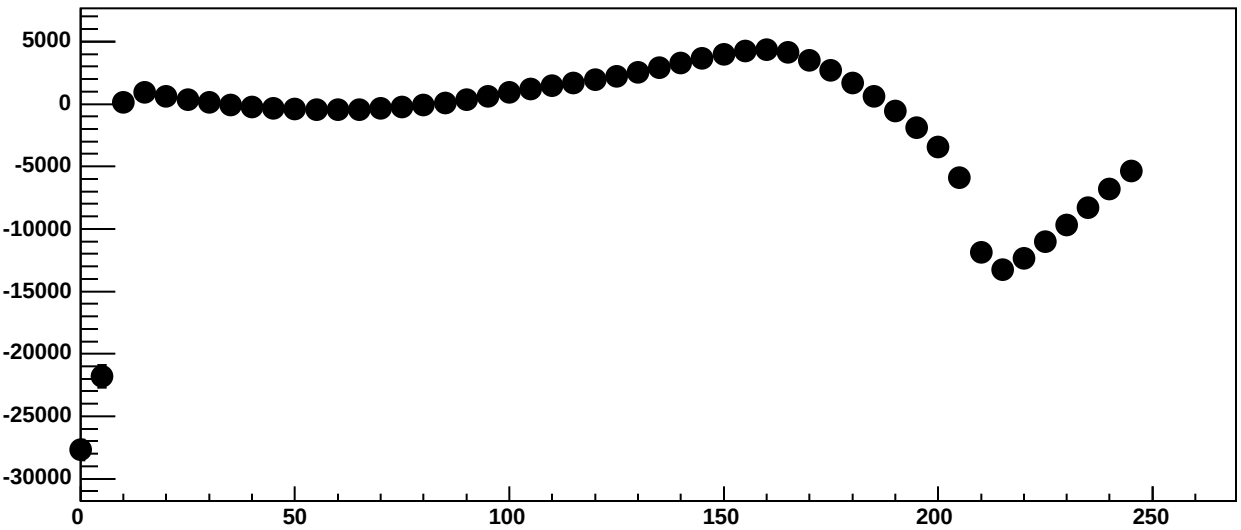
Chip 6, Channel 9, Enable 0!, DAC=1600, ADC Mean vs Hold

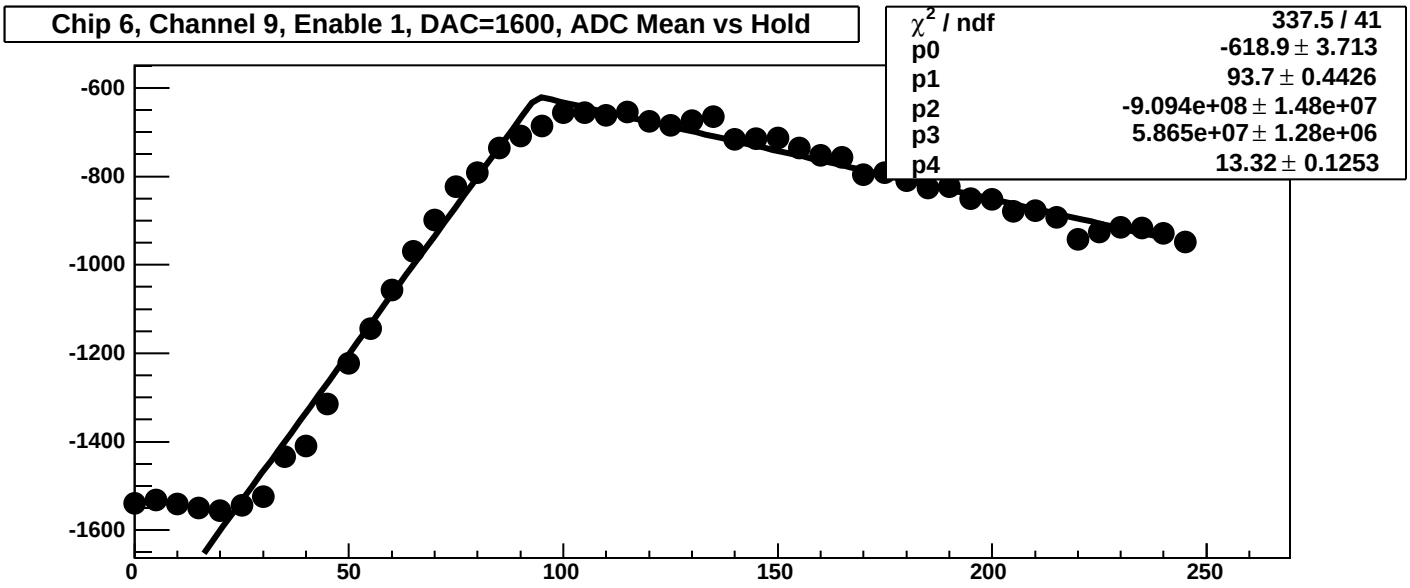


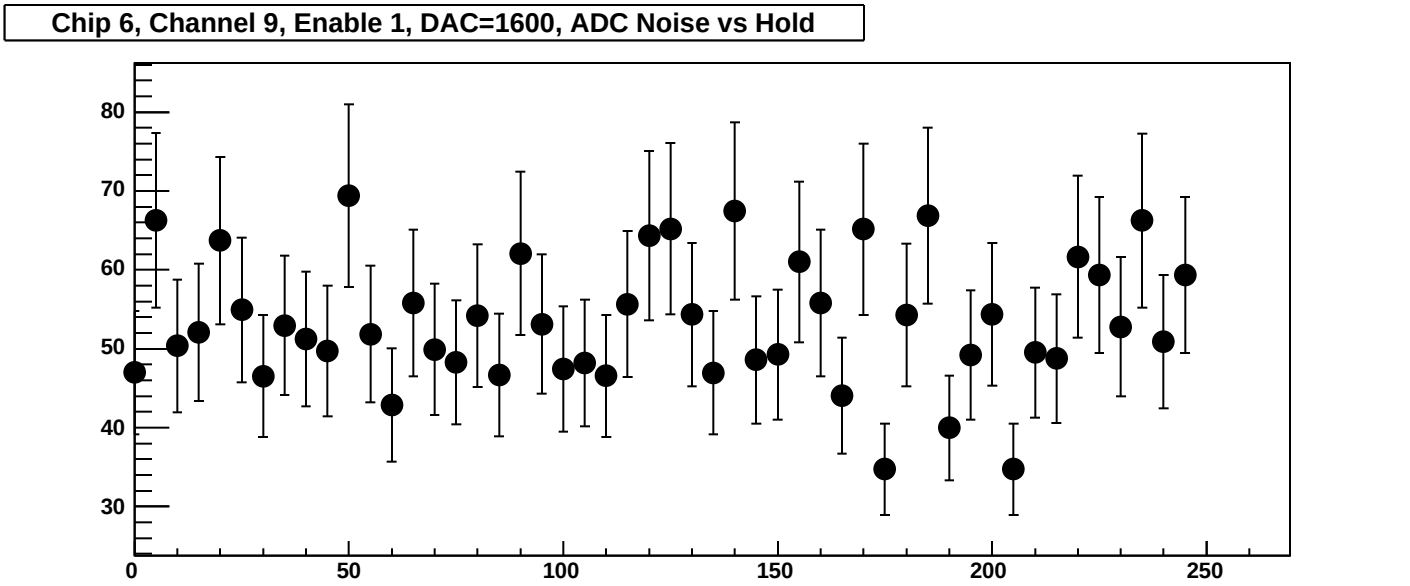
Chip 6, Channel 9, Enable 0!, DAC=1600, ADC Noise vs Hold

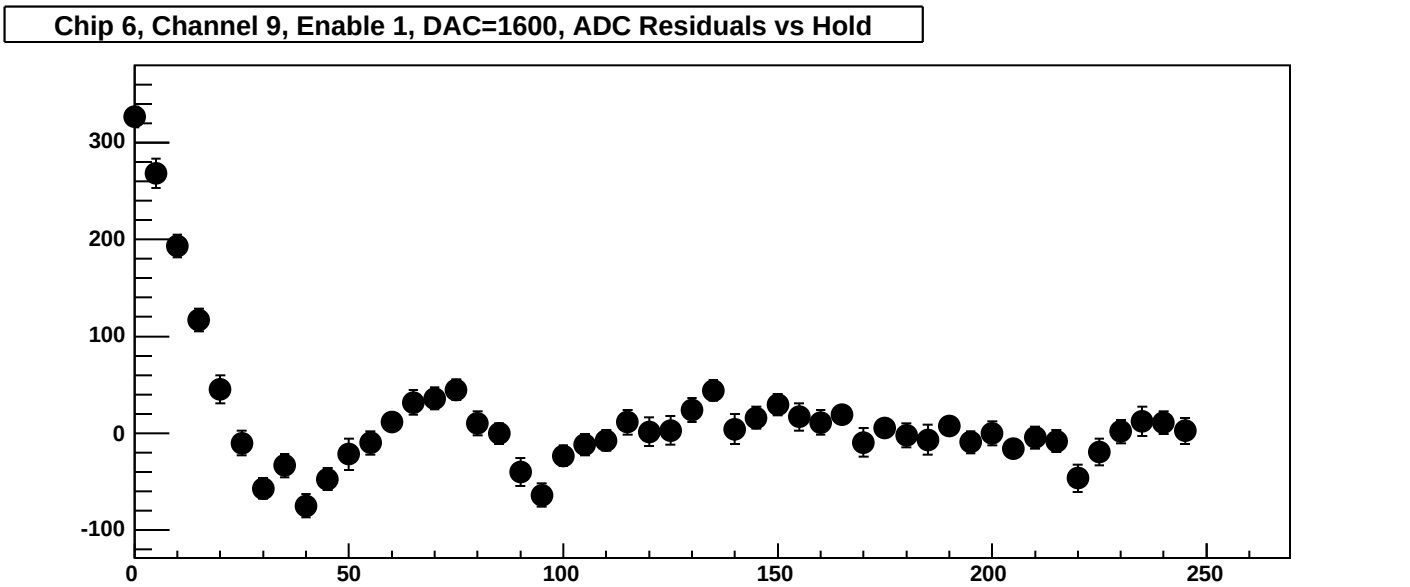


Chip 6, Channel 9, Enable 0!, DAC=1600, ADC Residuals vs Hold

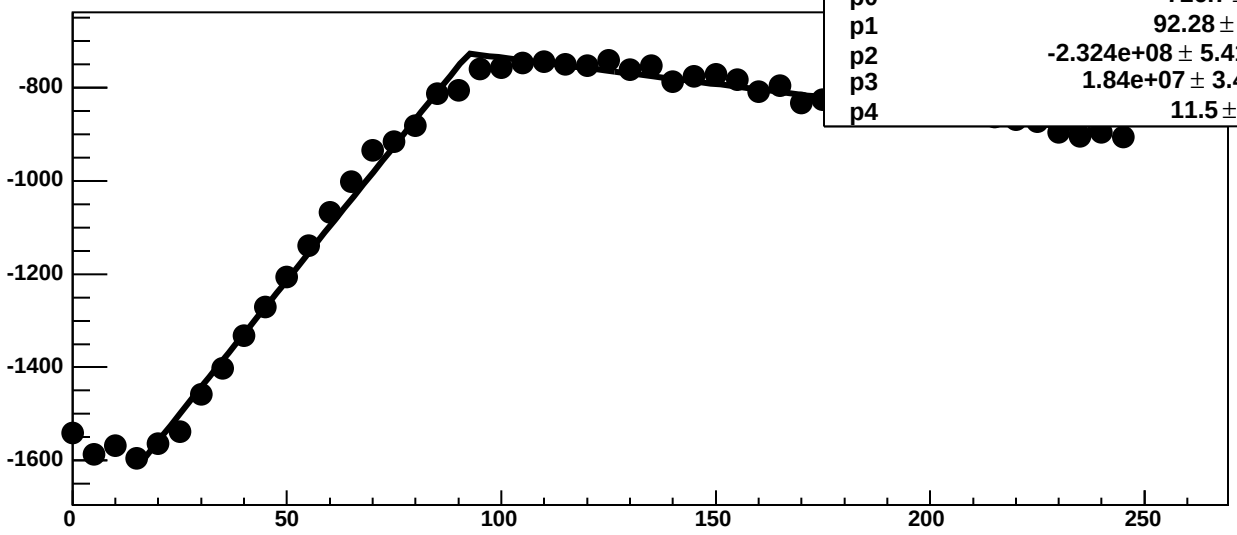



Chip 6, Channel 9, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 337.5 / 41
p0 -618.9 ± 3.713
p1 93.7 ± 0.4426
p2 -9.094e+08 ± 1.48e+07
p3 5.865e+07 ± 1.28e+06
p4 13.32 ± 0.1253
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250

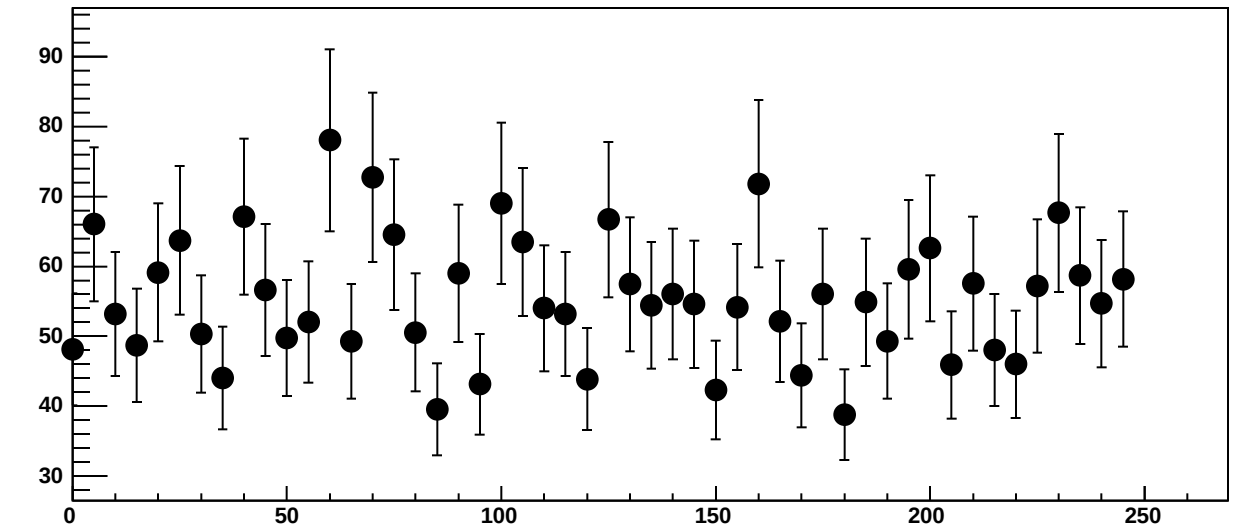

Chip 6, Channel 9, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 6, Channel 9, Enable 1, DAC=1600, ADC Residuals vs Hold
300
200
100
0
-100
0
50
100
150
200
250

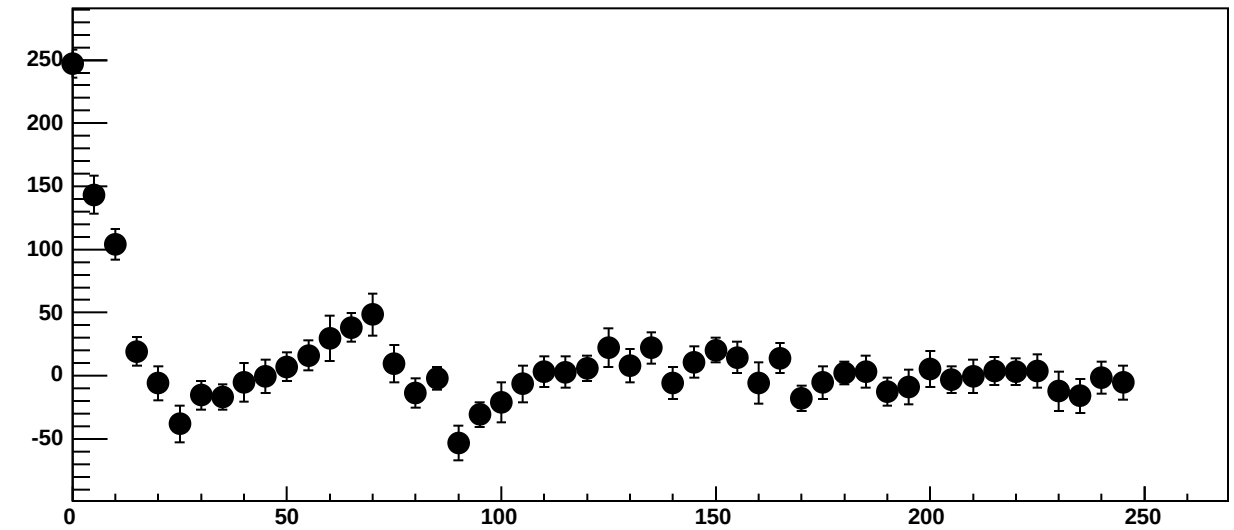
Chip 6, Channel 9, Enable 2, DAC=1600, ADC Mean vs Hold



Chip 6, Channel 9, Enable 2, DAC=1600, ADC Noise vs Hold

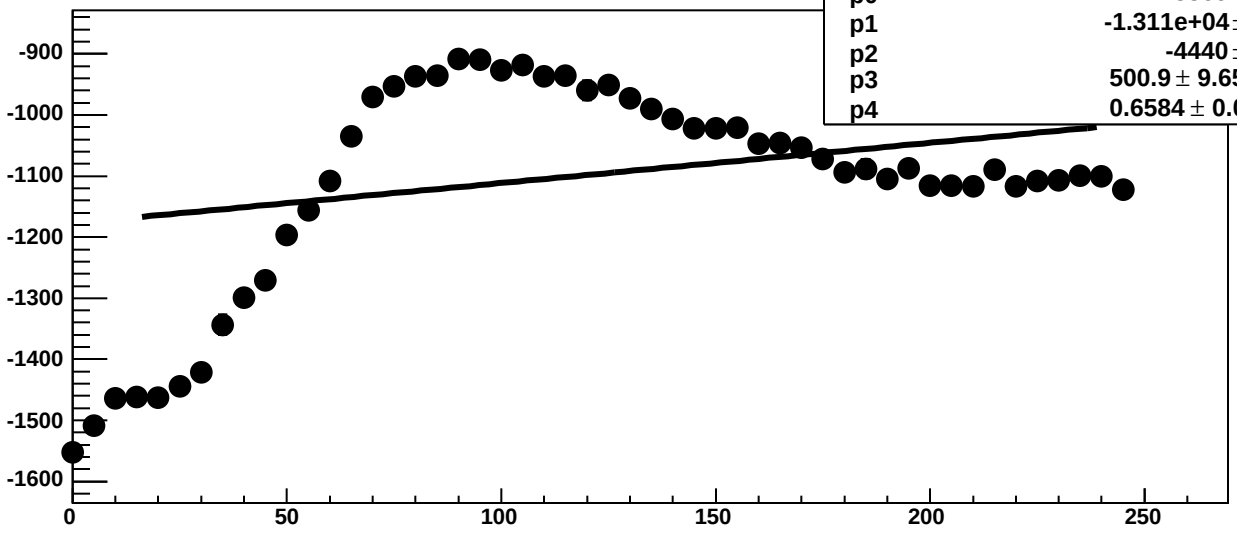


Chip 6, Channel 9, Enable 2, DAC=1600, ADC Residuals vs Hold

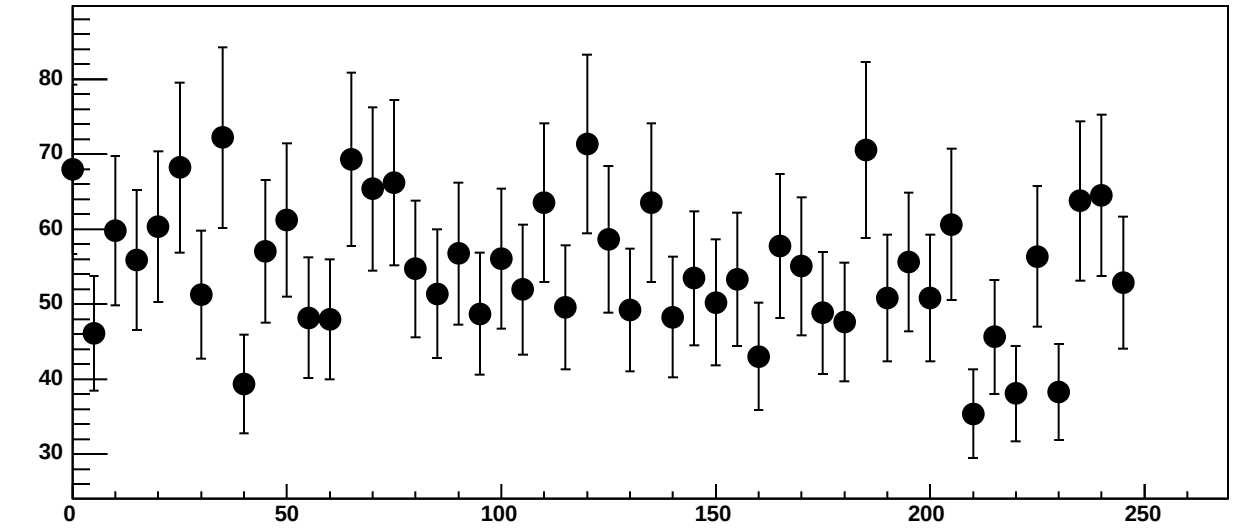


Chip 6, Channel 9, Enable 3, DAC=1600, ADC Mean vs Hold

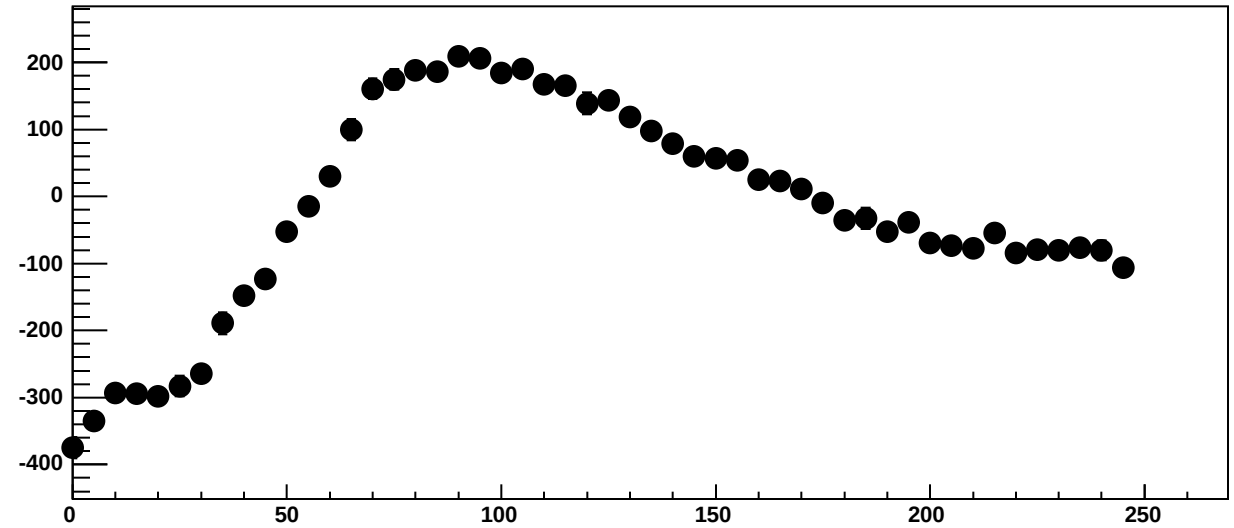
χ^2 / ndf	5465 / 41
p0	-5369 ± 24.33
p1	-1.311e+04 ± 36.96
p2	-4440 ± 24.33
p3	500.9 ± 9.656e+04
p4	0.6584 ± 0.001833



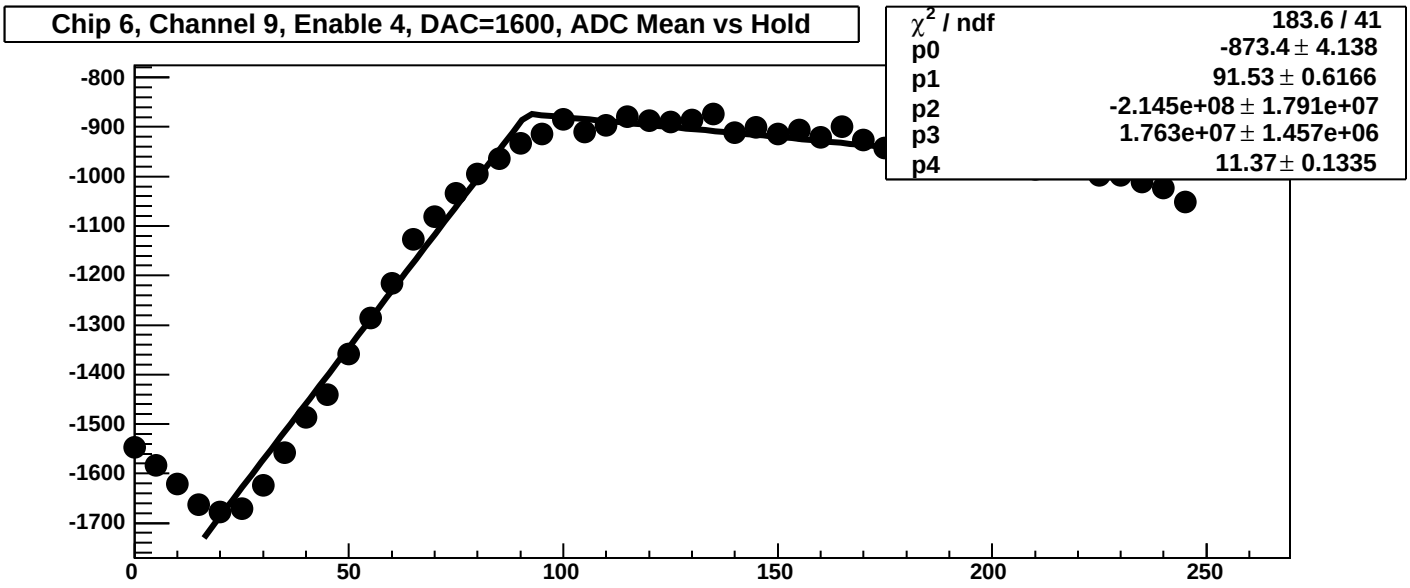
Chip 6, Channel 9, Enable 3, DAC=1600, ADC Noise vs Hold



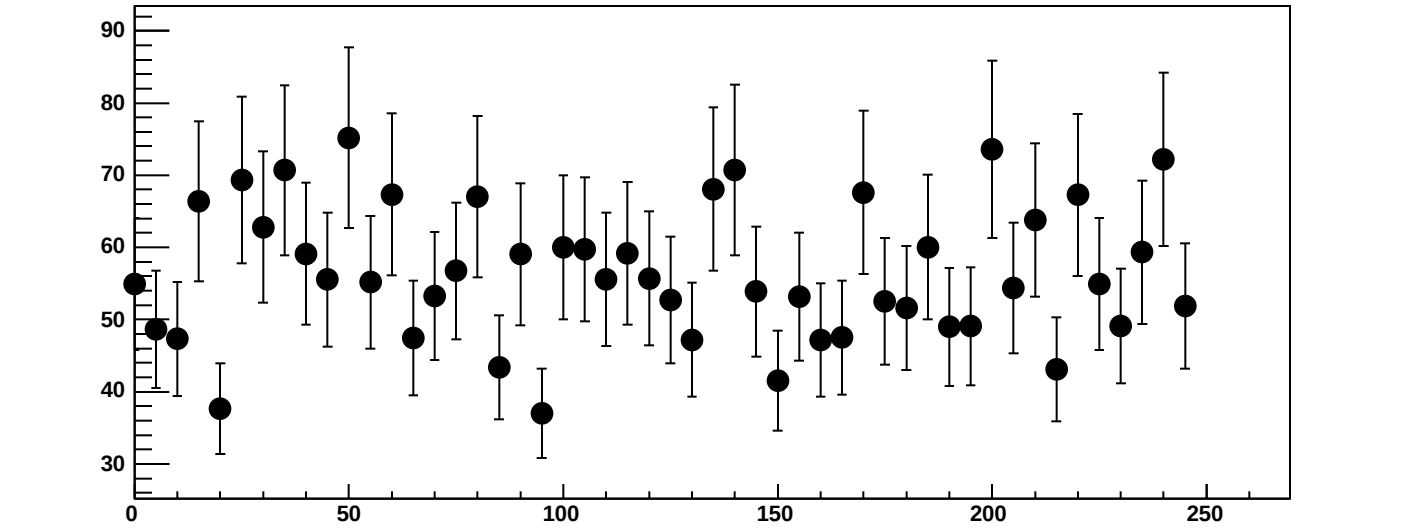
Chip 6, Channel 9, Enable 3, DAC=1600, ADC Residuals vs Hold



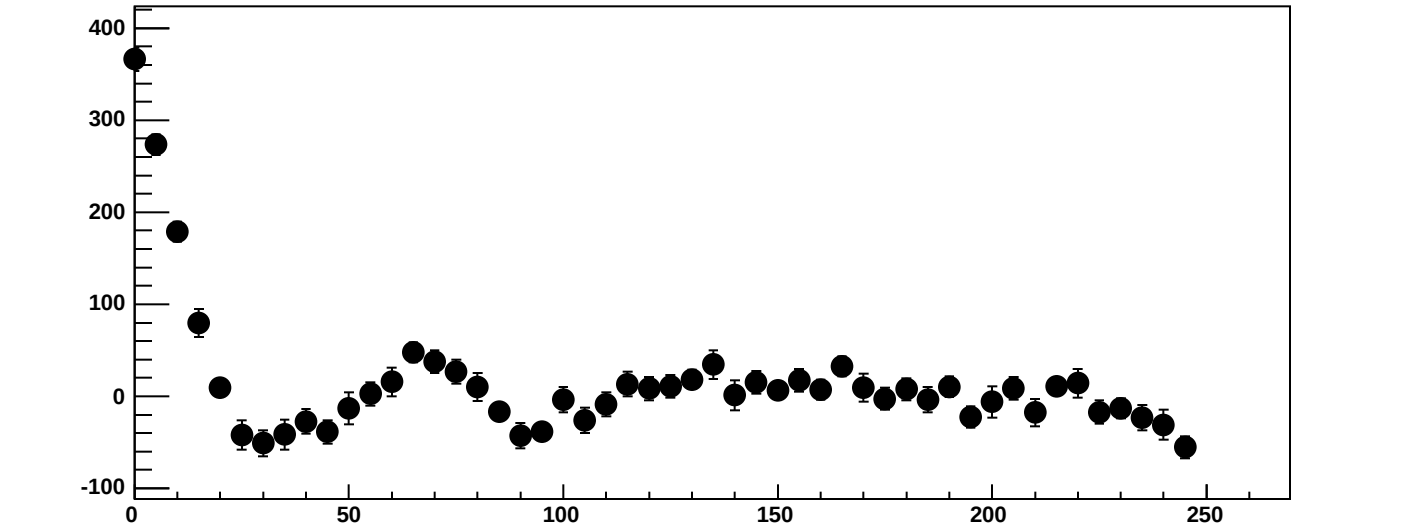
Chip 6, Channel 9, Enable 4, DAC=1600, ADC Mean vs Hold



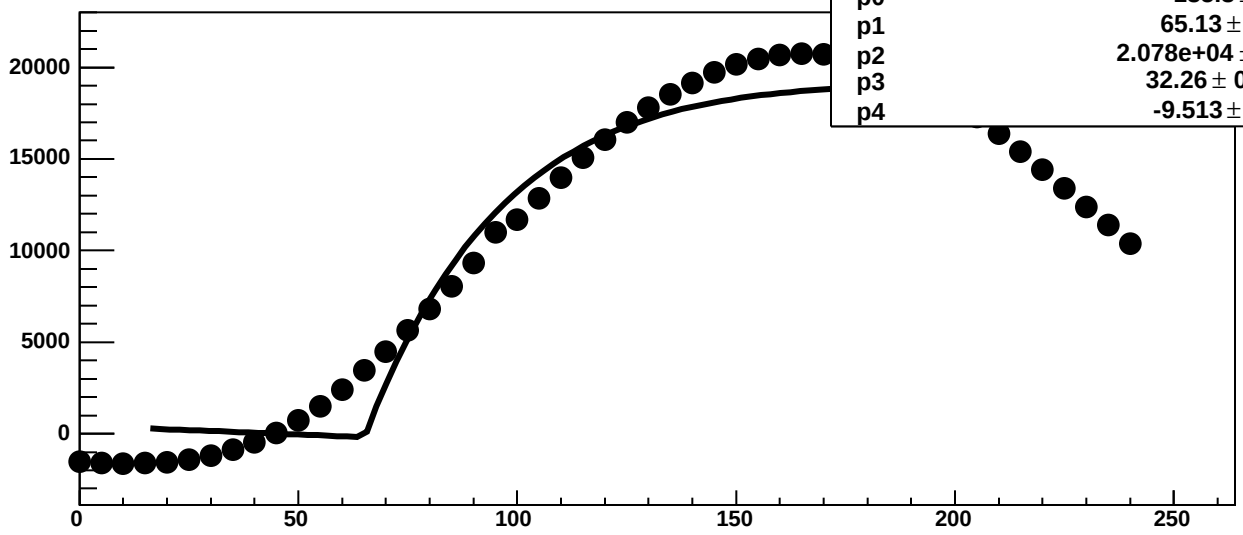
Chip 6, Channel 9, Enable 4, DAC=1600, ADC Noise vs Hold



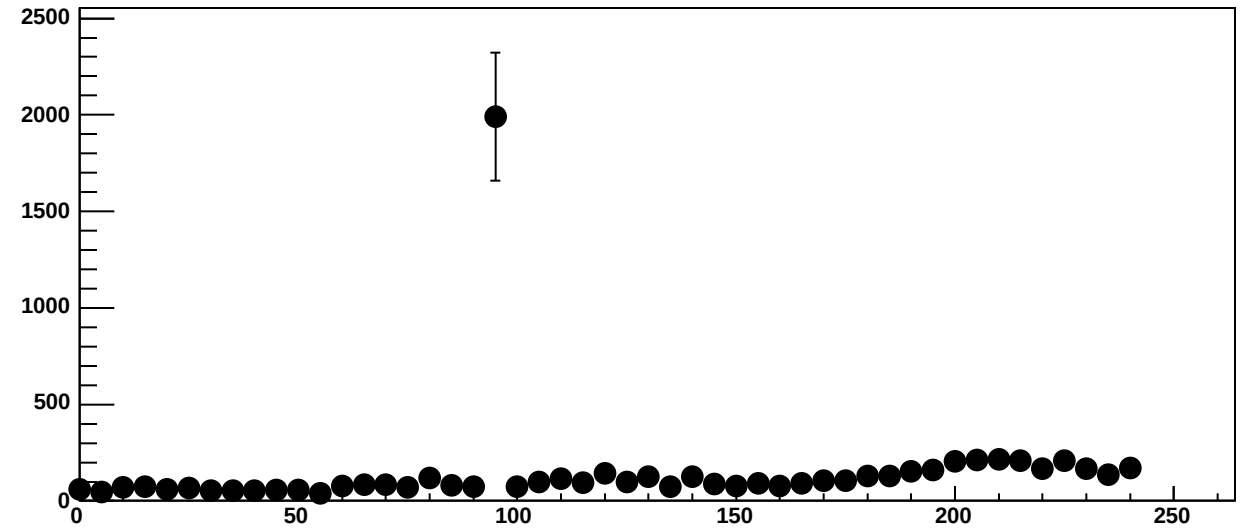
Chip 6, Channel 9, Enable 4, DAC=1600, ADC Residuals vs Hold



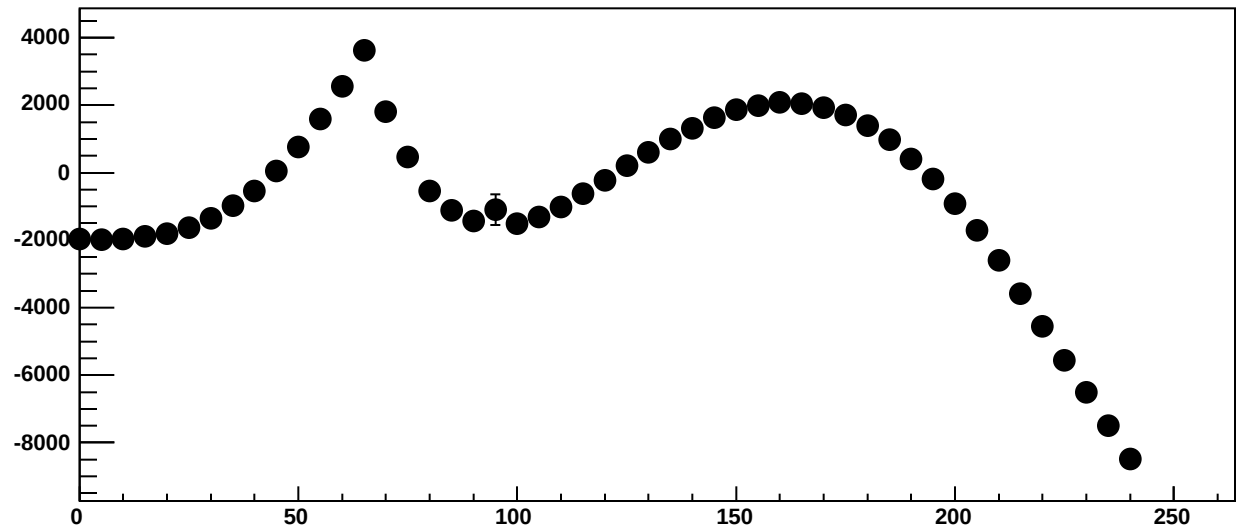
Chip 6, Channel 9, Enable 5, DAC=1600, ADC Mean vs Hold

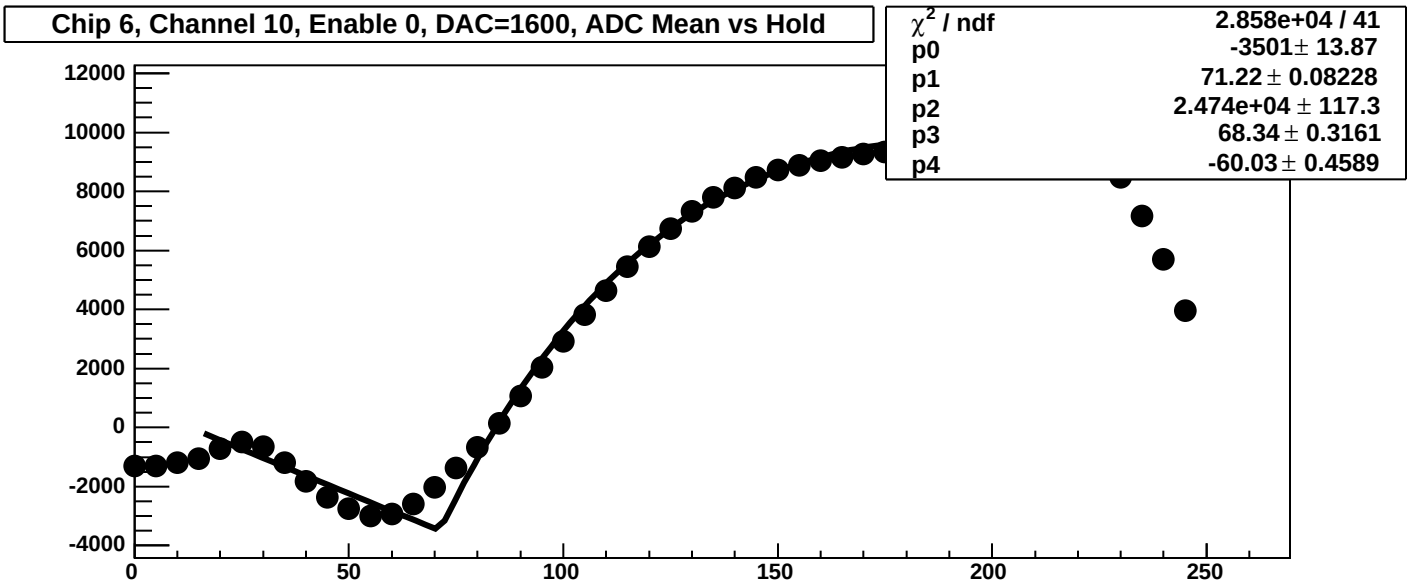


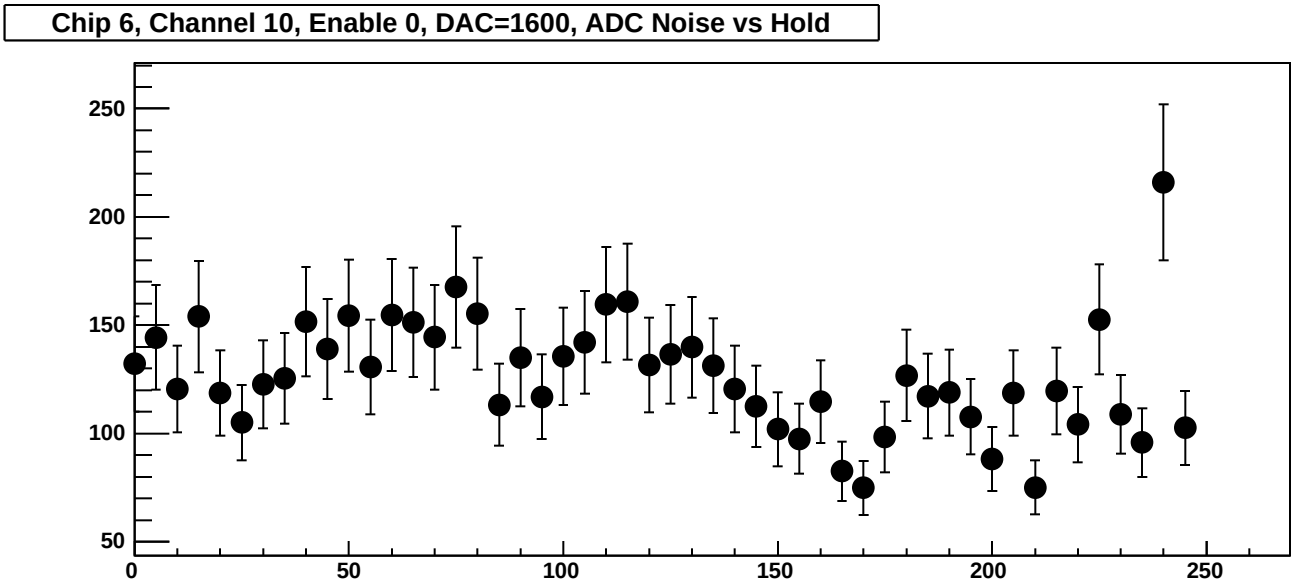
Chip 6, Channel 9, Enable 5, DAC=1600, ADC Noise vs Hold

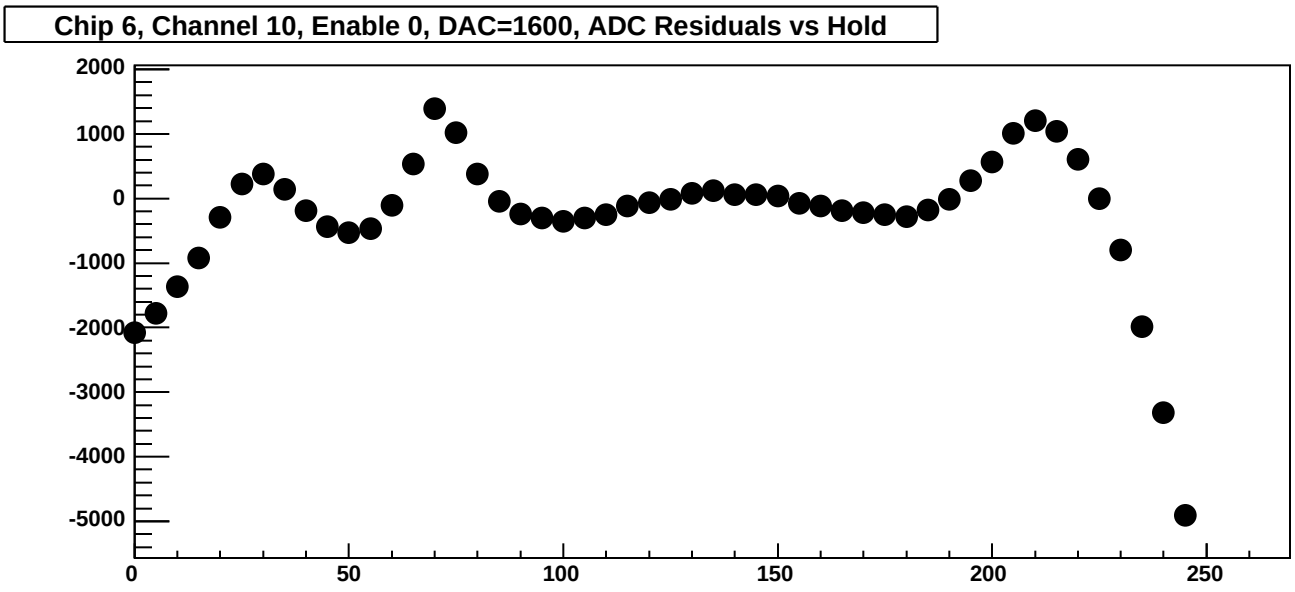


Chip 6, Channel 9, Enable 5, DAC=1600, ADC Residuals vs Hold

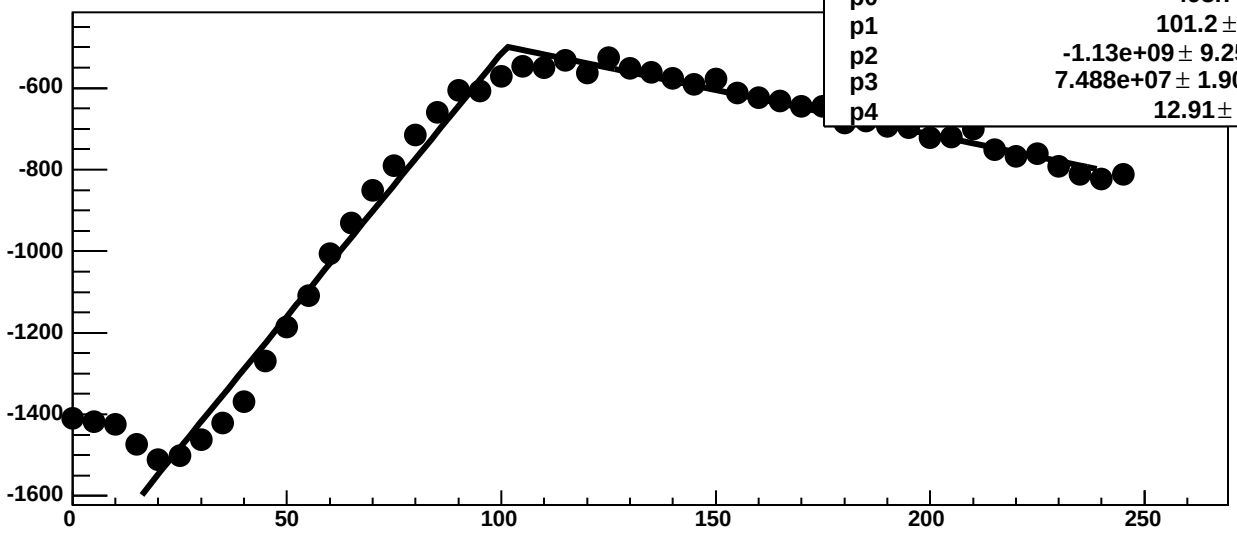



Chip 6, Channel 10, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.858e+04 / 41
p0 -3501 ± 13.87
p1 71.22 ± 0.08228
p2 2.474e+04 ± 117.3
p3 68.34 ± 0.3161
p4 -60.03 ± 0.4589
12000
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

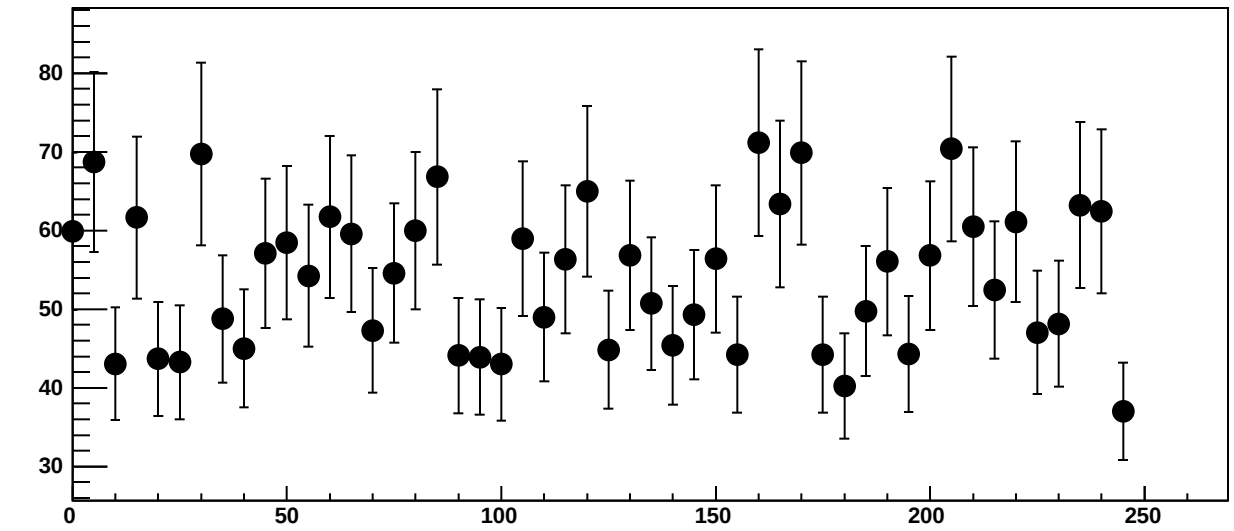

Chip 6, Channel 10, Enable 0, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
50
100
150
200
250


Chip 6, Channel 10, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

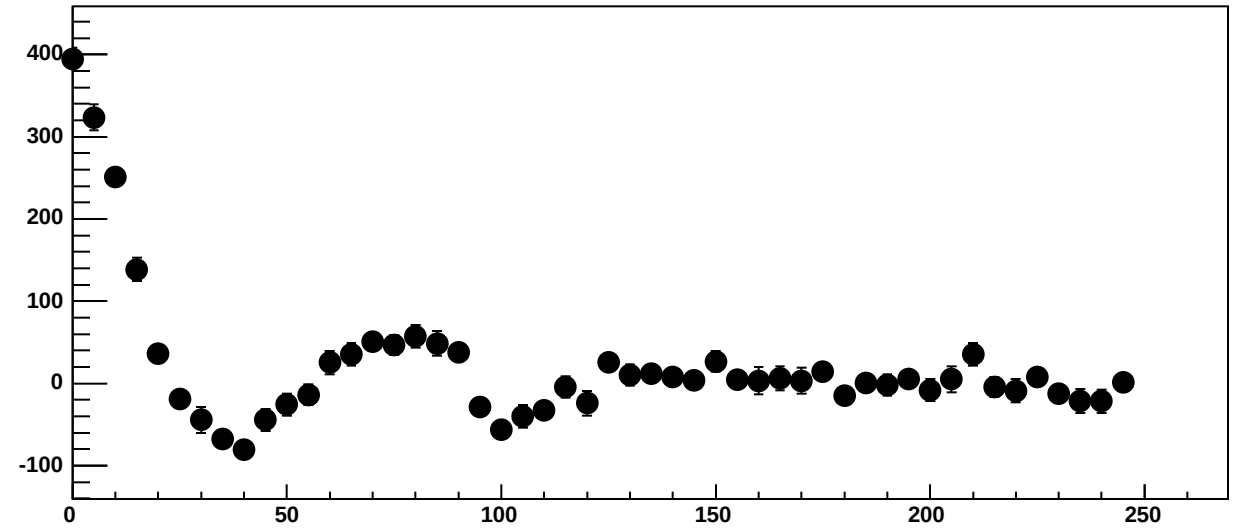
Chip 6, Channel 10, Enable 1, DAC=1600, ADC Mean vs Hold

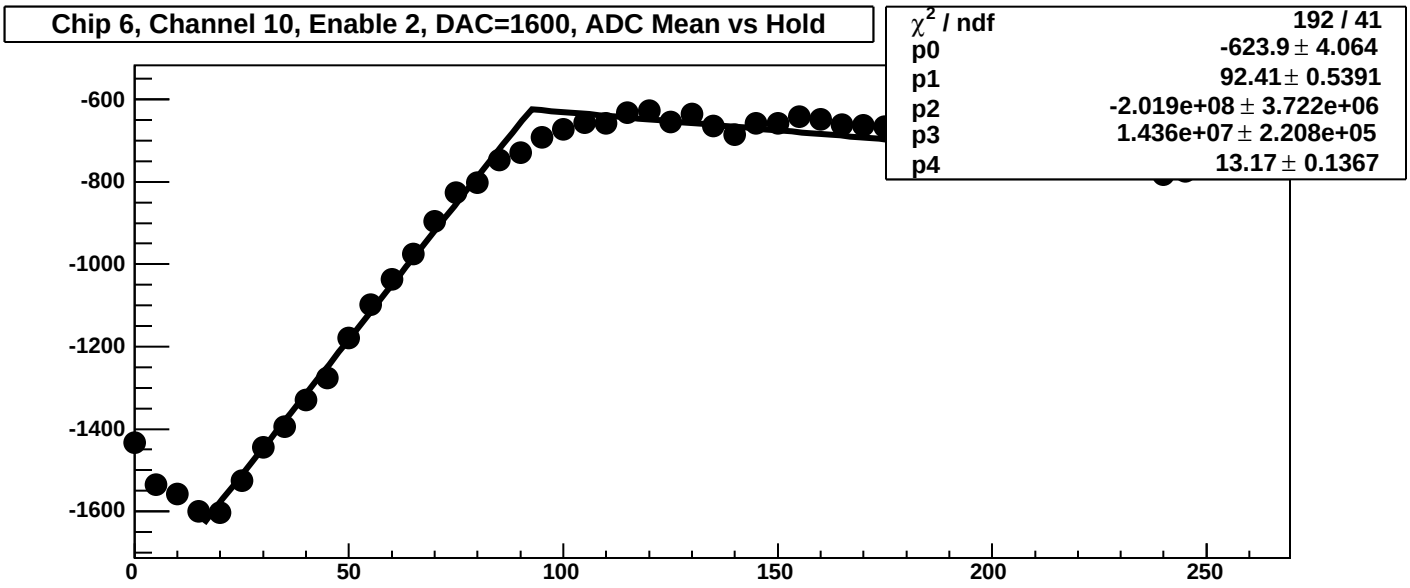


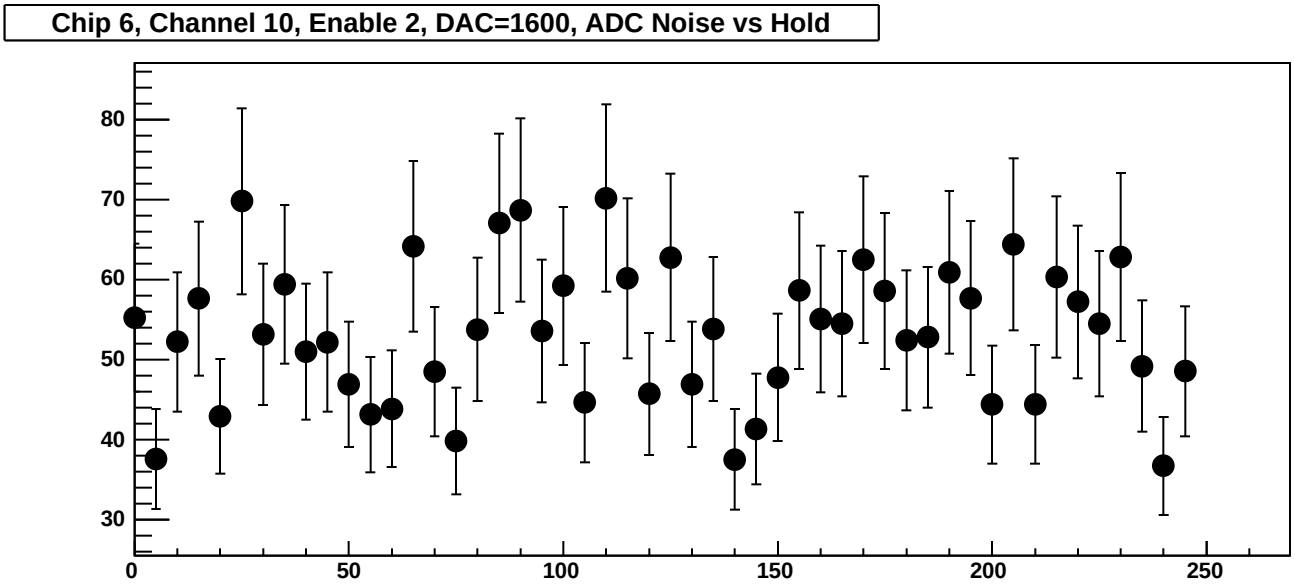
Chip 6, Channel 10, Enable 1, DAC=1600, ADC Noise vs Hold

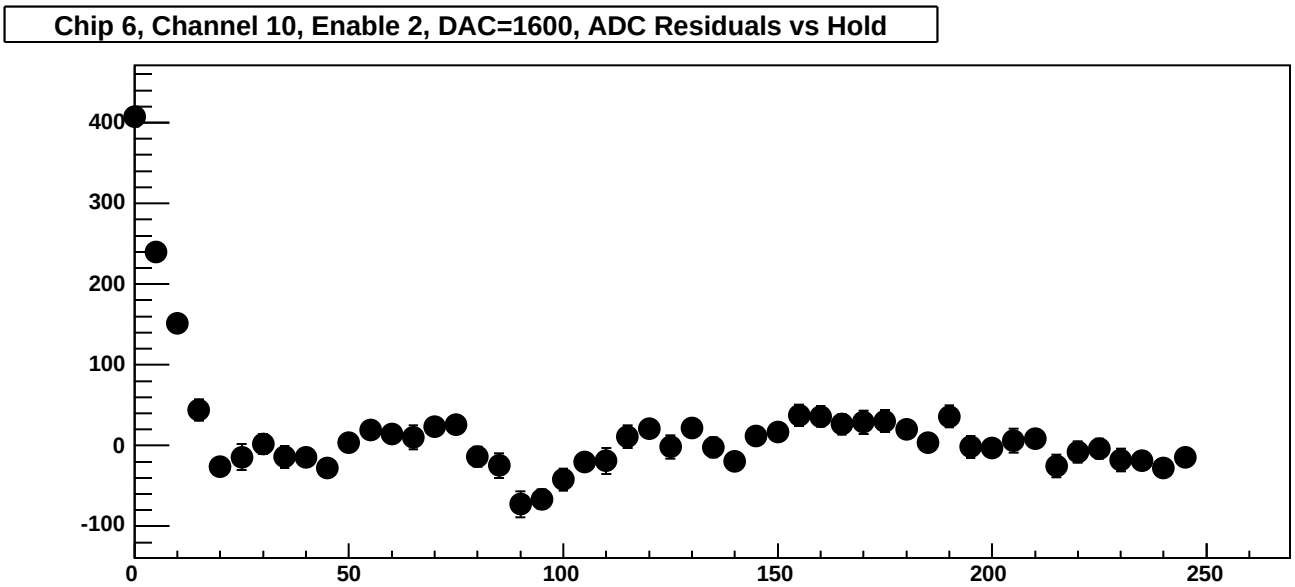


Chip 6, Channel 10, Enable 1, DAC=1600, ADC Residuals vs Hold



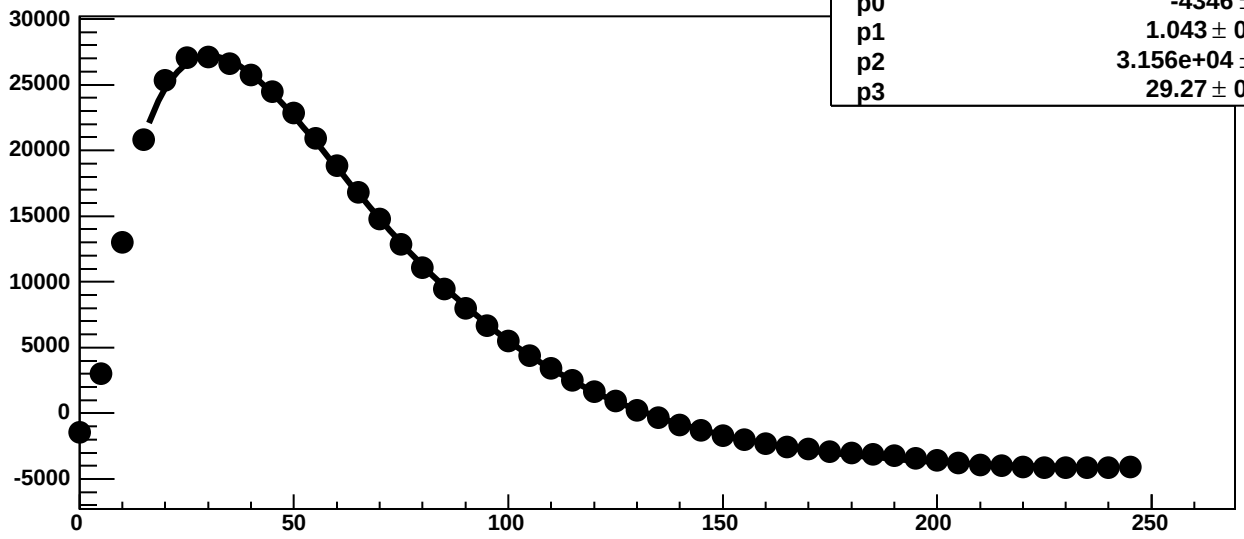

Chip 6, Channel 10, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 192 / 41
p0 -623.9 ± 4.064
p1 92.41 ± 0.5391
p2 -2.019e+08 ± 3.722e+06
p3 1.436e+07 ± 2.208e+05
p4 13.17 ± 0.1367
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250


Chip 6, Channel 10, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

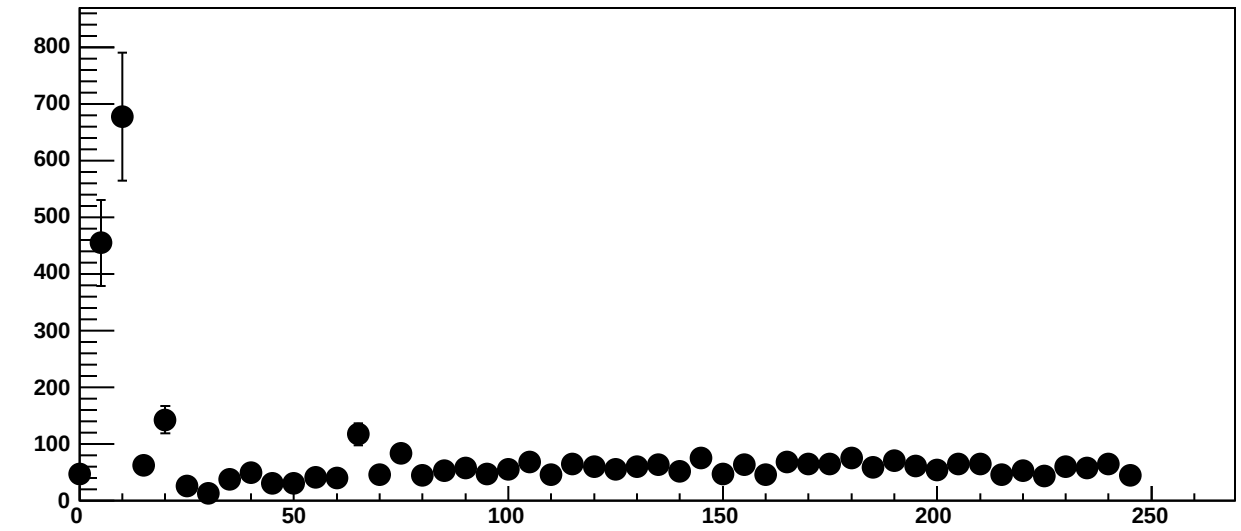

Chip 6, Channel 10, Enable 2, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

Chip 6, Channel 10, Enable 3!, DAC=1600, ADC Mean vs Hold

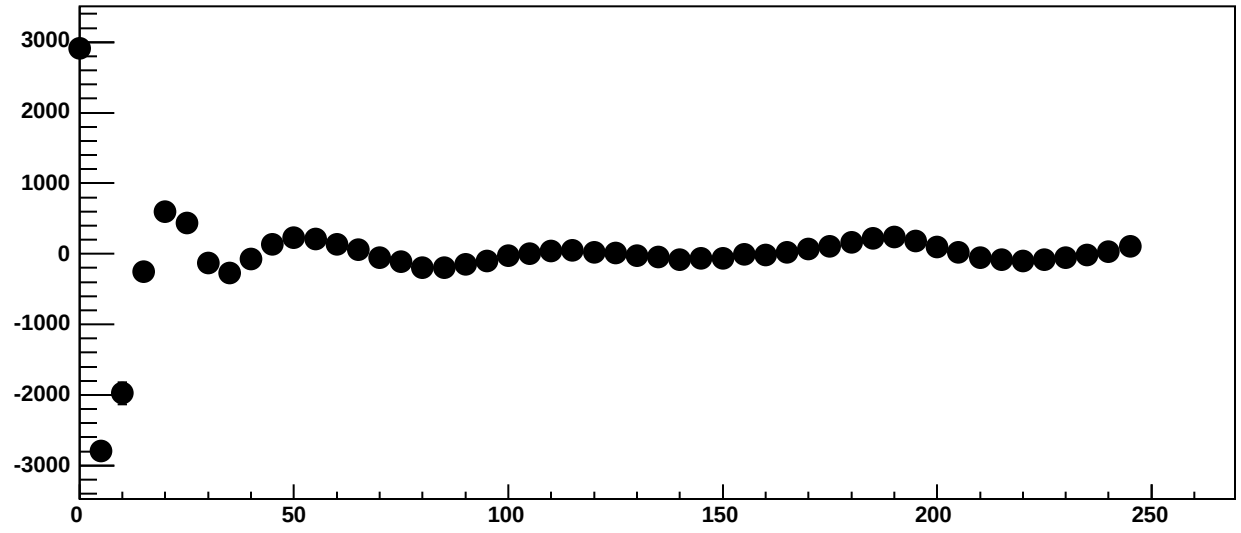
χ^2 / ndf	1.304e+04 / 42
p0	-4346 ± 3.702
p1	1.043 ± 0.01553
p2	3.156e+04 ± 4.094
p3	29.27 ± 0.00946



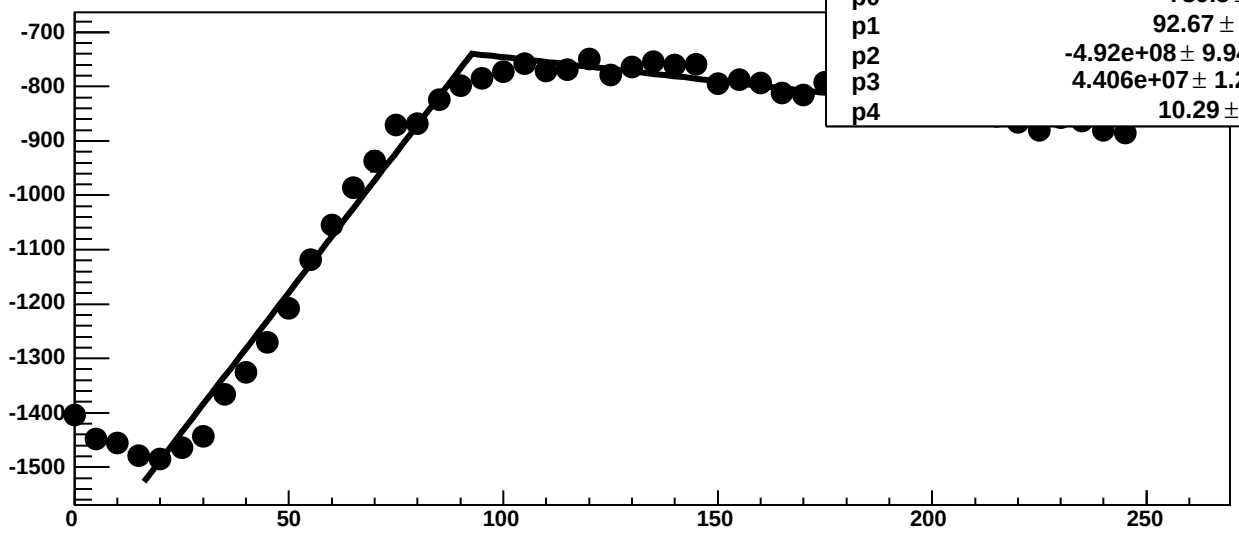
Chip 6, Channel 10, Enable 3!, DAC=1600, ADC Noise vs Hold



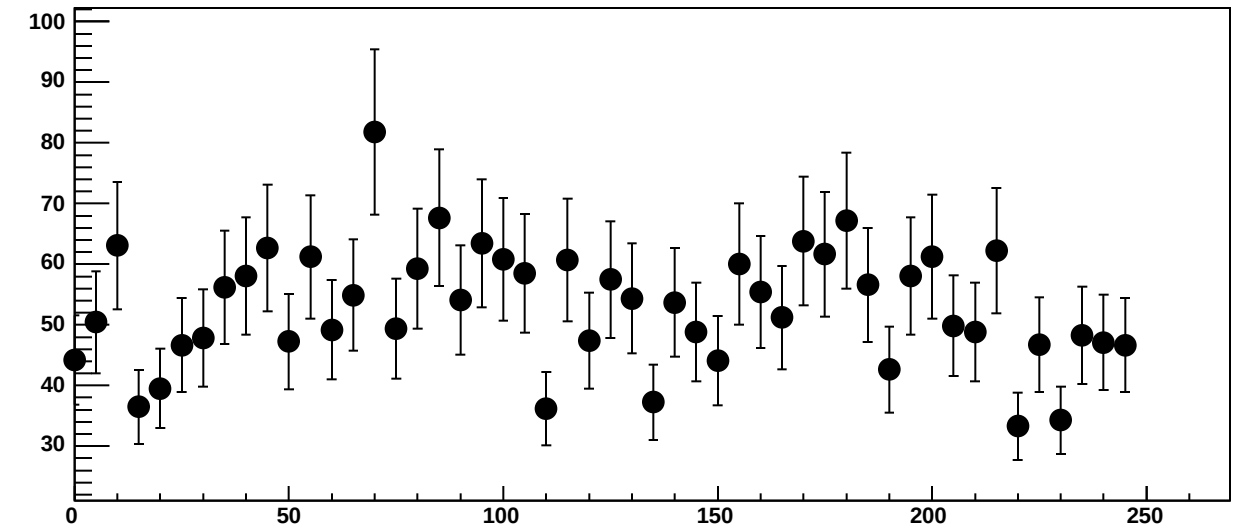
Chip 6, Channel 10, Enable 3!, DAC=1600, ADC Residuals vs Hold



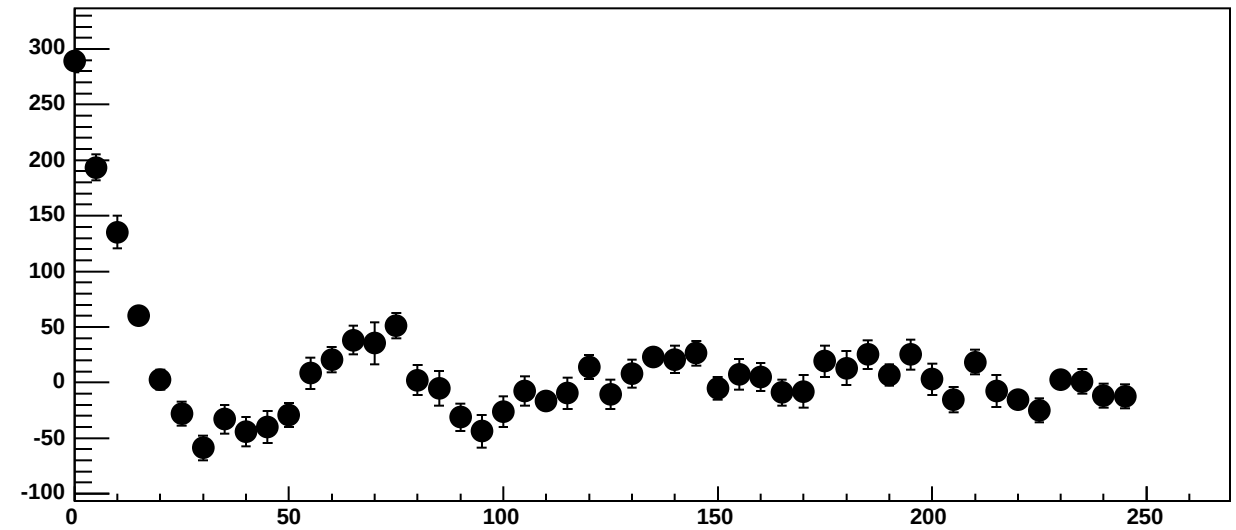
Chip 6, Channel 10, Enable 4, DAC=1600, ADC Mean vs Hold

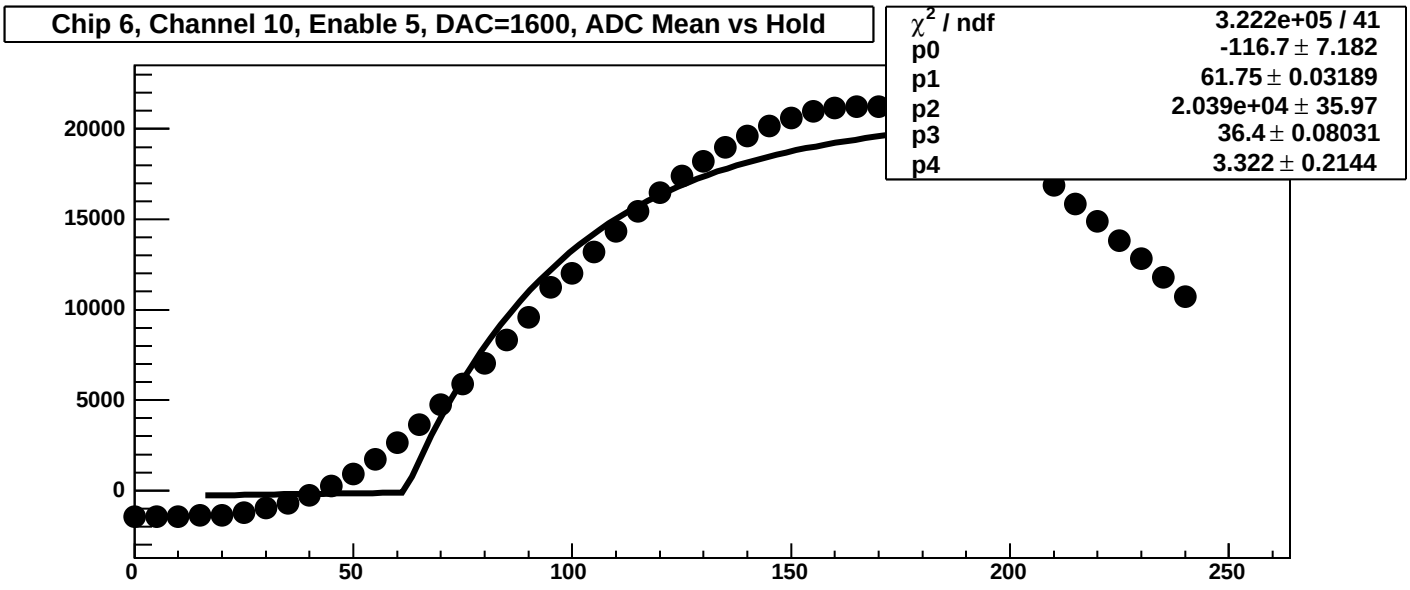


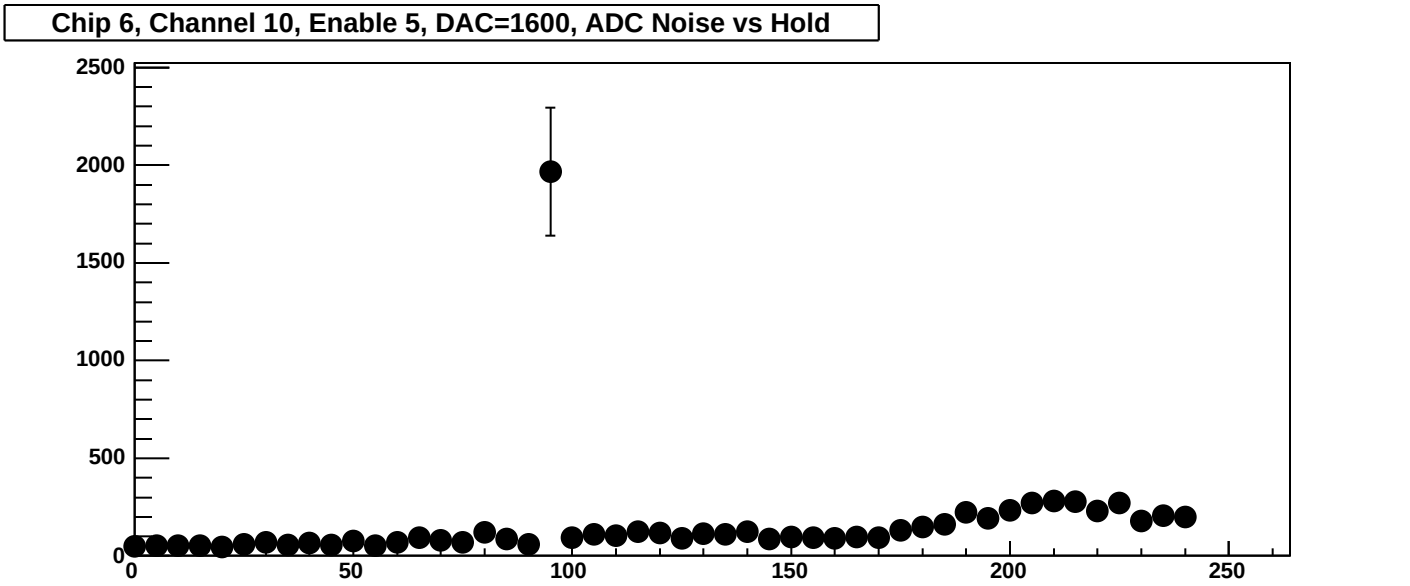
Chip 6, Channel 10, Enable 4, DAC=1600, ADC Noise vs Hold

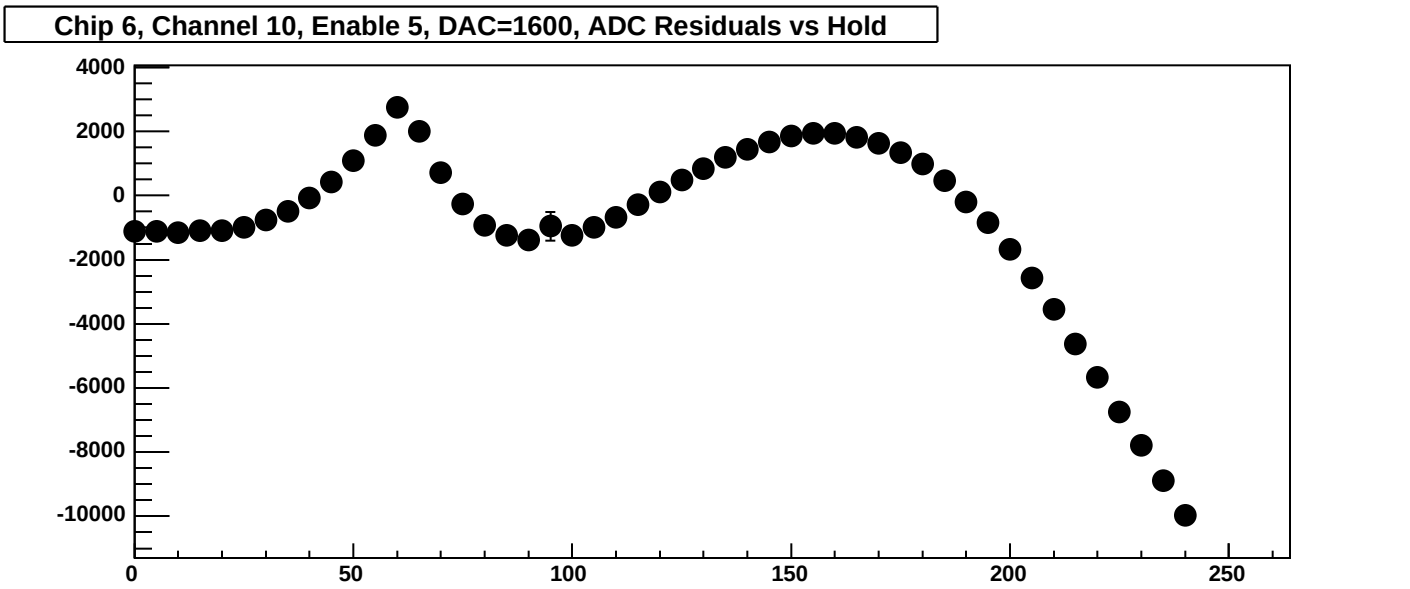


Chip 6, Channel 10, Enable 4, DAC=1600, ADC Residuals vs Hold

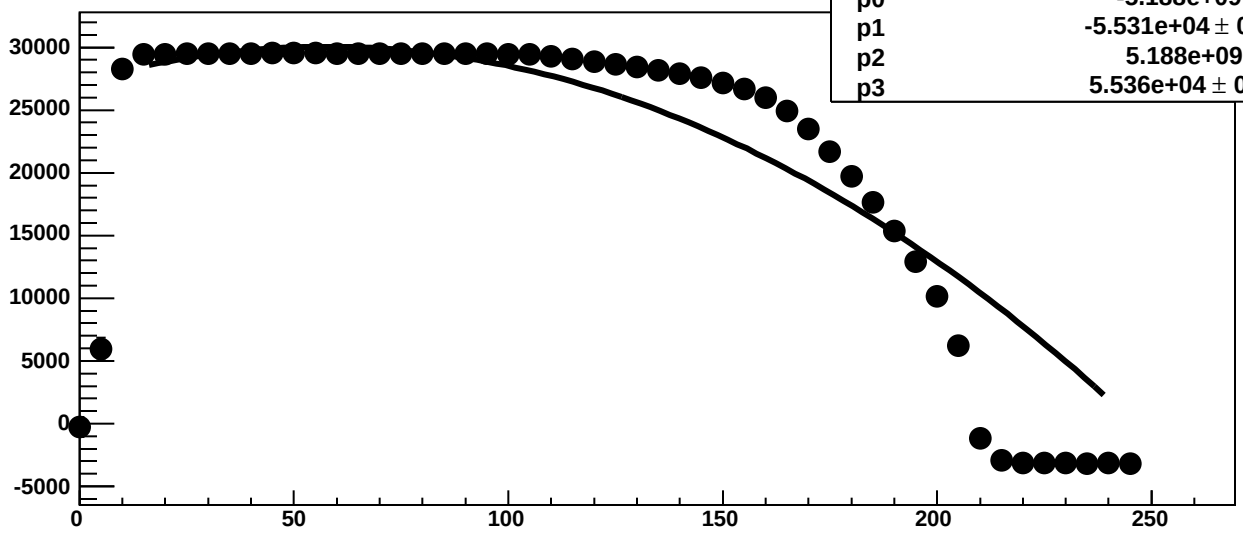



Chip 6, Channel 10, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.222e+05 / 41
p0 -116.7 ± 7.182
p1 61.75 ± 0.03189
p2 2.039e+04 ± 35.97
p3 36.4 ± 0.08031
p4 3.322 ± 0.2144
20000
15000
10000
5000
0
0
50
100
150
200
250

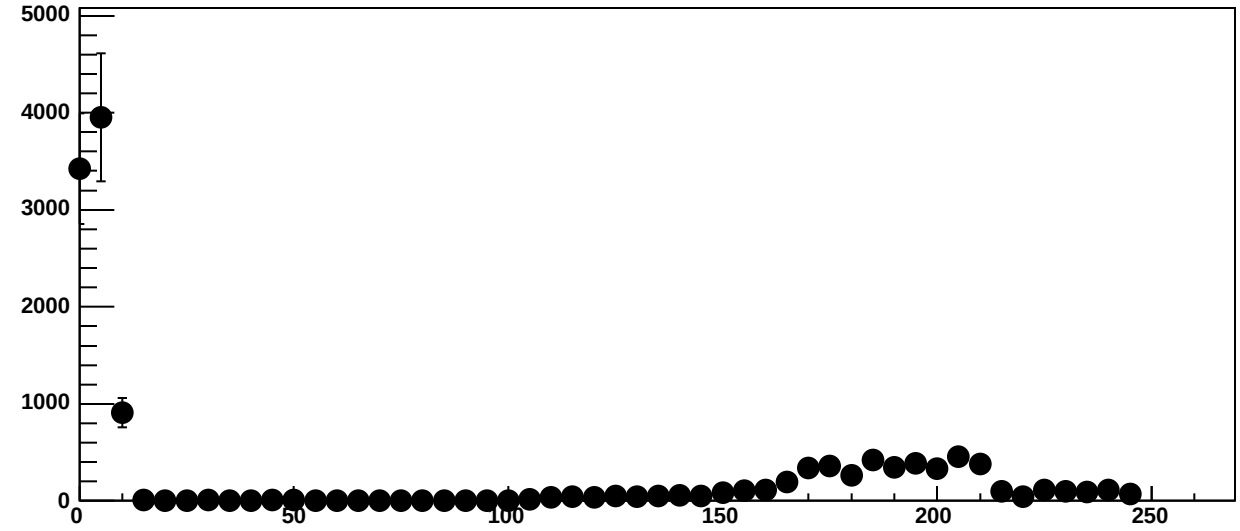

Chip 6, Channel 10, Enable 5, DAC=1600, ADC Noise vs Hold
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 6, Channel 10, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

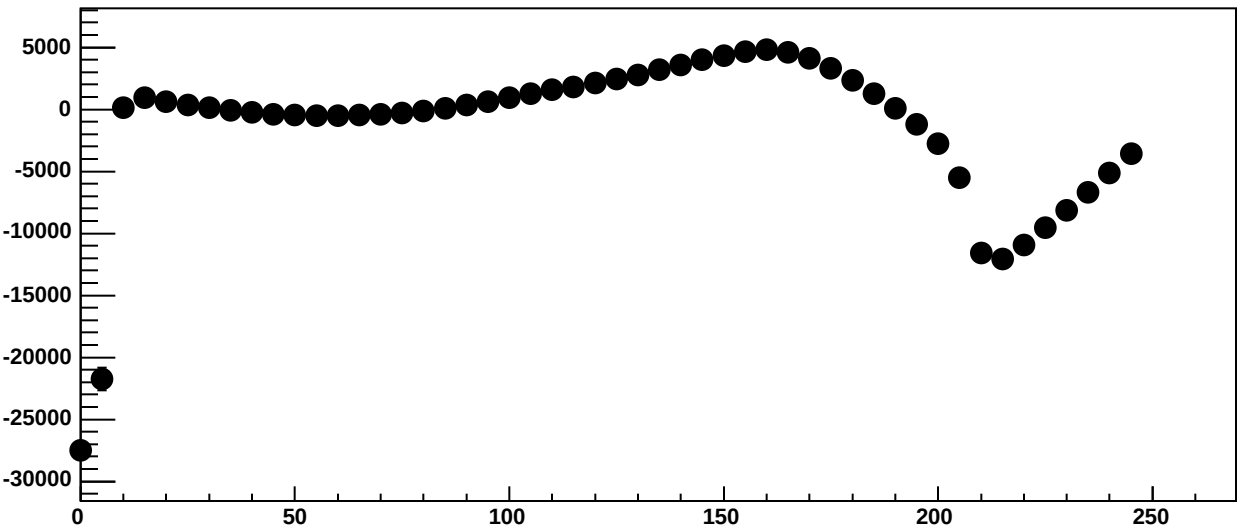
Chip 6, Channel 11, Enable 0!, DAC=1600, ADC Mean vs Hold



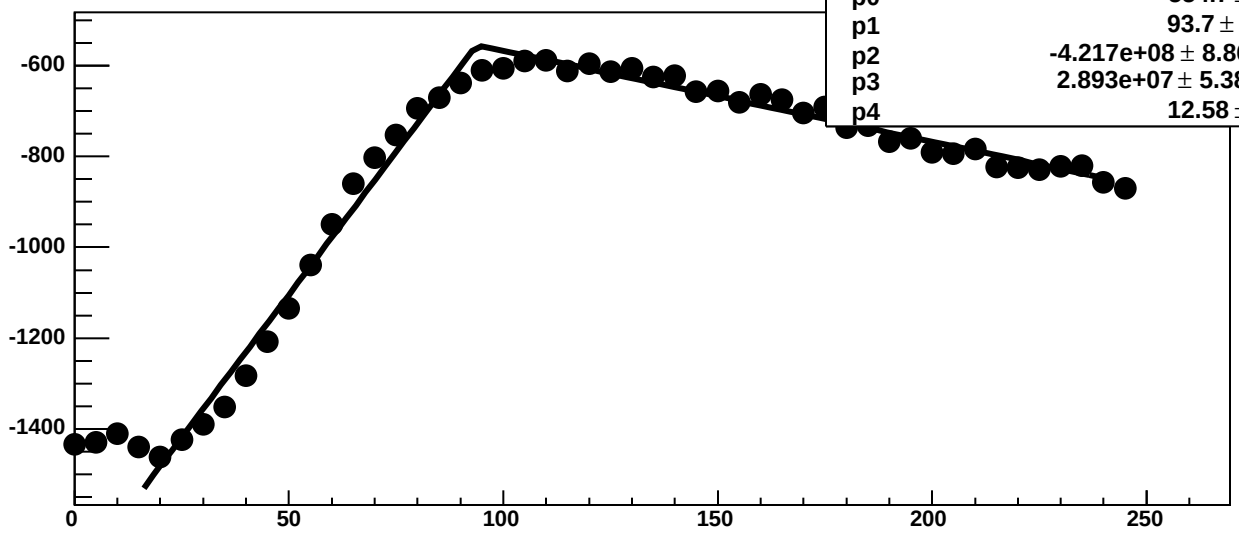
Chip 6, Channel 11, Enable 0!, DAC=1600, ADC Noise vs Hold



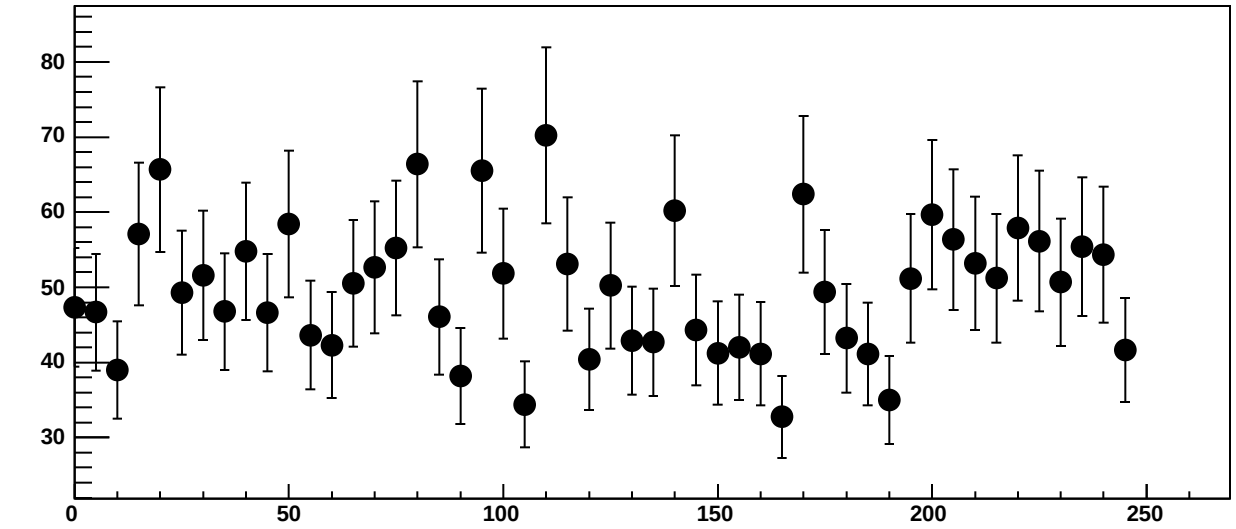
Chip 6, Channel 11, Enable 0!, DAC=1600, ADC Residuals vs Hold



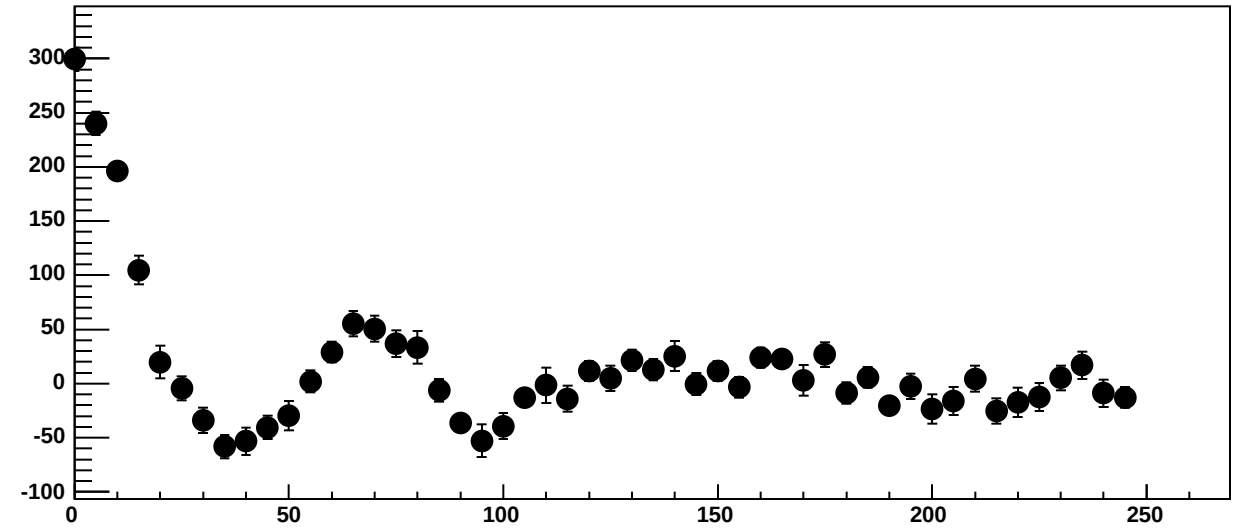
Chip 6, Channel 11, Enable 1, DAC=1600, ADC Mean vs Hold

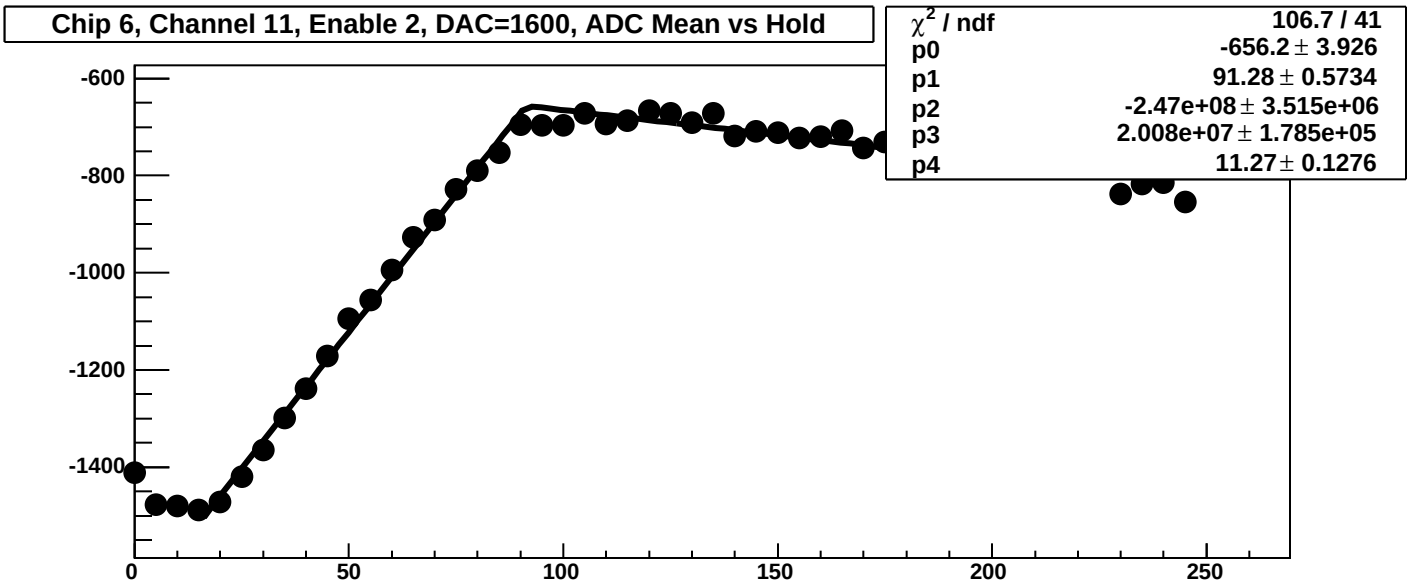


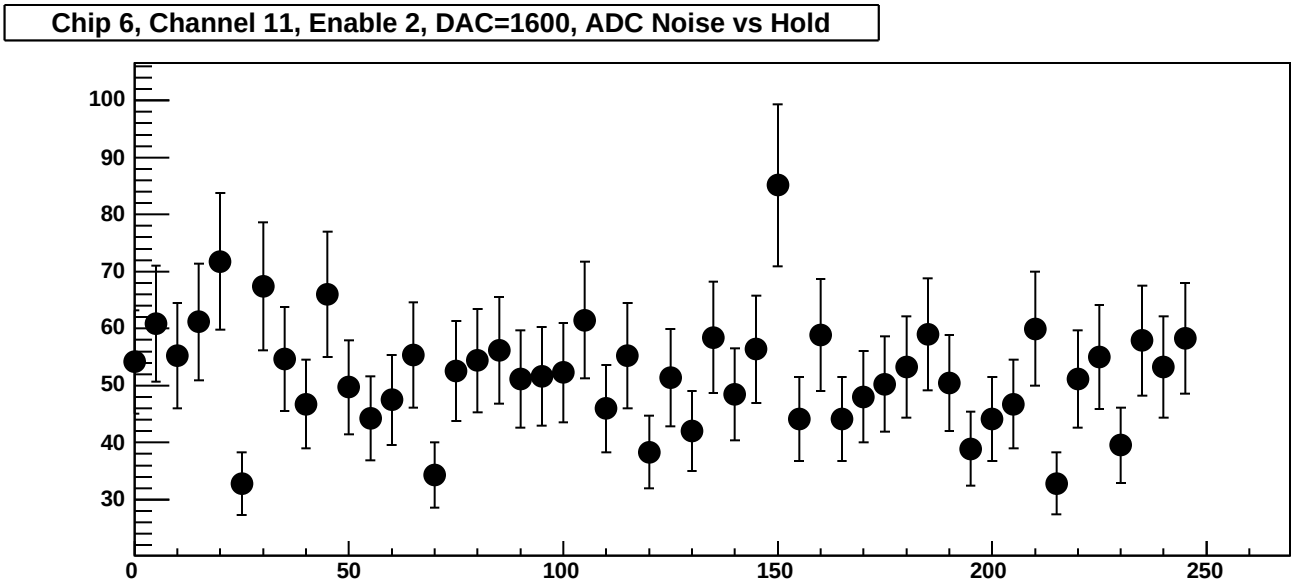
Chip 6, Channel 11, Enable 1, DAC=1600, ADC Noise vs Hold

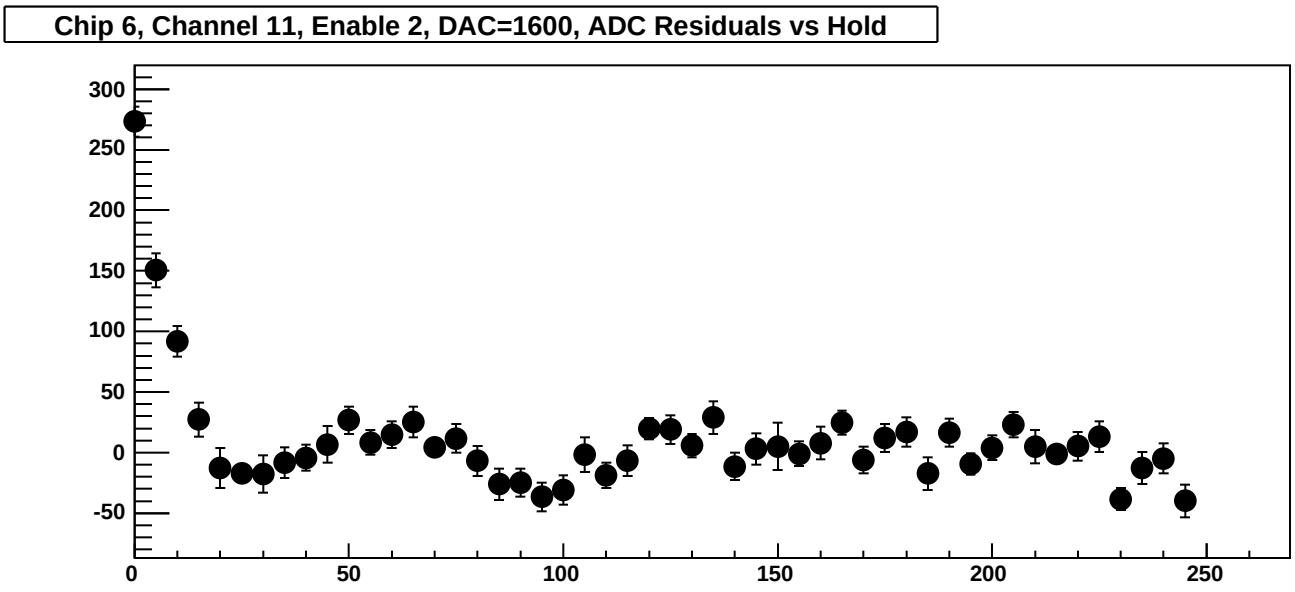


Chip 6, Channel 11, Enable 1, DAC=1600, ADC Residuals vs Hold

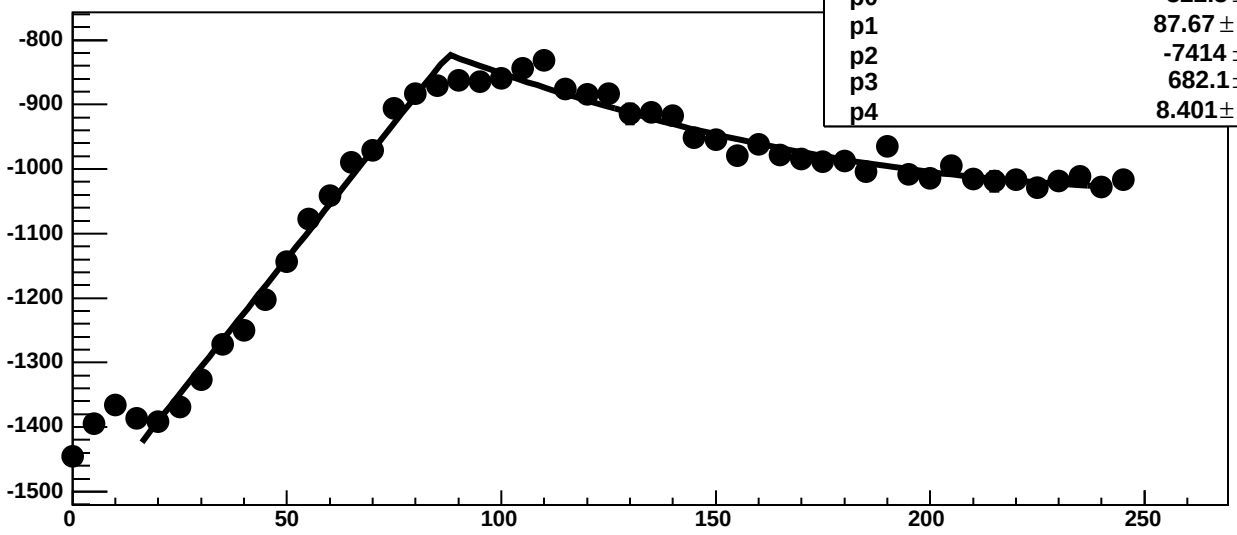



Chip 6, Channel 11, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 106.7 / 41
p0 -656.2 ± 3.926
p1 91.28 ± 0.5734
p2 -2.47e+08 ± 3.515e+06
p3 2.008e+07 ± 1.785e+05
p4 11.27 ± 0.1276
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

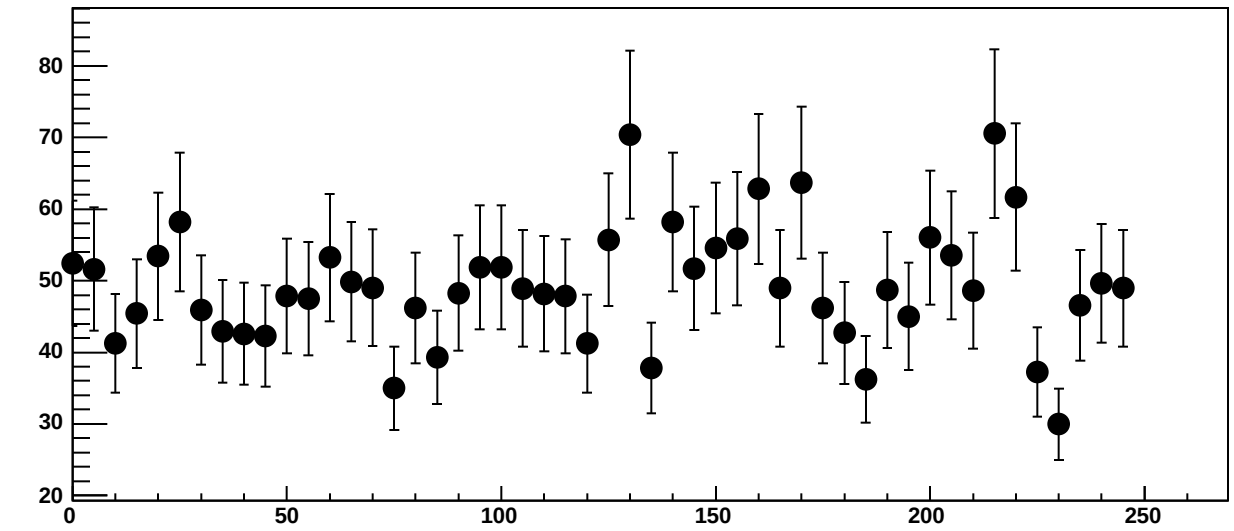

Chip 6, Channel 11, Enable 2, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 6, Channel 11, Enable 2, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
0
50
100
150
200
250

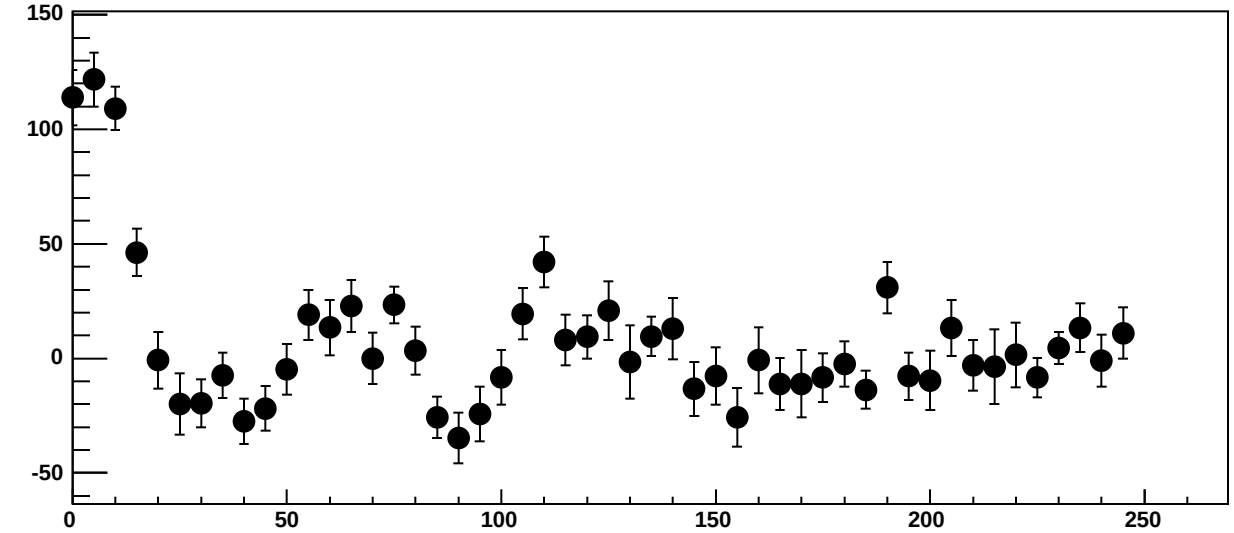
Chip 6, Channel 11, Enable 3, DAC=1600, ADC Mean vs Hold



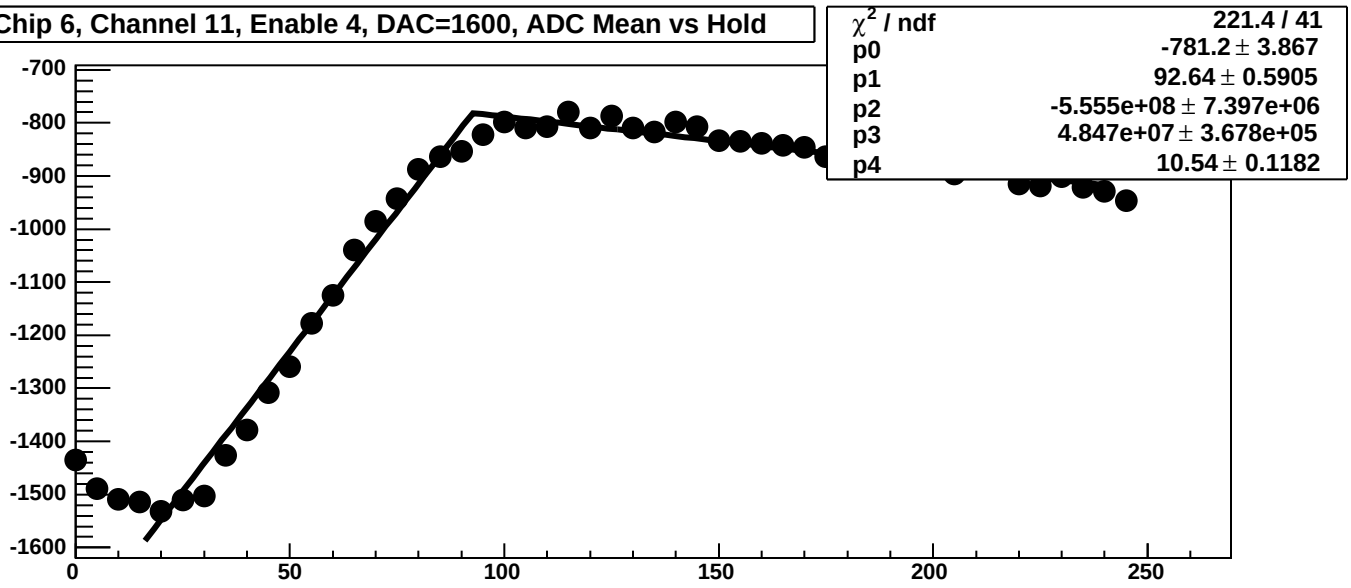
Chip 6, Channel 11, Enable 3, DAC=1600, ADC Noise vs Hold



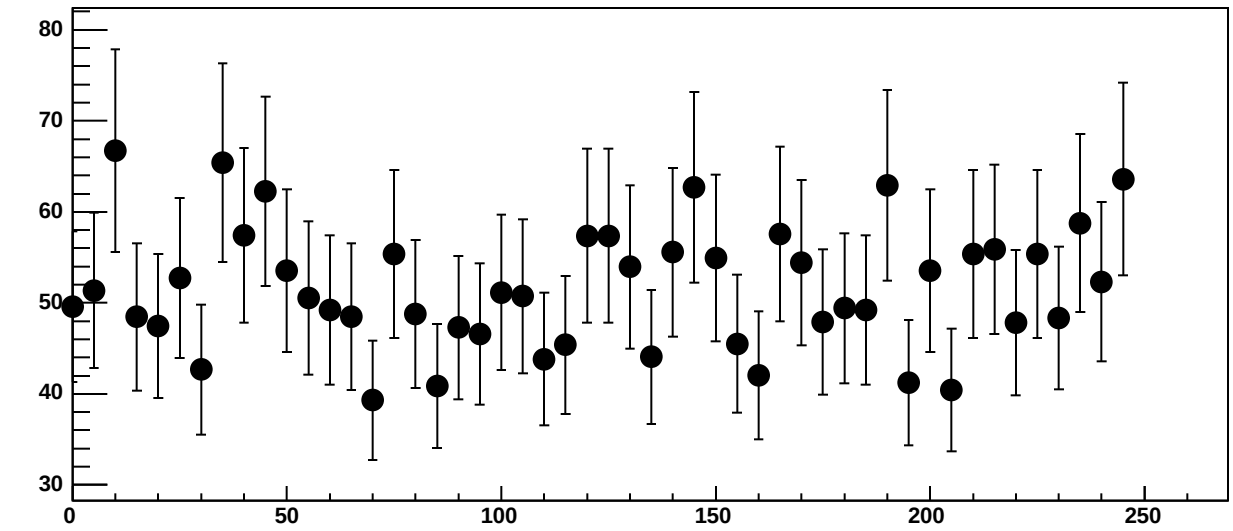
Chip 6, Channel 11, Enable 3, DAC=1600, ADC Residuals vs Hold



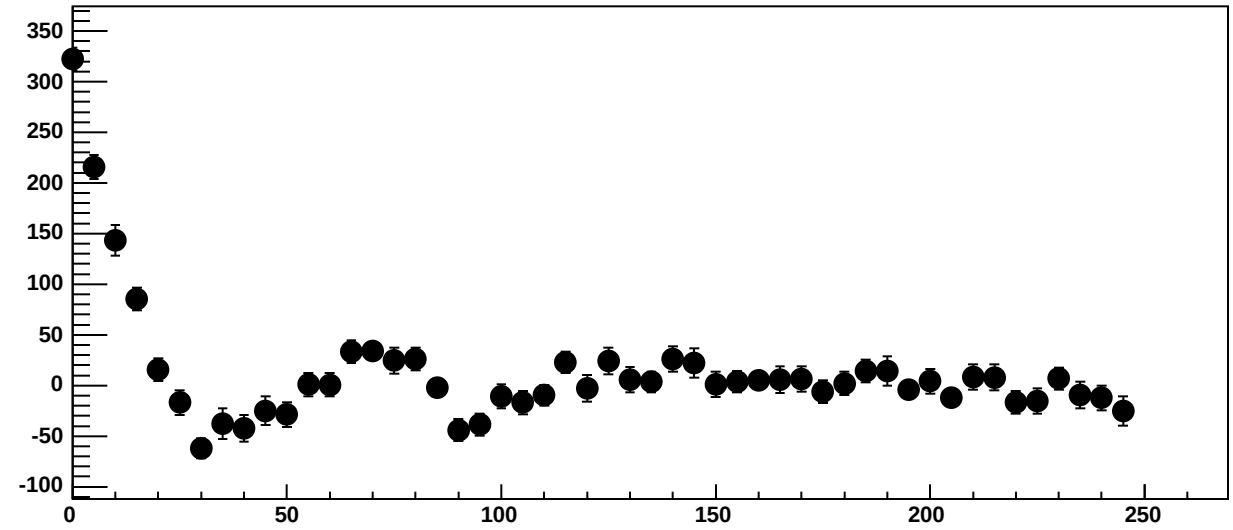
Chip 6, Channel 11, Enable 4, DAC=1600, ADC Mean vs Hold

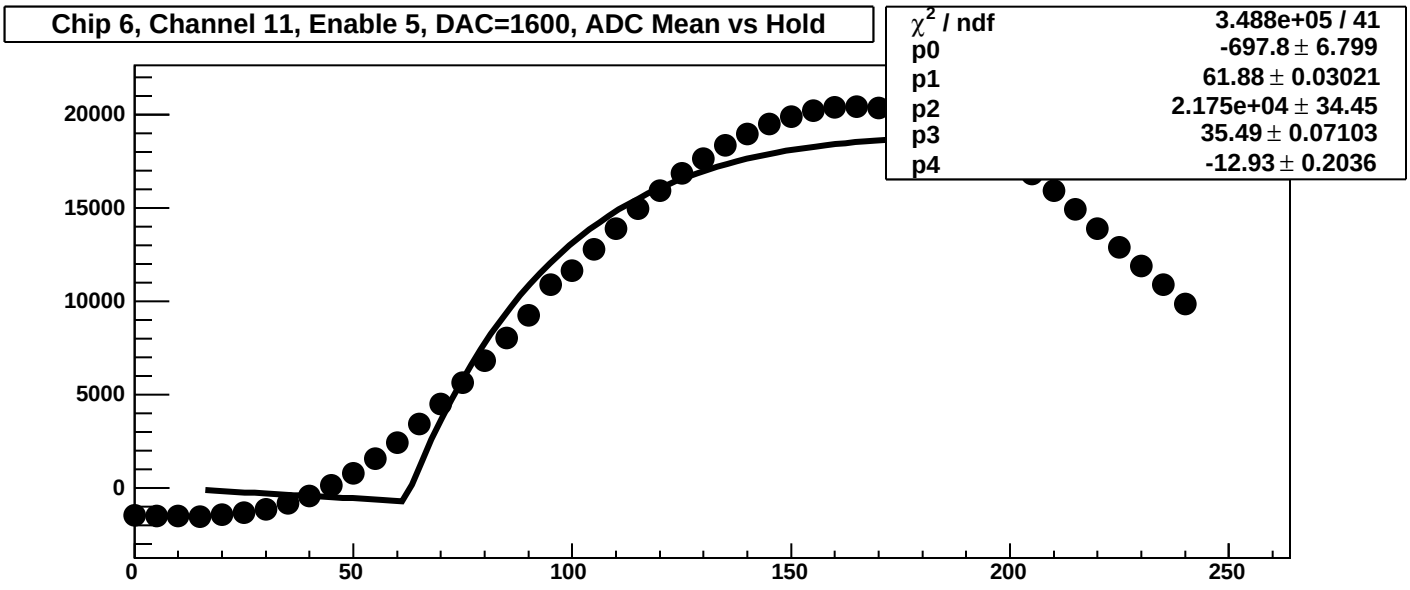


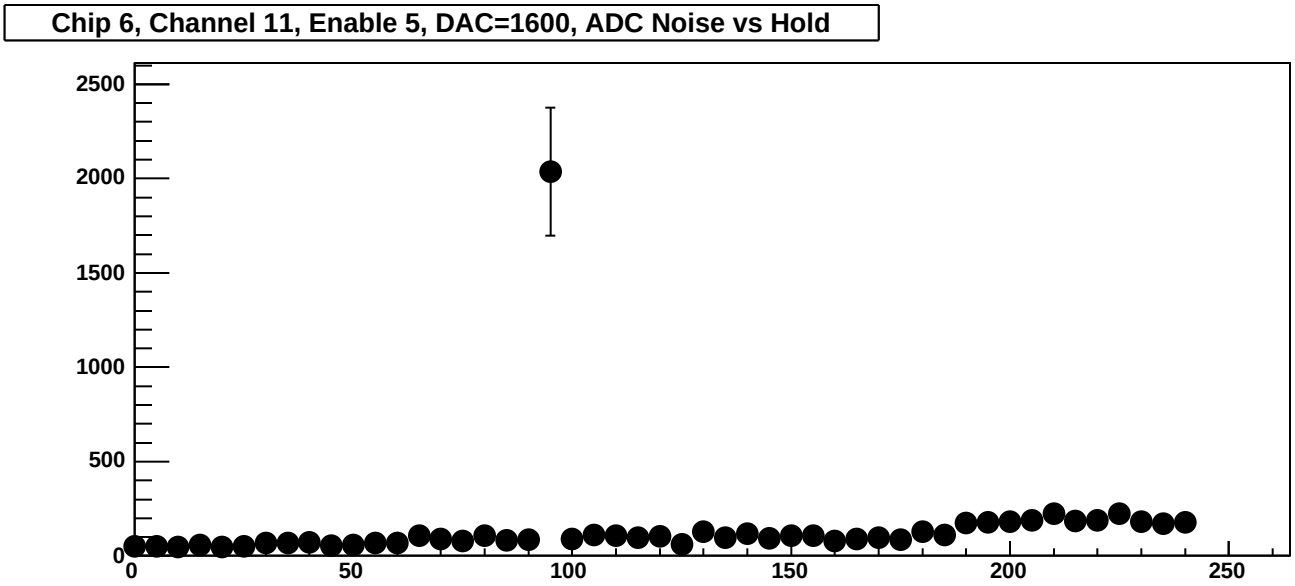
Chip 6, Channel 11, Enable 4, DAC=1600, ADC Noise vs Hold

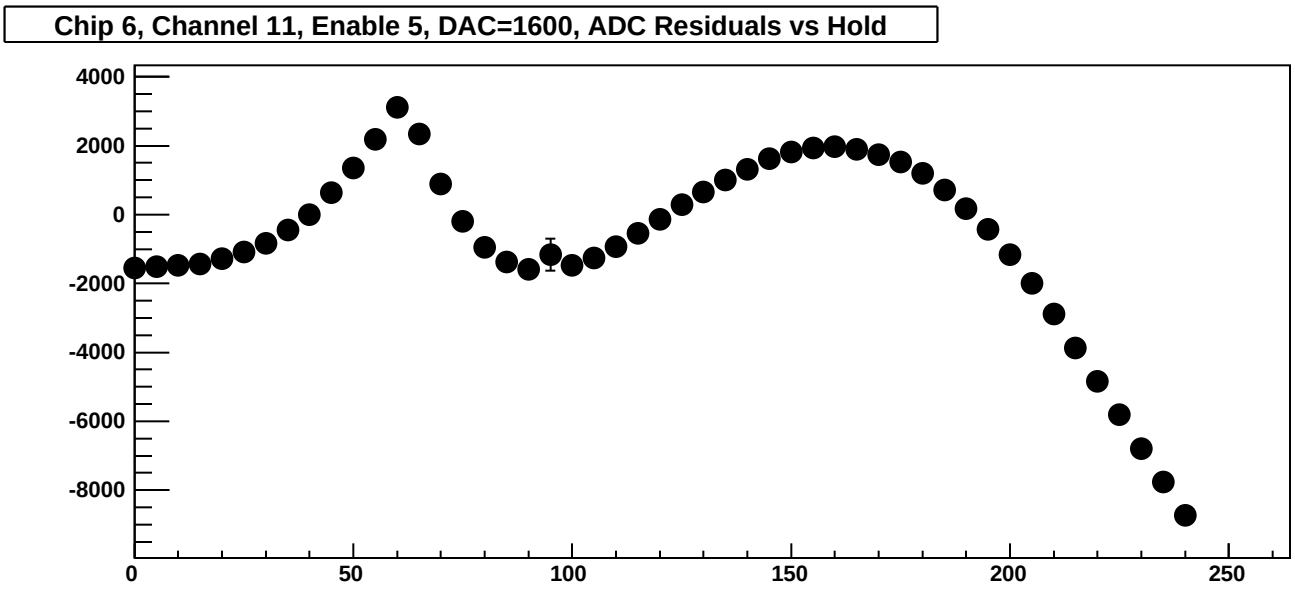


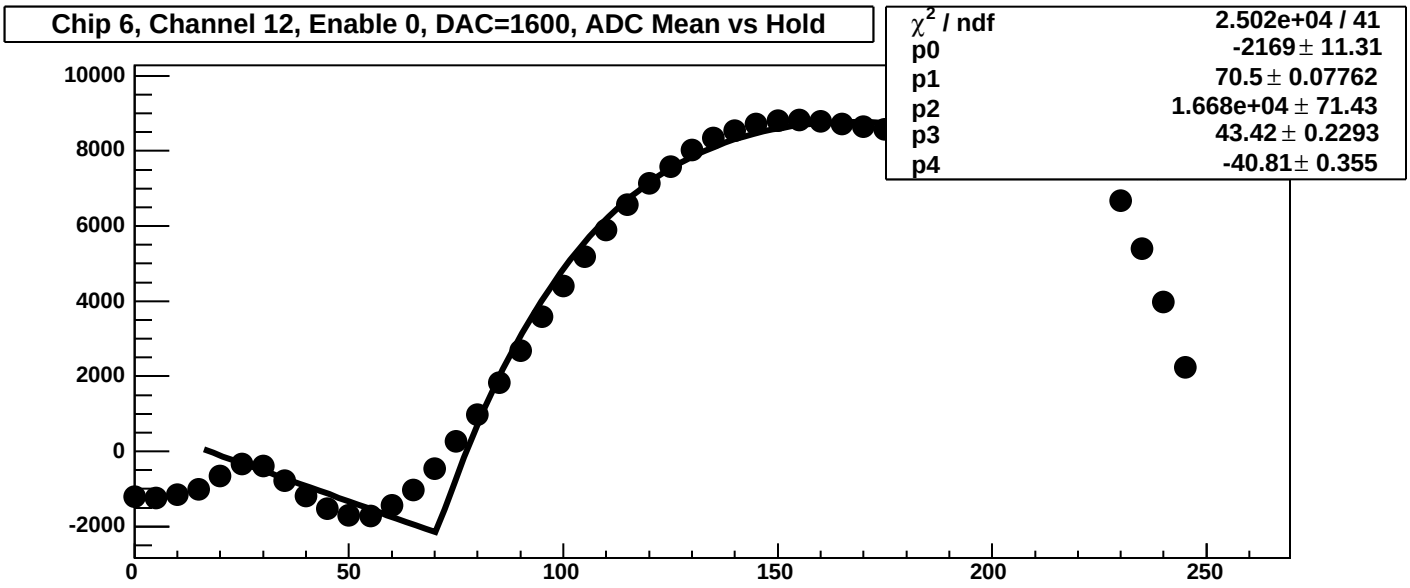
Chip 6, Channel 11, Enable 4, DAC=1600, ADC Residuals vs Hold

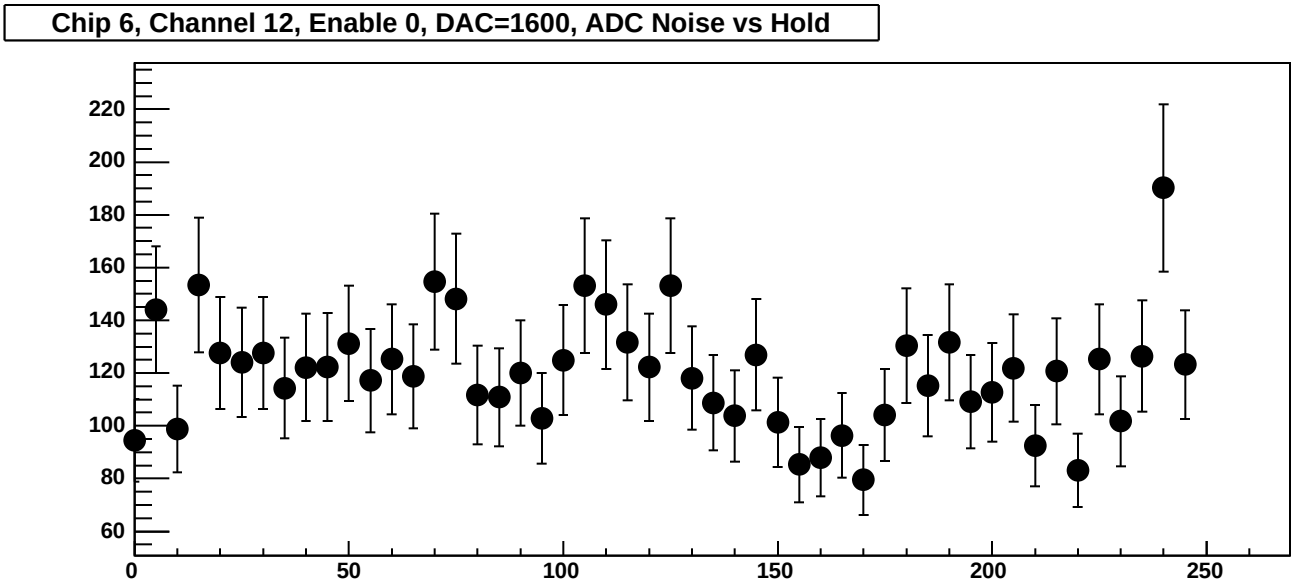


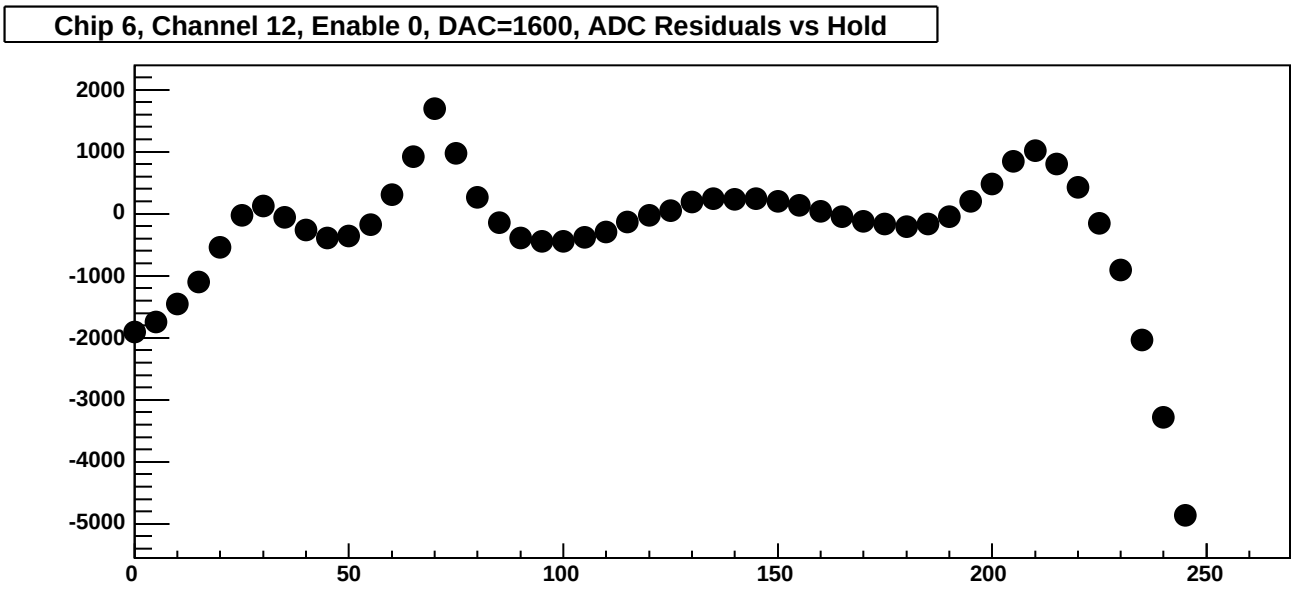

Chip 6, Channel 11, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.488e+05 / 41
p0 -697.8 ± 6.799
p1 61.88 ± 0.03021
p2 2.175e+04 ± 34.45
p3 35.49 ± 0.07103
p4 -12.93 ± 0.2036
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 6, Channel 11, Enable 5, DAC=1600, ADC Noise vs Hold
2500
2000
1500
1000
500
0
0
50
100
150
200
250

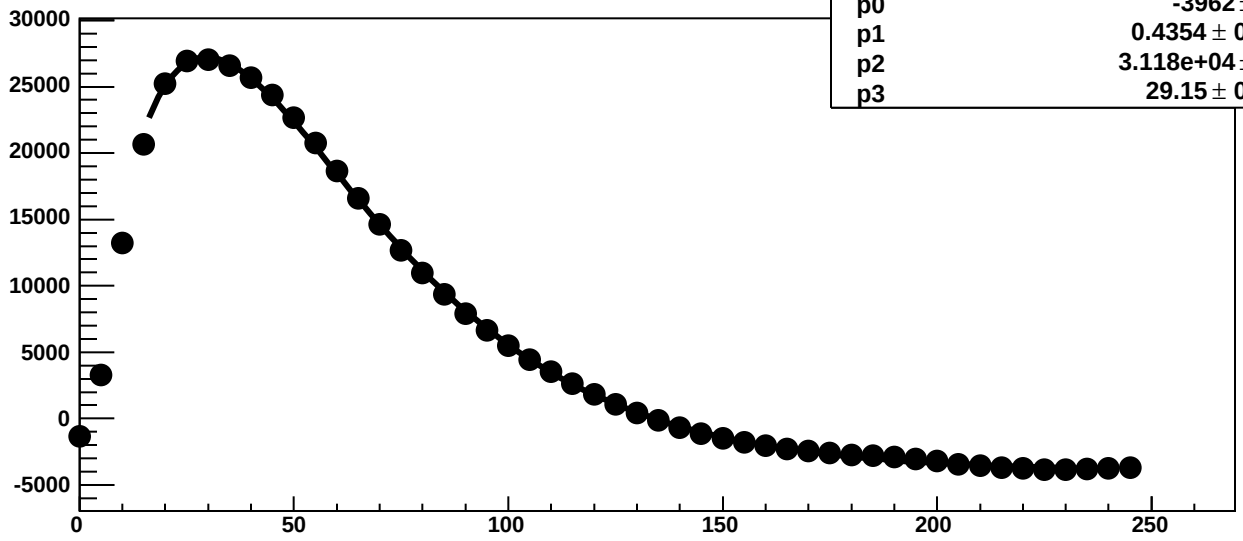

Chip 6, Channel 11, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250


Chip 6, Channel 12, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.502e+04 / 41
p0 -2169 ± 11.31
p1 70.5 ± 0.07762
p2 1.668e+04 ± 71.43
p3 43.42 ± 0.2293
p4 -40.81 ± 0.355
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

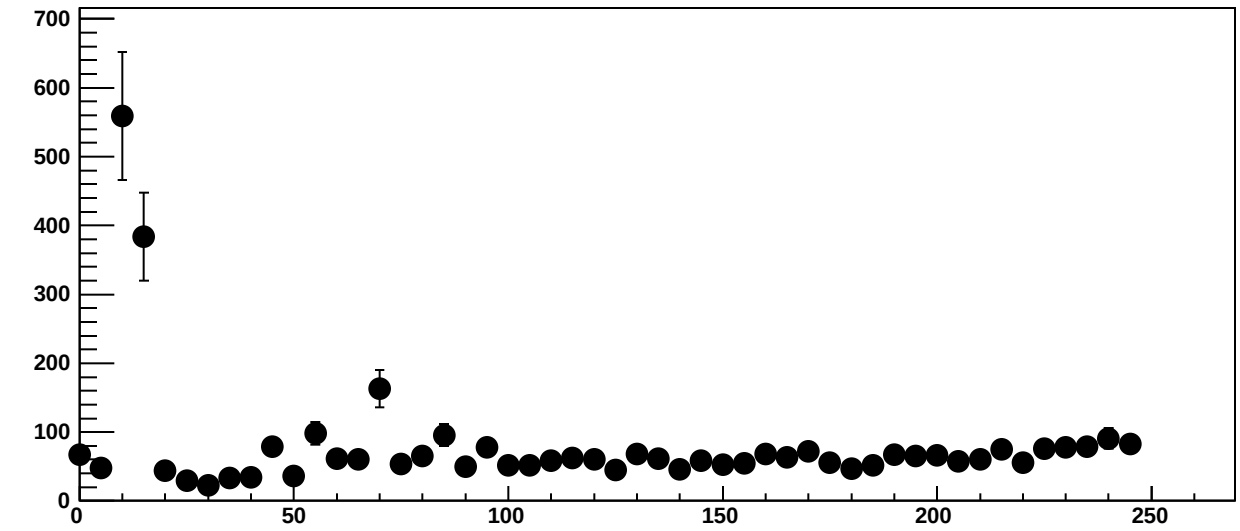

Chip 6, Channel 12, Enable 0, DAC=1600, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
0
50
100
150
200
250


Chip 6, Channel 12, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

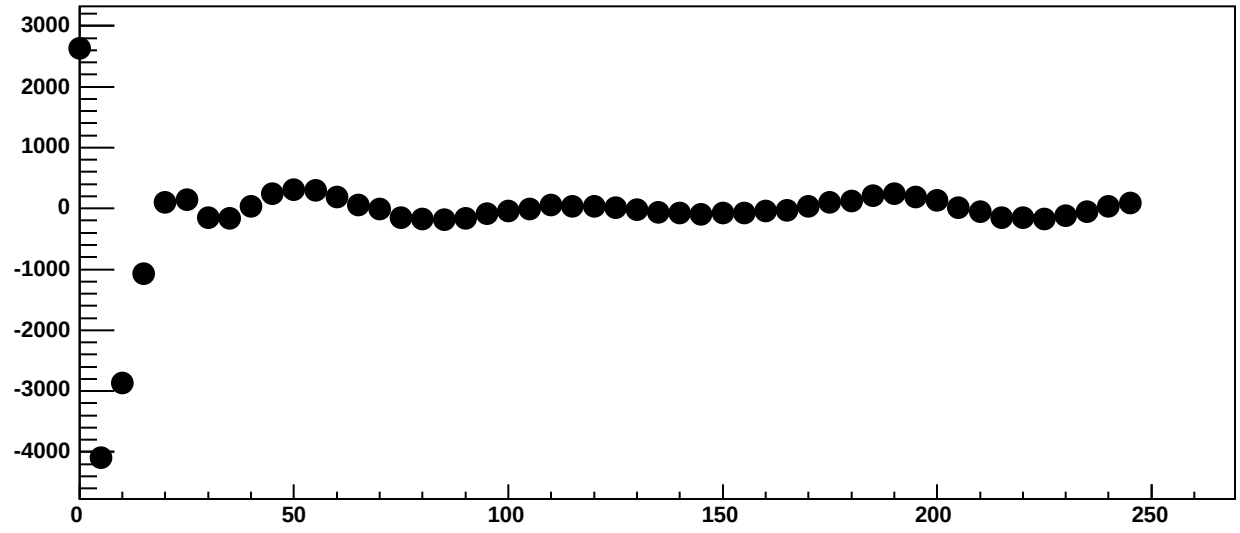
Chip 6, Channel 12, Enable 1!, DAC=1600, ADC Mean vs Hold

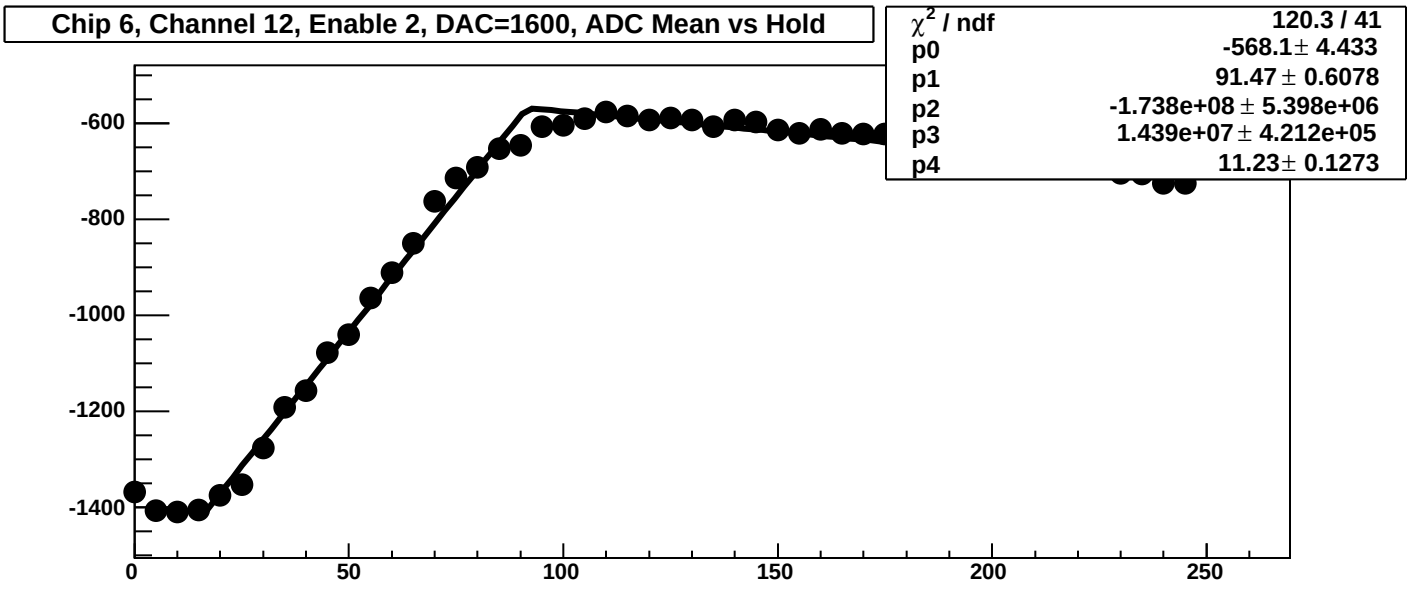


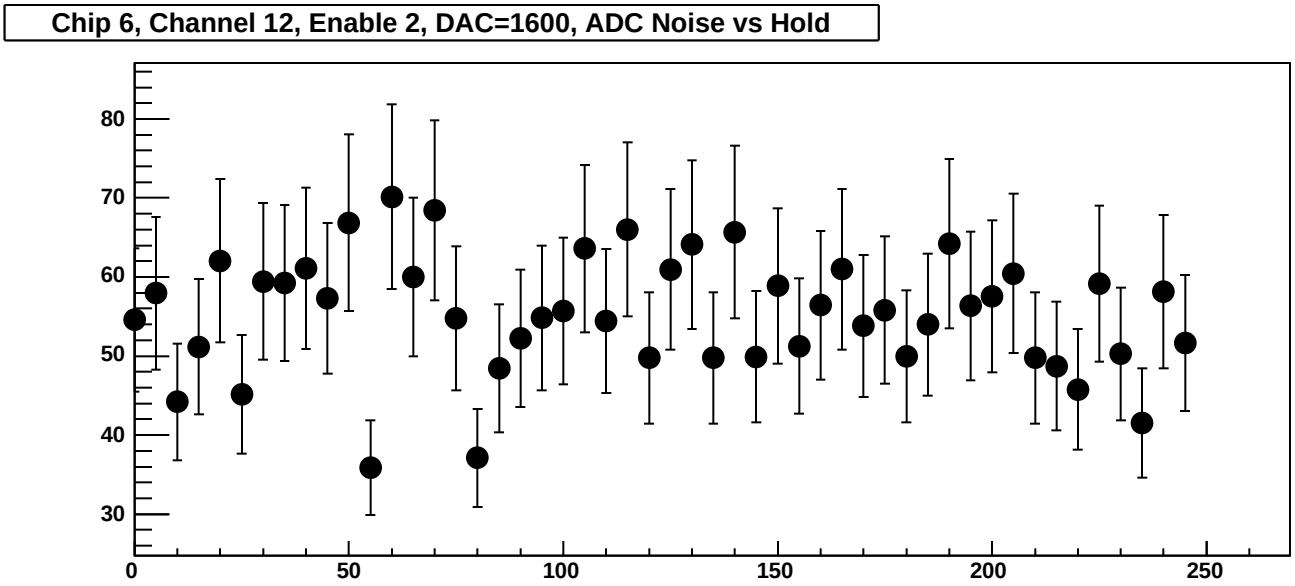
Chip 6, Channel 12, Enable 1!, DAC=1600, ADC Noise vs Hold

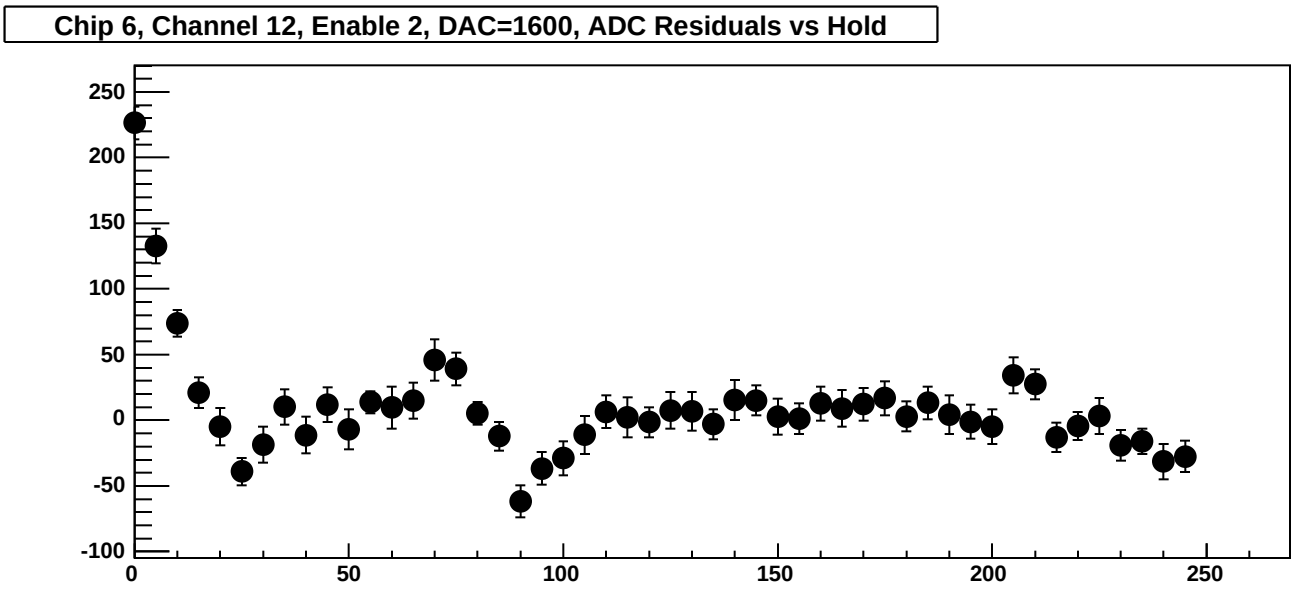


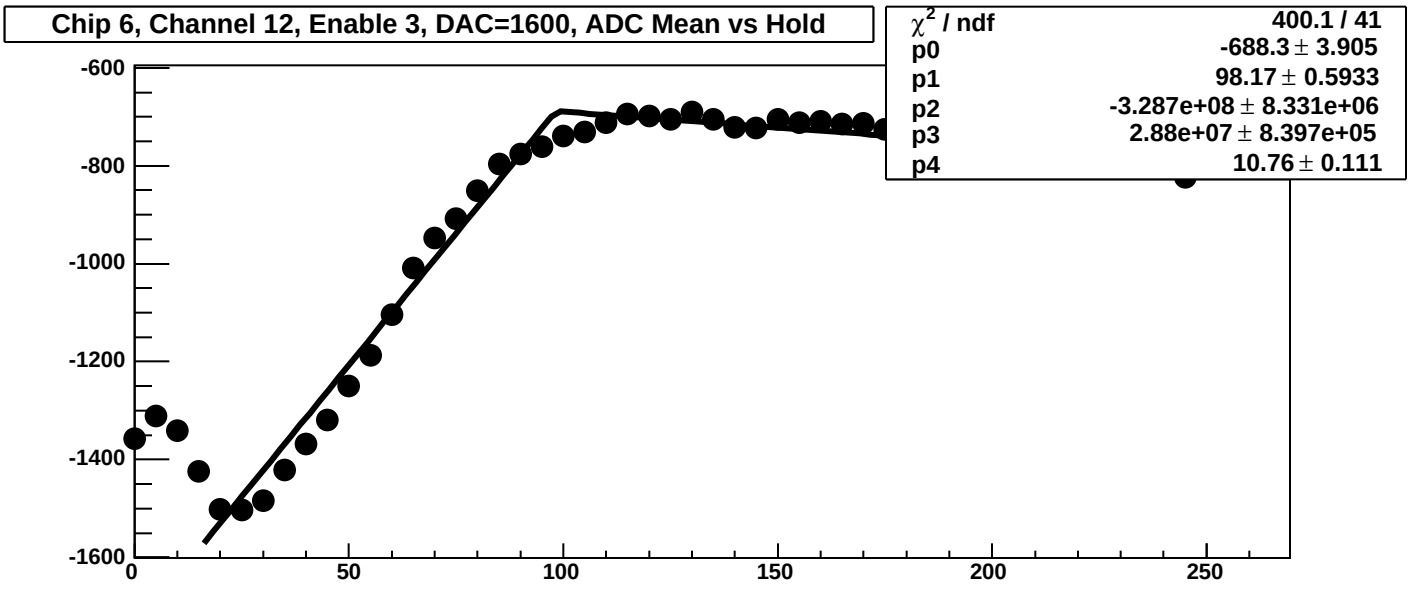
Chip 6, Channel 12, Enable 1!, DAC=1600, ADC Residuals vs Hold

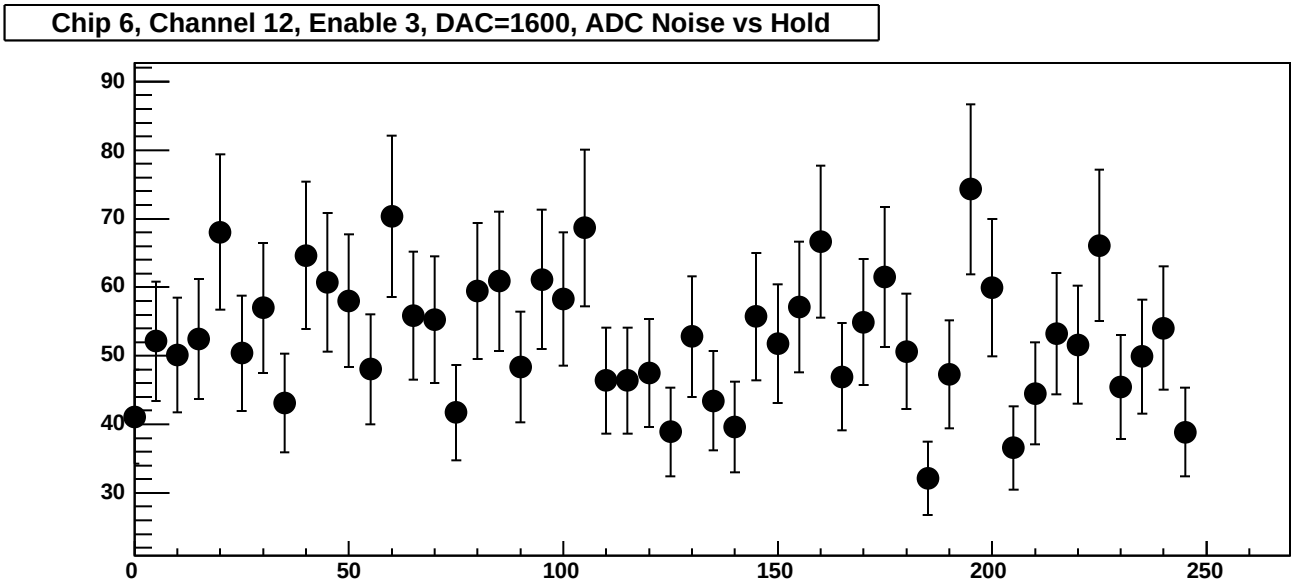


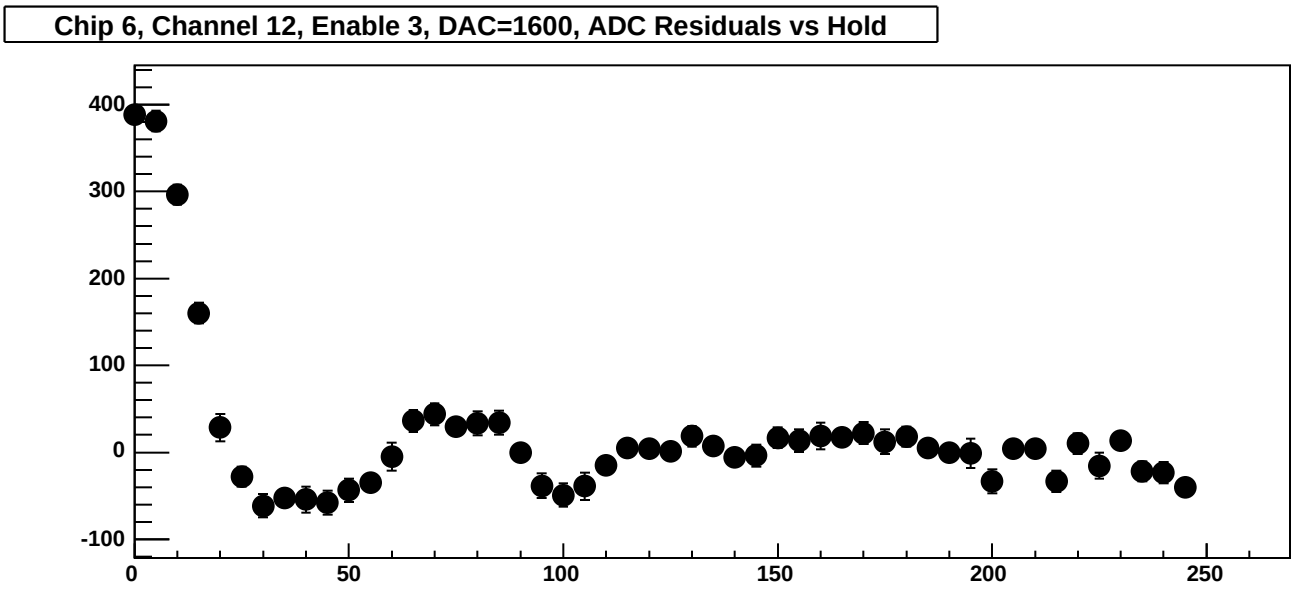

Chip 6, Channel 12, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 120.3 / 41
p0 -568.1 ± 4.433
p1 91.47 ± 0.6078
p2 -1.738e+08 ± 5.398e+06
p3 1.439e+07 ± 4.212e+05
p4 11.23 ± 0.1273
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

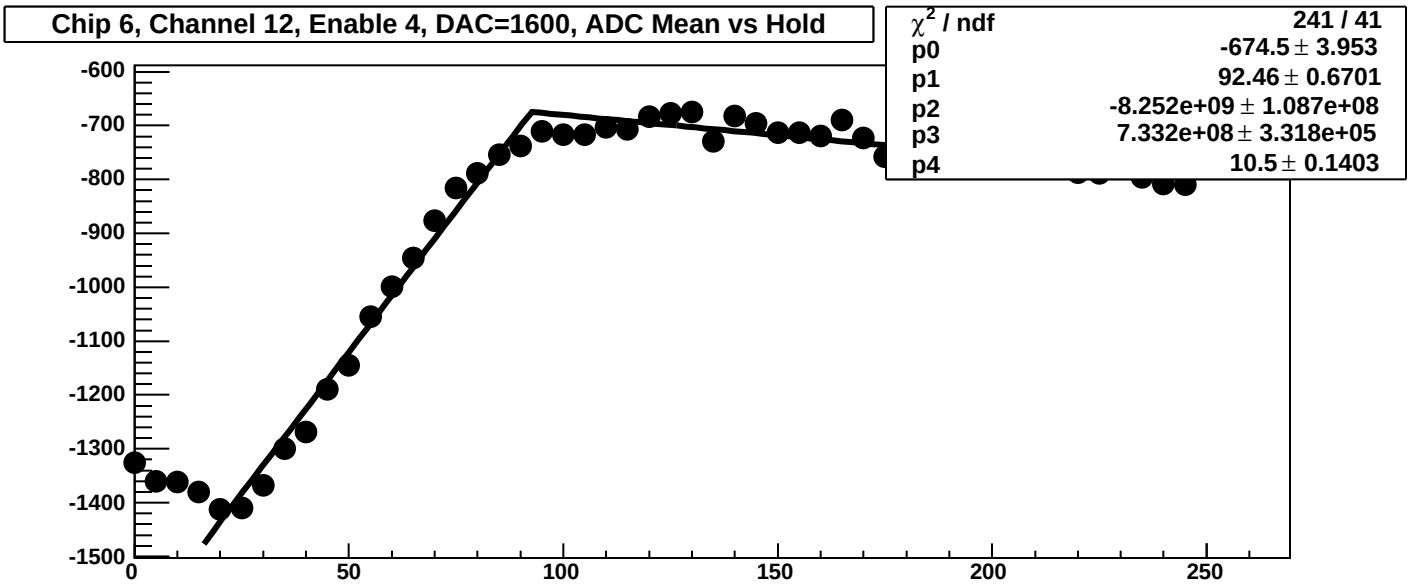

Chip 6, Channel 12, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

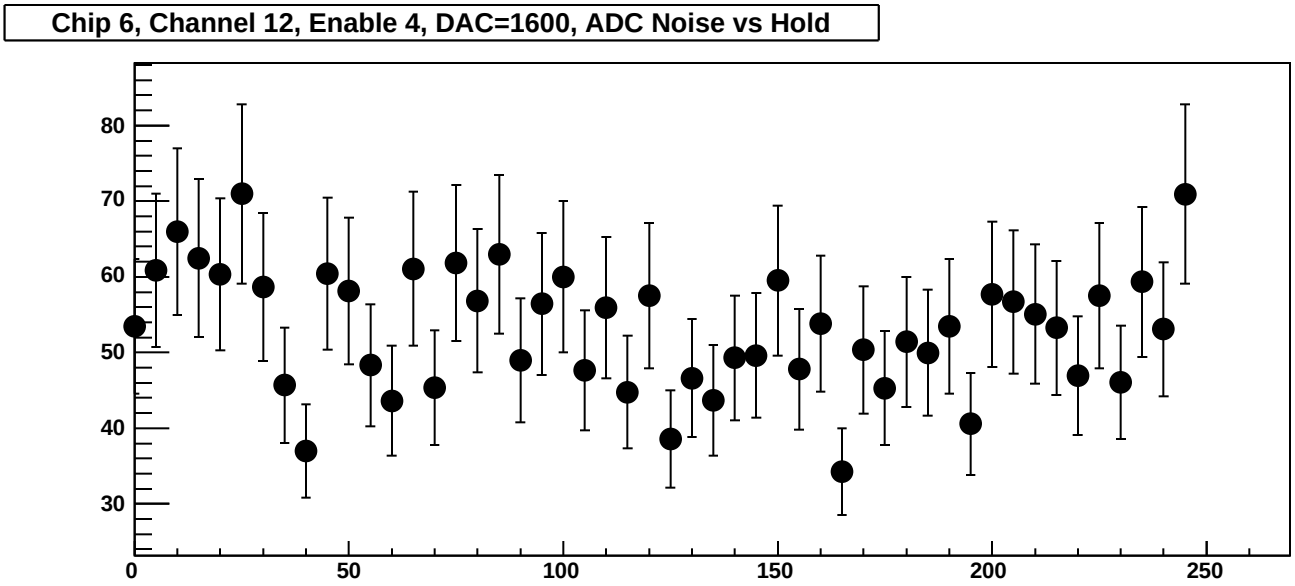

Chip 6, Channel 12, Enable 2, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

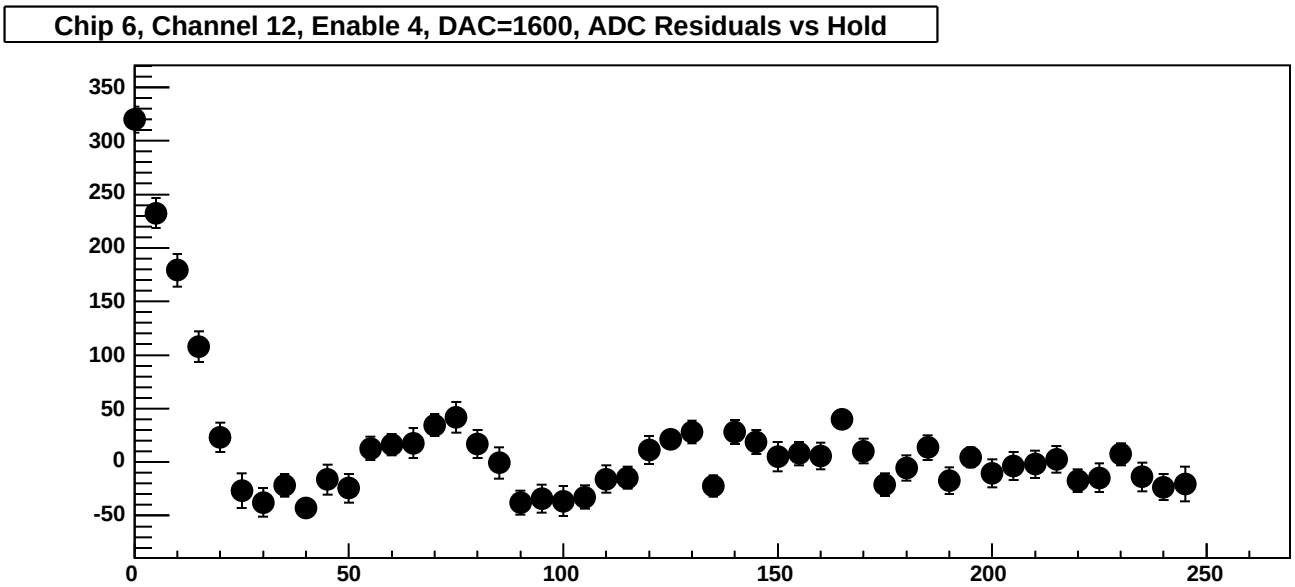

Chip 6, Channel 12, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 400.1 / 41
p0 -688.3 ± 3.905
p1 98.17 ± 0.5933
p2 -3.287e+08 ± 8.331e+06
p3 2.88e+07 ± 8.397e+05
p4 10.76 ± 0.111
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250


Chip 6, Channel 12, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

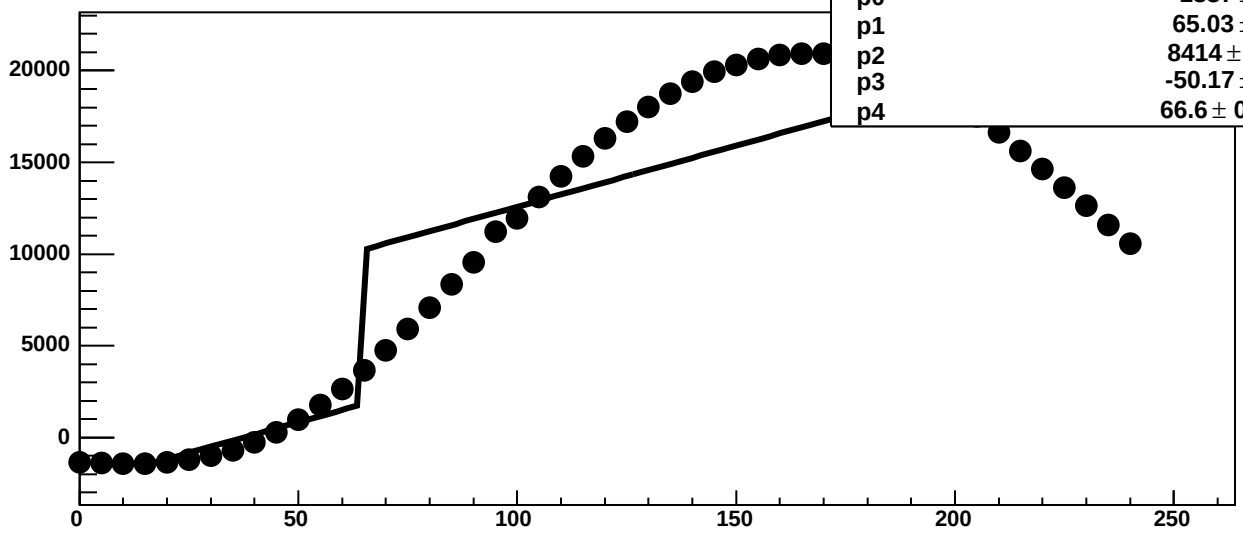

Chip 6, Channel 12, Enable 3, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 6, Channel 12, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 241 / 41
p0 -674.5 ± 3.953
p1 92.46 ± 0.6701
p2 -8.252e+09 ± 1.087e+08
p3 7.332e+08 ± 3.318e+05
p4 10.5 ± 0.1403
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250

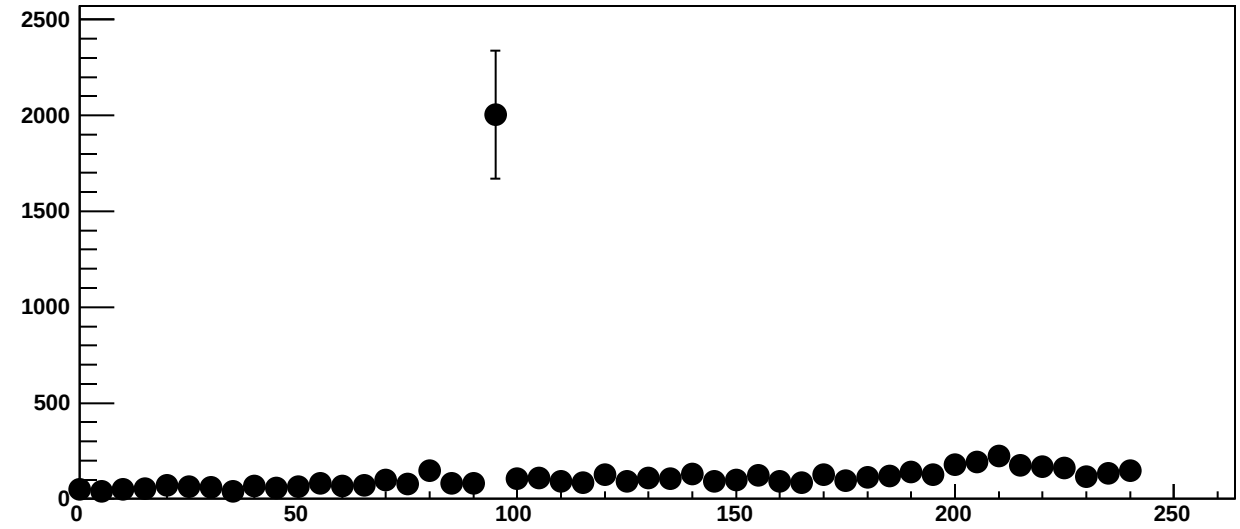

Chip 6, Channel 12, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 6, Channel 12, Enable 4, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
0
50
100
150
200
250

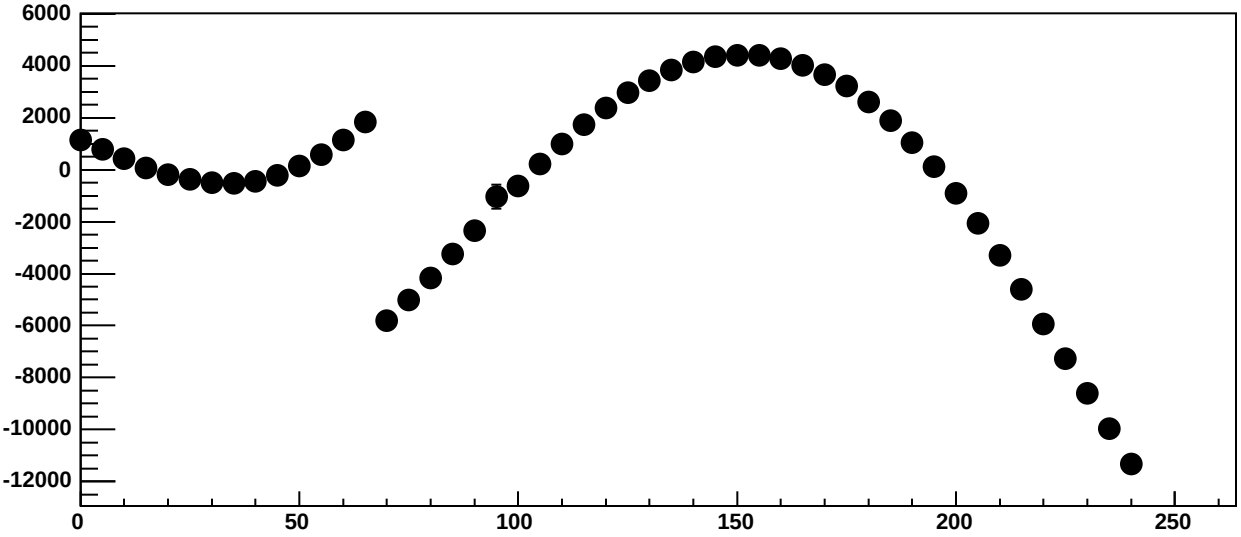
Chip 6, Channel 12, Enable 5, DAC=1600, ADC Mean vs Hold

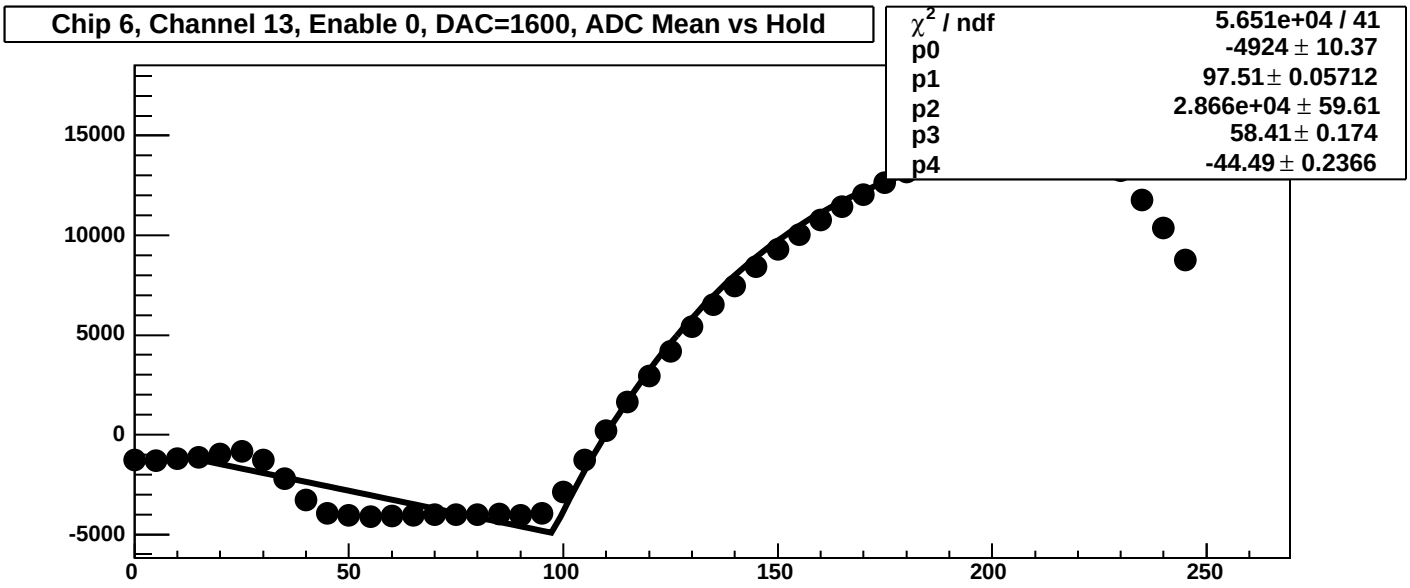


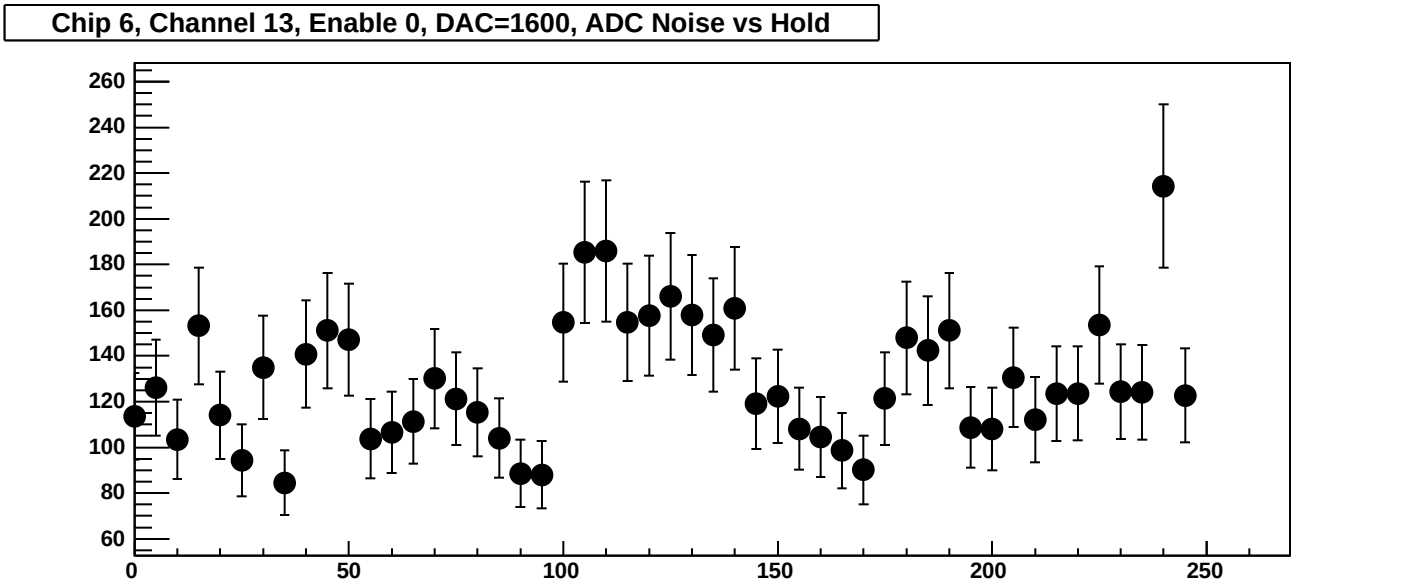
Chip 6, Channel 12, Enable 5, DAC=1600, ADC Noise vs Hold

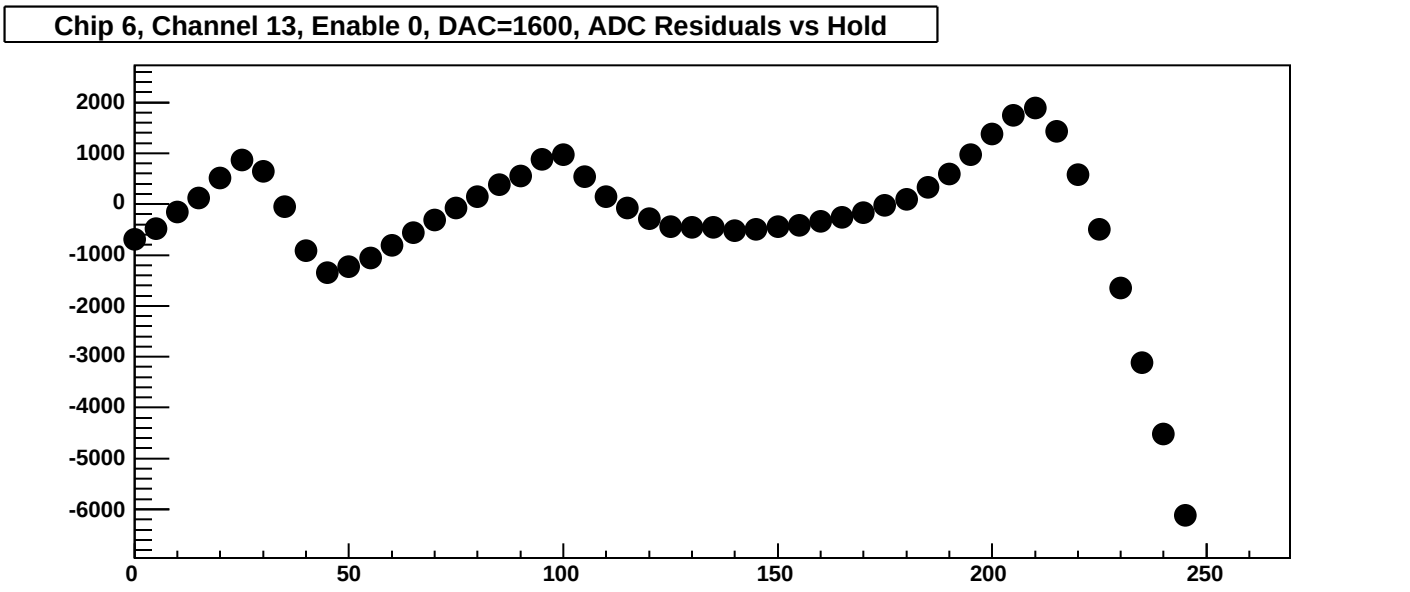


Chip 6, Channel 12, Enable 5, DAC=1600, ADC Residuals vs Hold

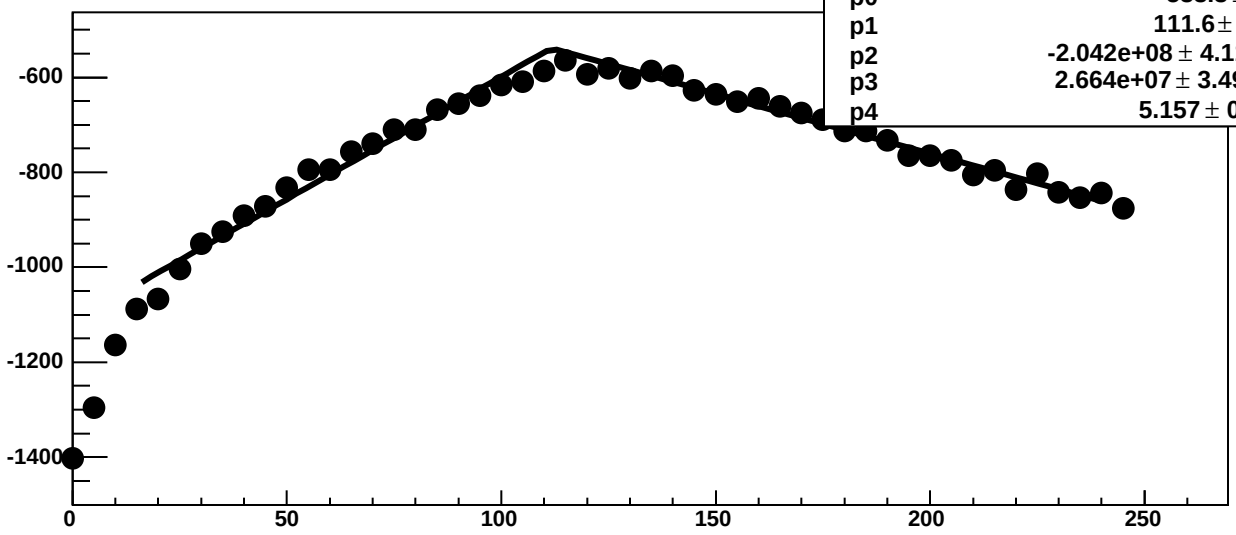



Chip 6, Channel 13, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.651e+04 / 41
p0 -4924 ± 10.37
p1 97.51 ± 0.05712
p2 2.866e+04 ± 59.61
p3 58.41 ± 0.174
p4 -44.49 ± 0.2366
15000
10000
5000
0
-5000
0
50
100
150
200
250

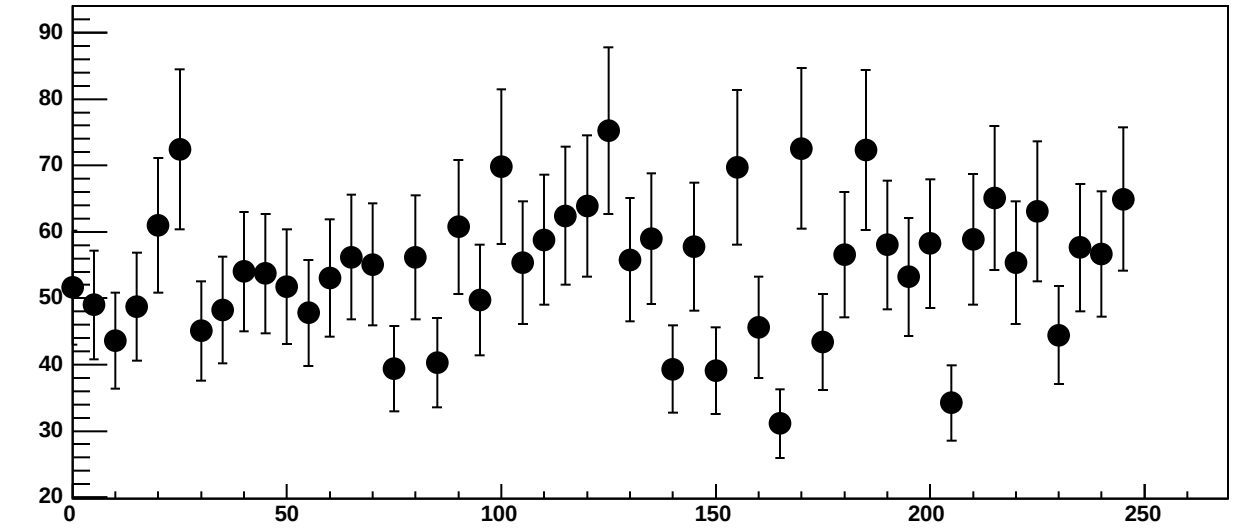

Chip 6, Channel 13, Enable 0, DAC=1600, ADC Noise vs Hold
260
240
220
200
180
160
140
120
100
80
60
0
50
100
150
200
250


Chip 6, Channel 13, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
50
100
150
200
250

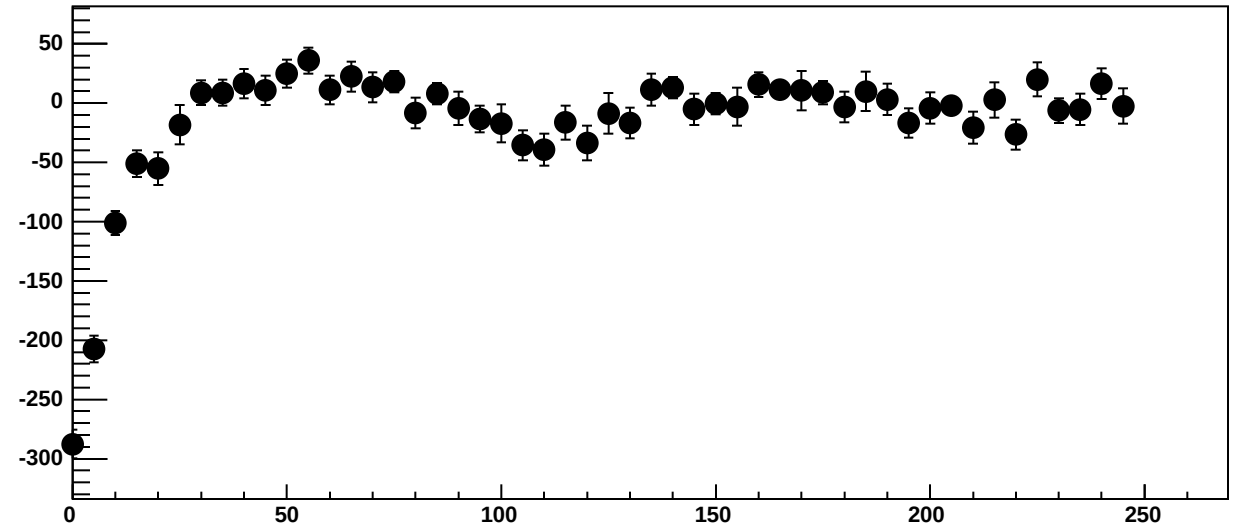
Chip 6, Channel 13, Enable 1, DAC=1600, ADC Mean vs Hold

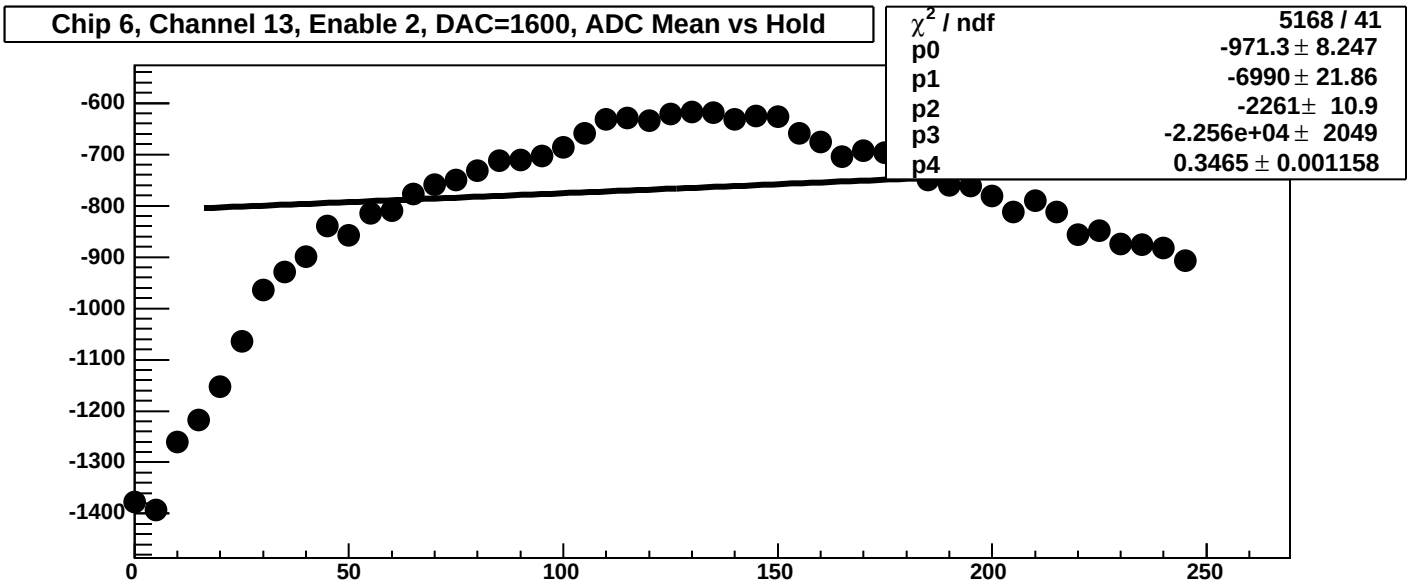


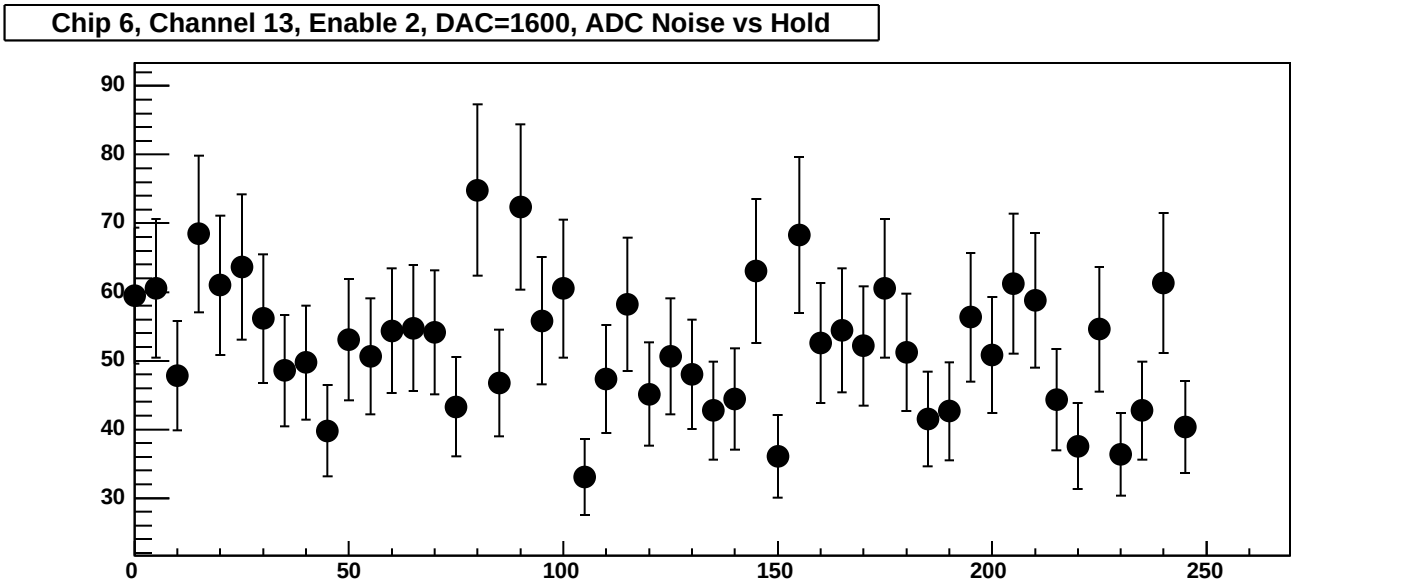
Chip 6, Channel 13, Enable 1, DAC=1600, ADC Noise vs Hold

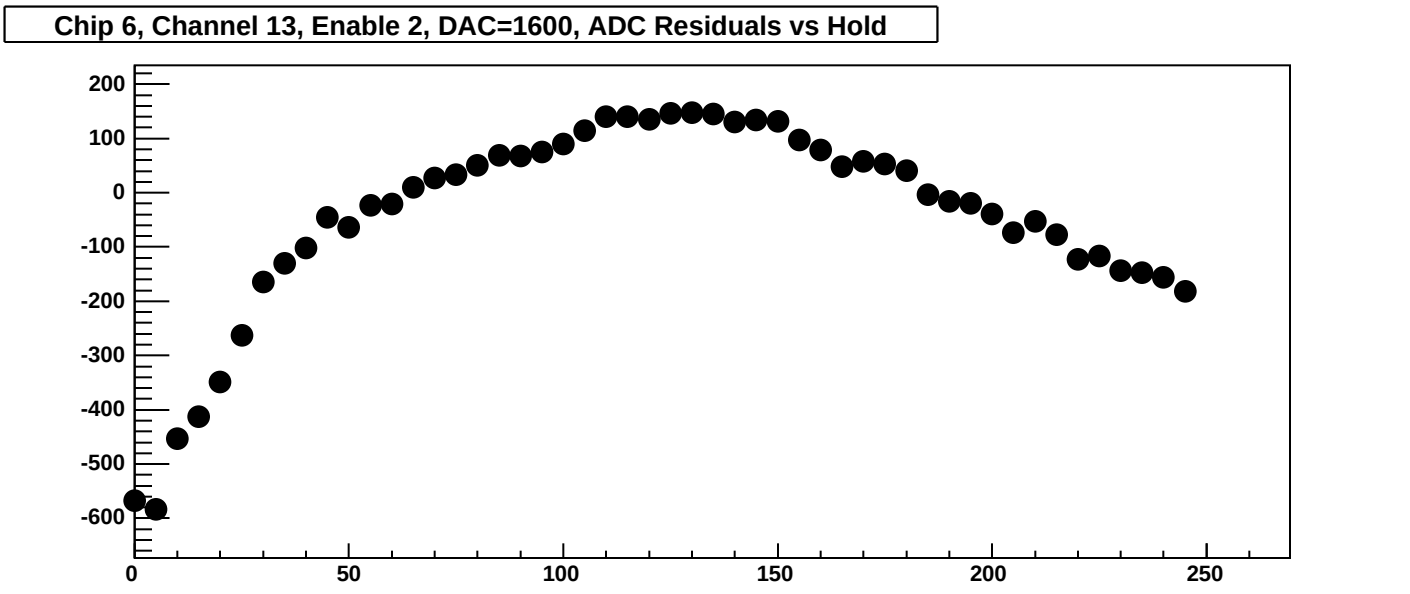


Chip 6, Channel 13, Enable 1, DAC=1600, ADC Residuals vs Hold

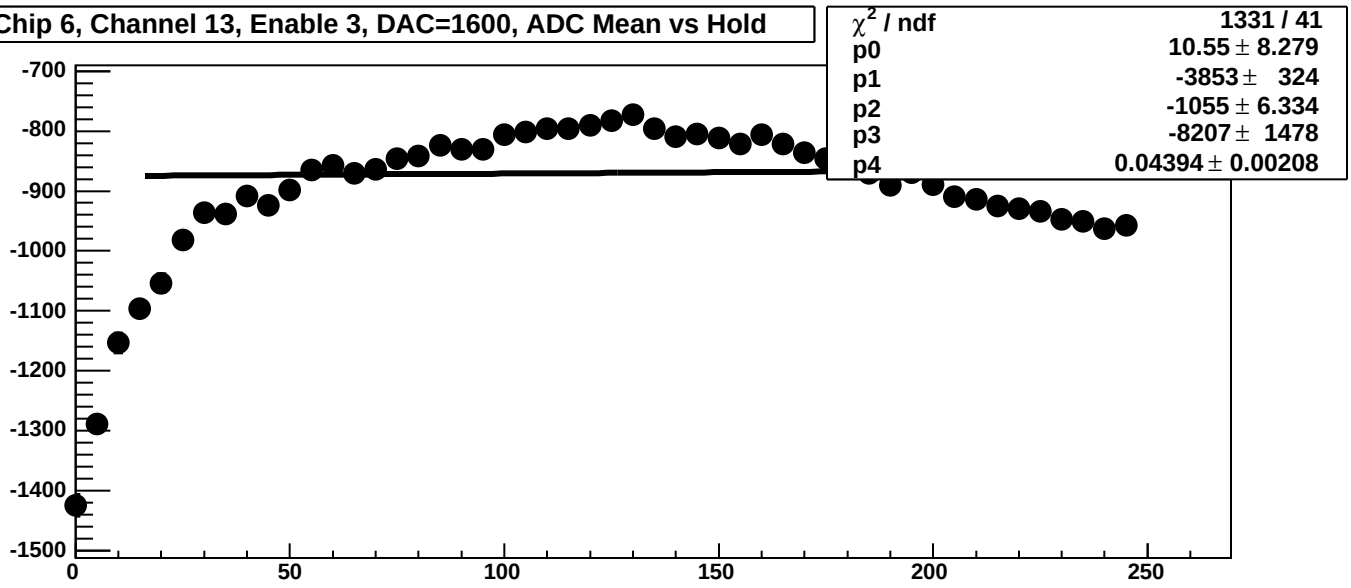



Chip 6, Channel 13, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 5168 / 41
p0 -971.3 ± 8.247
p1 -6990 ± 21.86
p2 -2261 ± 10.9
p3 -2.256e+04 ± 2049
p4 0.3465 ± 0.001158
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
50
100
150
200
250

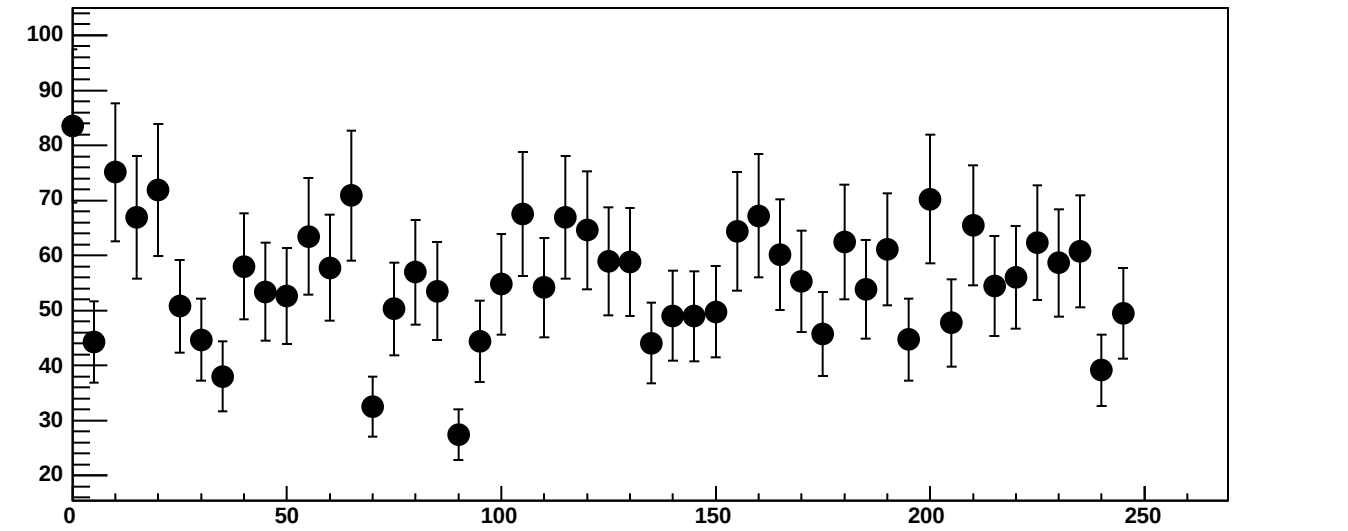

Chip 6, Channel 13, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 6, Channel 13, Enable 2, DAC=1600, ADC Residuals vs Hold
200
100
0
-100
-200
-300
-400
-500
-600
0
50
100
150
200
250

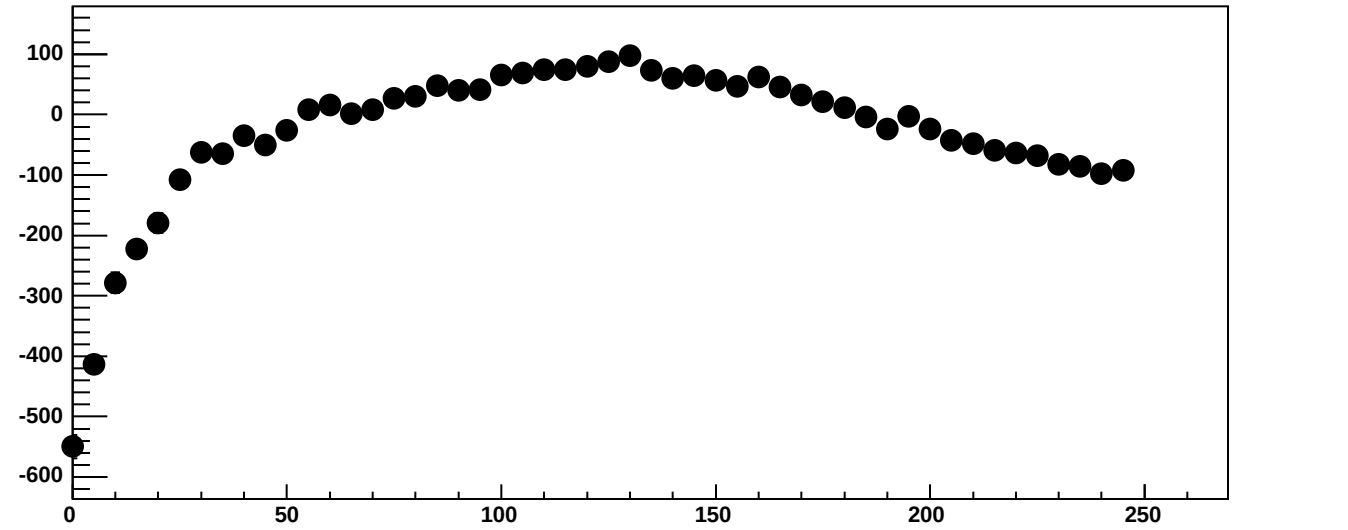
Chip 6, Channel 13, Enable 3, DAC=1600, ADC Mean vs Hold

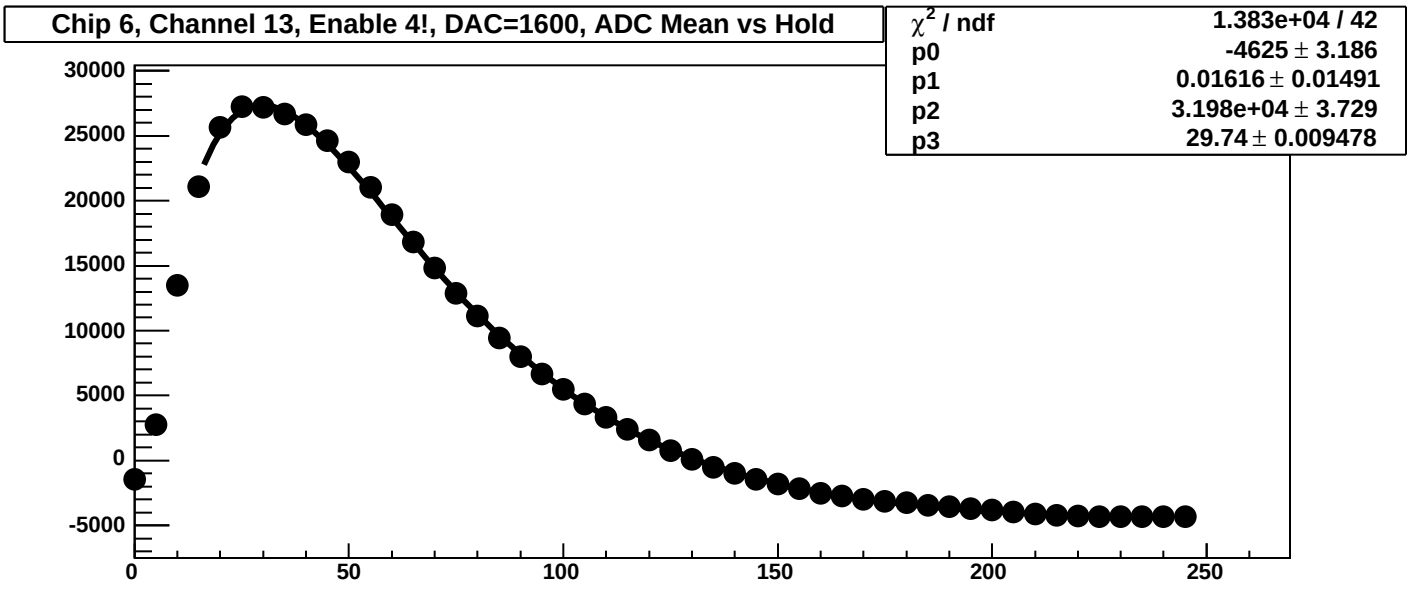


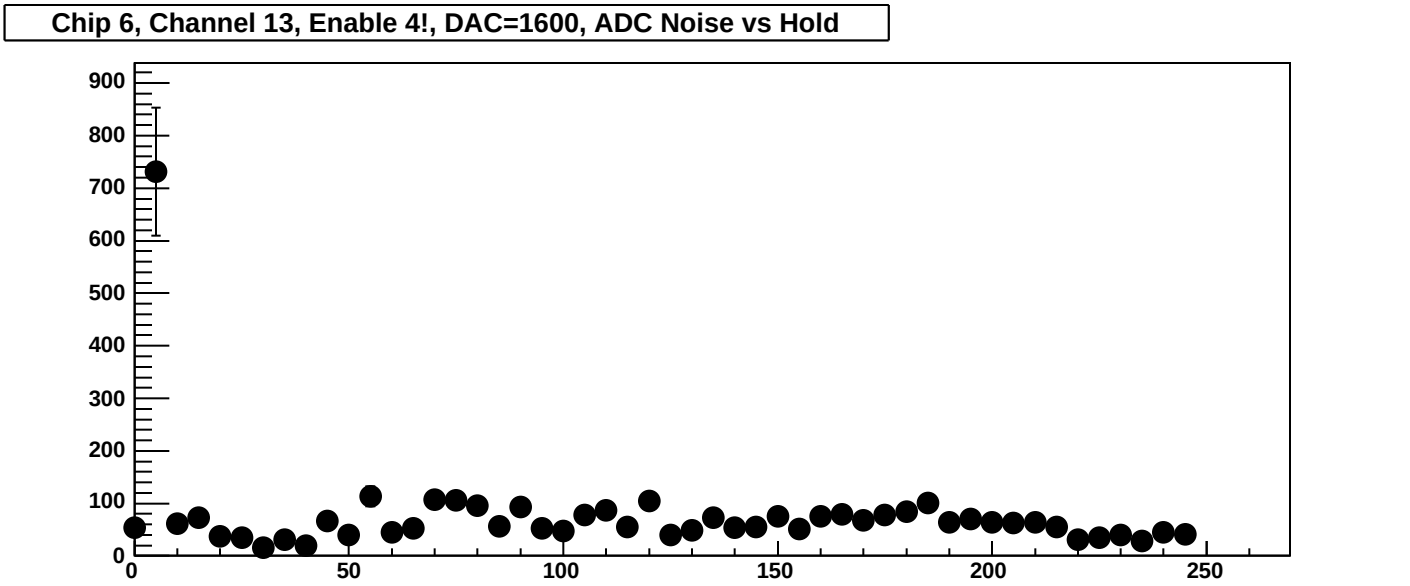
Chip 6, Channel 13, Enable 3, DAC=1600, ADC Noise vs Hold

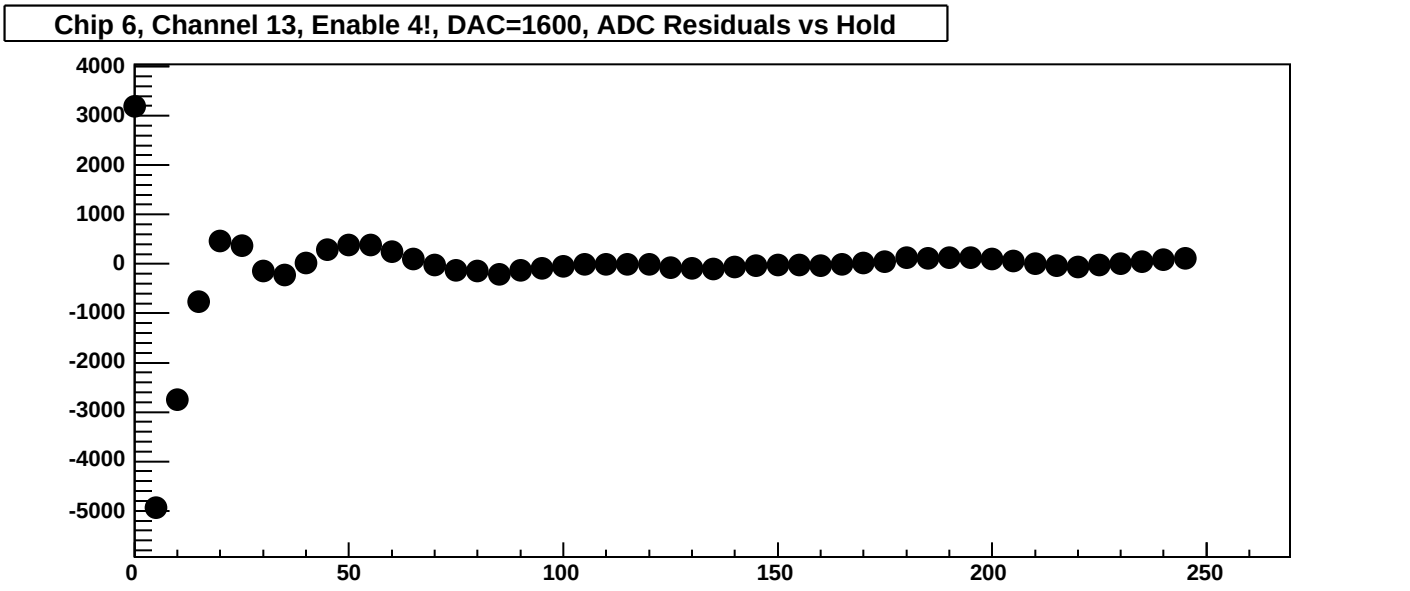


Chip 6, Channel 13, Enable 3, DAC=1600, ADC Residuals vs Hold

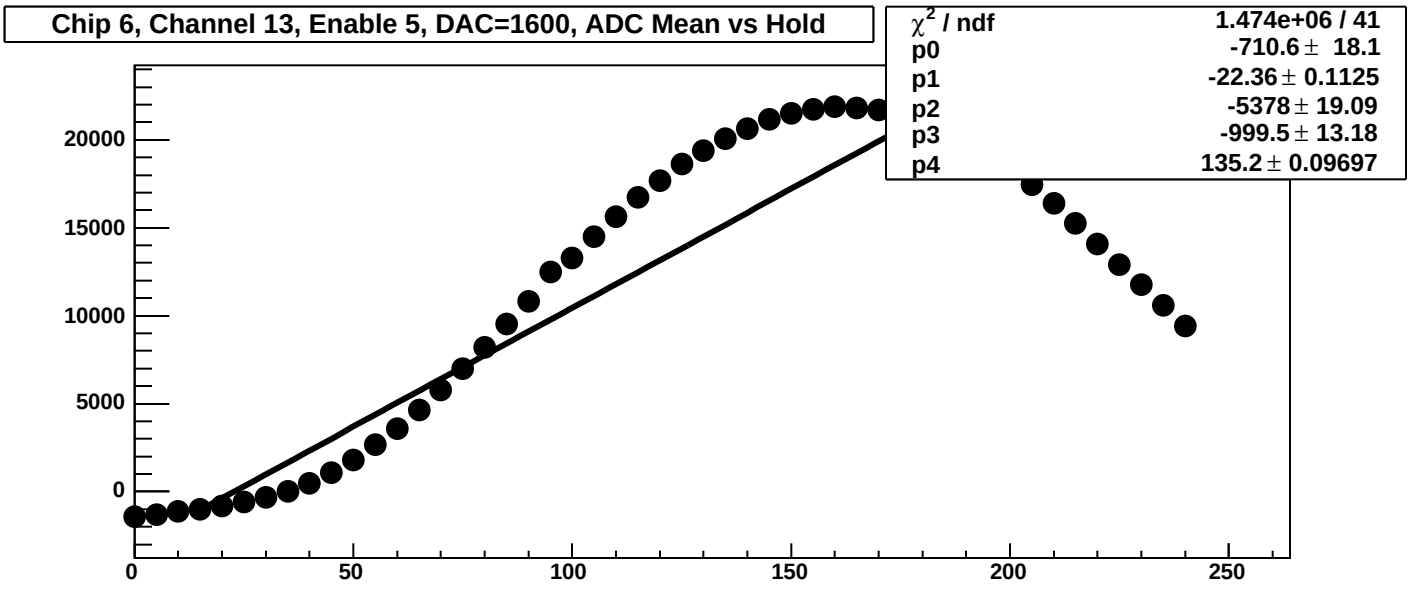



Chip 6, Channel 13, Enable 4!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.383e+04 / 42
p0 -4625 ± 3.186
p1 0.01616 ± 0.01491
p2 3.198e+04 ± 3.729
p3 29.74 ± 0.009478
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

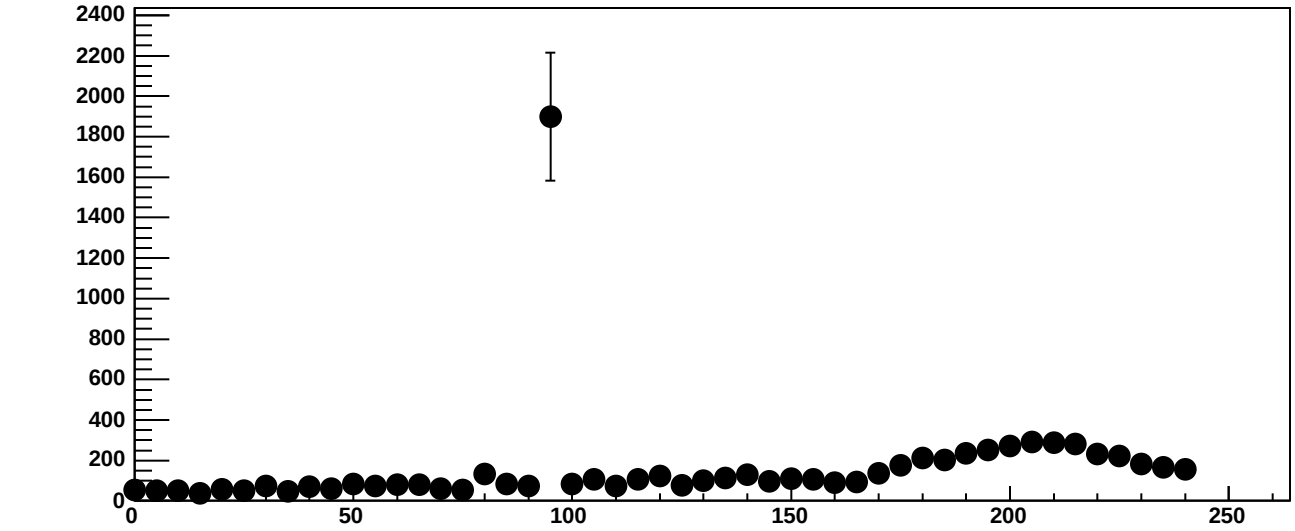

Chip 6, Channel 13, Enable 4!, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 6, Channel 13, Enable 4!, DAC=1600, ADC Residuals vs Hold
4000
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

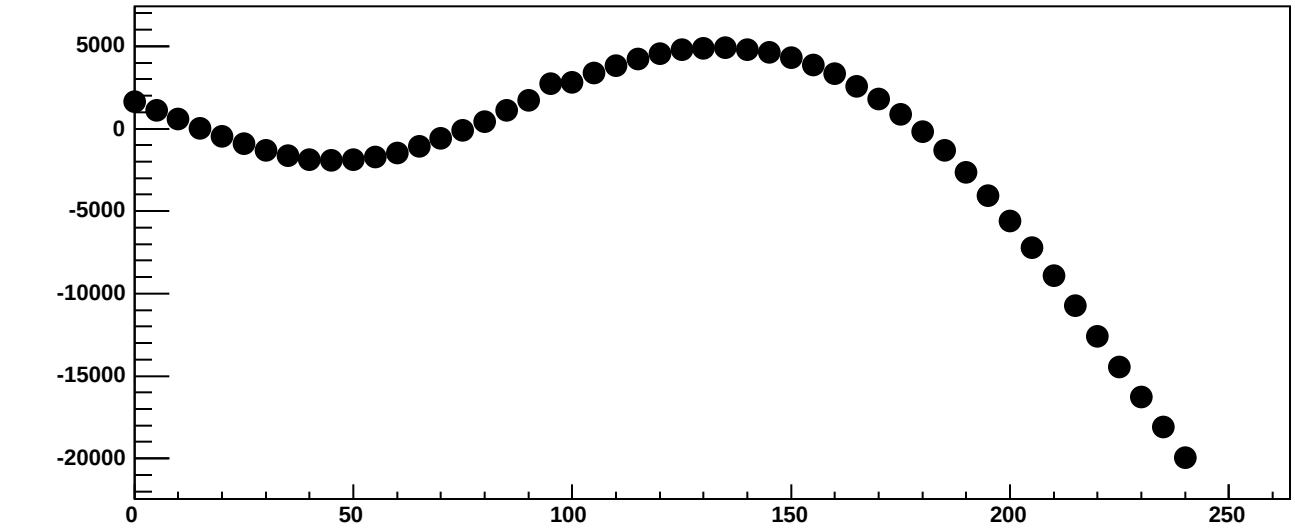
Chip 6, Channel 13, Enable 5, DAC=1600, ADC Mean vs Hold



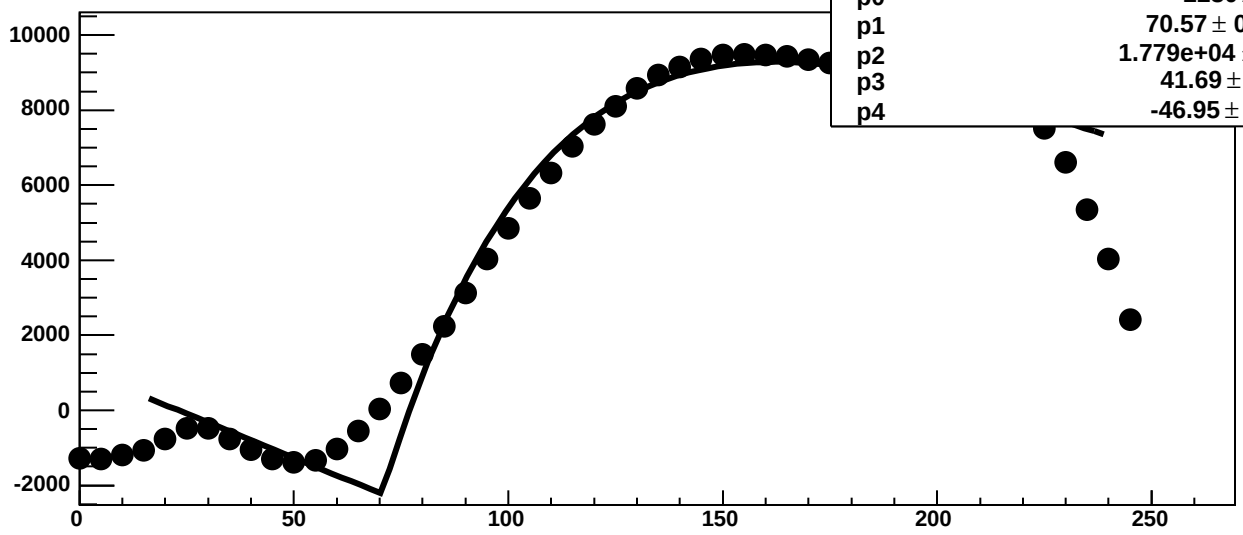
Chip 6, Channel 13, Enable 5, DAC=1600, ADC Noise vs Hold



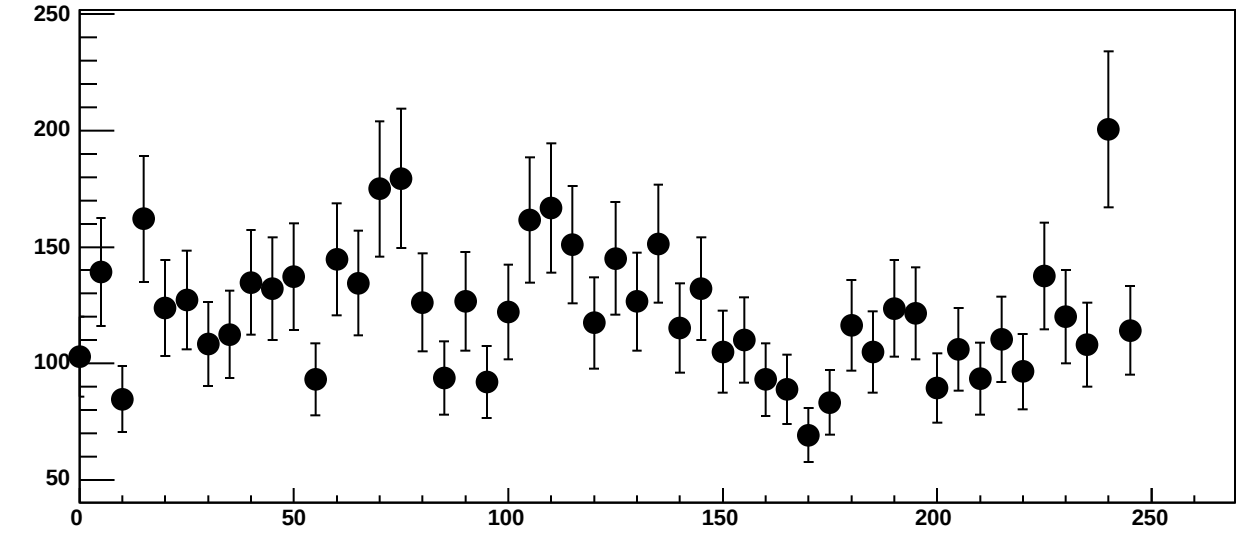
Chip 6, Channel 13, Enable 5, DAC=1600, ADC Residuals vs Hold



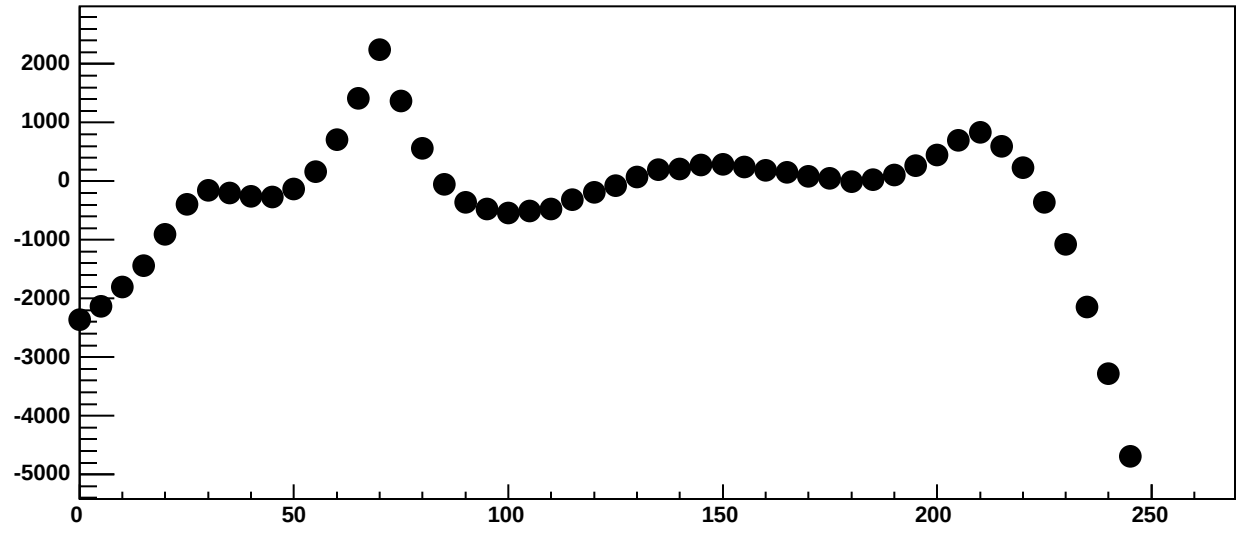
Chip 6, Channel 14, Enable 0, DAC=1600, ADC Mean vs Hold



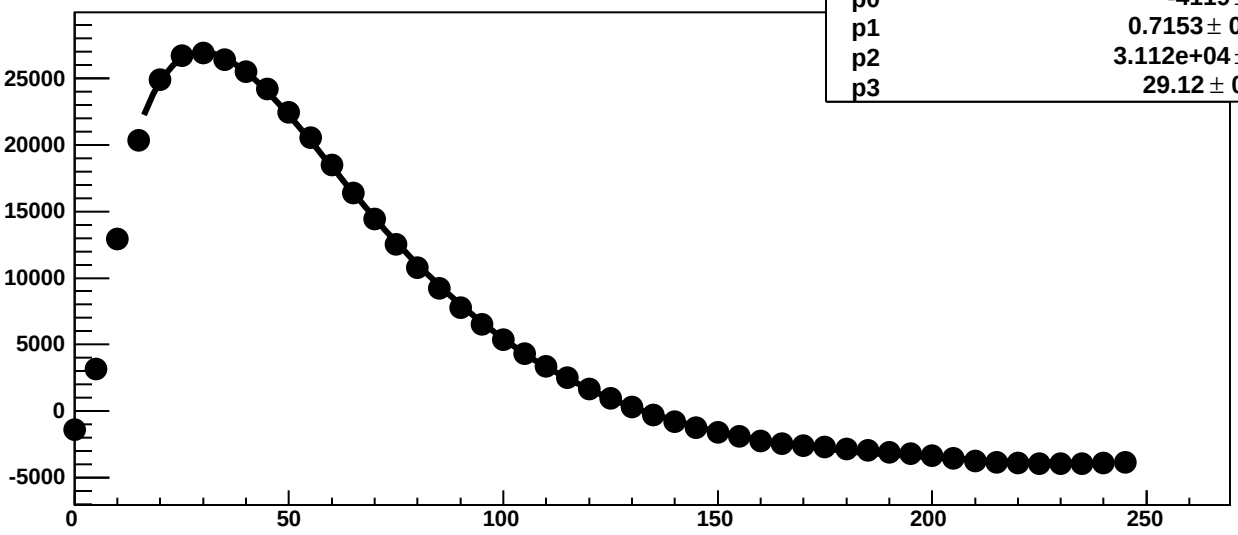
Chip 6, Channel 14, Enable 0, DAC=1600, ADC Noise vs Hold



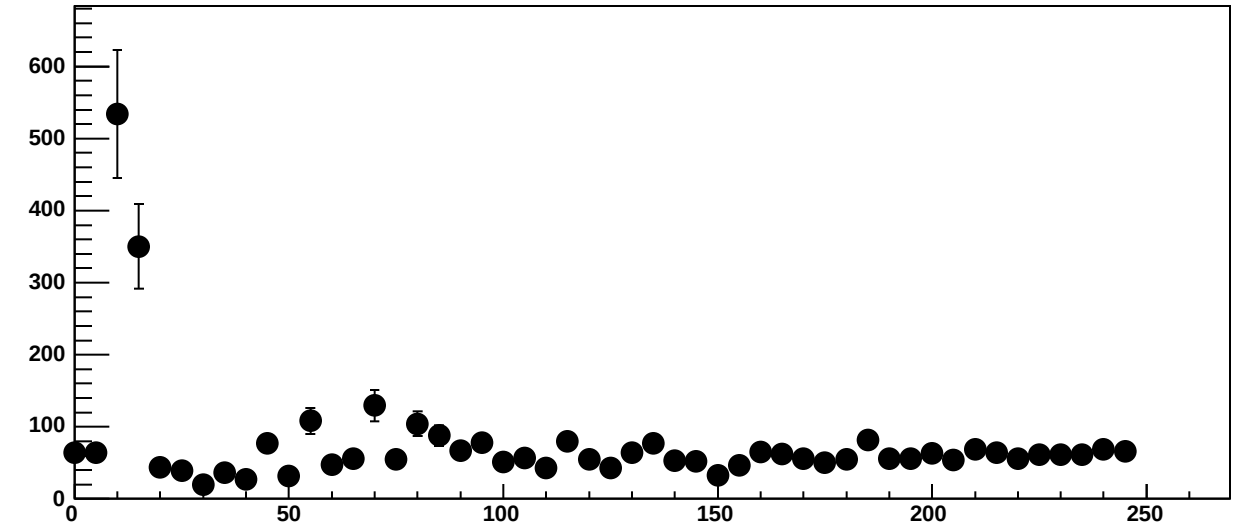
Chip 6, Channel 14, Enable 0, DAC=1600, ADC Residuals vs Hold



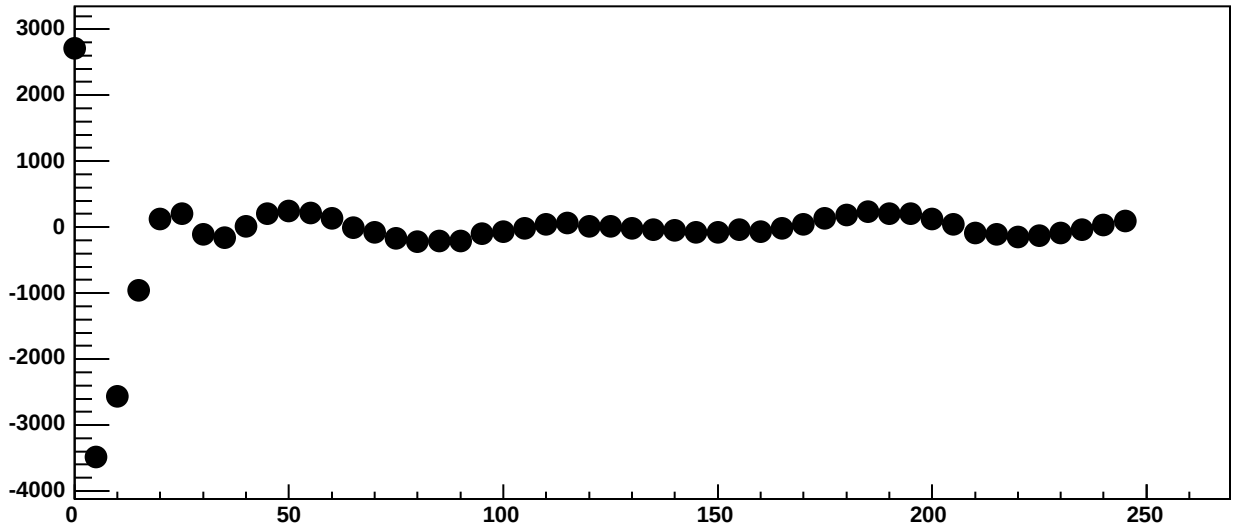
Chip 6, Channel 14, Enable 1!, DAC=1600, ADC Mean vs Hold

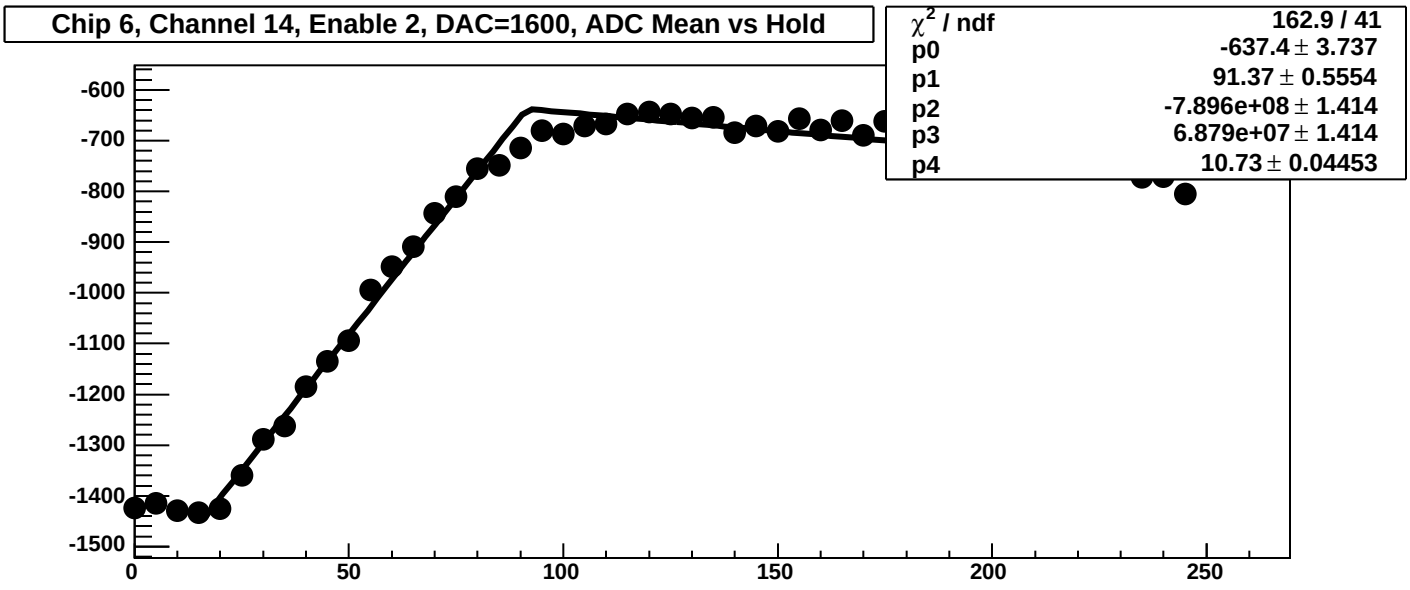


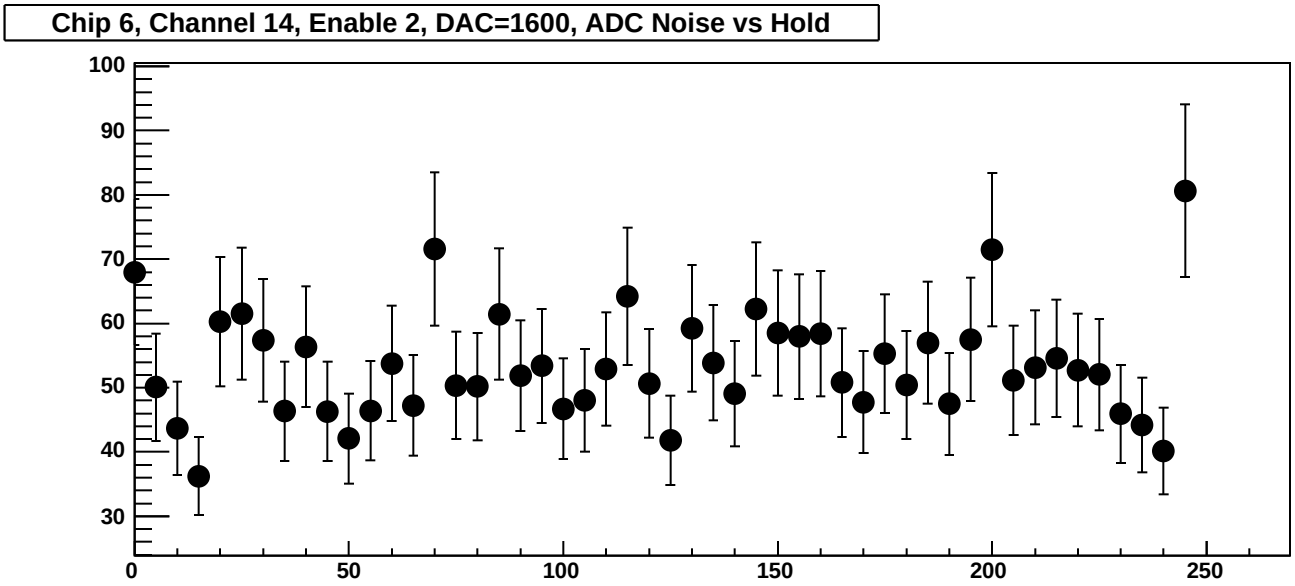
Chip 6, Channel 14, Enable 1!, DAC=1600, ADC Noise vs Hold

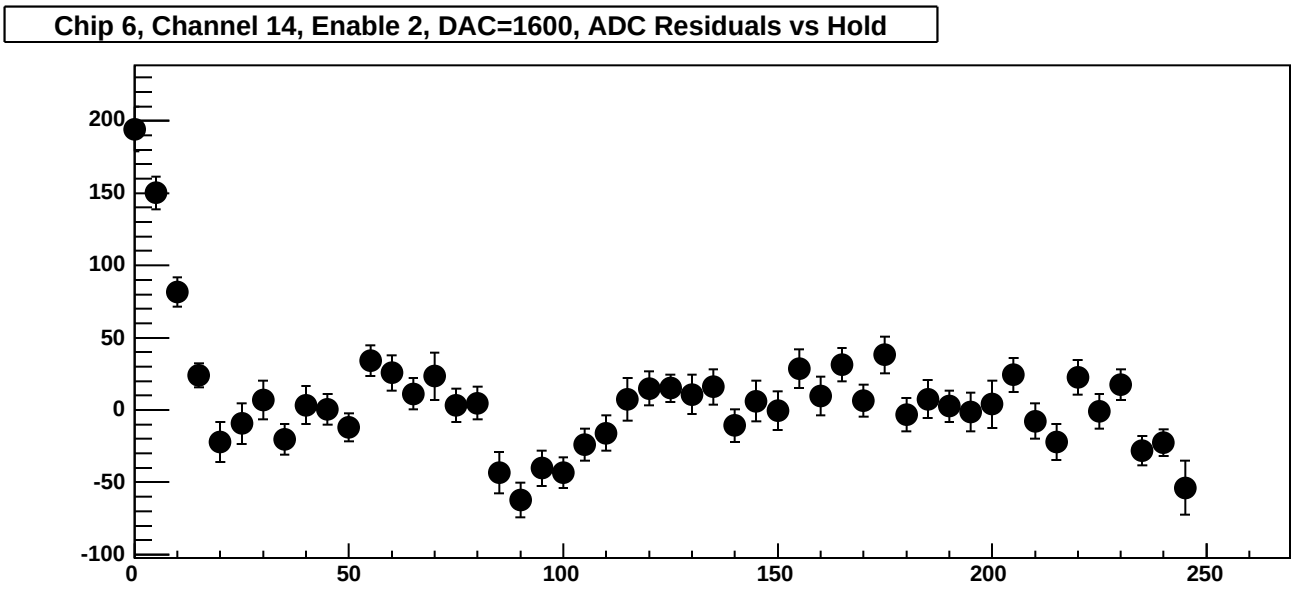


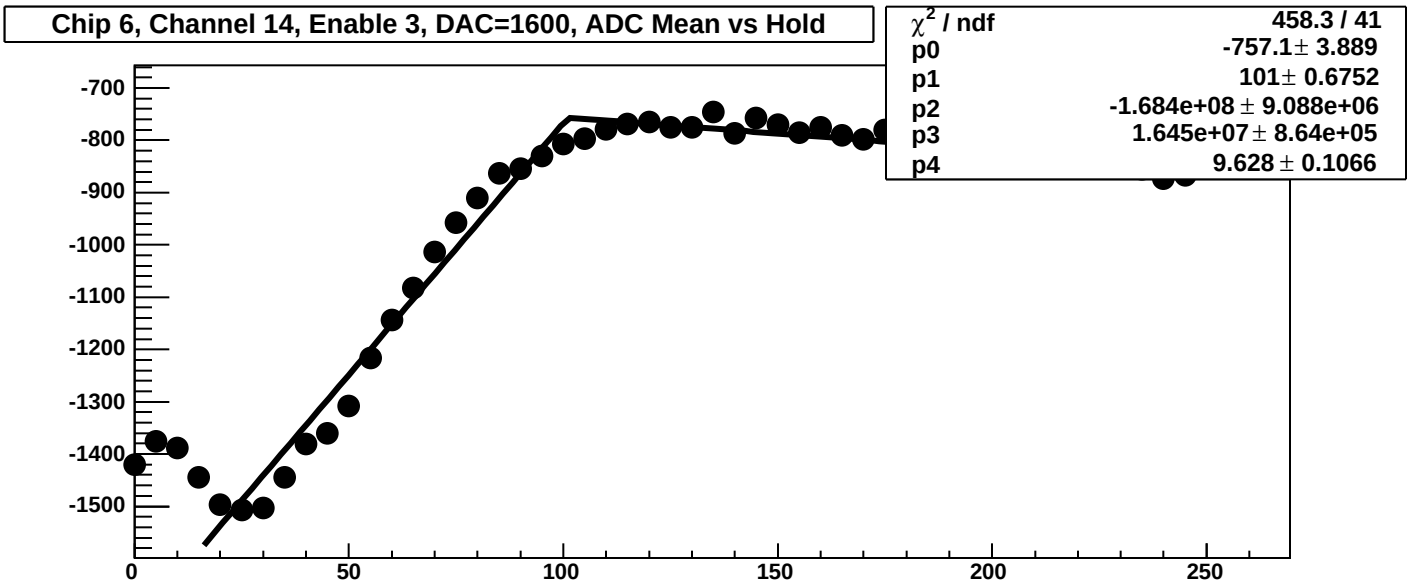
Chip 6, Channel 14, Enable 1!, DAC=1600, ADC Residuals vs Hold

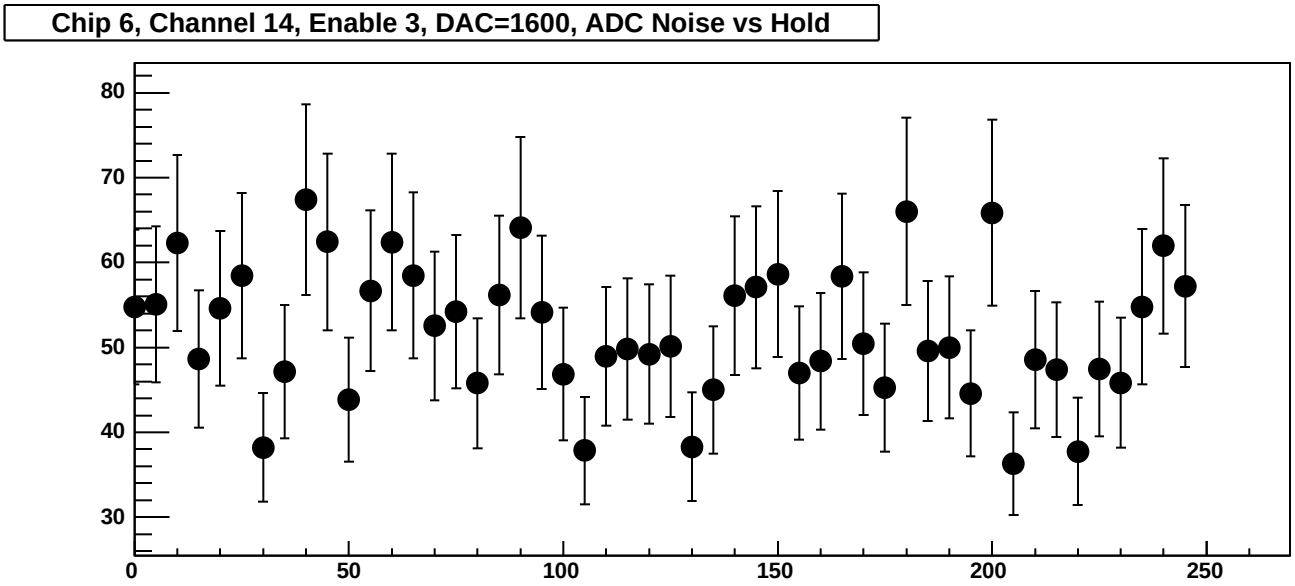


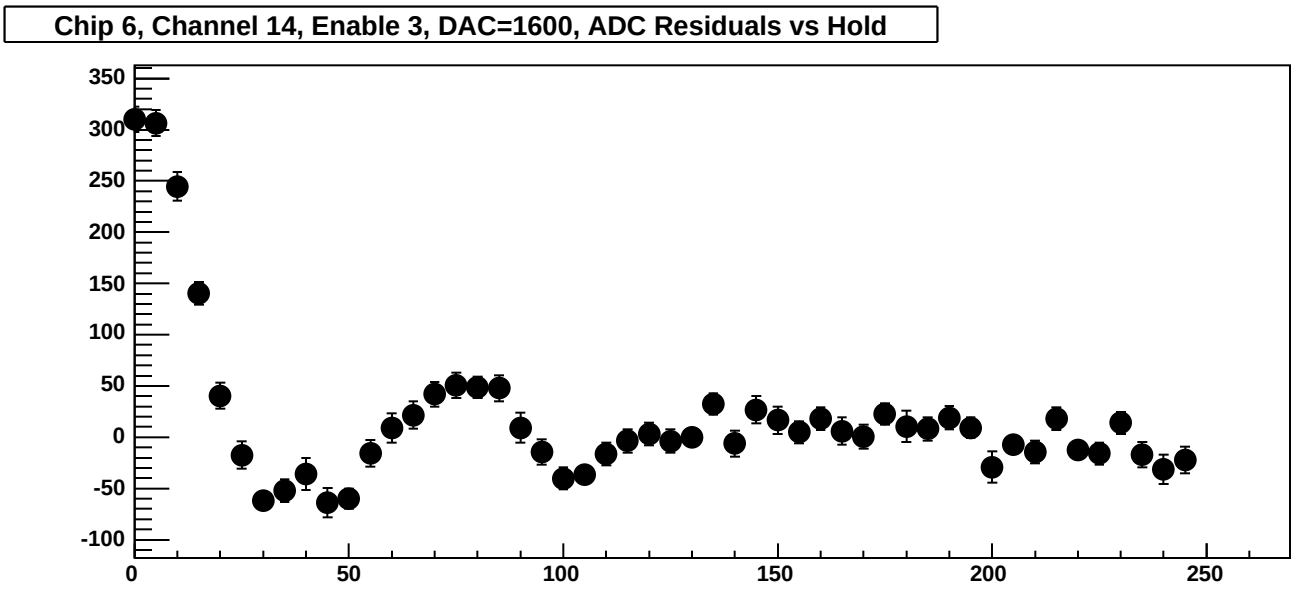

Chip 6, Channel 14, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 162.9 / 41
p0 -637.4 ± 3.737
p1 91.37 ± 0.5554
p2 -7.896e+08 ± 1.414
p3 6.879e+07 ± 1.414
p4 10.73 ± 0.04453
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250

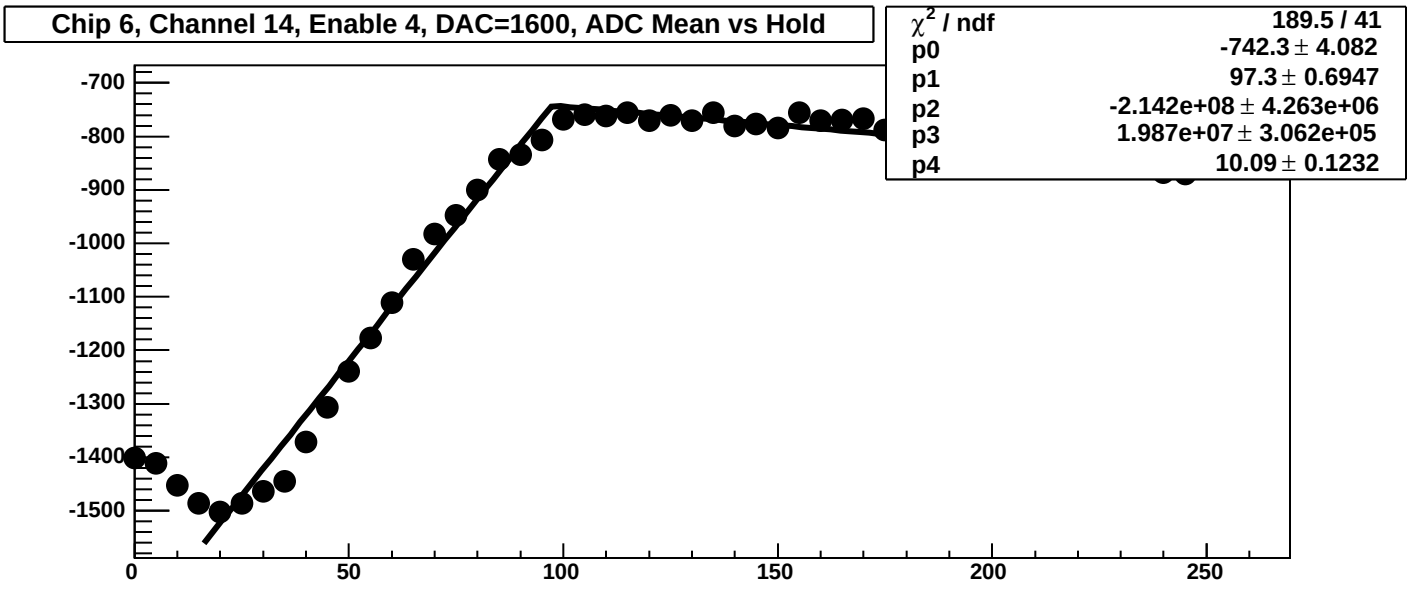

Chip 6, Channel 14, Enable 2, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250

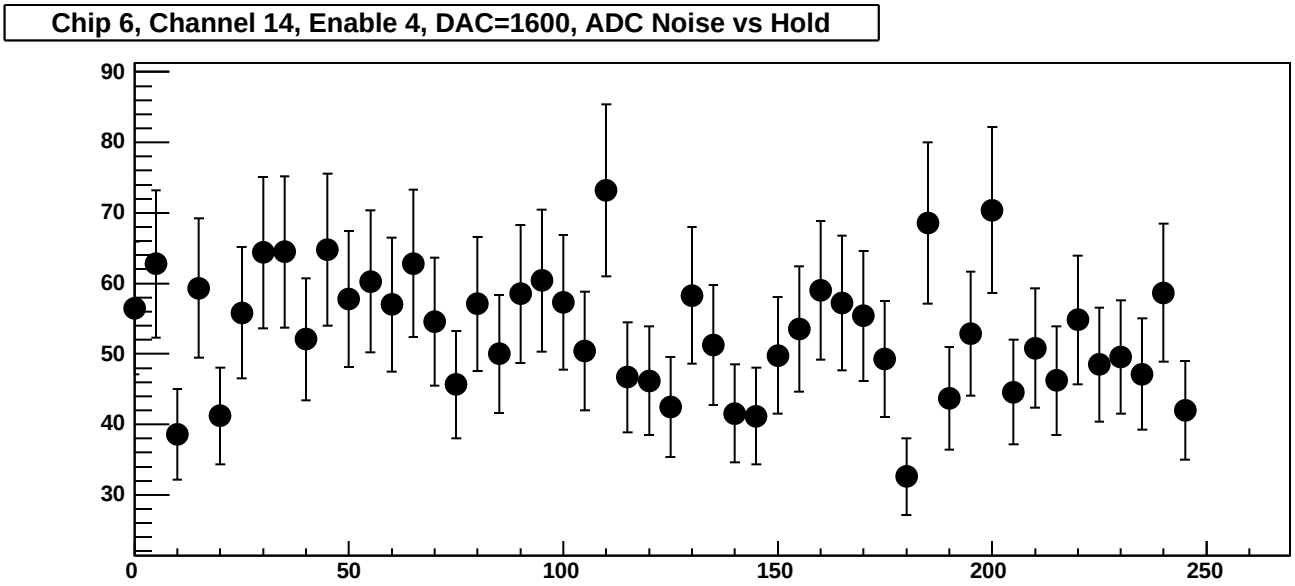

Chip 6, Channel 14, Enable 2, DAC=1600, ADC Residuals vs Hold
200
150
100
50
0
-50
-100
0
50
100
150
200
250

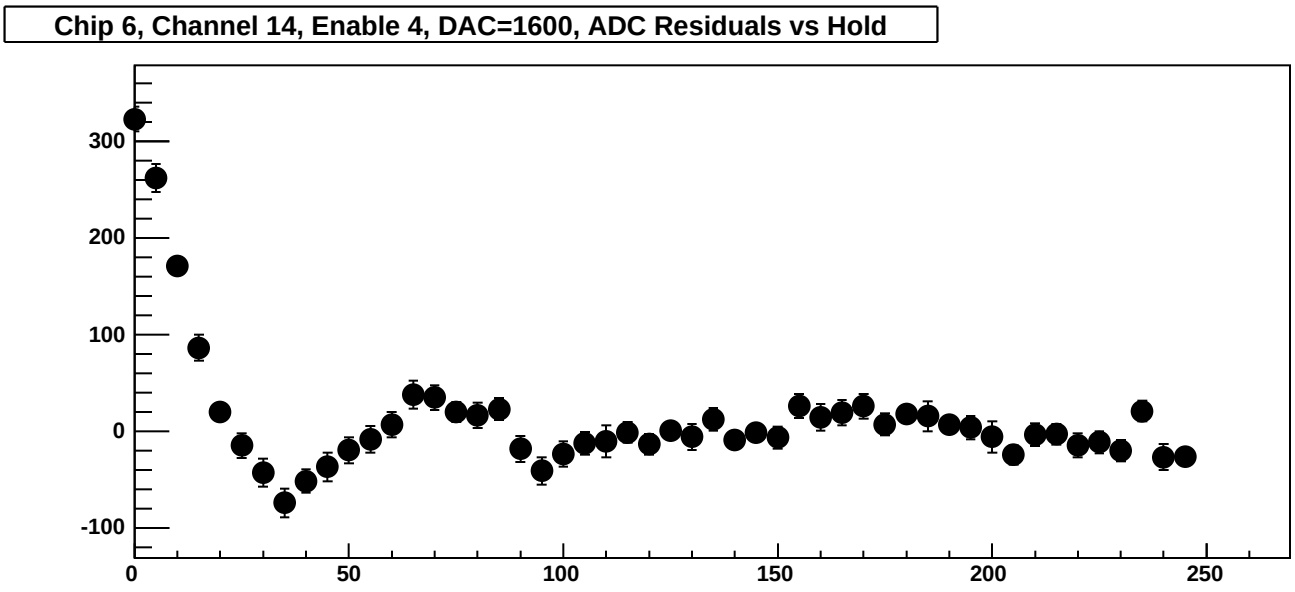

Chip 6, Channel 14, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 458.3 / 41
p0 -757.1 ± 3.889
p1 101 ± 0.6752
p2 -1.684e+08 ± 9.088e+06
p3 1.645e+07 ± 8.64e+05
p4 9.628 ± 0.1066
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250

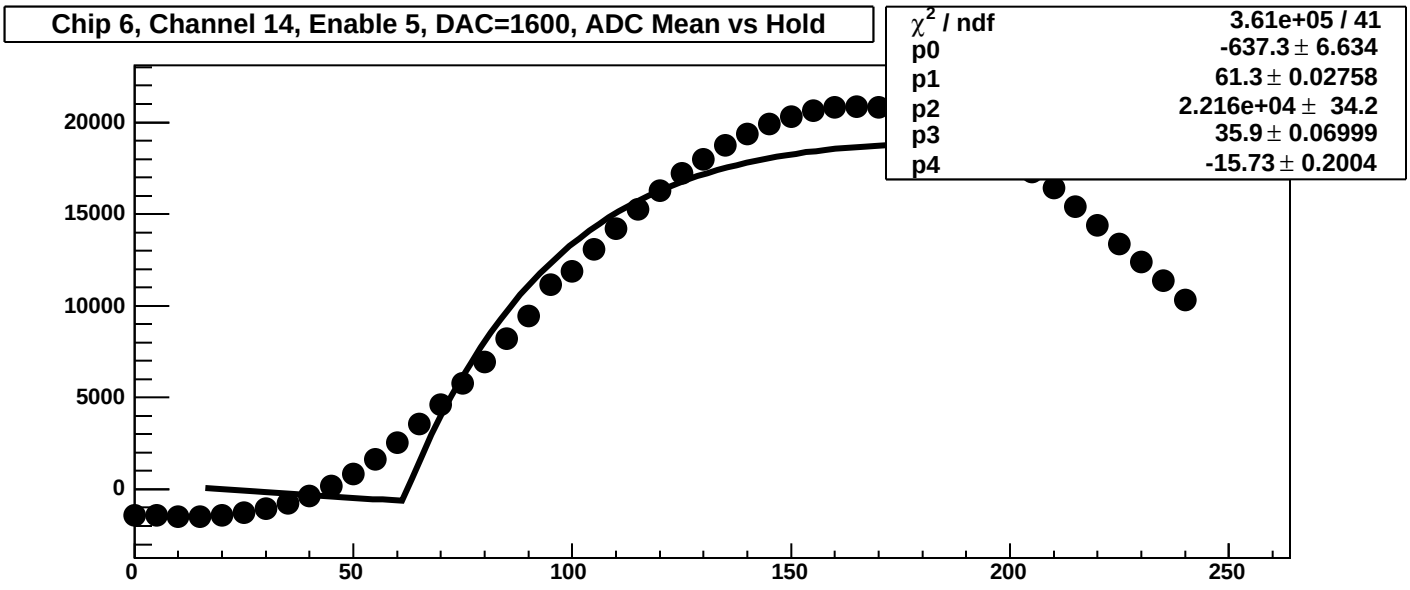

Chip 6, Channel 14, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

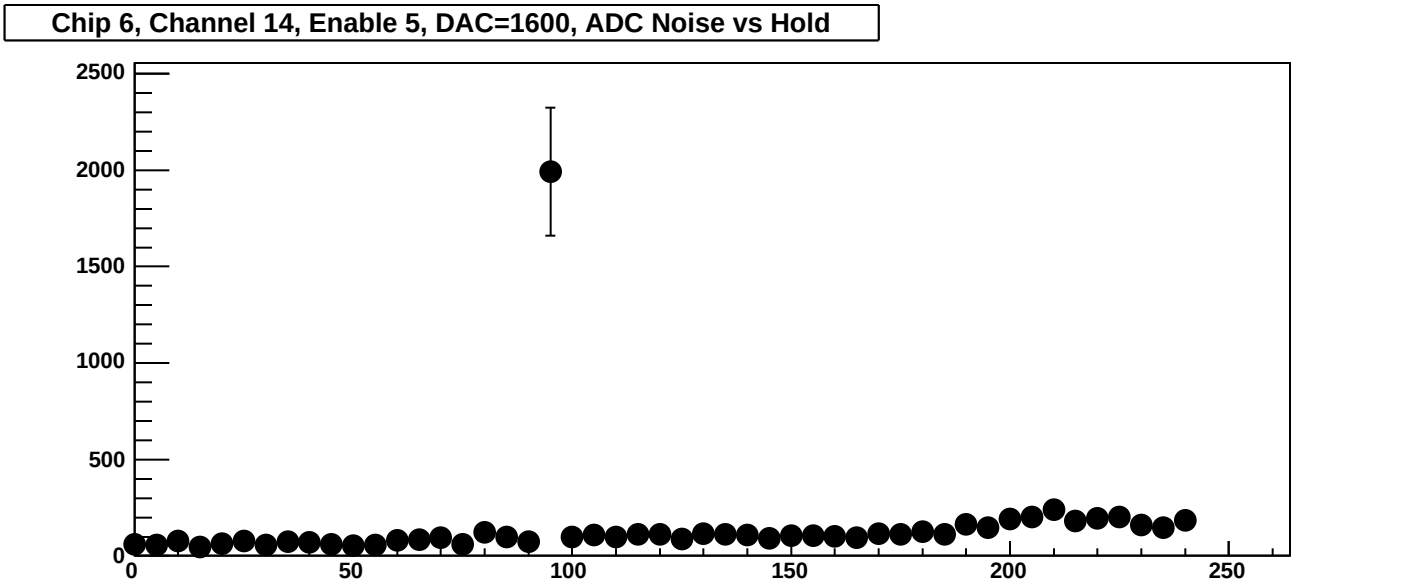

Chip 6, Channel 14, Enable 3, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

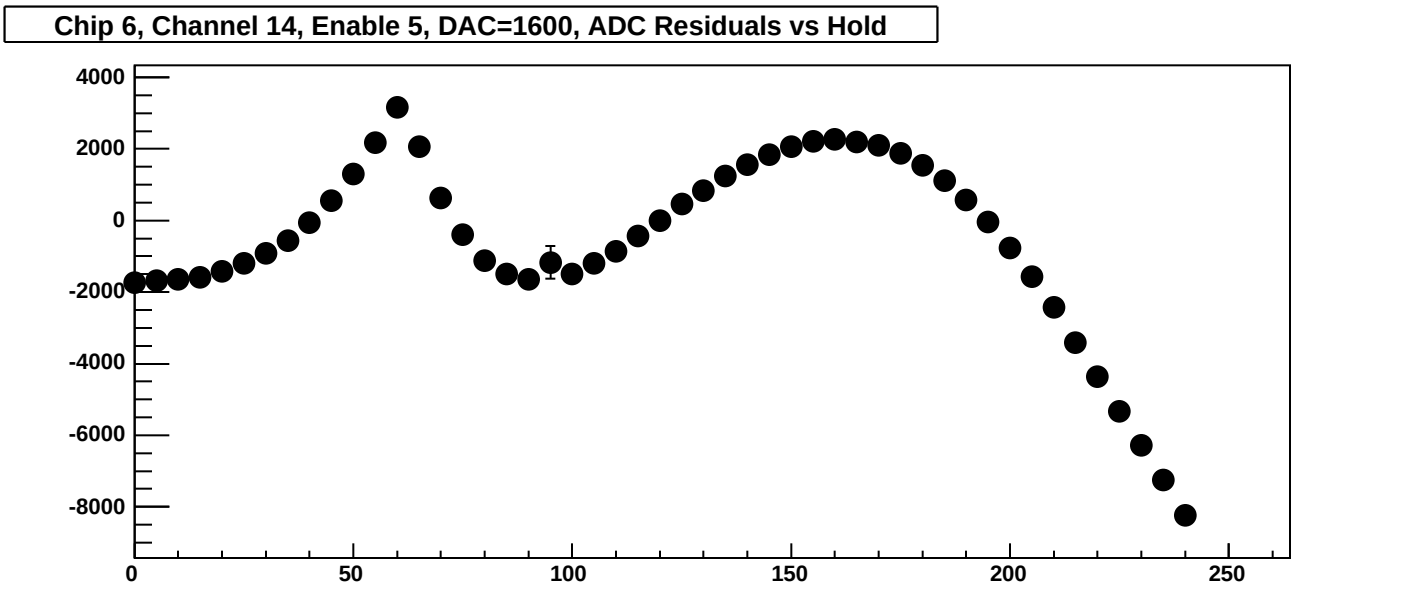

Chip 6, Channel 14, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 189.5 / 41
p0 -742.3 ± 4.082
p1 97.3 ± 0.6947
p2 -2.142e+08 ± 4.263e+06
p3 1.987e+07 ± 3.062e+05
p4 10.09 ± 0.1232
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250


Chip 6, Channel 14, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

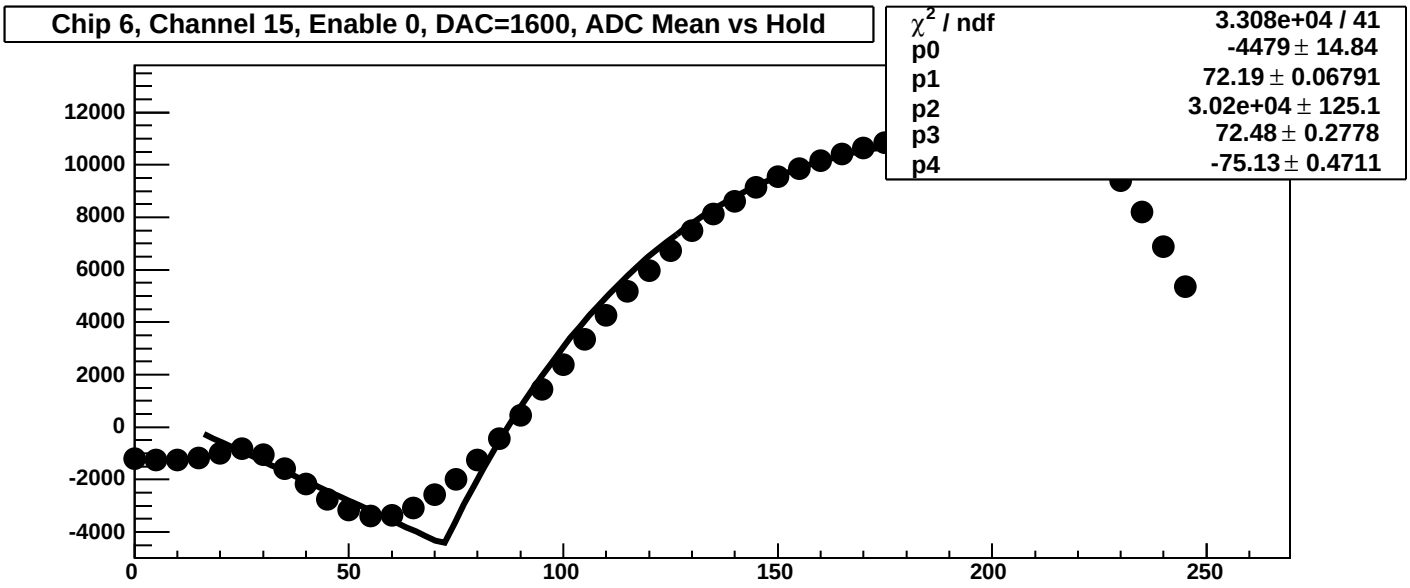

Chip 6, Channel 14, Enable 4, DAC=1600, ADC Residuals vs Hold
300
200
100
0
-100
0
50
100
150
200
250


Chip 6, Channel 14, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.61e+05 / 41
p0 -637.3 ± 6.634
p1 61.3 ± 0.02758
p2 2.216e+04 ± 34.2
p3 35.9 ± 0.06999
p4 -15.73 ± 0.2004
20000
15000
10000
5000
0
0
50
100
150
200
250

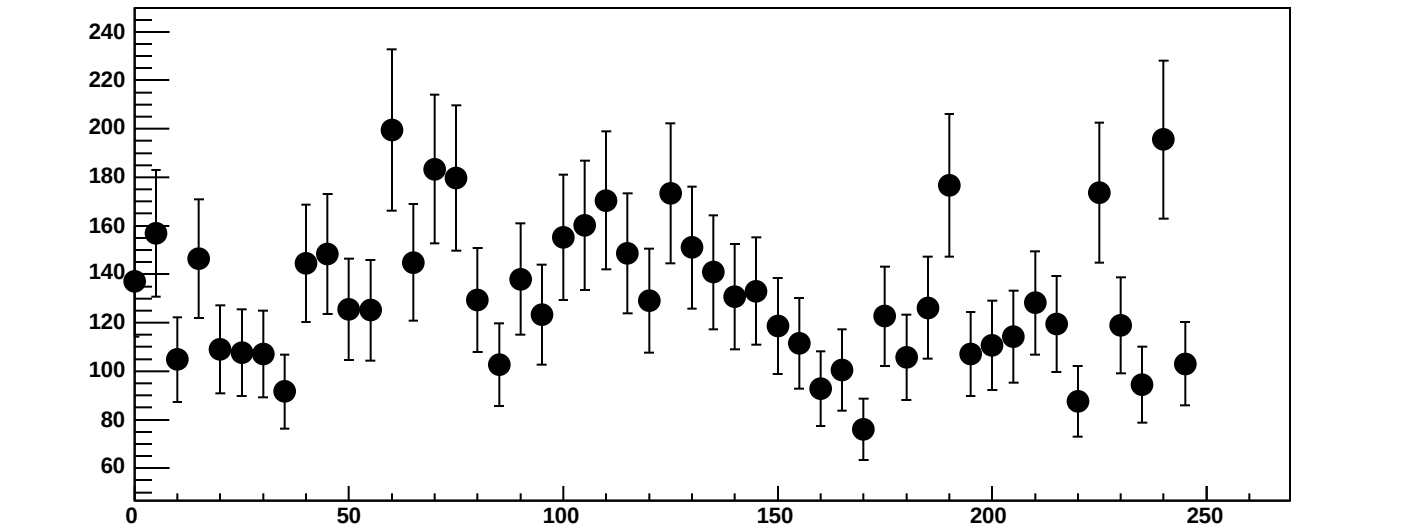

Chip 6, Channel 14, Enable 5, DAC=1600, ADC Noise vs Hold
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 6, Channel 14, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250

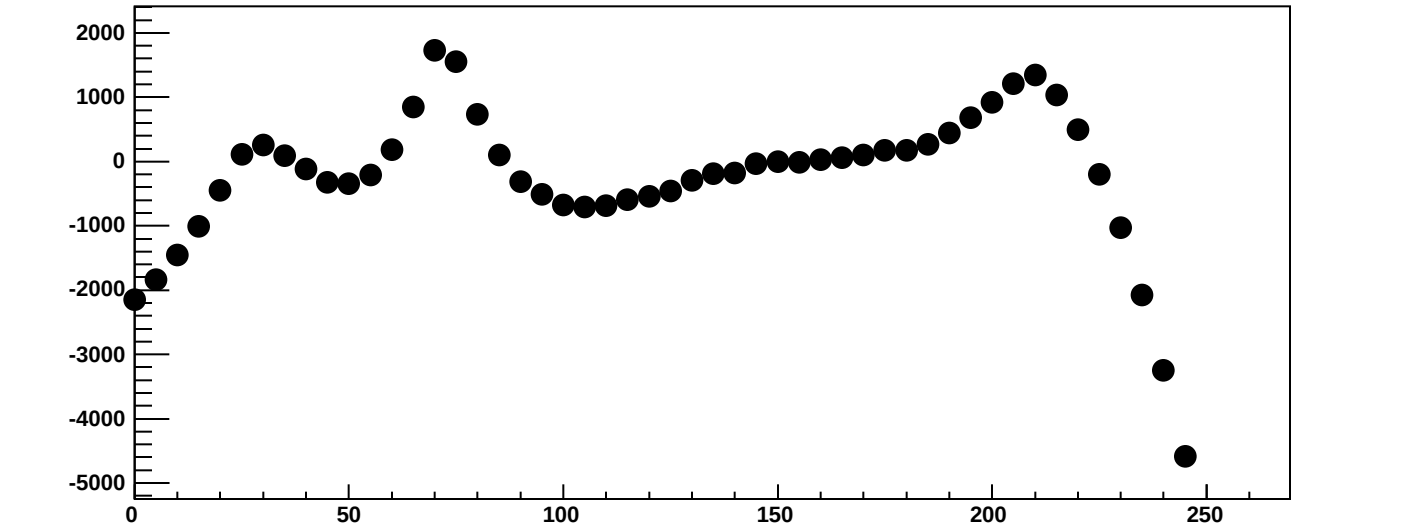
Chip 6, Channel 15, Enable 0, DAC=1600, ADC Mean vs Hold



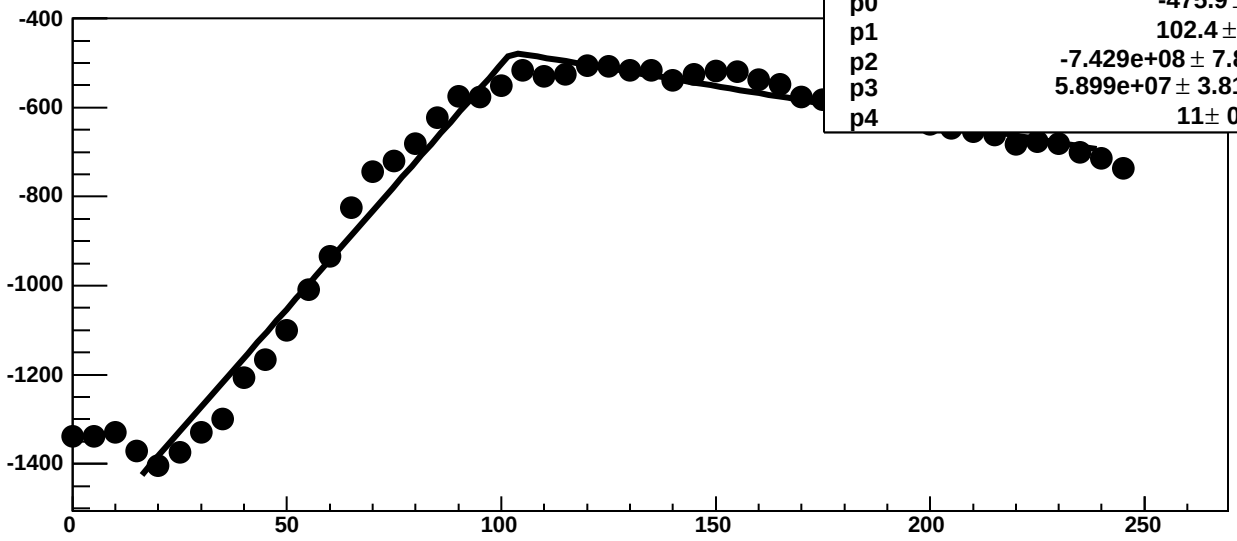
Chip 6, Channel 15, Enable 0, DAC=1600, ADC Noise vs Hold



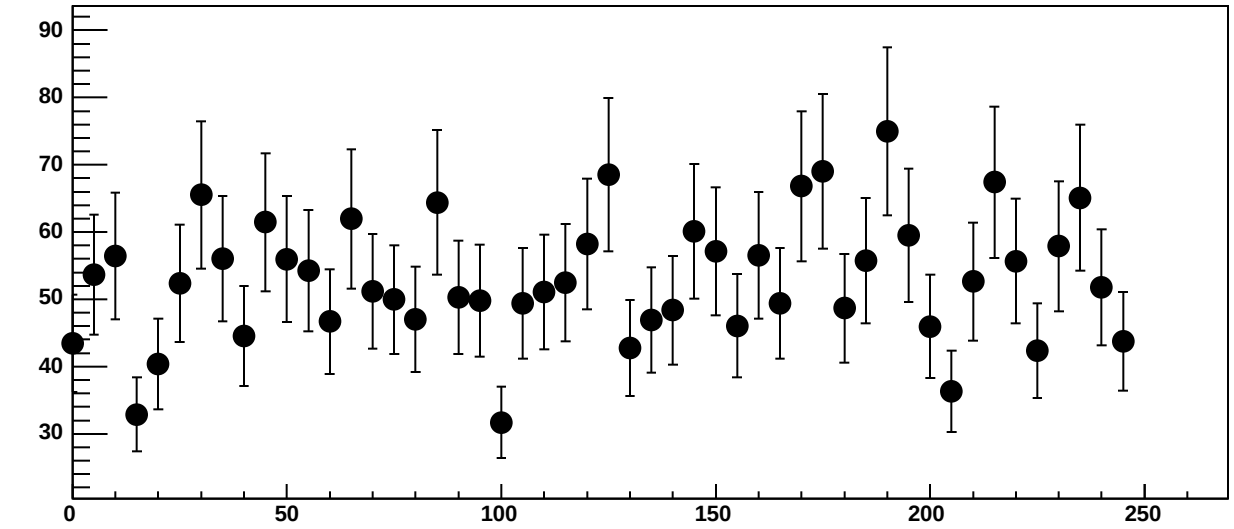
Chip 6, Channel 15, Enable 0, DAC=1600, ADC Residuals vs Hold



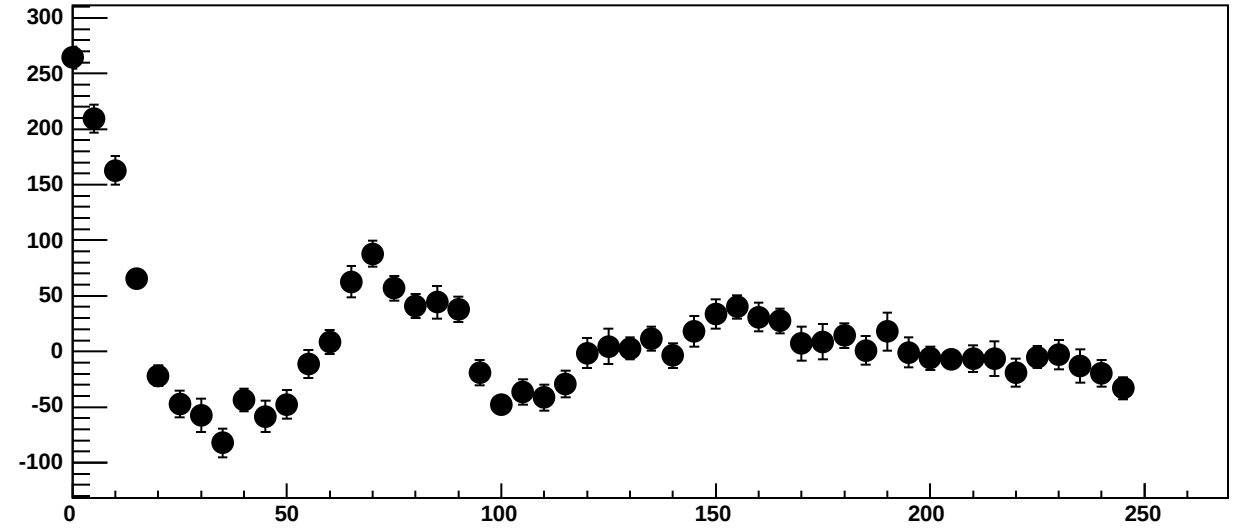
Chip 6, Channel 15, Enable 1, DAC=1600, ADC Mean vs Hold

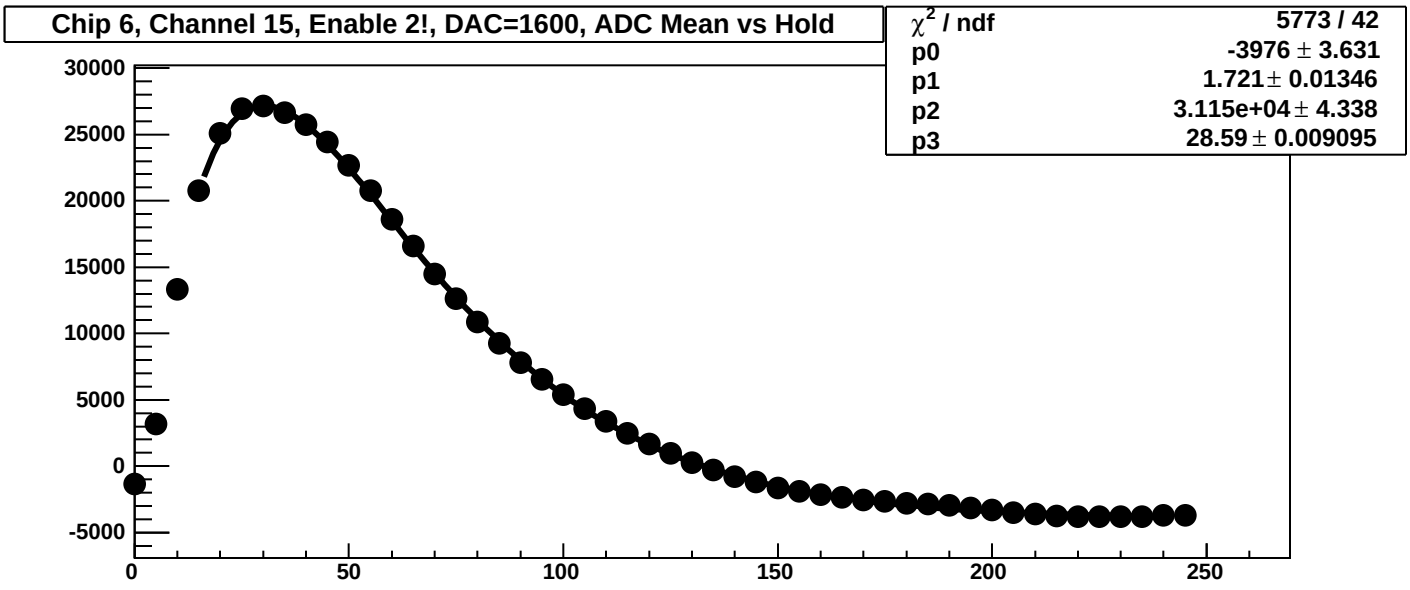


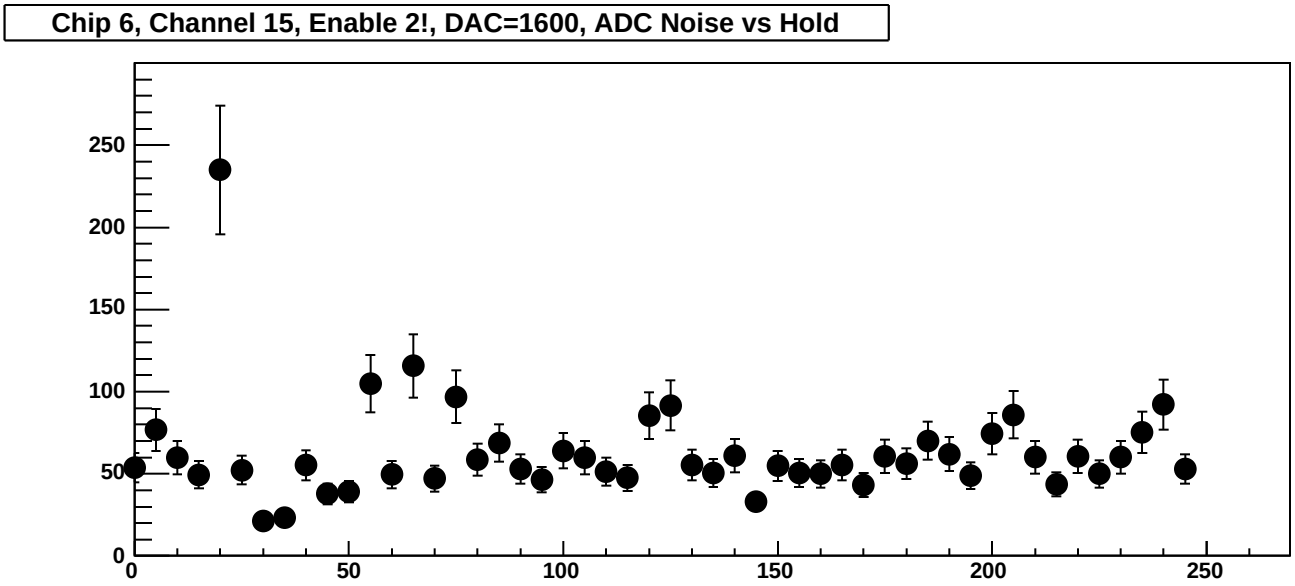
Chip 6, Channel 15, Enable 1, DAC=1600, ADC Noise vs Hold

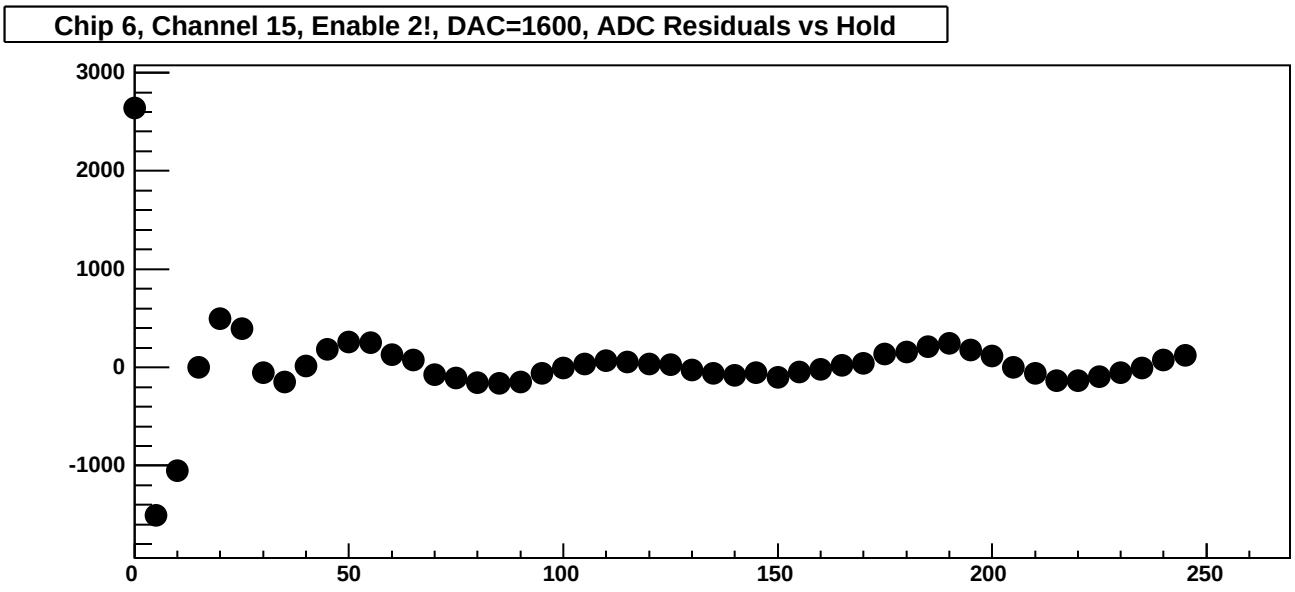


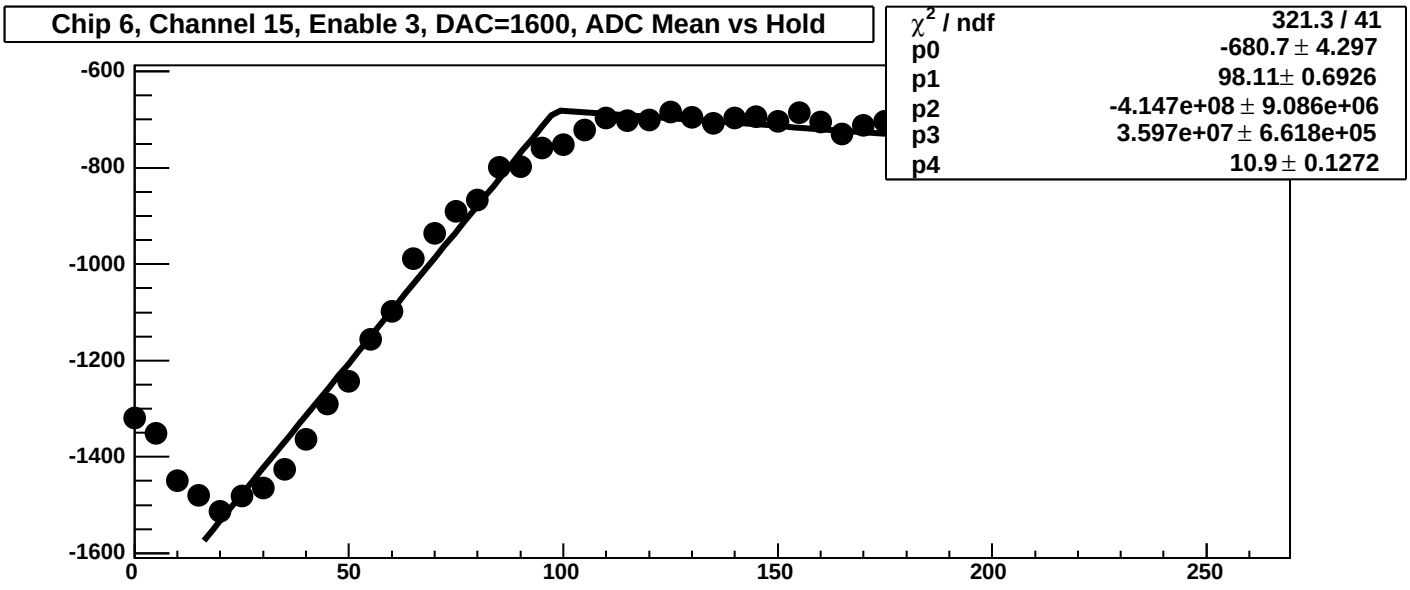
Chip 6, Channel 15, Enable 1, DAC=1600, ADC Residuals vs Hold

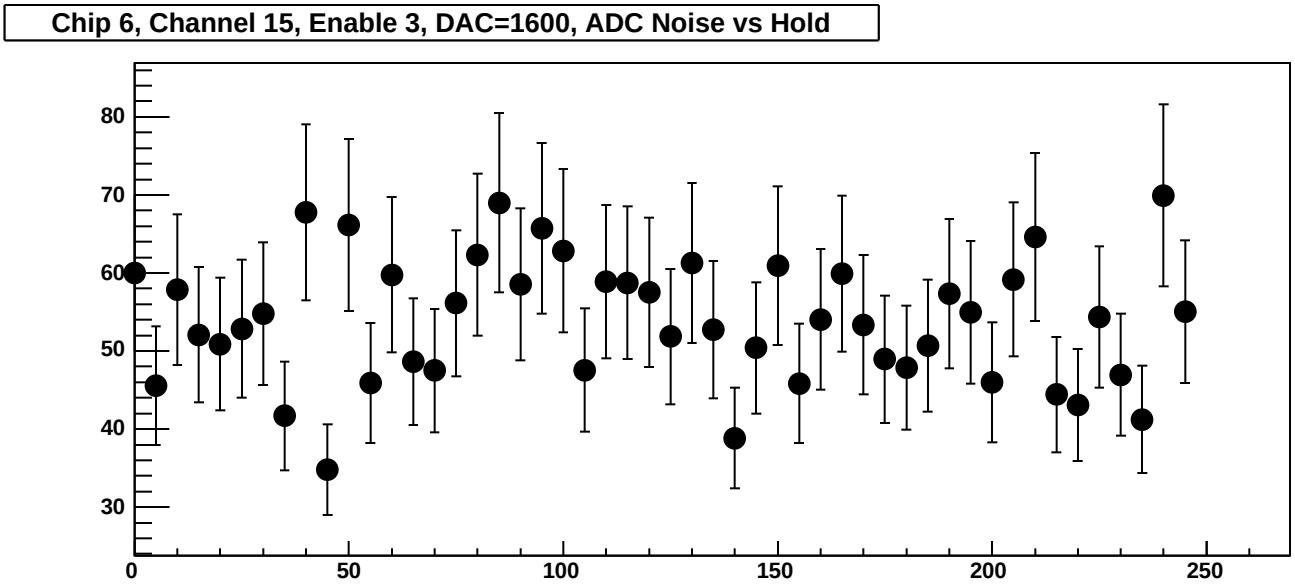


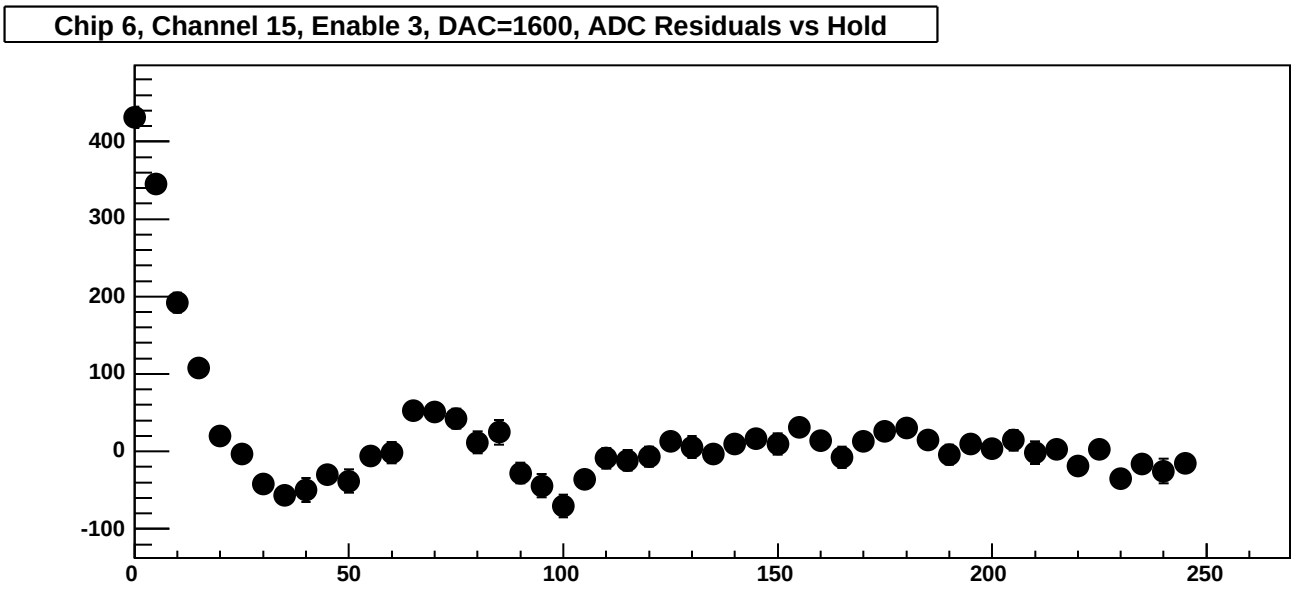

Chip 6, Channel 15, Enable 2!, DAC=1600, ADC Mean vs Hold
χ² / ndf 5773 / 42
p0 -3976 ± 3.631
p1 1.721 ± 0.01346
p2 3.115e+04 ± 4.338
p3 28.59 ± 0.009095
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

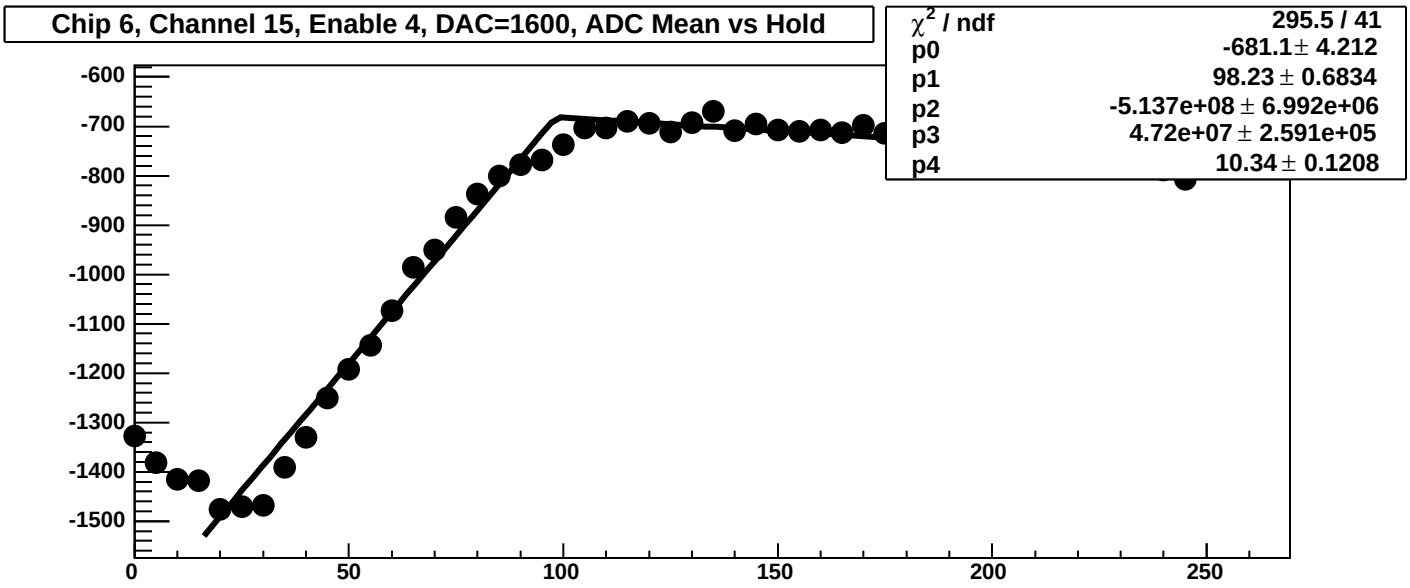

Chip 6, Channel 15, Enable 2!, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250

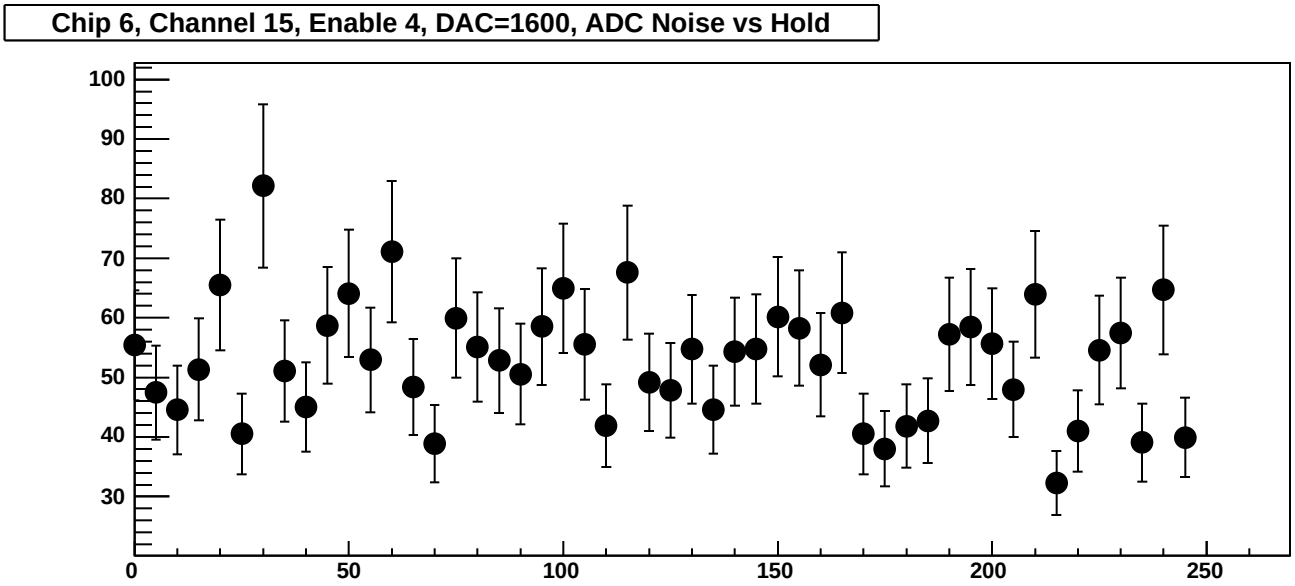

Chip 6, Channel 15, Enable 2!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
0
50
100
150
200
250

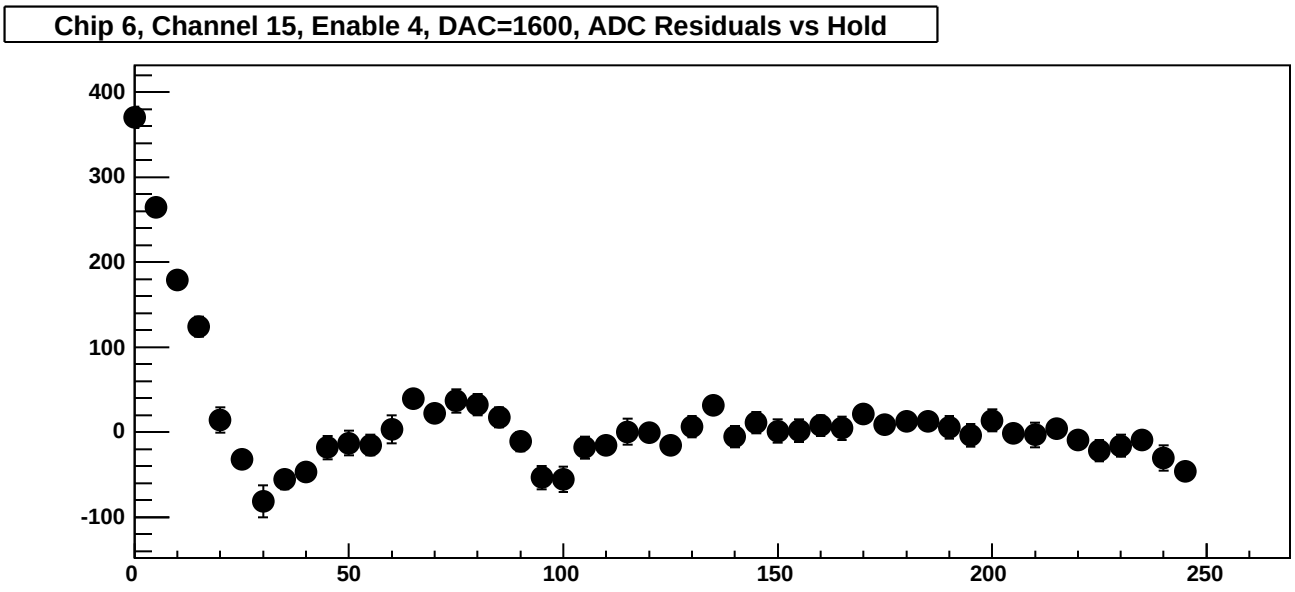

Chip 6, Channel 15, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 321.3 / 41
p0 -680.7 ± 4.297
p1 98.11 ± 0.6926
p2 -4.147e+08 ± 9.086e+06
p3 3.597e+07 ± 6.618e+05
p4 10.9 ± 0.1272
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250


Chip 6, Channel 15, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

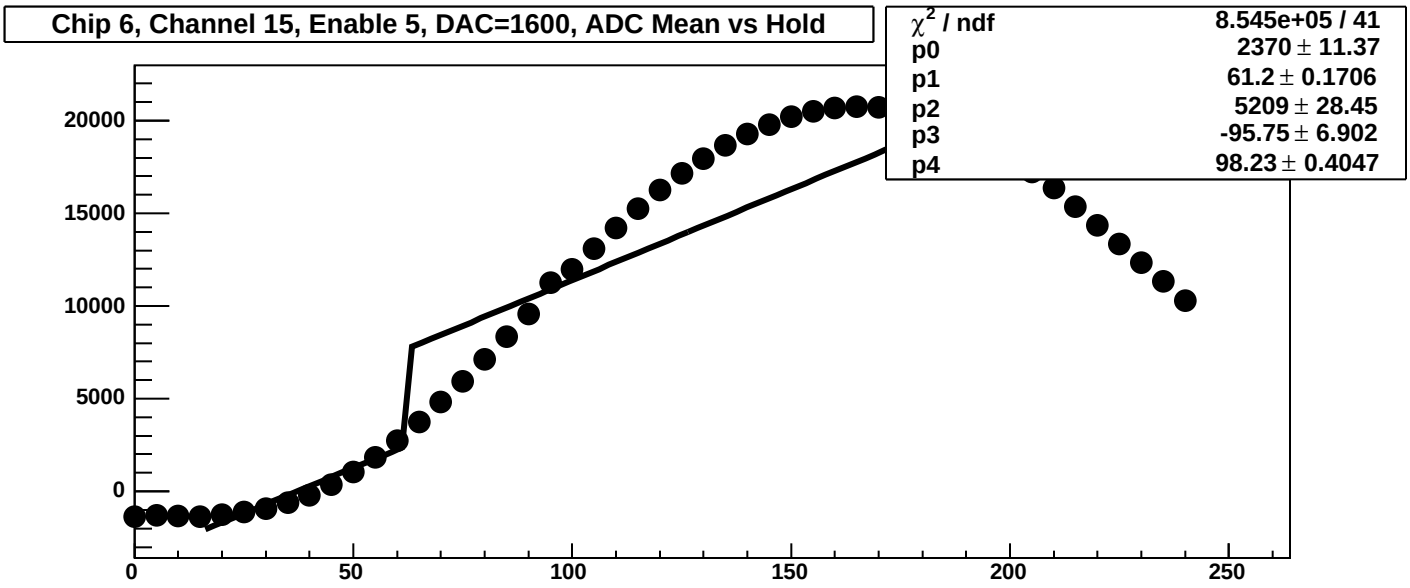

Chip 6, Channel 15, Enable 3, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 6, Channel 15, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 295.5 / 41
p0 -681.1 ± 4.212
p1 98.23 ± 0.6834
p2 -5.137e+08 ± 6.992e+06
p3 4.72e+07 ± 2.591e+05
p4 10.34 ± 0.1208
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250

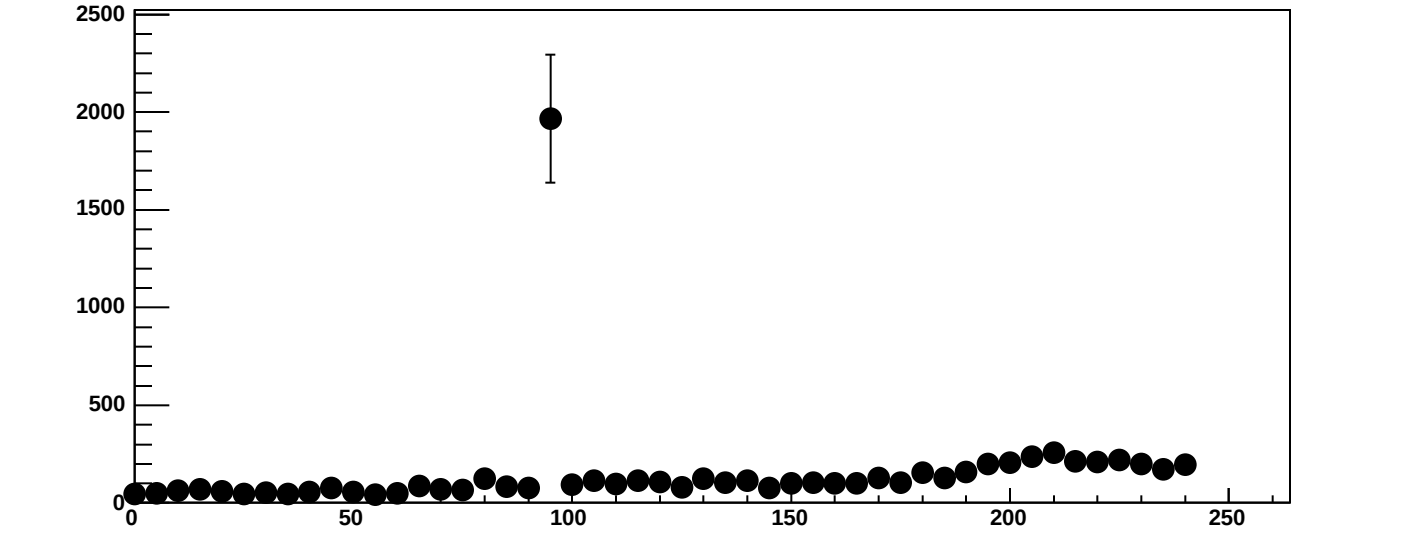

Chip 6, Channel 15, Enable 4, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 6, Channel 15, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

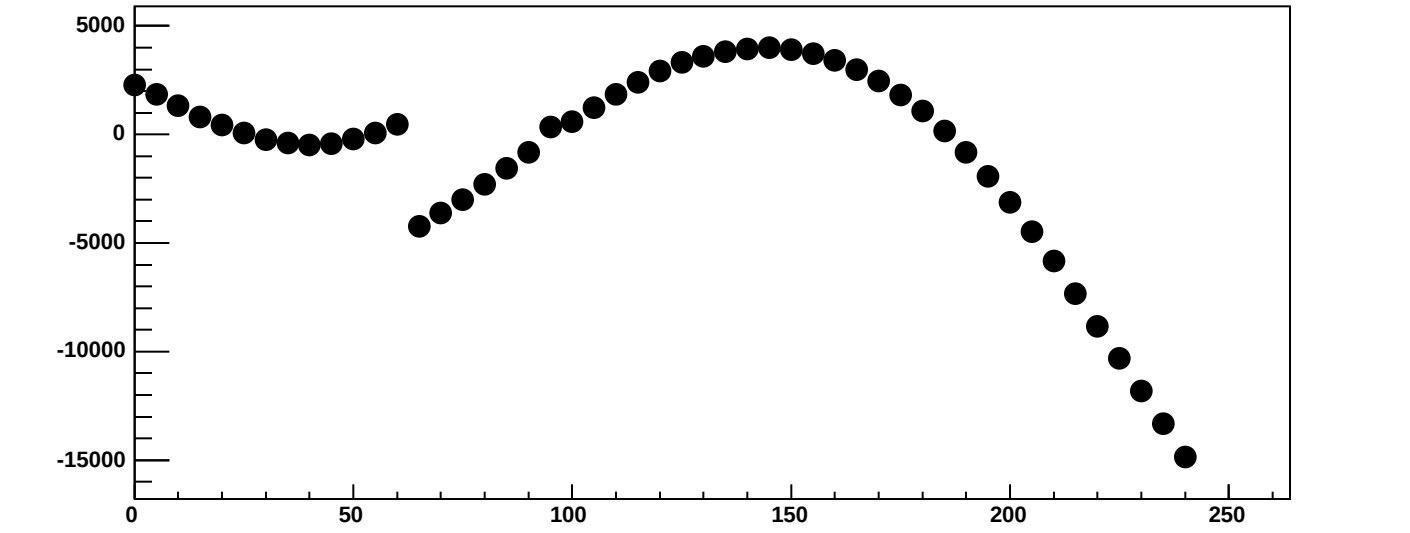
Chip 6, Channel 15, Enable 5, DAC=1600, ADC Mean vs Hold

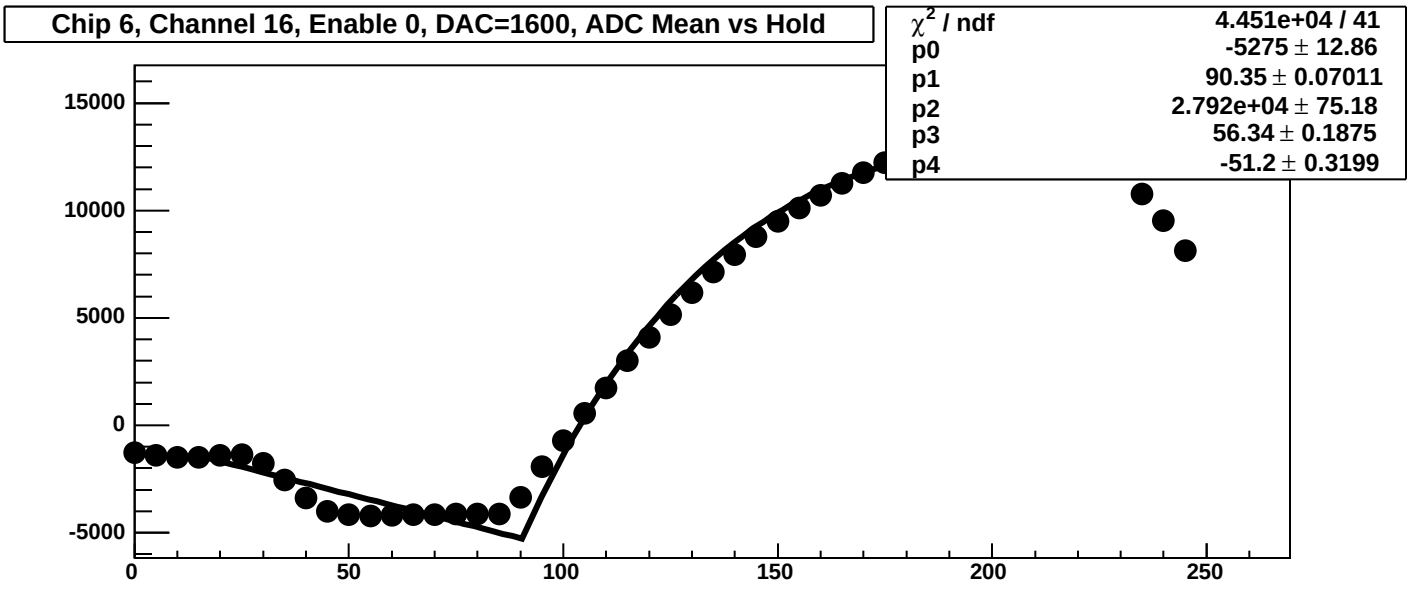


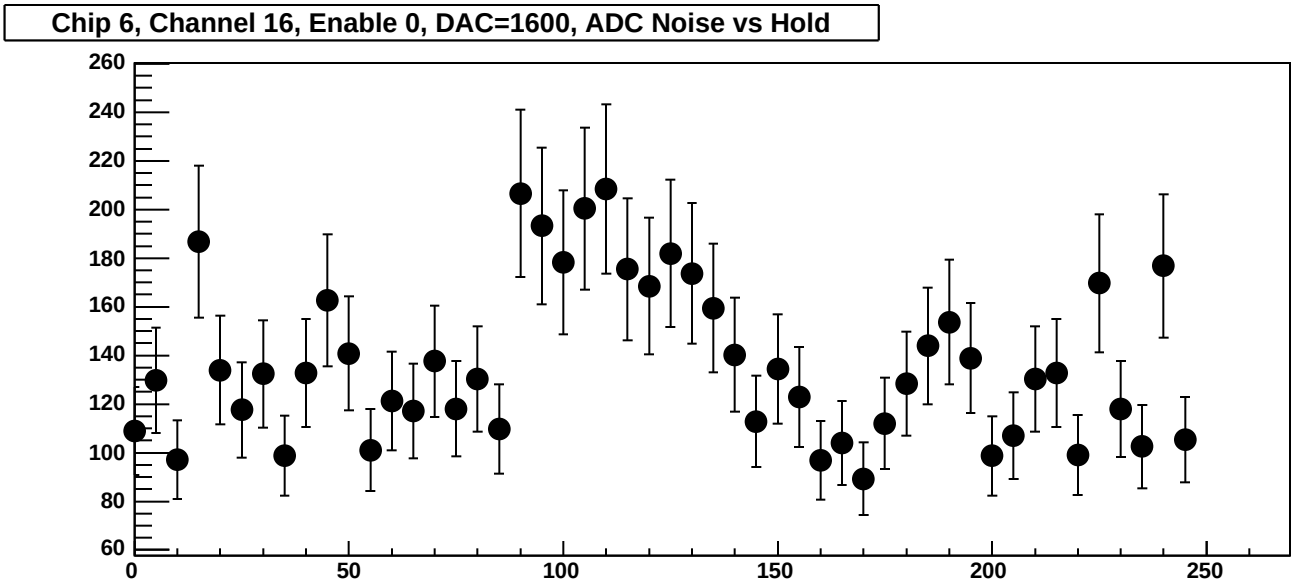
Chip 6, Channel 15, Enable 5, DAC=1600, ADC Noise vs Hold

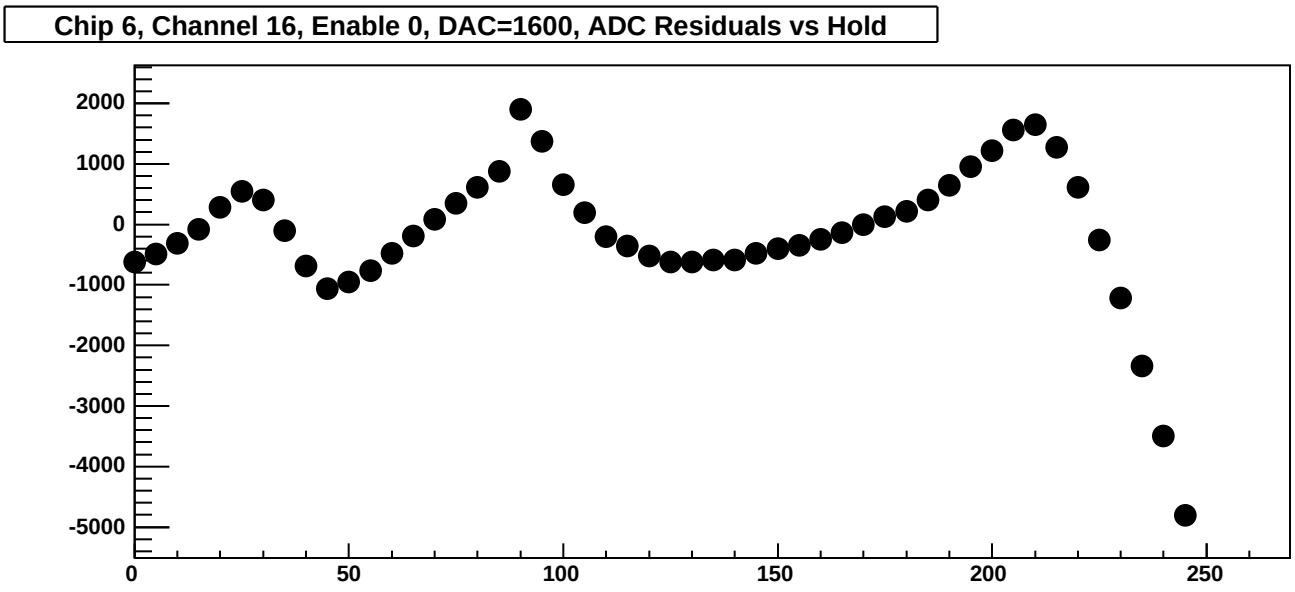


Chip 6, Channel 15, Enable 5, DAC=1600, ADC Residuals vs Hold

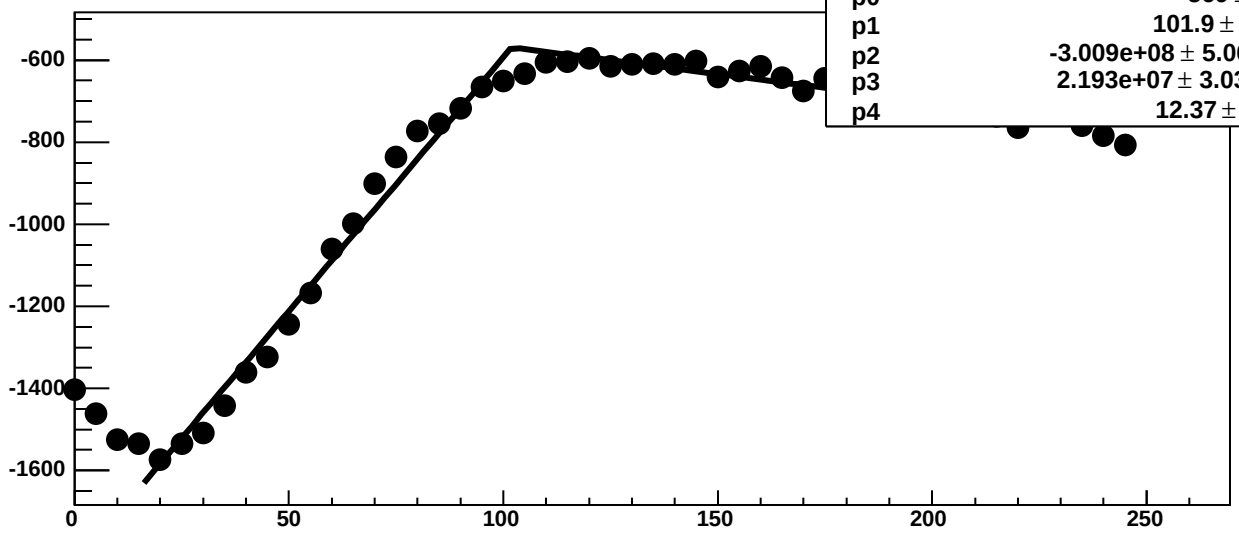



Chip 6, Channel 16, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.451e+04 / 41
p0 -5275 ± 12.86
p1 90.35 ± 0.07011
p2 2.792e+04 ± 75.18
p3 56.34 ± 0.1875
p4 -51.2 ± 0.3199
15000
10000
5000
0
-5000
0
50
100
150
200
250

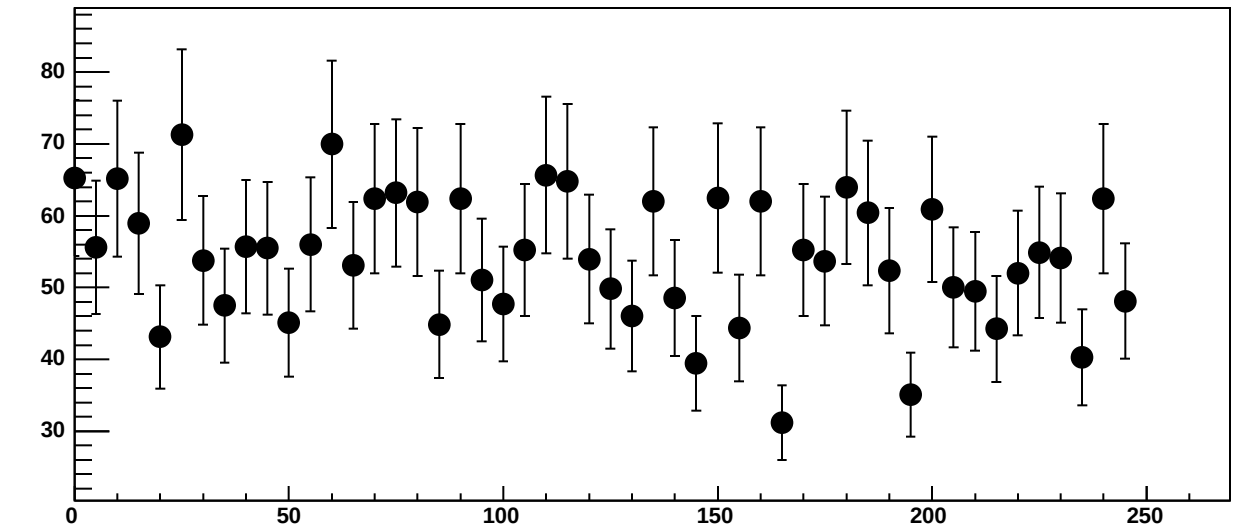

Chip 6, Channel 16, Enable 0, DAC=1600, ADC Noise vs Hold
260
240
220
200
180
160
140
120
100
80
60
0
50
100
150
200
250


Chip 6, Channel 16, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

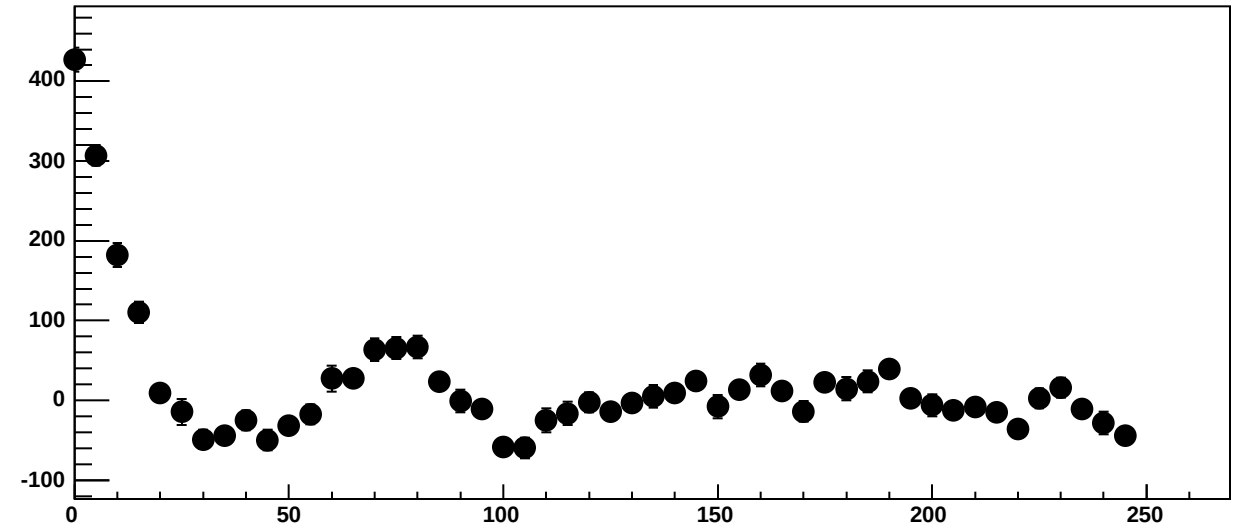
Chip 6, Channel 16, Enable 1, DAC=1600, ADC Mean vs Hold



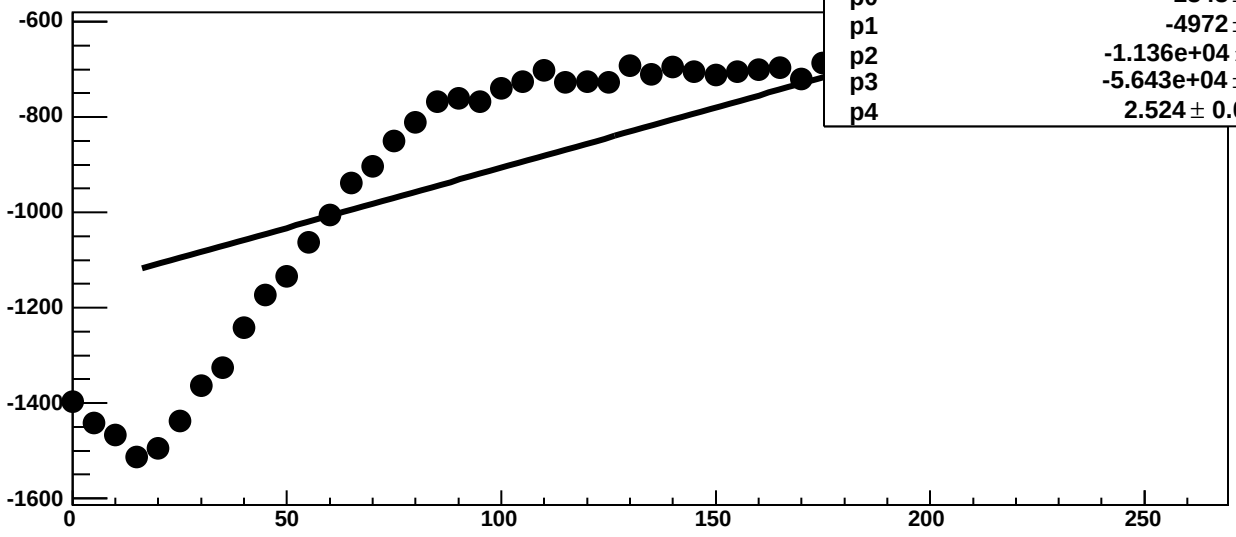
Chip 6, Channel 16, Enable 1, DAC=1600, ADC Noise vs Hold



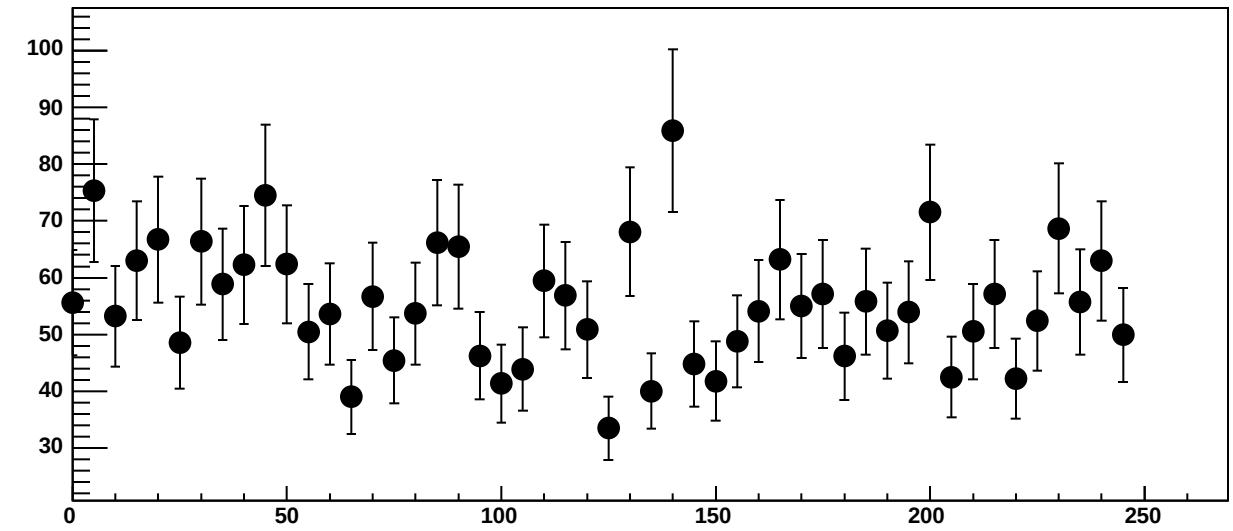
Chip 6, Channel 16, Enable 1, DAC=1600, ADC Residuals vs Hold



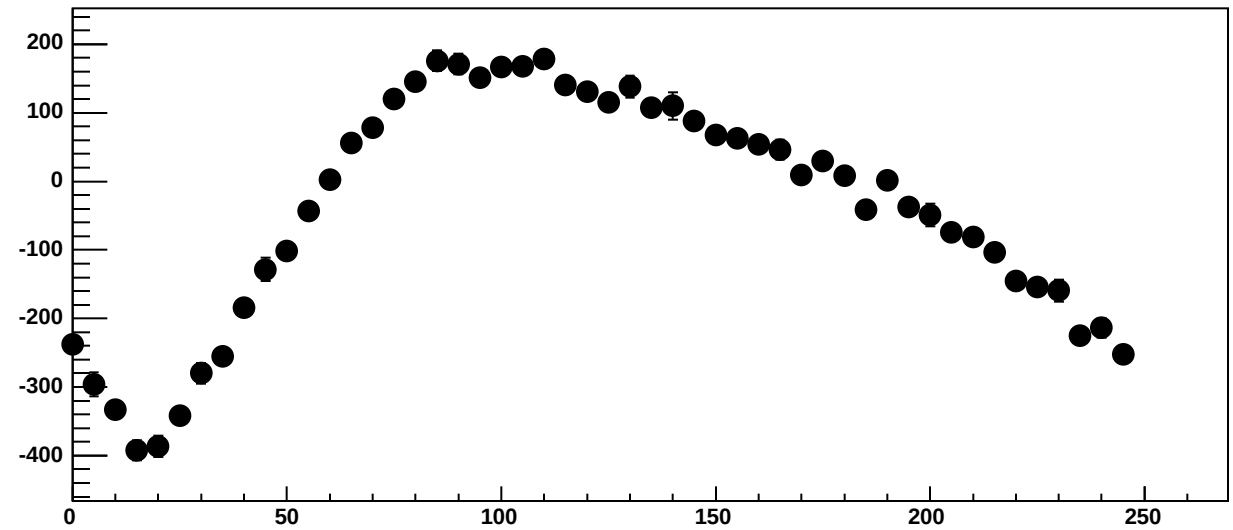
Chip 6, Channel 16, Enable 2, DAC=1600, ADC Mean vs Hold

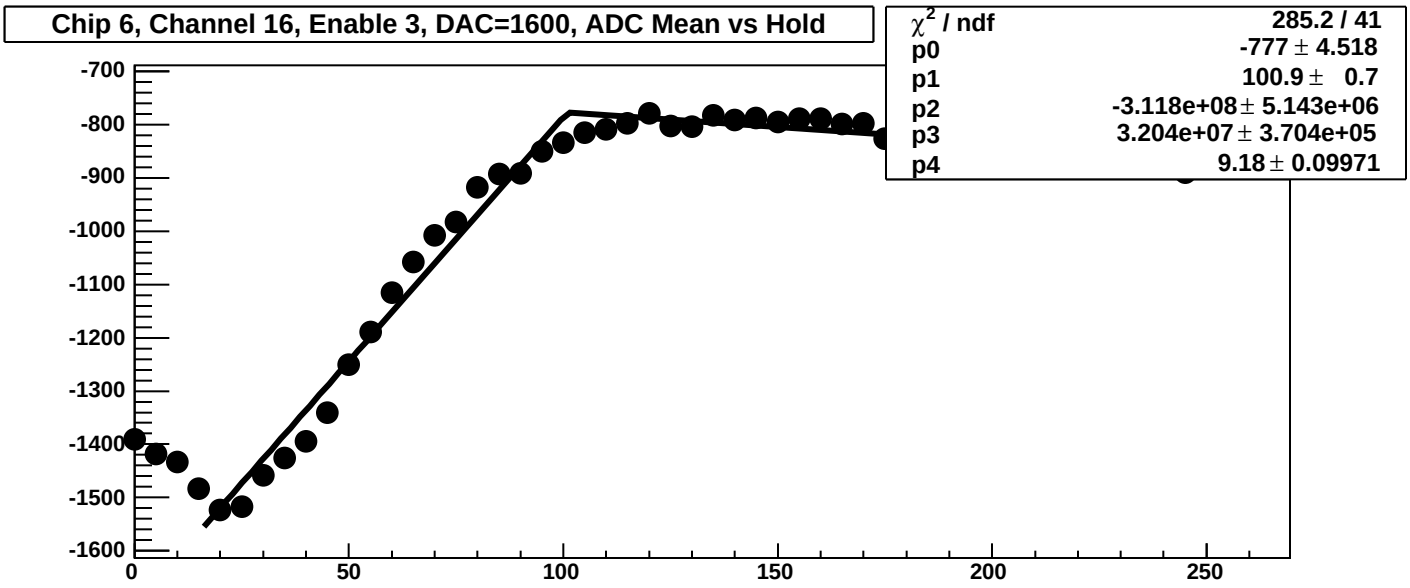


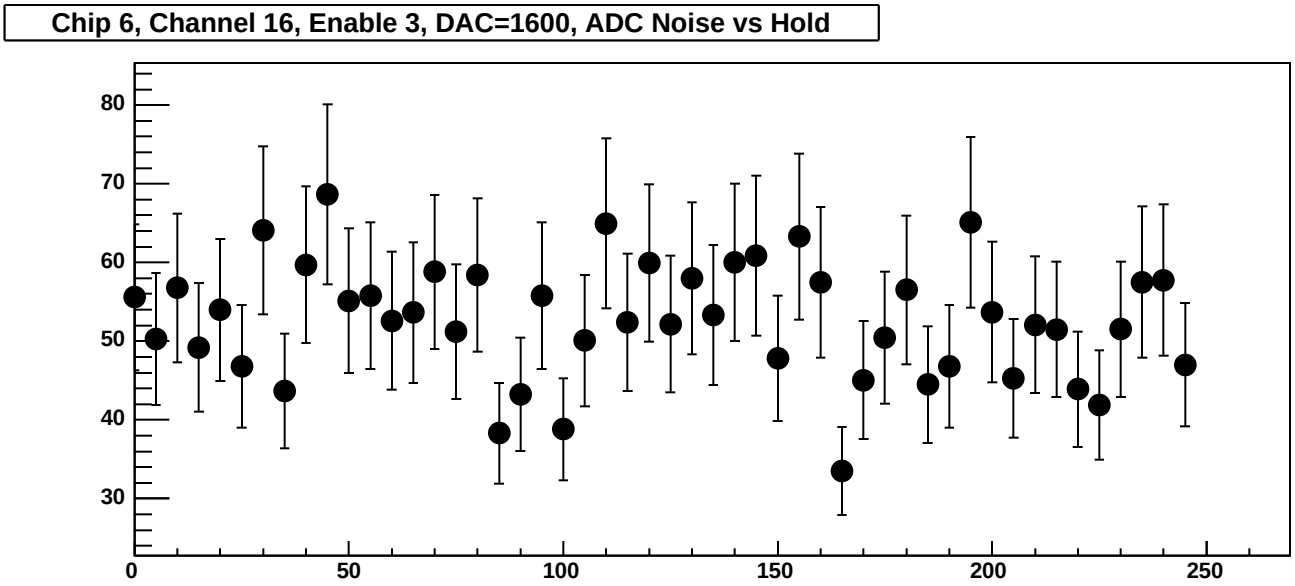
Chip 6, Channel 16, Enable 2, DAC=1600, ADC Noise vs Hold

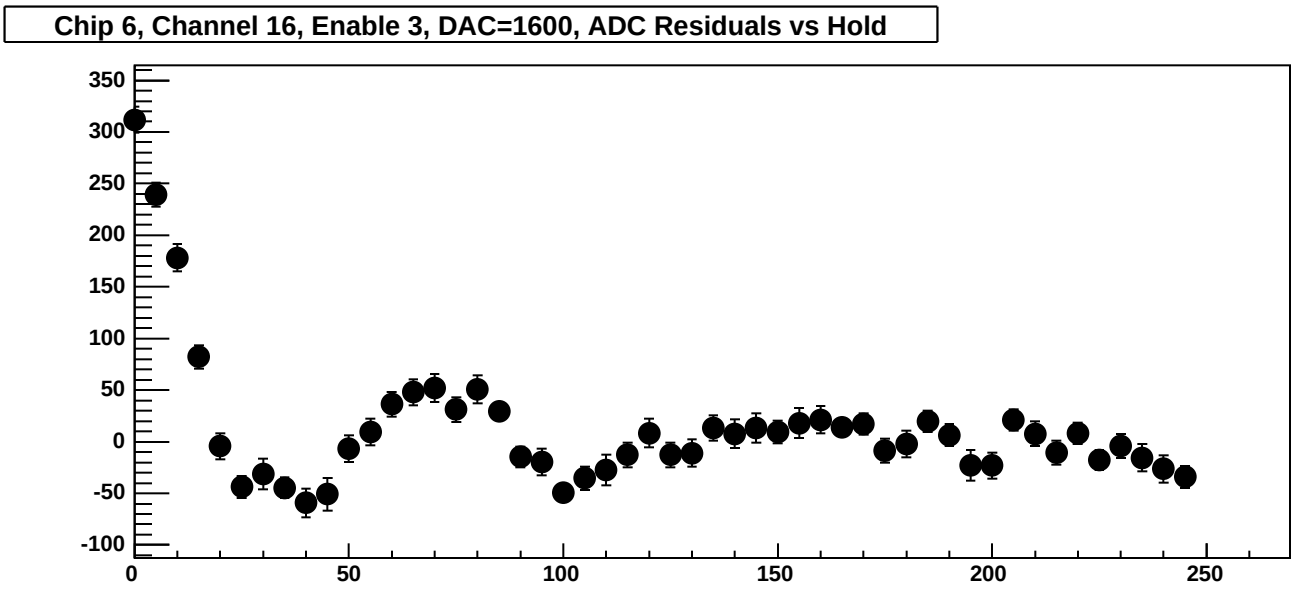


Chip 6, Channel 16, Enable 2, DAC=1600, ADC Residuals vs Hold

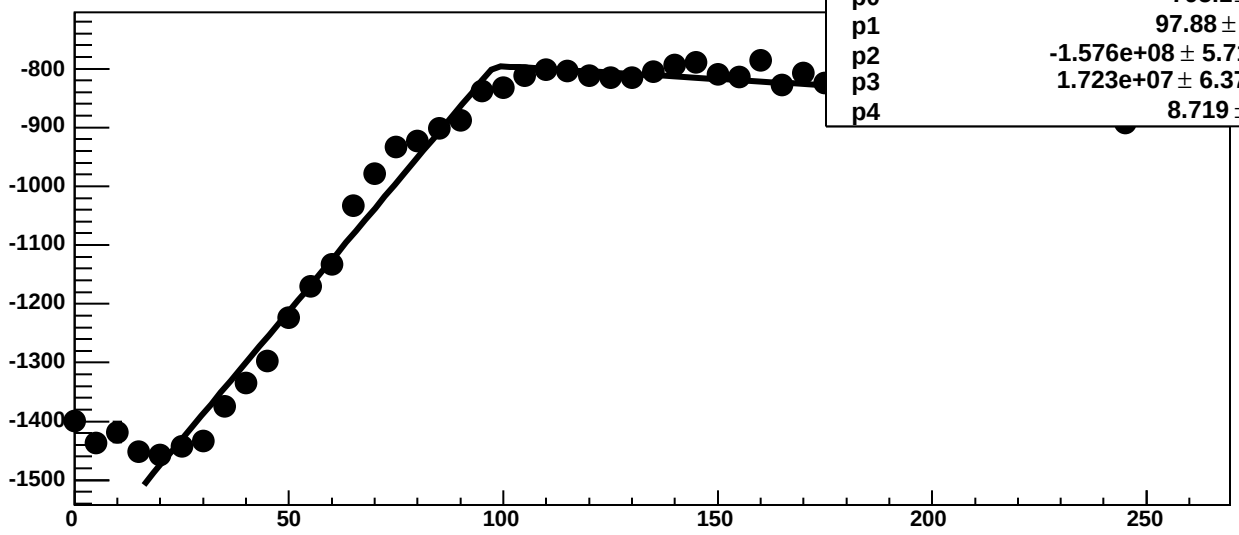



Chip 6, Channel 16, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 285.2 / 41
p0 -777 ± 4.518
p1 100.9 ± 0.7
p2 -3.118e+08 ± 5.143e+06
p3 3.204e+07 ± 3.704e+05
p4 9.18 ± 0.09971
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
50
100
150
200
250

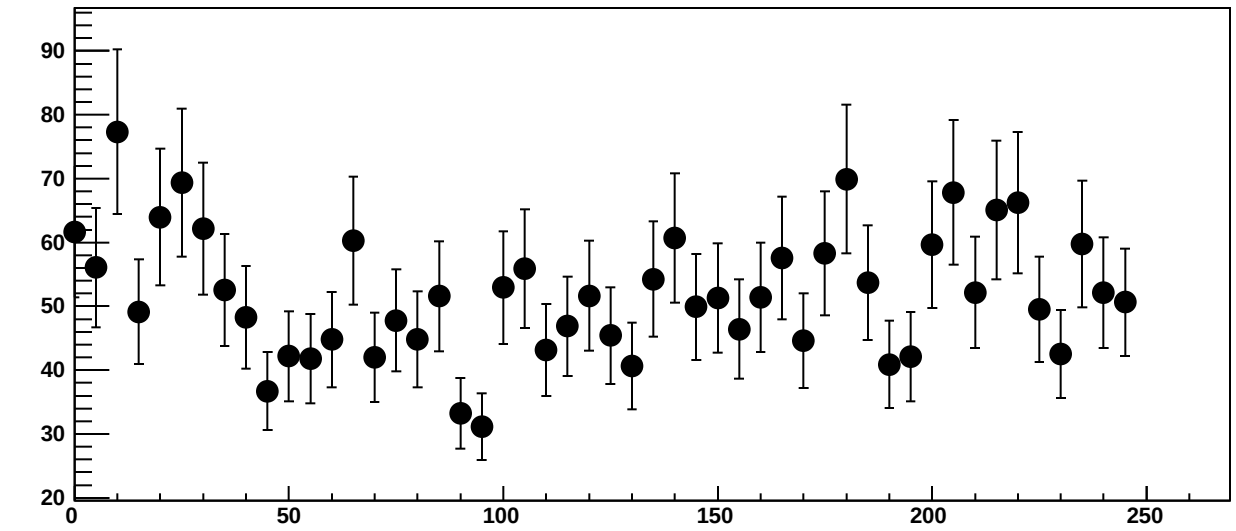

Chip 6, Channel 16, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 6, Channel 16, Enable 3, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

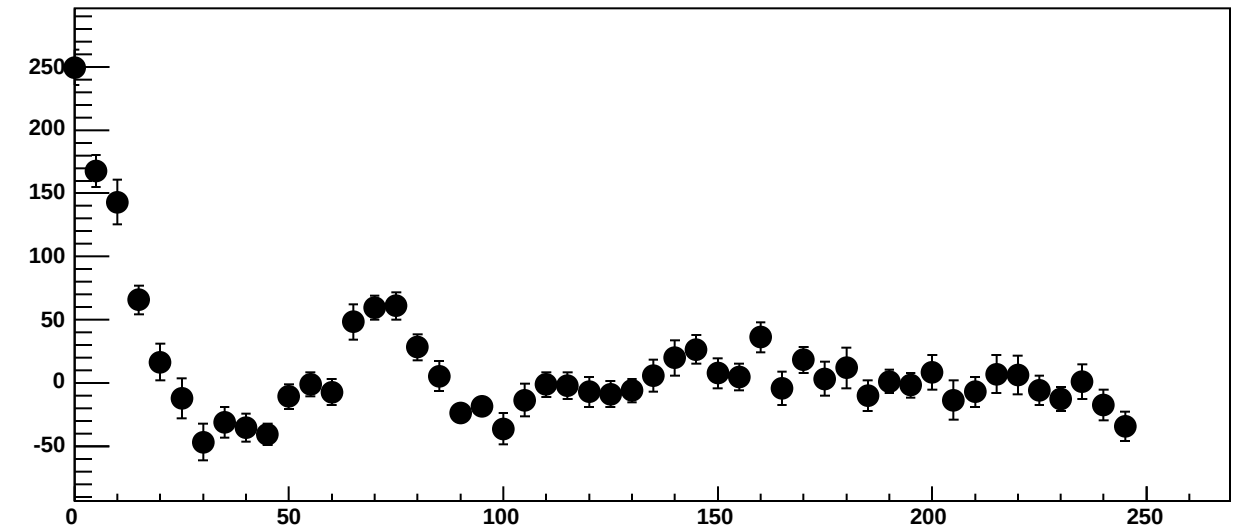
Chip 6, Channel 16, Enable 4, DAC=1600, ADC Mean vs Hold

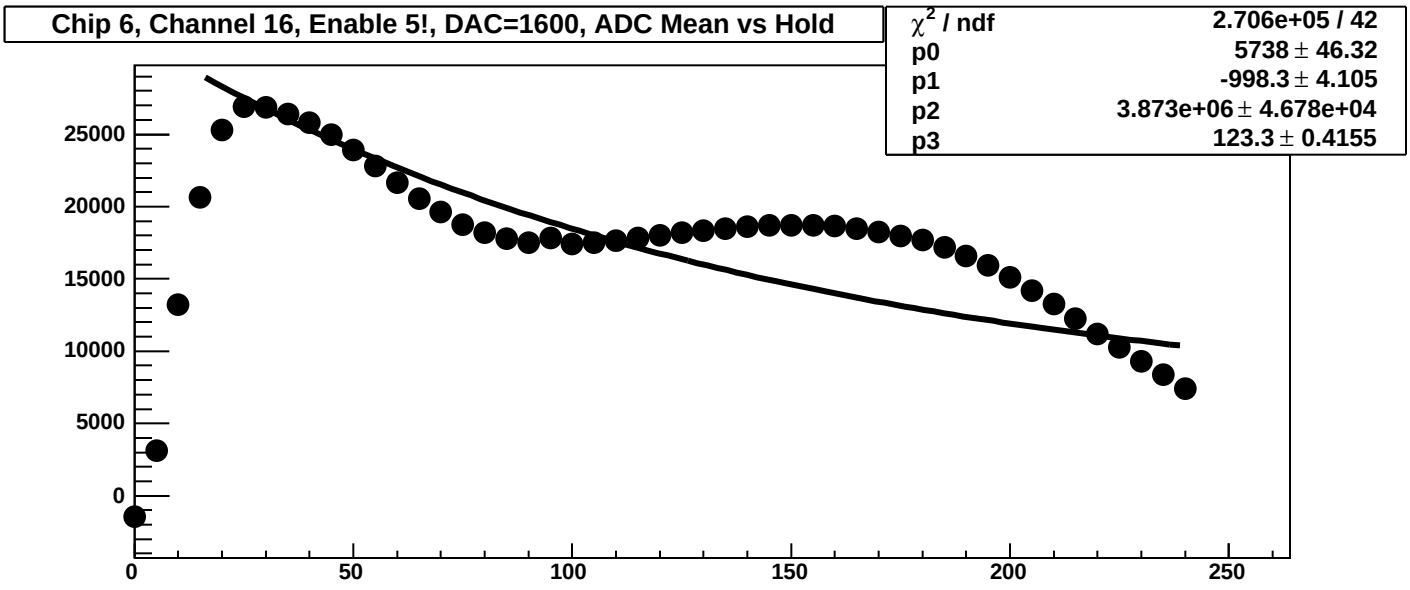


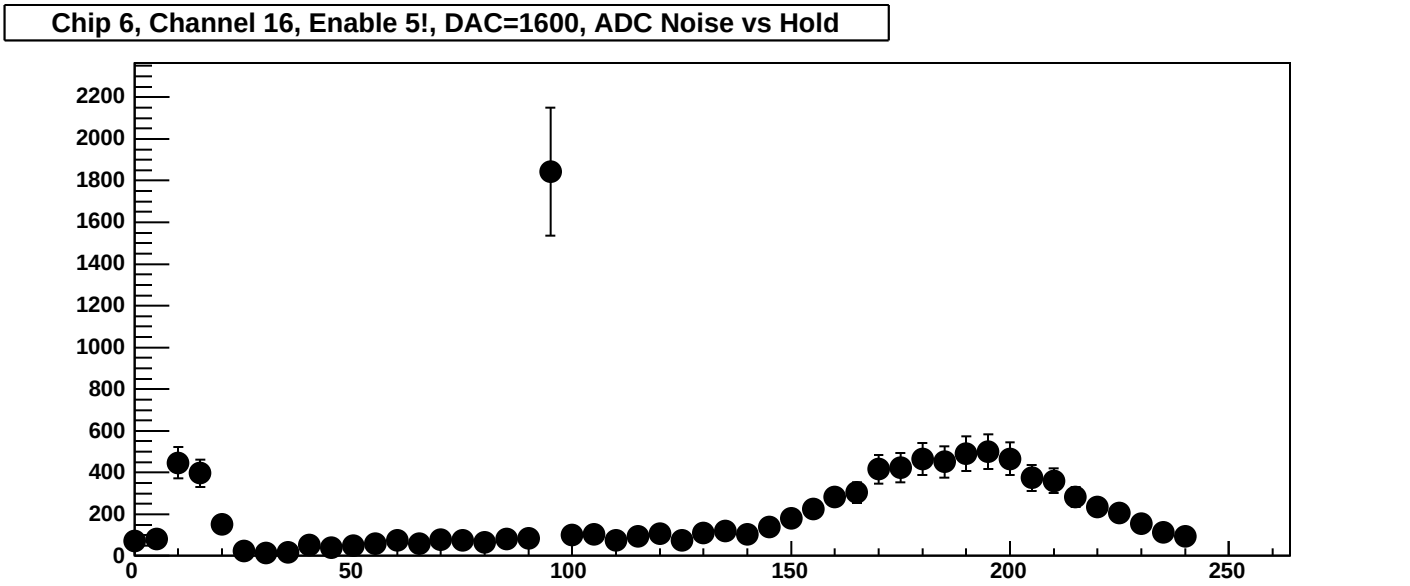
Chip 6, Channel 16, Enable 4, DAC=1600, ADC Noise vs Hold

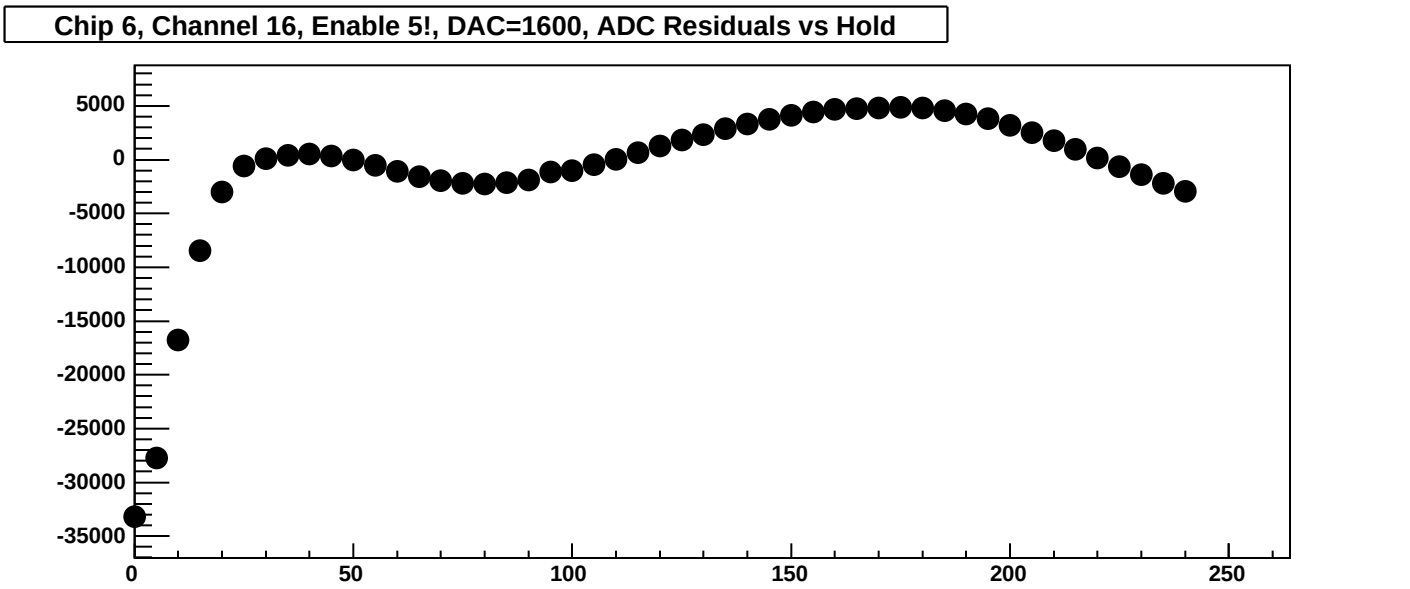


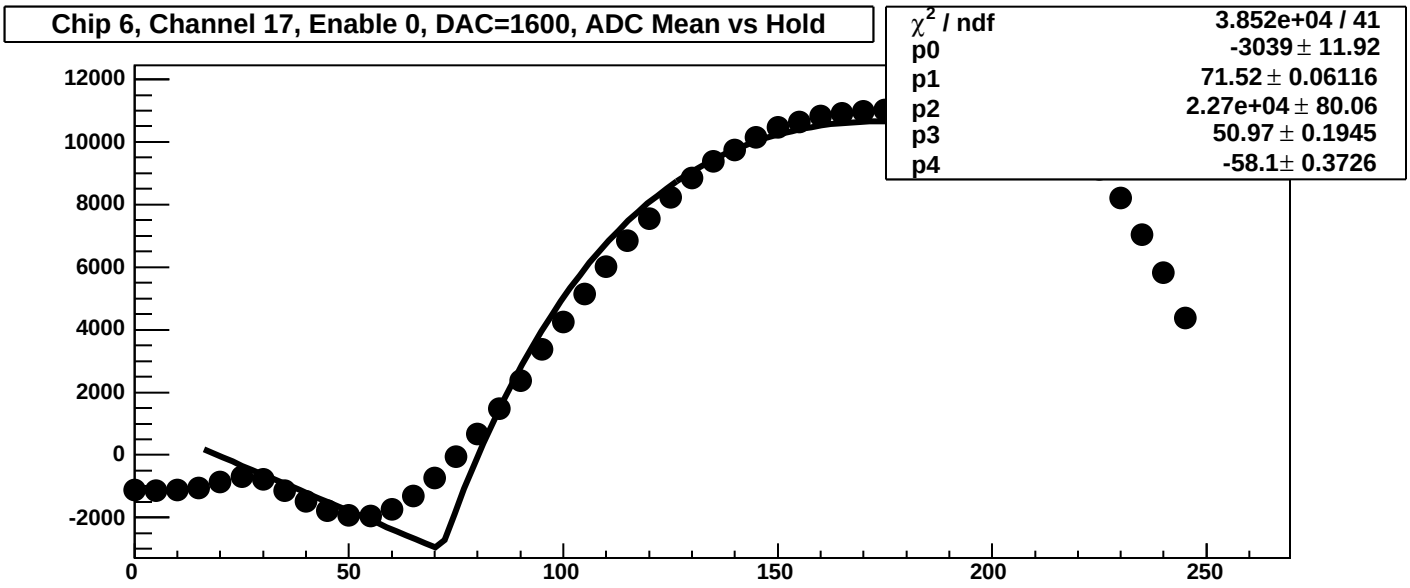
Chip 6, Channel 16, Enable 4, DAC=1600, ADC Residuals vs Hold

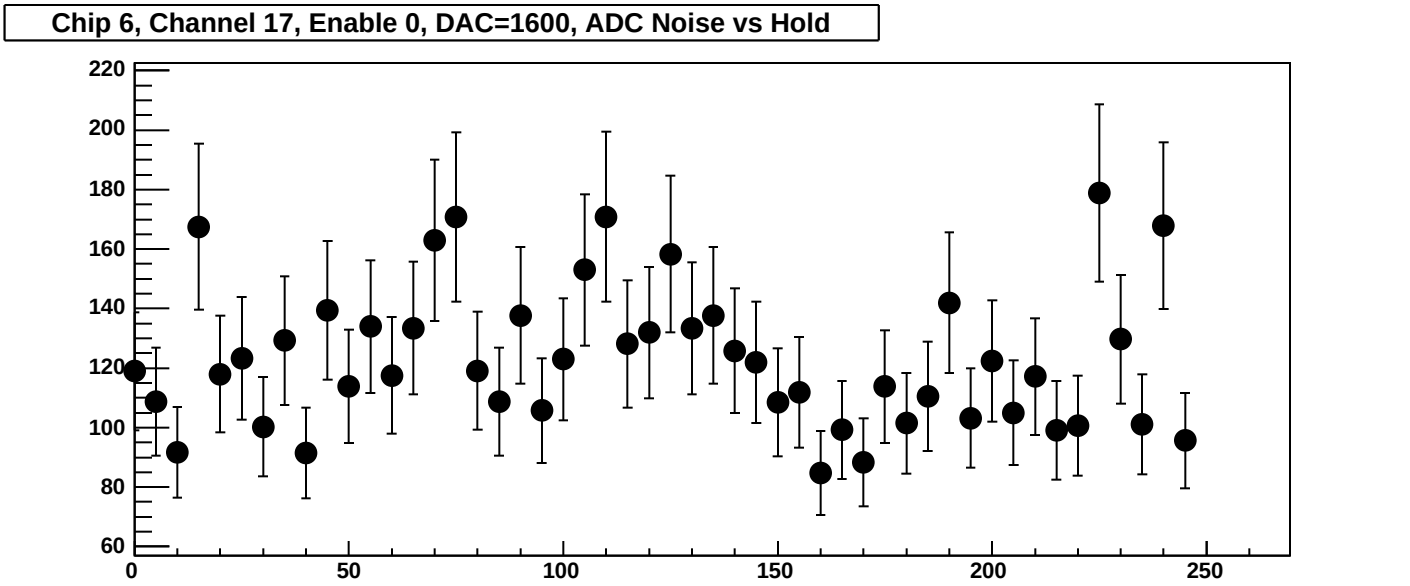


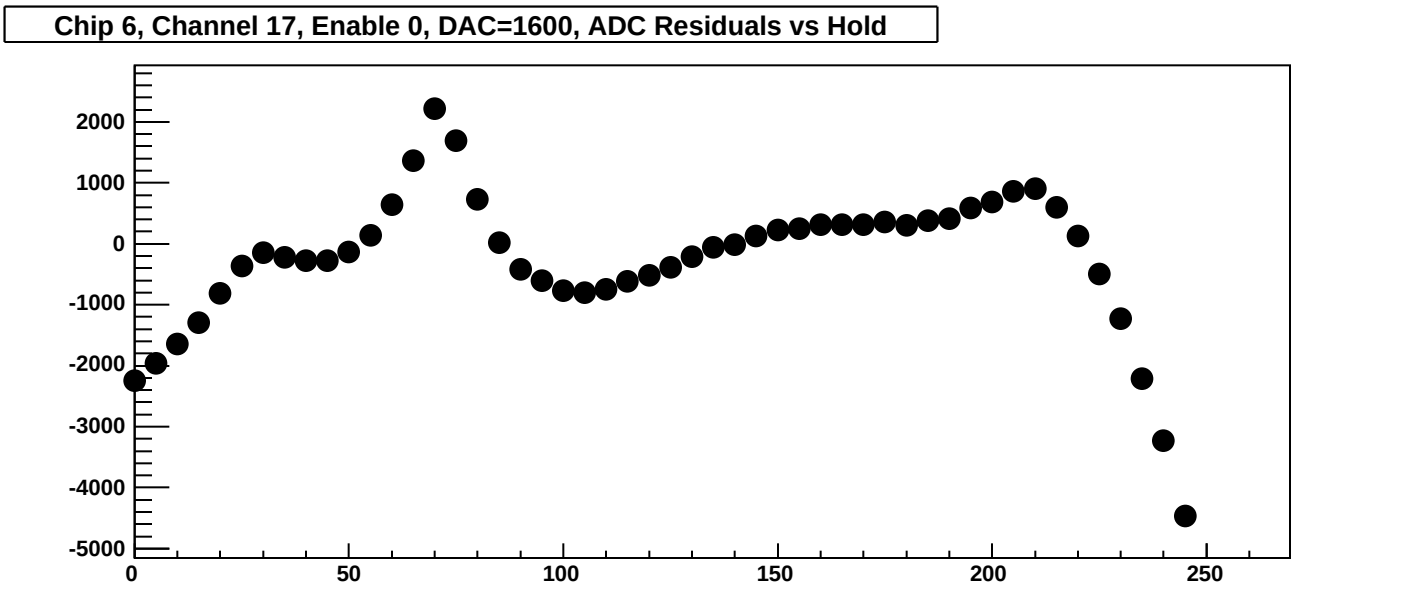

Chip 6, Channel 16, Enable 5!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.706e+05 / 42
p0 5738 ± 46.32
p1 -998.3 ± 4.105
p2 3.873e+06 ± 4.678e+04
p3 123.3 ± 0.4155
25000
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 6, Channel 16, Enable 5!, DAC=1600, ADC Noise vs Hold
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

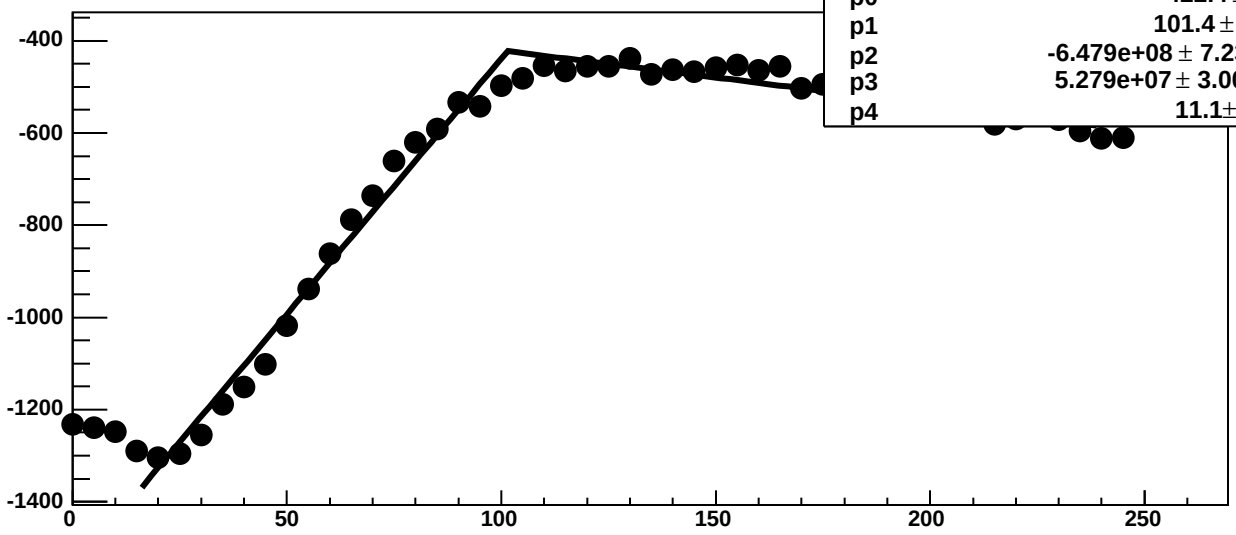

Chip 6, Channel 16, Enable 5!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
-35000
0
50
100
150
200
250


Chip 6, Channel 17, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.852e+04 / 41
p0 -3039 ± 11.92
p1 71.52 ± 0.06116
p2 2.27e+04 ± 80.06
p3 50.97 ± 0.1945
p4 -58.1 ± 0.3726
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

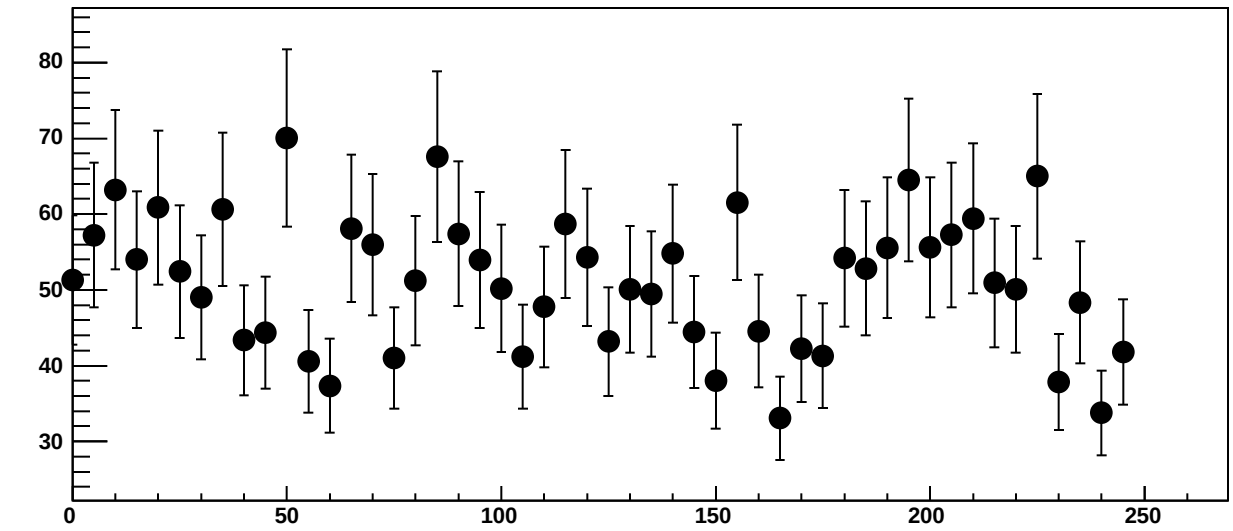

Chip 6, Channel 17, Enable 0, DAC=1600, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
0
50
100
150
200
250


Chip 6, Channel 17, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

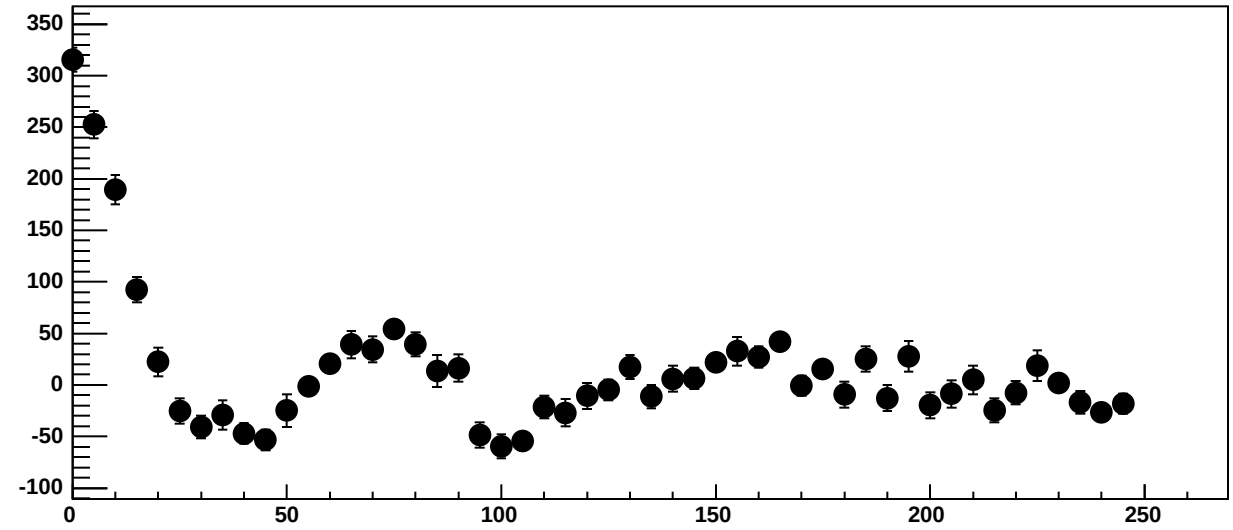
Chip 6, Channel 17, Enable 1, DAC=1600, ADC Mean vs Hold



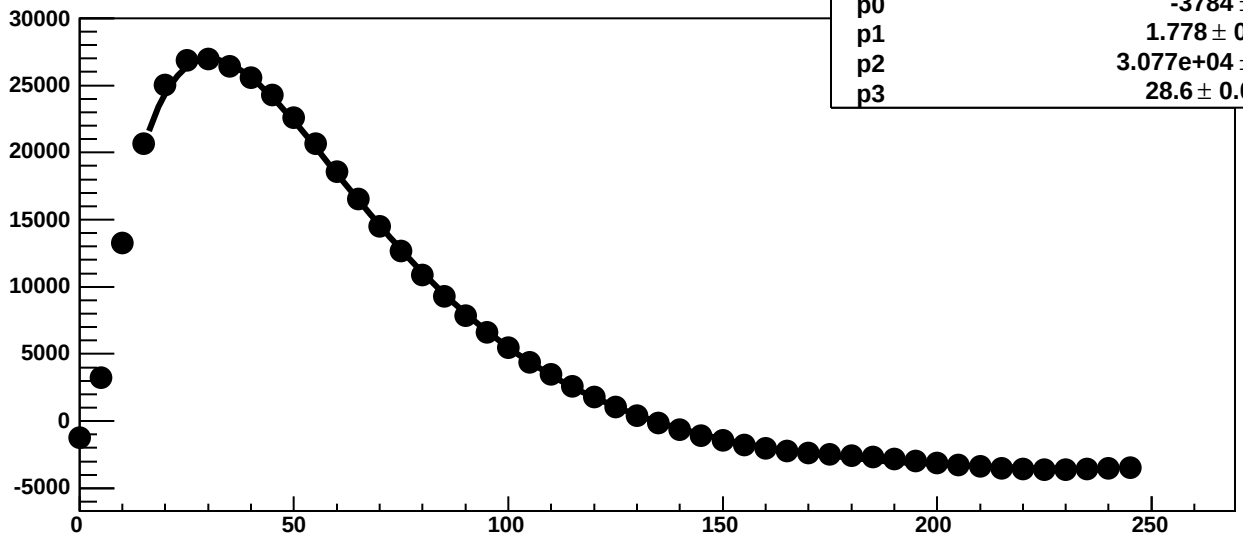
Chip 6, Channel 17, Enable 1, DAC=1600, ADC Noise vs Hold



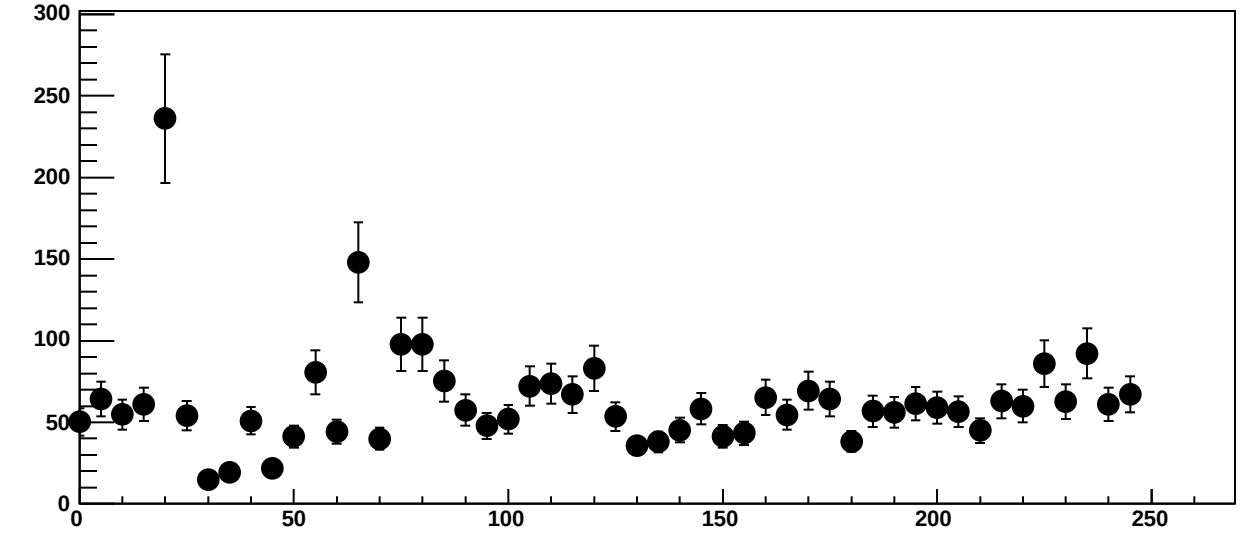
Chip 6, Channel 17, Enable 1, DAC=1600, ADC Residuals vs Hold



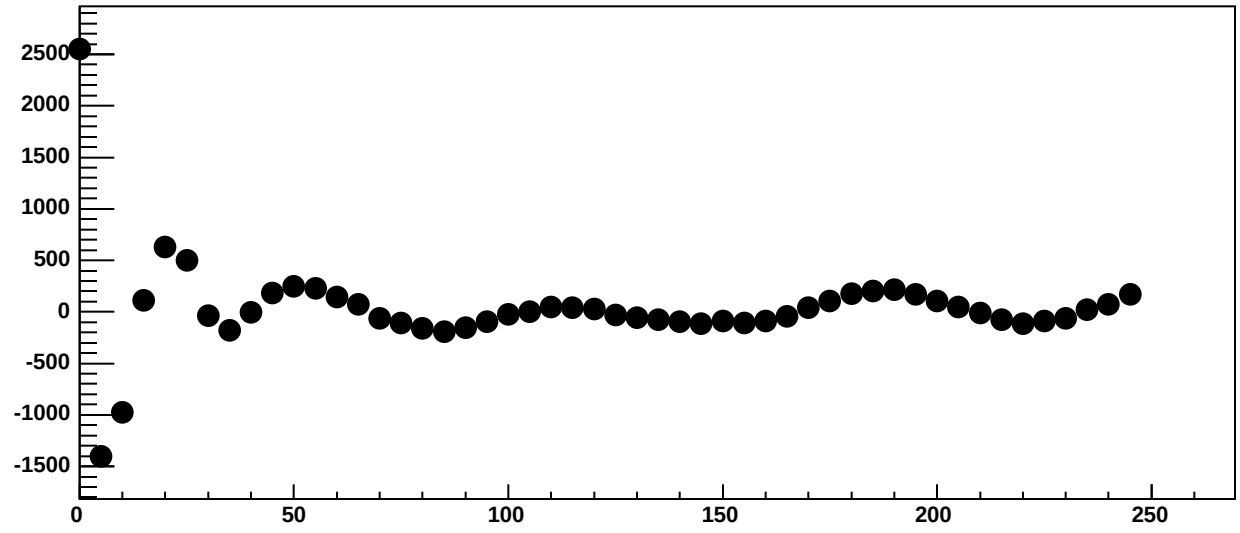
Chip 6, Channel 17, Enable 2!, DAC=1600, ADC Mean vs Hold

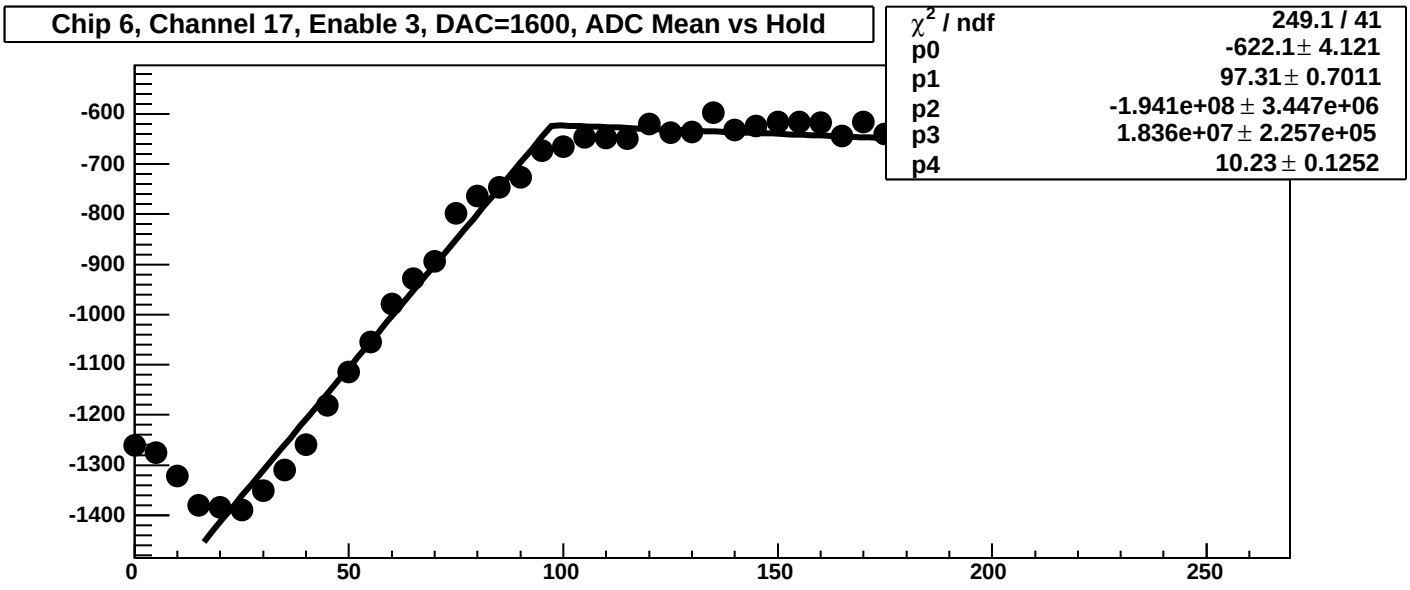


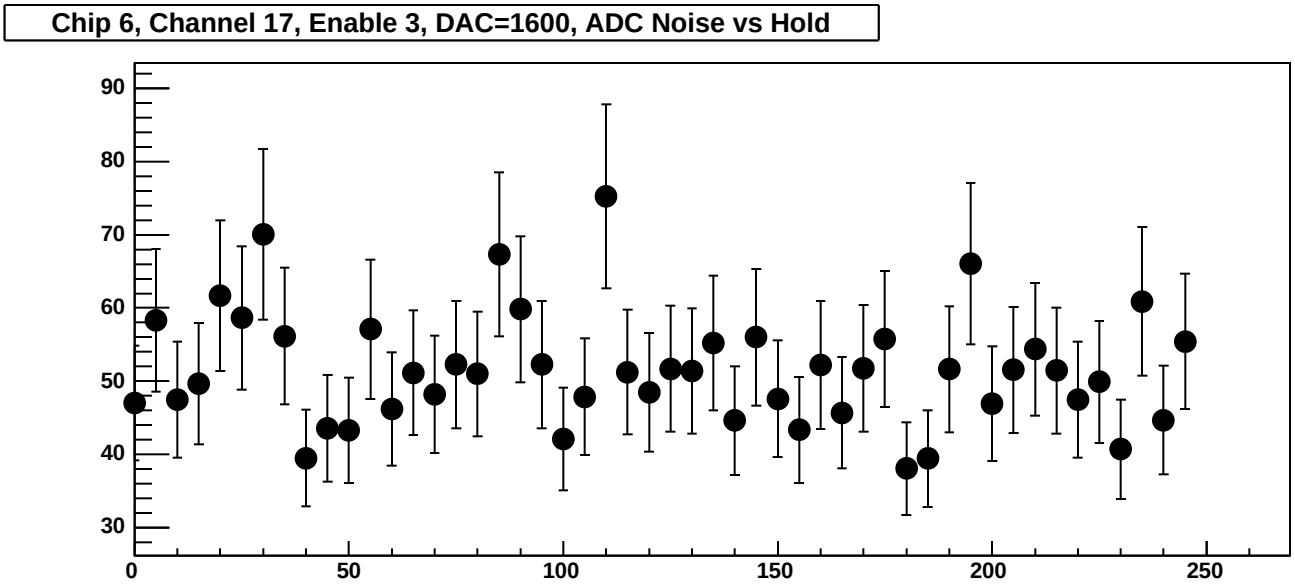
Chip 6, Channel 17, Enable 2!, DAC=1600, ADC Noise vs Hold

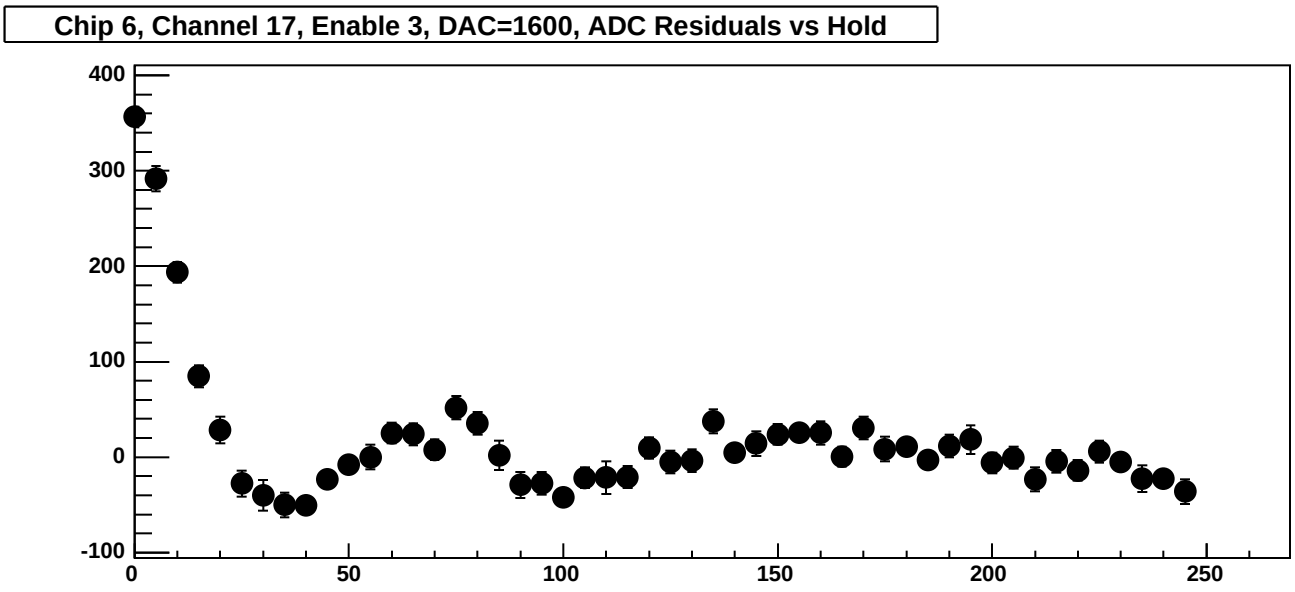


Chip 6, Channel 17, Enable 2!, DAC=1600, ADC Residuals vs Hold

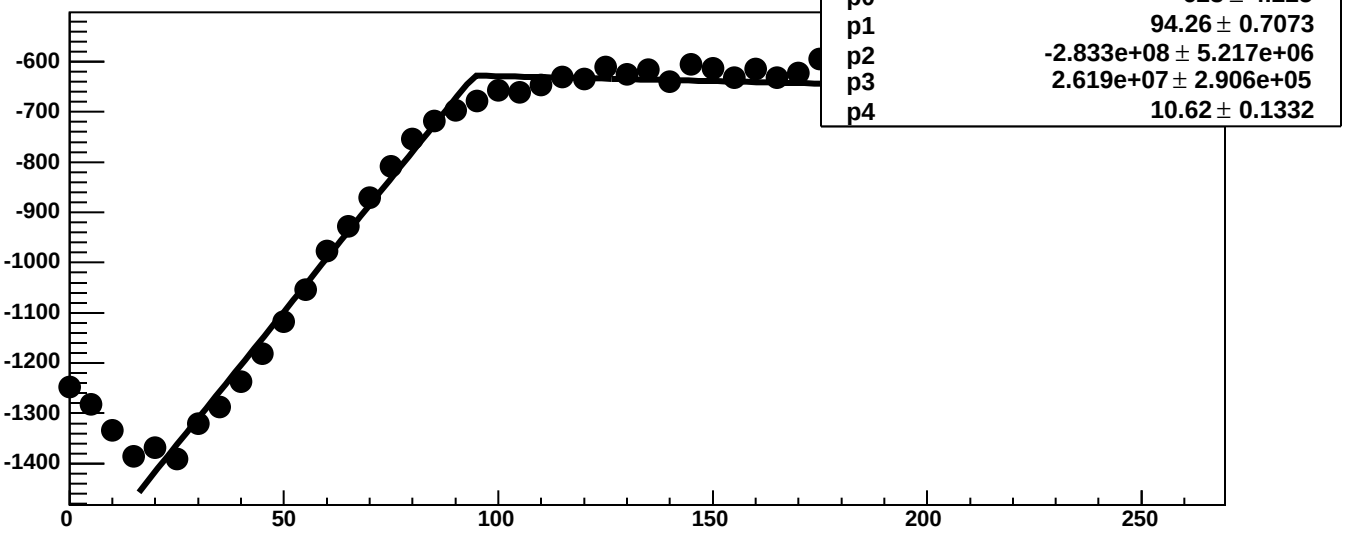



Chip 6, Channel 17, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 249.1 / 41
p0 -622.1 ± 4.121
p1 97.31 ± 0.7011
p2 -1.941e+08 ± 3.447e+06
p3 1.836e+07 ± 2.257e+05
p4 10.23 ± 0.1252
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
50
100
150
200
250

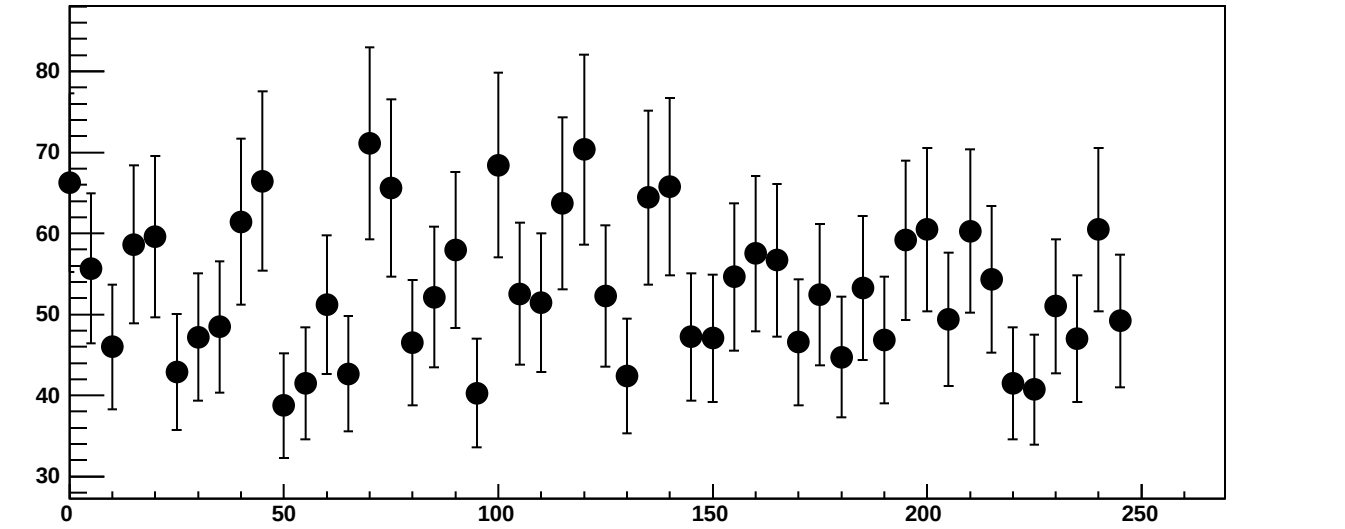

Chip 6, Channel 17, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 6, Channel 17, Enable 3, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

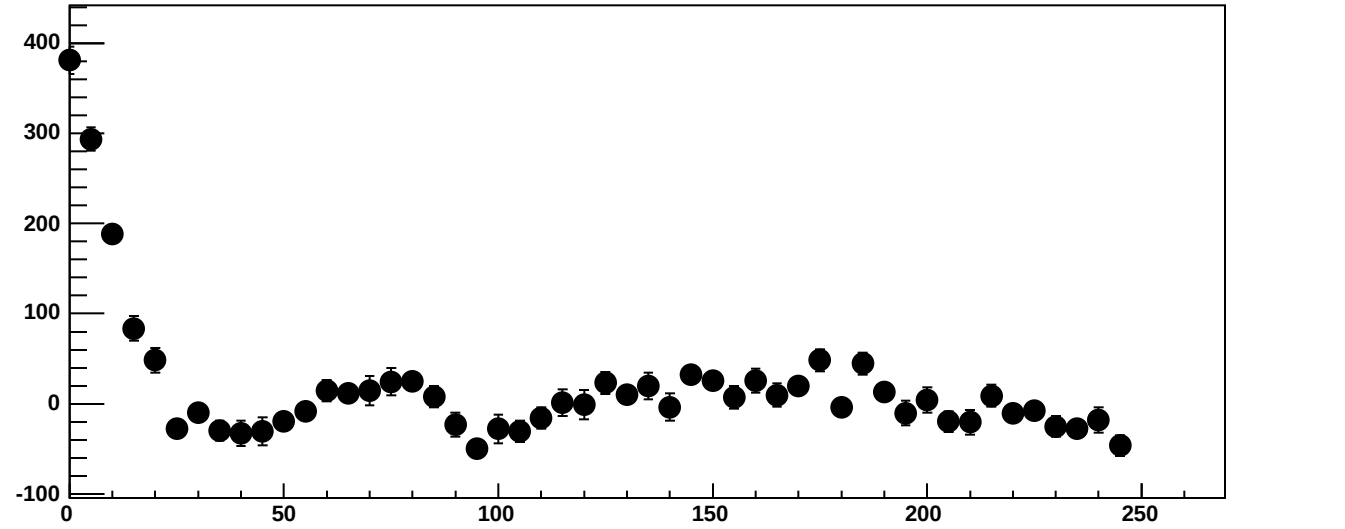
Chip 6, Channel 17, Enable 4, DAC=1600, ADC Mean vs Hold

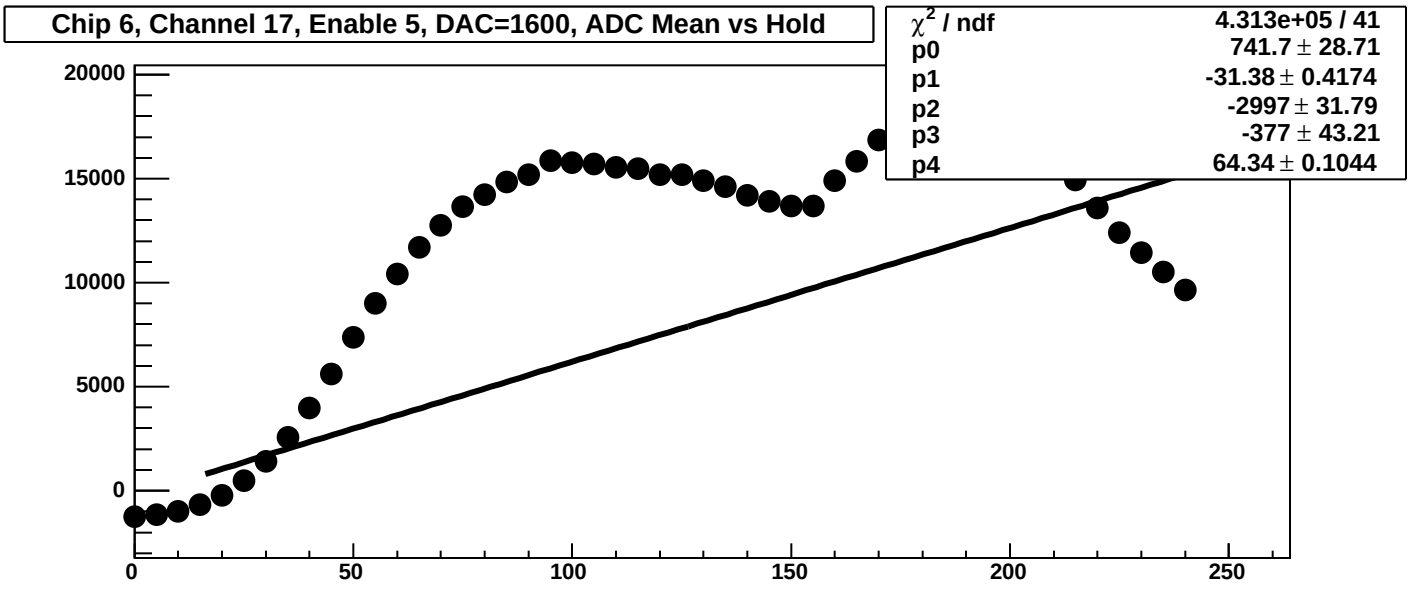


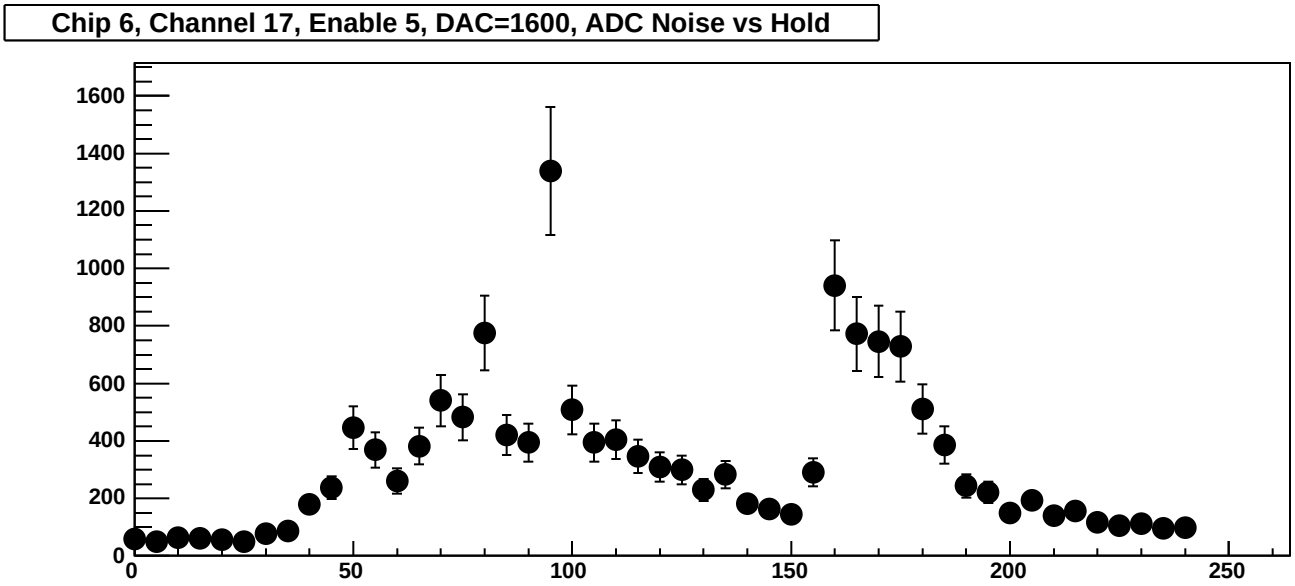
Chip 6, Channel 17, Enable 4, DAC=1600, ADC Noise vs Hold

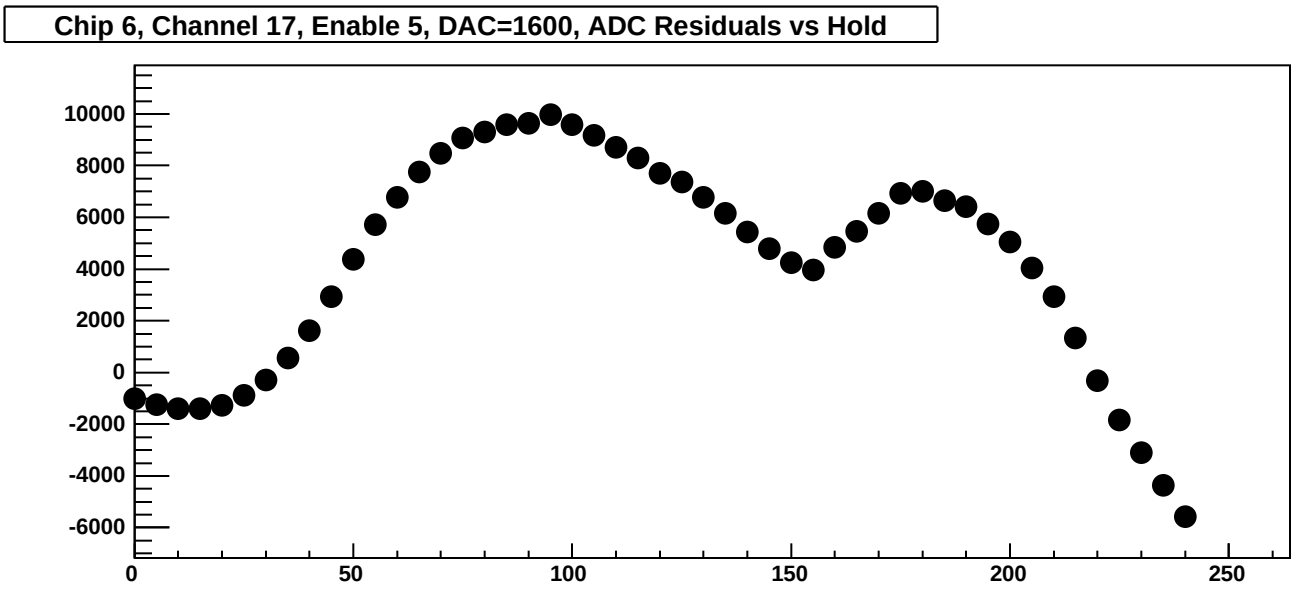


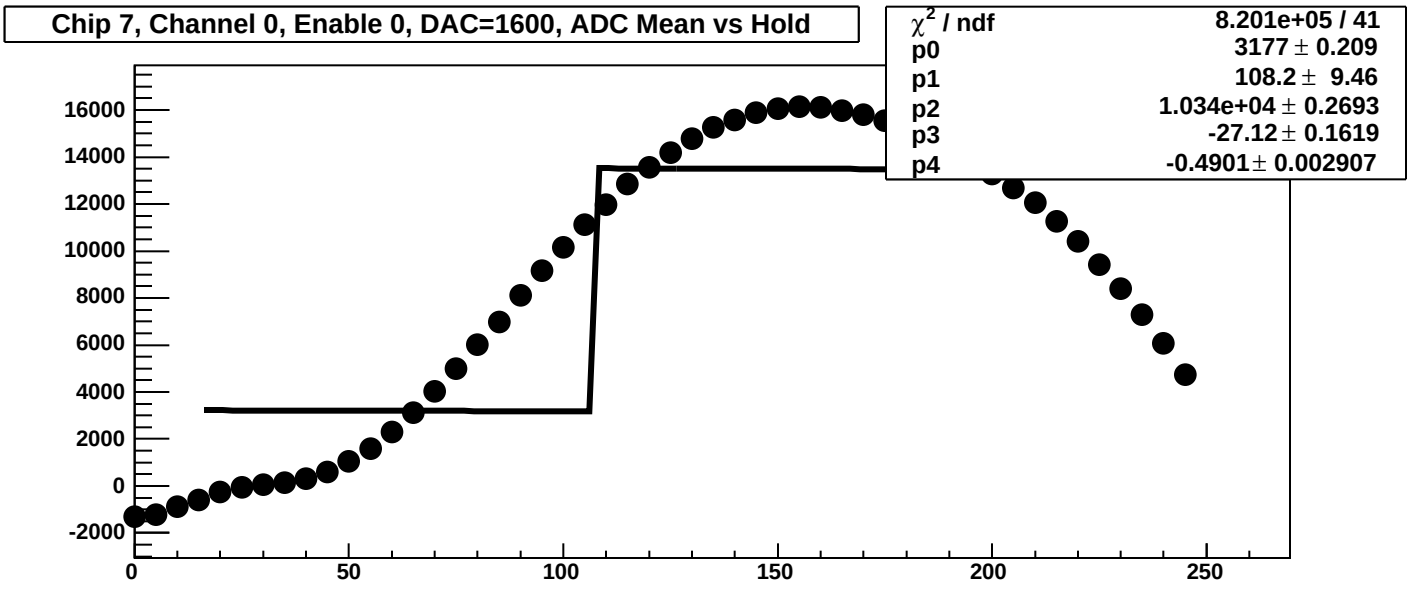
Chip 6, Channel 17, Enable 4, DAC=1600, ADC Residuals vs Hold

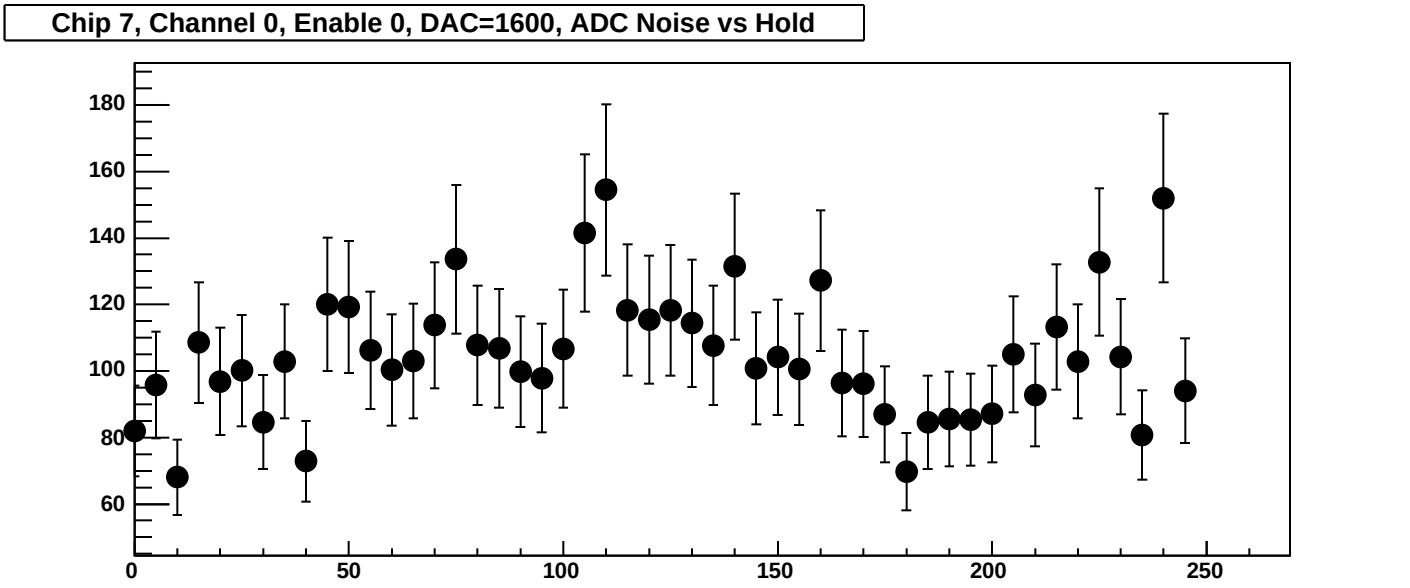


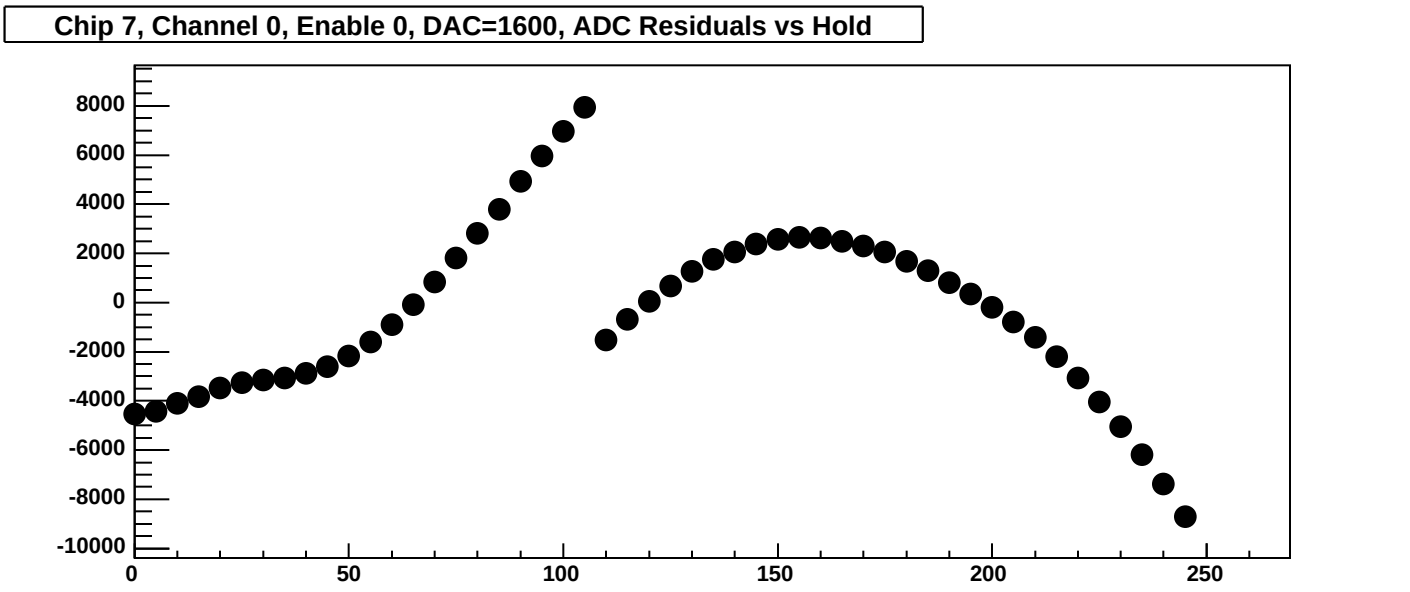

Chip 6, Channel 17, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.313e+05 / 41
p0 741.7 ± 28.71
p1 -31.38 ± 0.4174
p2 -2997 ± 31.79
p3 -377 ± 43.21
p4 64.34 ± 0.1044
20000
15000
10000
5000
0
0
50
100
150
200
250

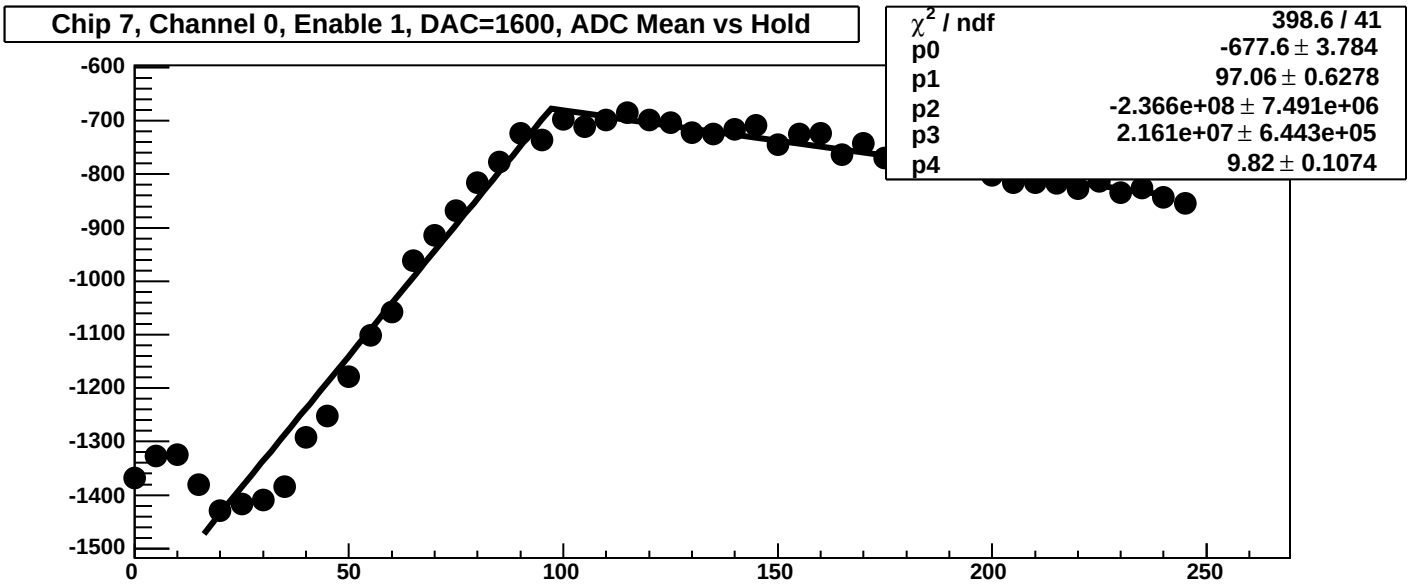

Chip 6, Channel 17, Enable 5, DAC=1600, ADC Noise vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

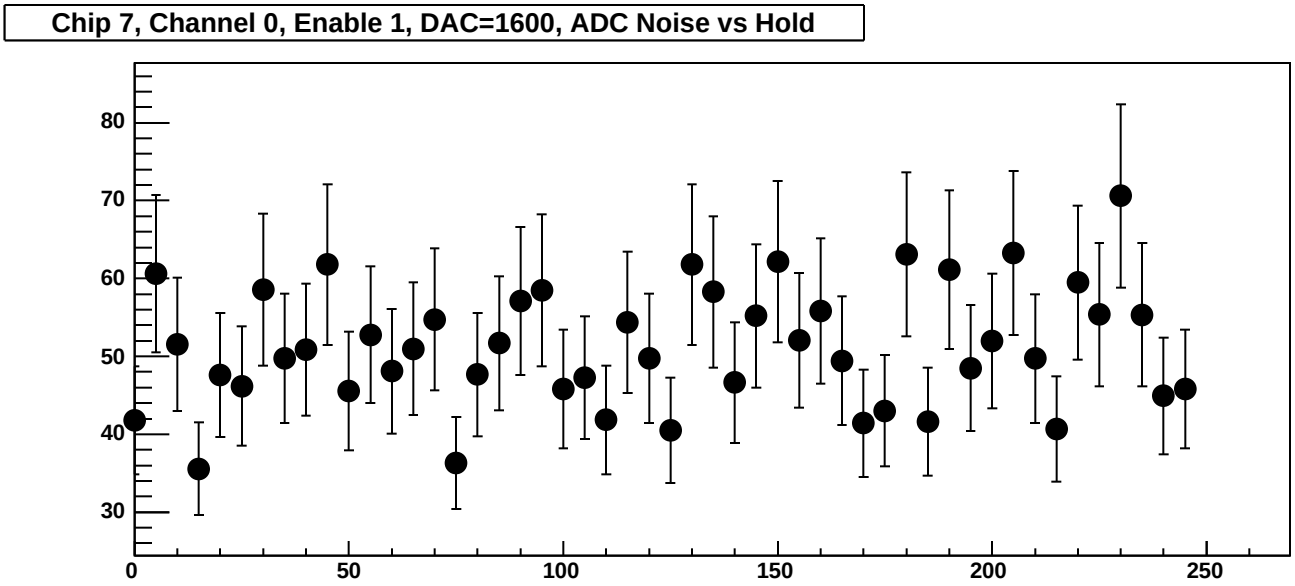

Chip 6, Channel 17, Enable 5, DAC=1600, ADC Residuals vs Hold
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

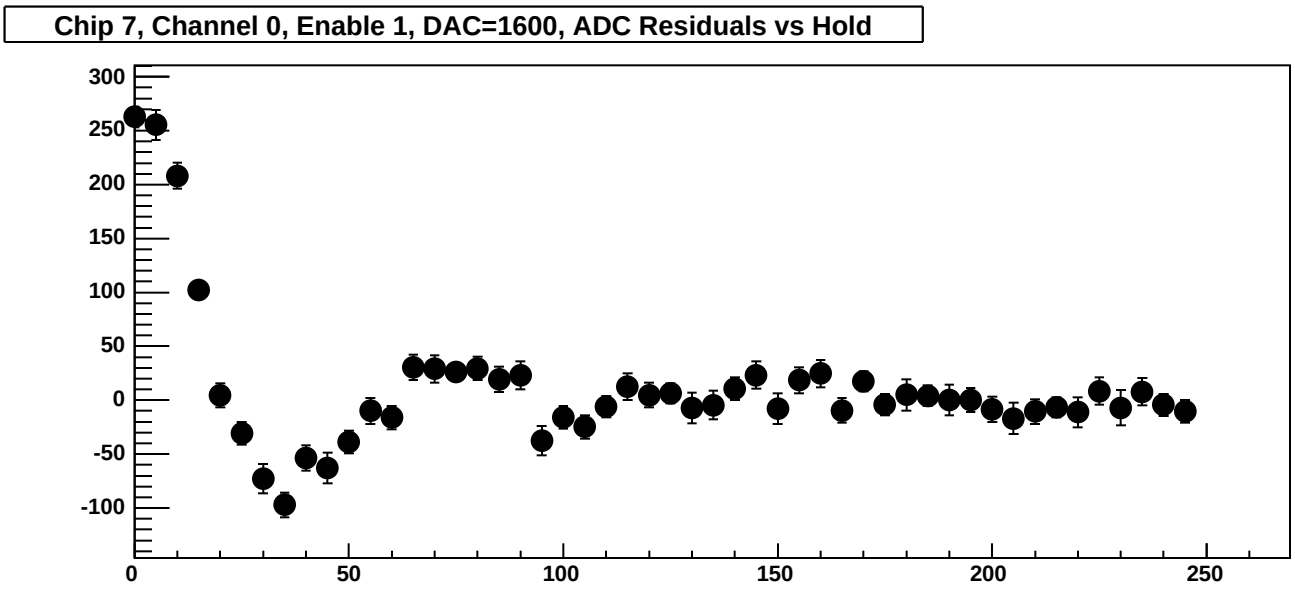

Chip 7, Channel 0, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 8.201e+05 / 41
p0 3177 ± 0.209
p1 108.2 ± 9.46
p2 1.034e+04 ± 0.2693
p3 -27.12 ± 0.1619
p4 -0.4901 ± 0.002907
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250


Chip 7, Channel 0, Enable 0, DAC=1600, ADC Noise vs Hold
180
160
140
120
100
80
60
0
50
100
150
200
250

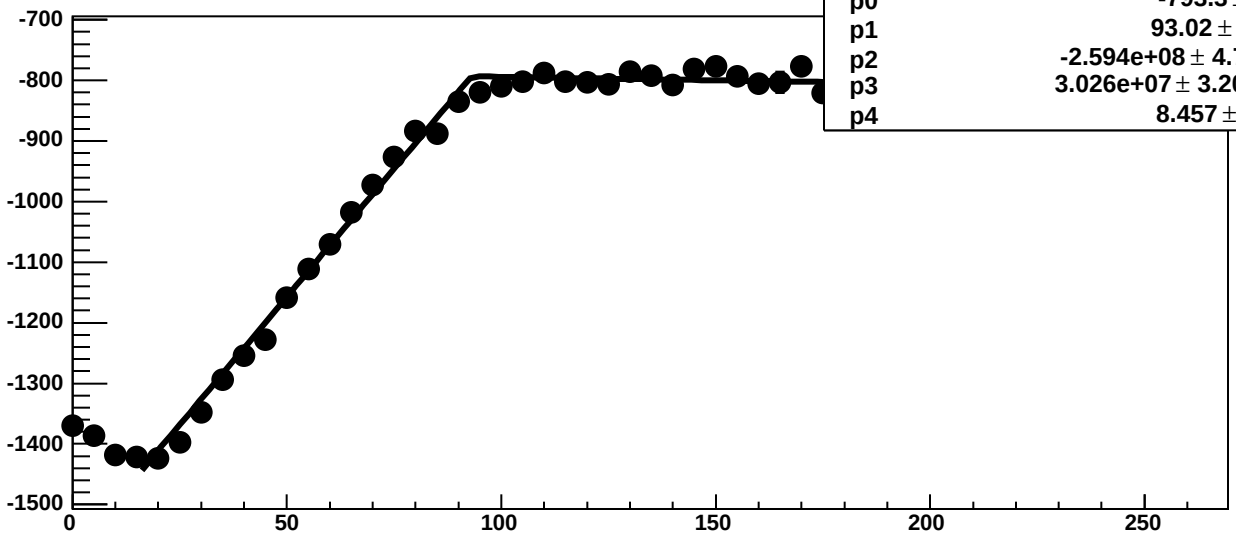

Chip 7, Channel 0, Enable 0, DAC=1600, ADC Residuals vs Hold
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250


Chip 7, Channel 0, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 398.6 / 41
p0 -677.6 ± 3.784
p1 97.06 ± 0.6278
p2 -2.366e+08 ± 7.491e+06
p3 2.161e+07 ± 6.443e+05
p4 9.82 ± 0.1074
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250

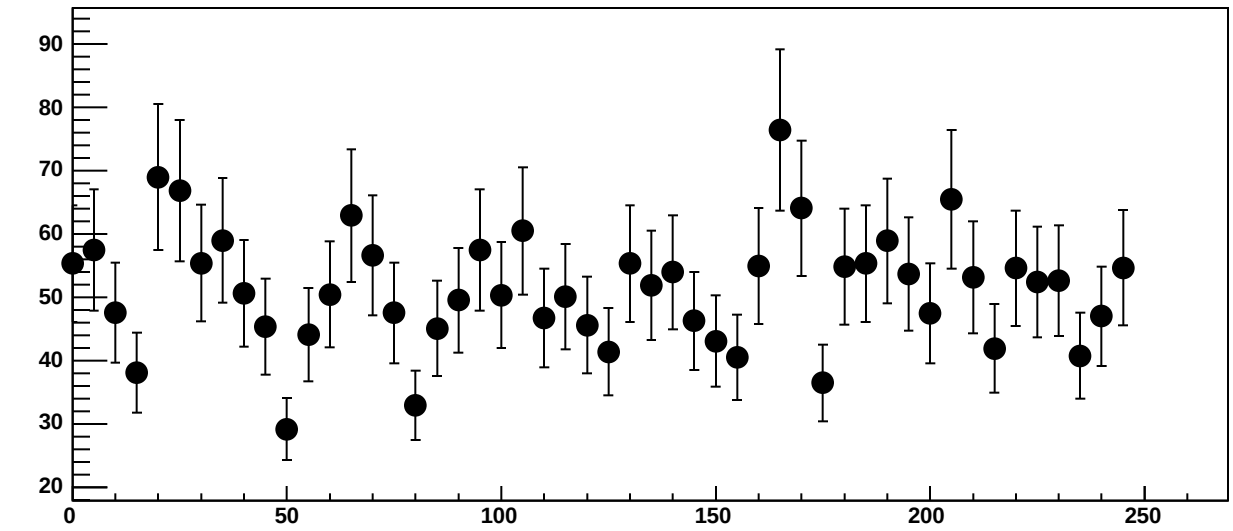

Chip 7, Channel 0, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 7, Channel 0, Enable 1, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

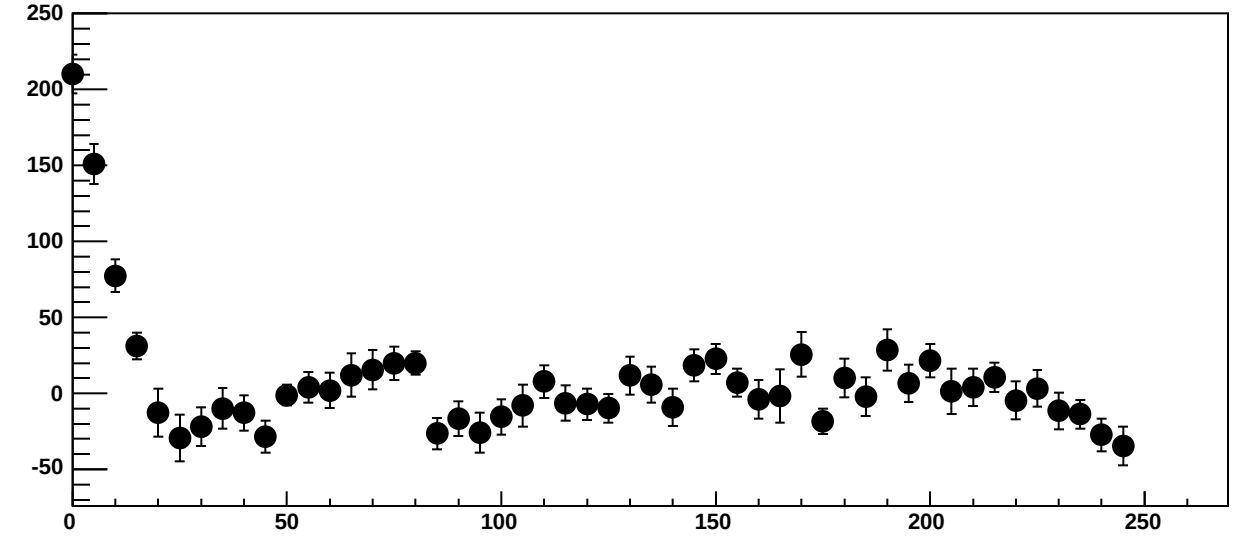
Chip 7, Channel 0, Enable 2, DAC=1600, ADC Mean vs Hold

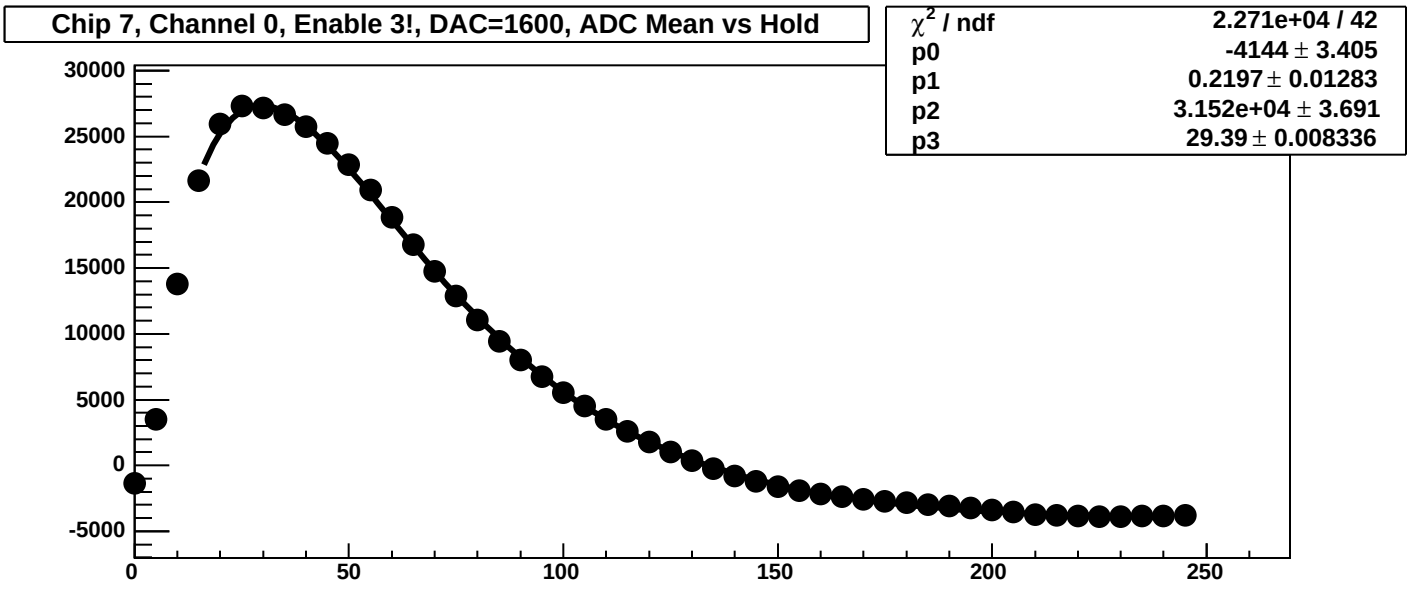


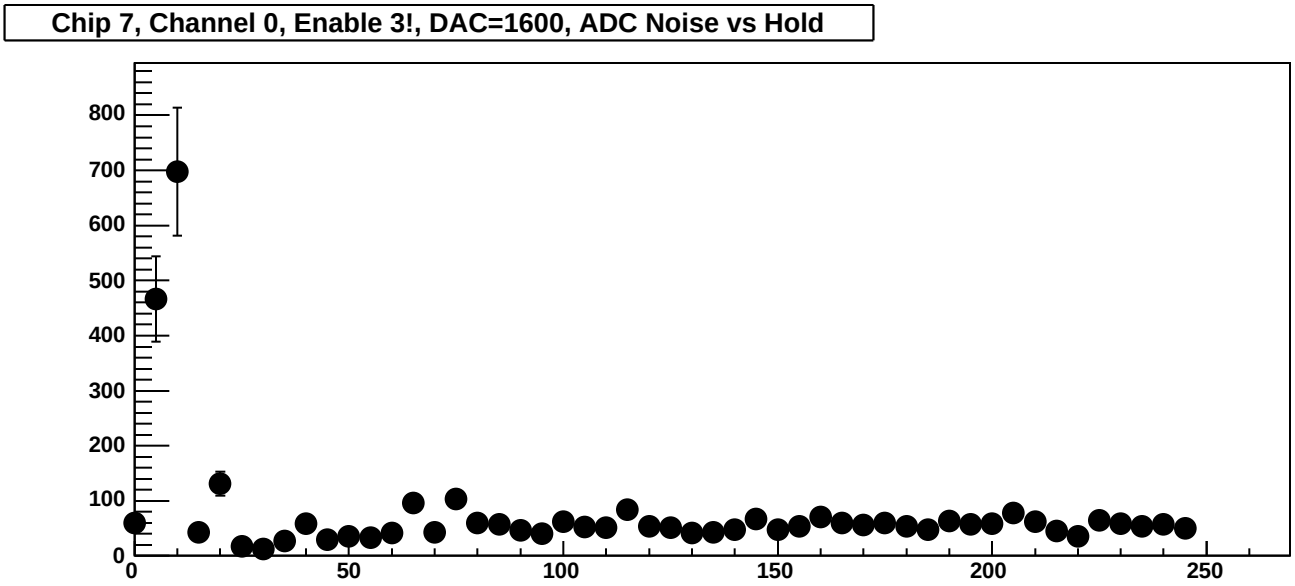
Chip 7, Channel 0, Enable 2, DAC=1600, ADC Noise vs Hold

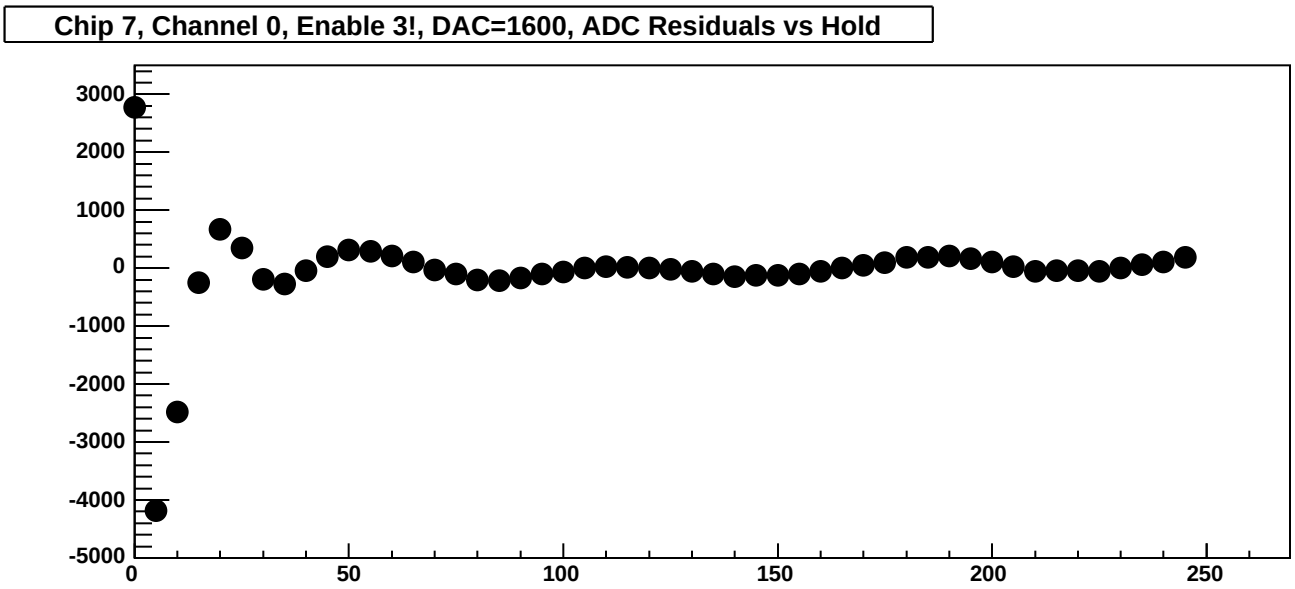


Chip 7, Channel 0, Enable 2, DAC=1600, ADC Residuals vs Hold

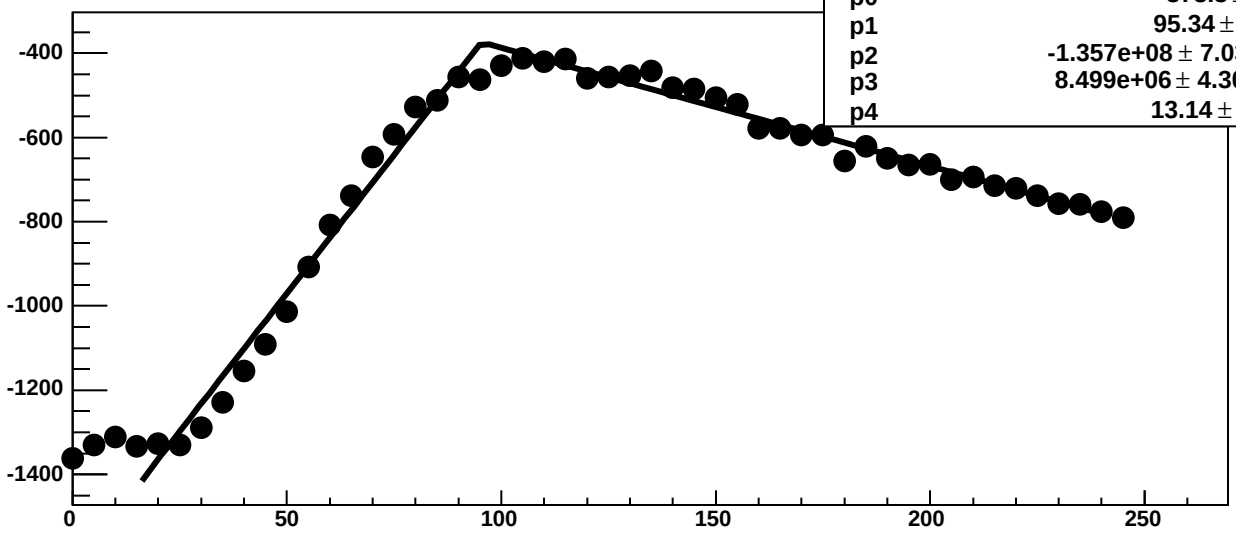



Chip 7, Channel 0, Enable 3!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.271e+04 / 42
p0 -4144 ± 3.405
p1 0.2197 ± 0.01283
p2 3.152e+04 ± 3.691
p3 29.39 ± 0.008336
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

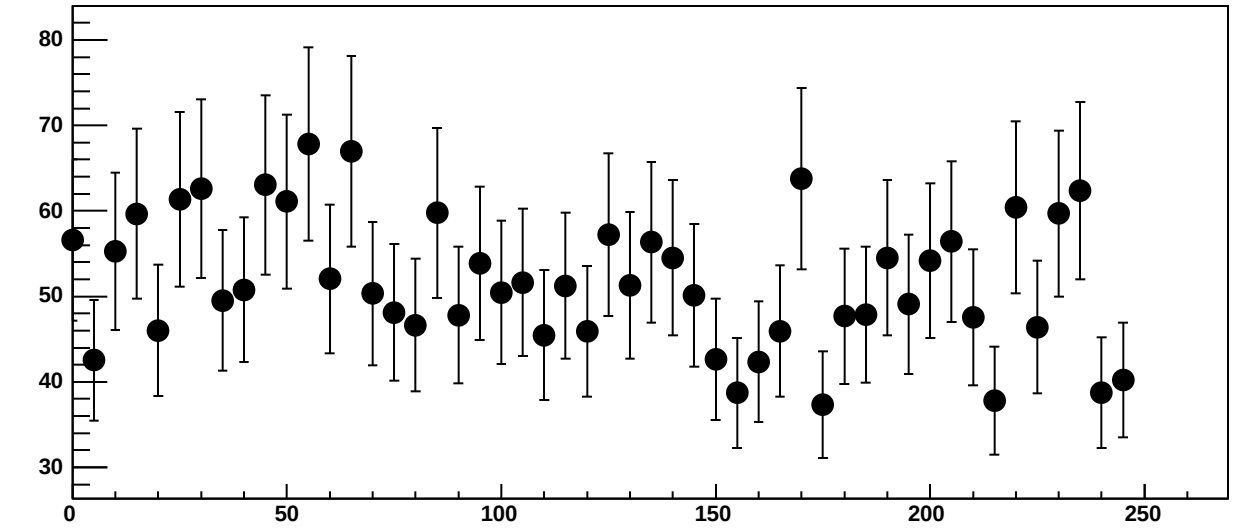

Chip 7, Channel 0, Enable 3!, DAC=1600, ADC Noise vs Hold
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 7, Channel 0, Enable 3!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

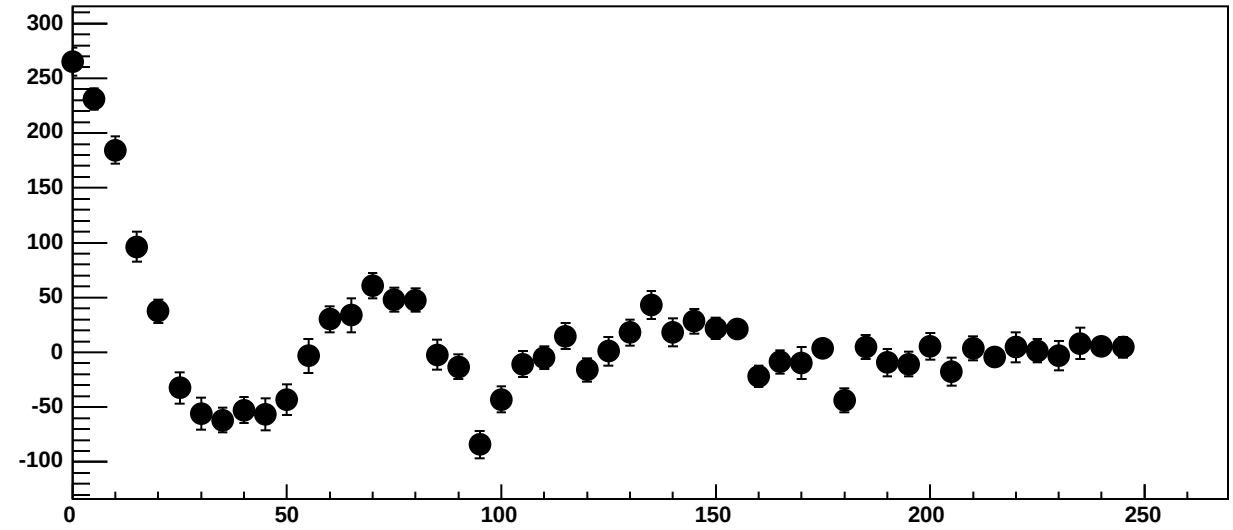
Chip 7, Channel 0, Enable 4, DAC=1600, ADC Mean vs Hold



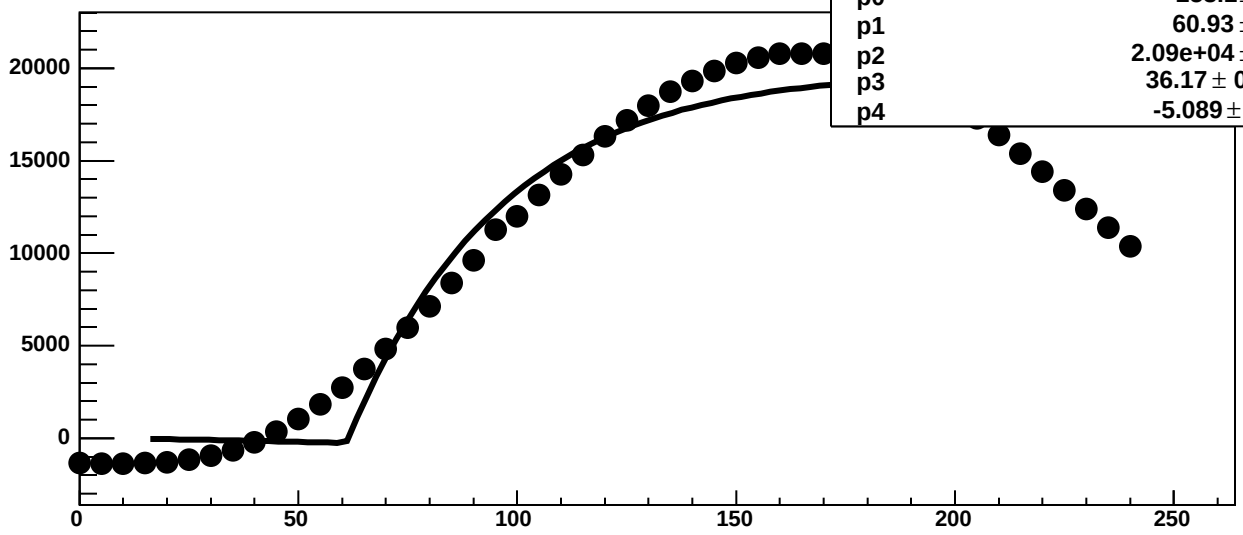
Chip 7, Channel 0, Enable 4, DAC=1600, ADC Noise vs Hold



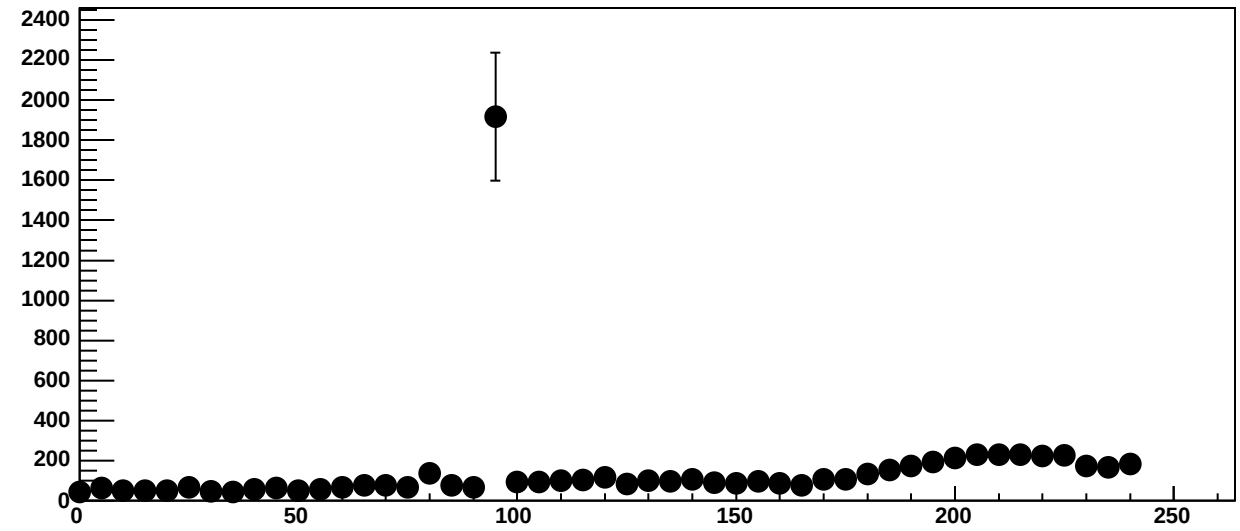
Chip 7, Channel 0, Enable 4, DAC=1600, ADC Residuals vs Hold



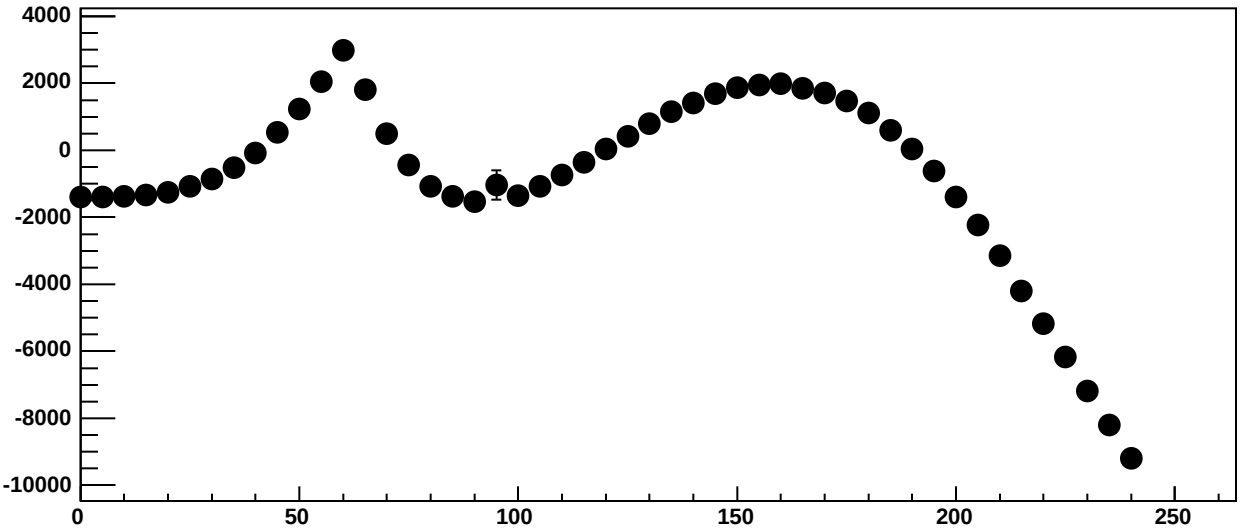
Chip 7, Channel 0, Enable 5, DAC=1600, ADC Mean vs Hold



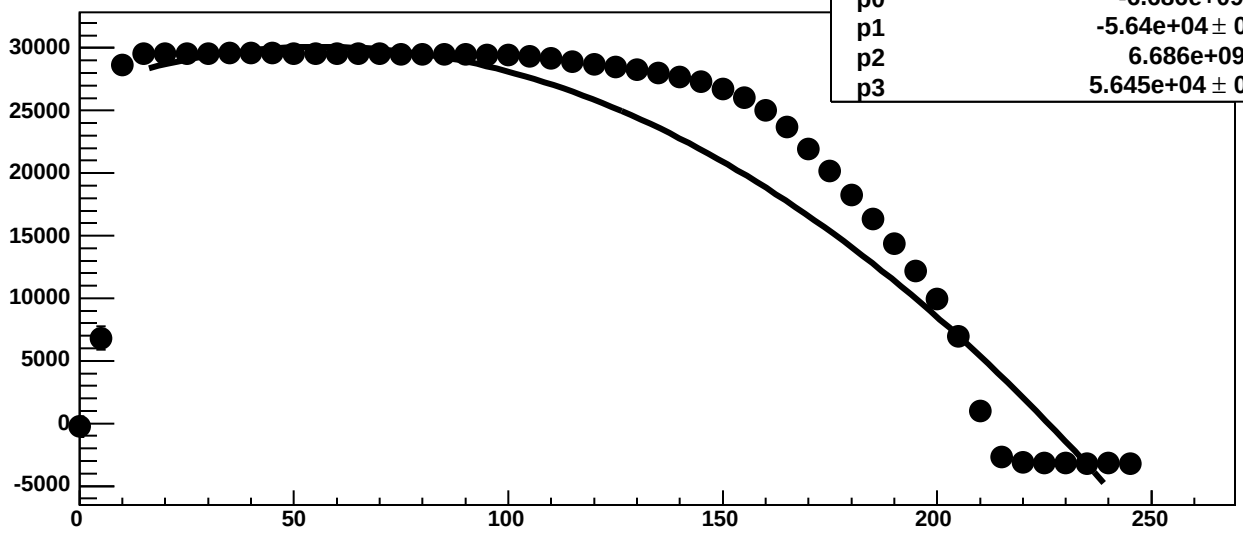
Chip 7, Channel 0, Enable 5, DAC=1600, ADC Noise vs Hold



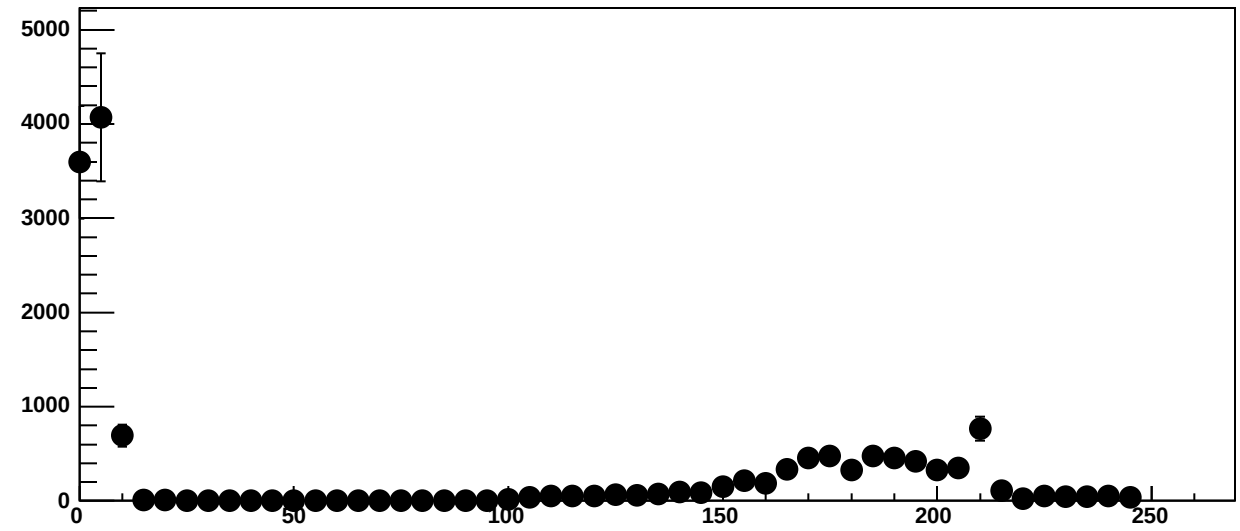
Chip 7, Channel 0, Enable 5, DAC=1600, ADC Residuals vs Hold



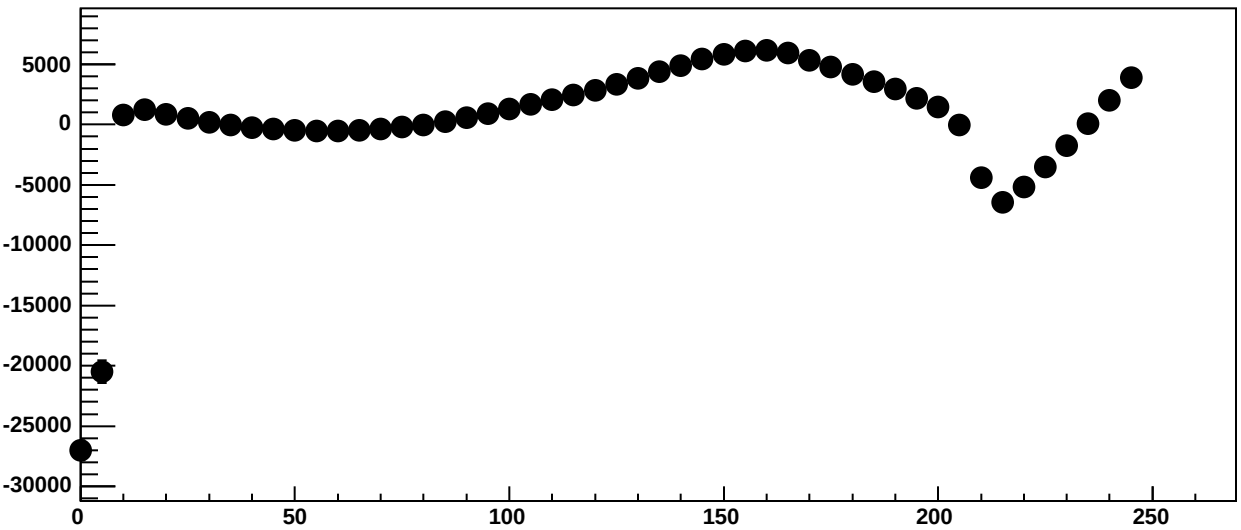
Chip 7, Channel 1, Enable 0!, DAC=1600, ADC Mean vs Hold



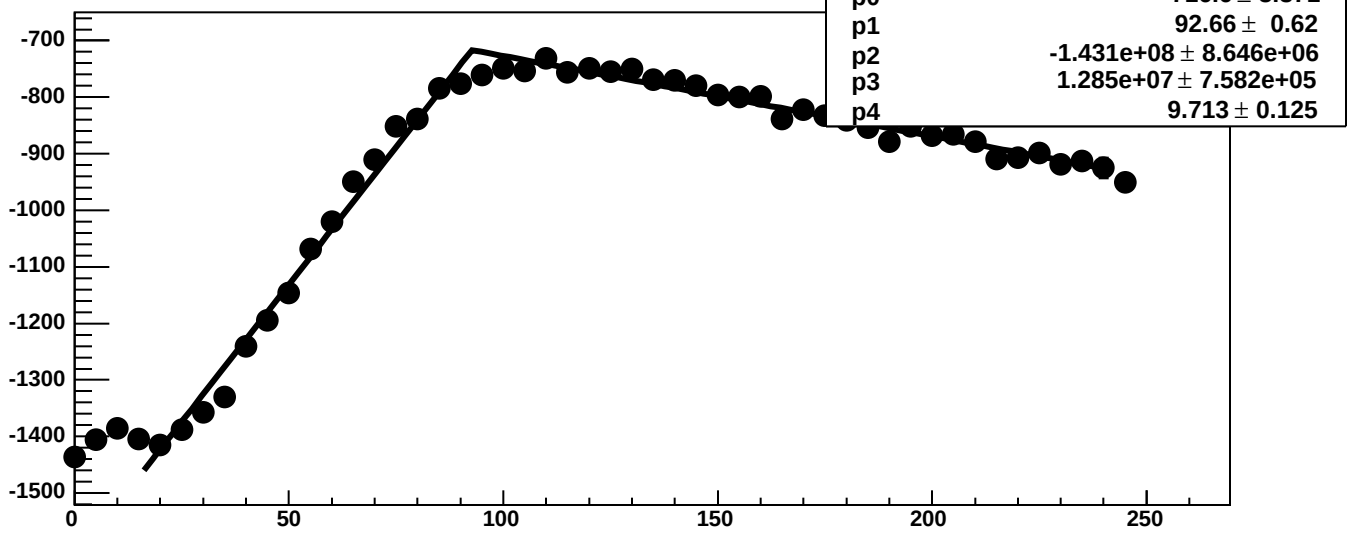
Chip 7, Channel 1, Enable 0!, DAC=1600, ADC Noise vs Hold



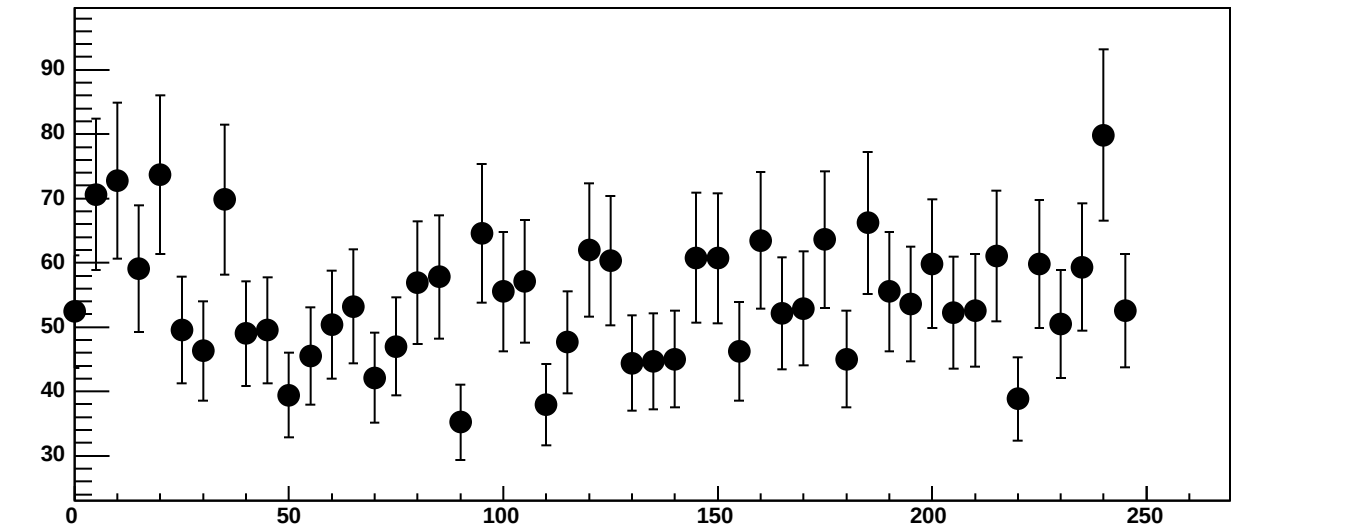
Chip 7, Channel 1, Enable 0!, DAC=1600, ADC Residuals vs Hold



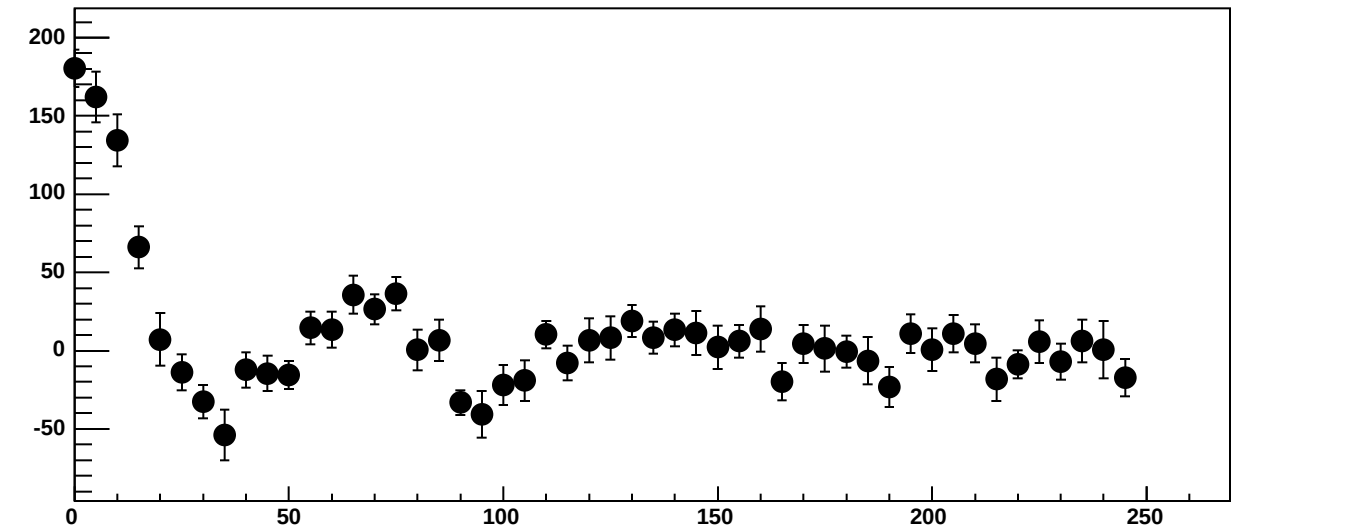
Chip 7, Channel 1, Enable 1, DAC=1600, ADC Mean vs Hold

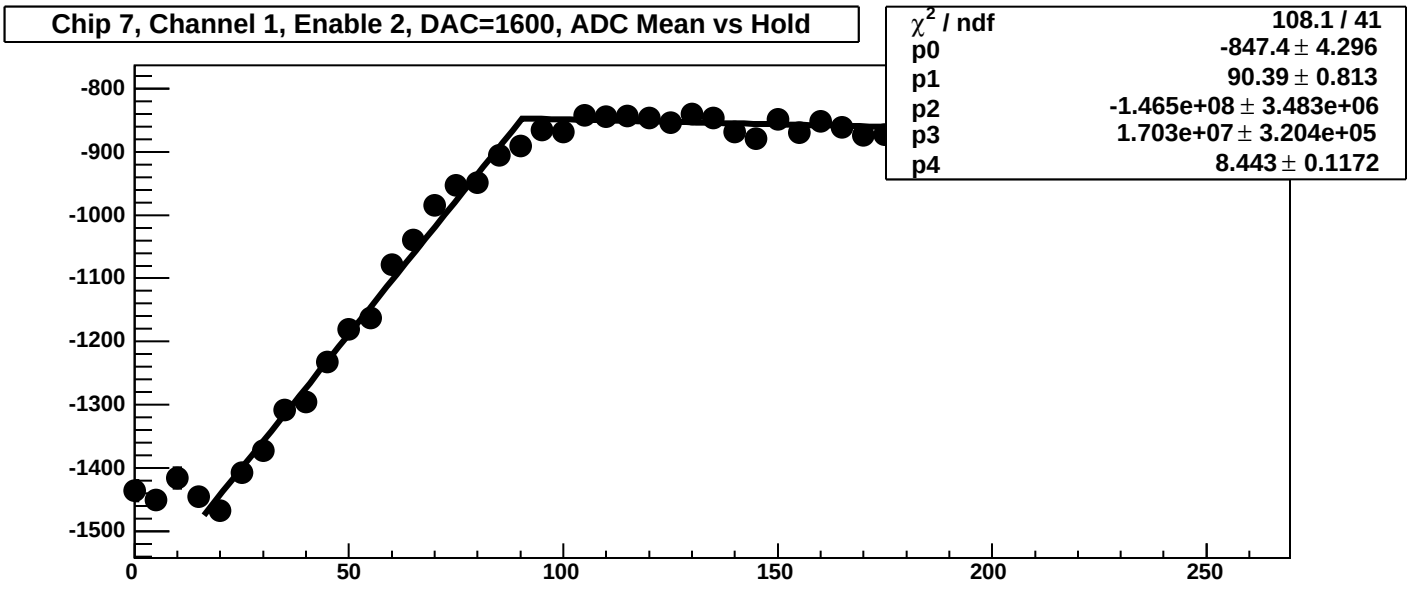


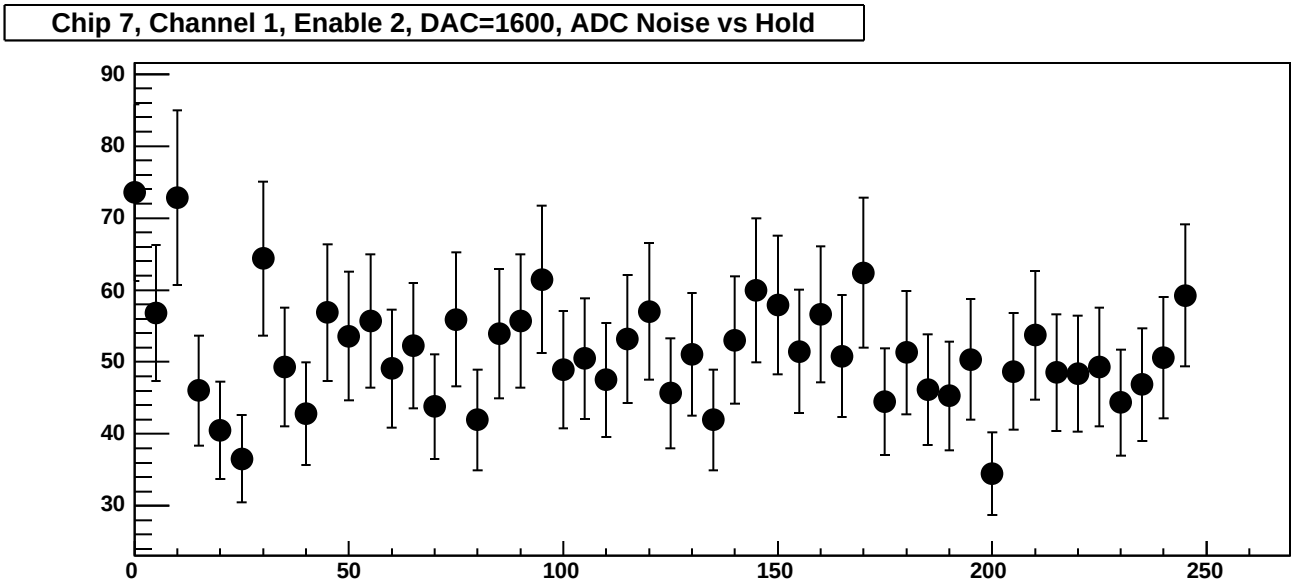
Chip 7, Channel 1, Enable 1, DAC=1600, ADC Noise vs Hold

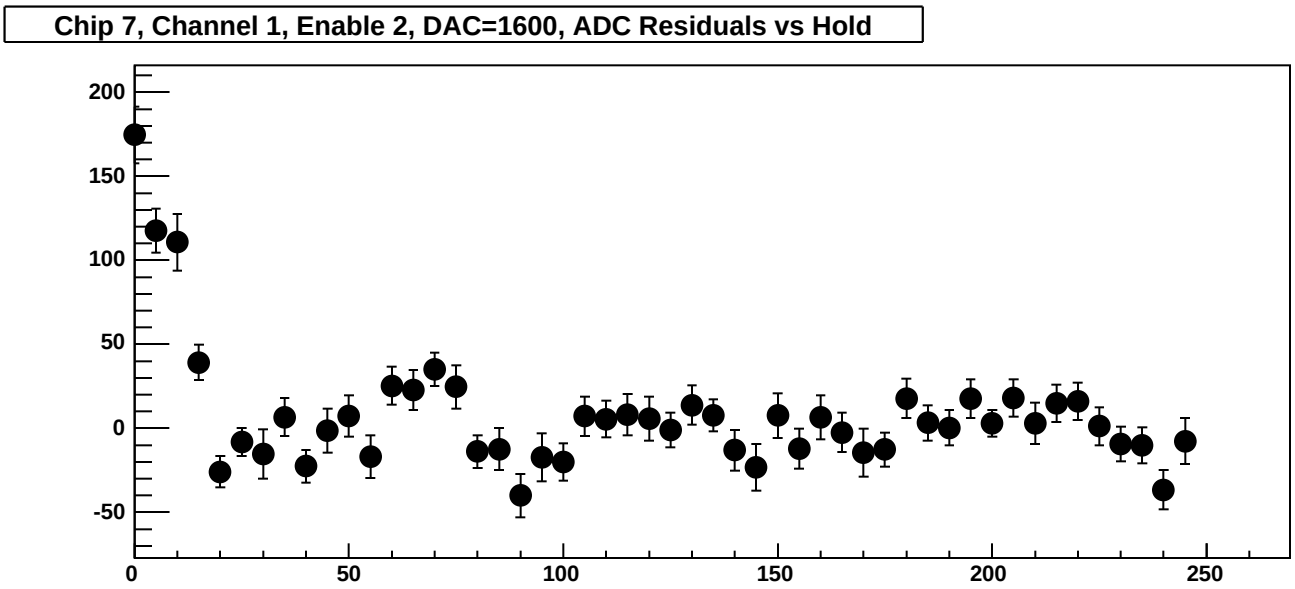


Chip 7, Channel 1, Enable 1, DAC=1600, ADC Residuals vs Hold

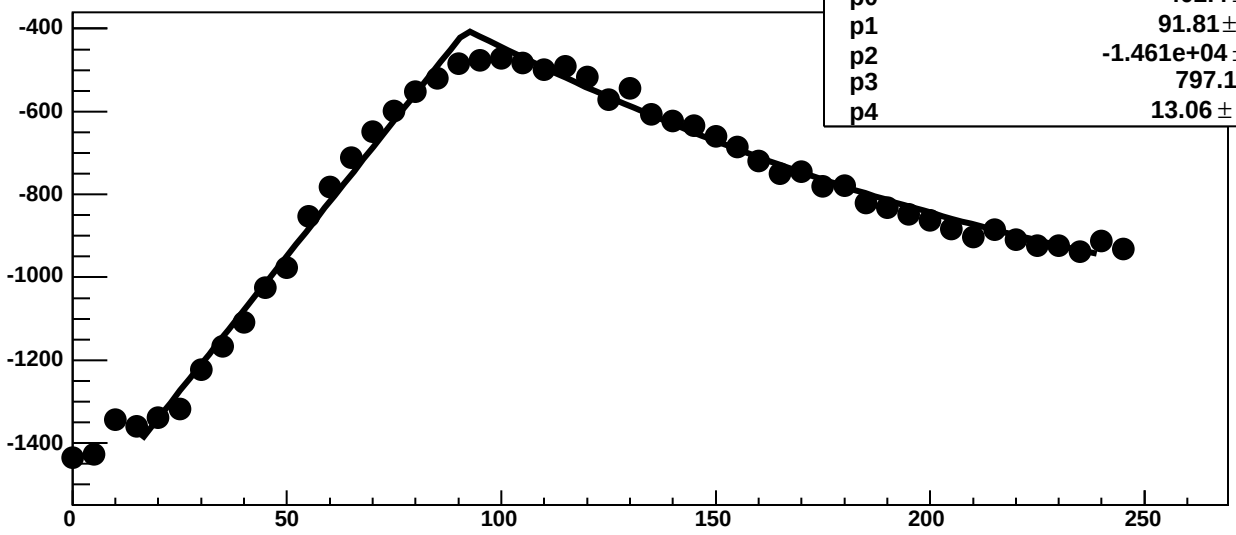



Chip 7, Channel 1, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 108.1 / 41
p0 -847.4 ± 4.296
p1 90.39 ± 0.813
p2 -1.465e+08 ± 3.483e+06
p3 1.703e+07 ± 3.204e+05
p4 8.443 ± 0.1172
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250

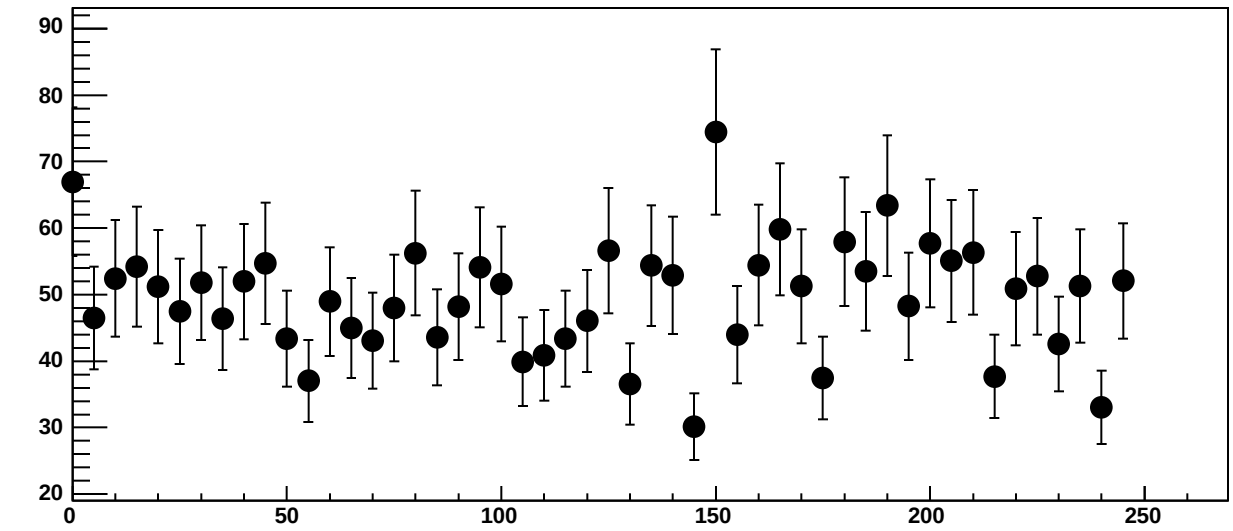

Chip 7, Channel 1, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 7, Channel 1, Enable 2, DAC=1600, ADC Residuals vs Hold
200
150
100
50
0
-50
0
50
100
150
200
250

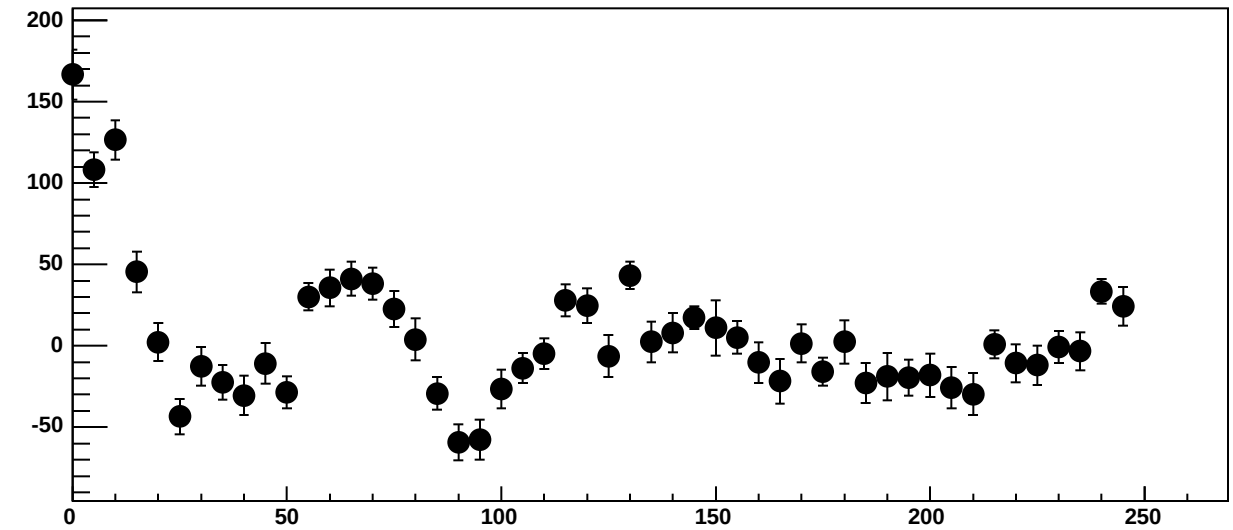
Chip 7, Channel 1, Enable 3, DAC=1600, ADC Mean vs Hold

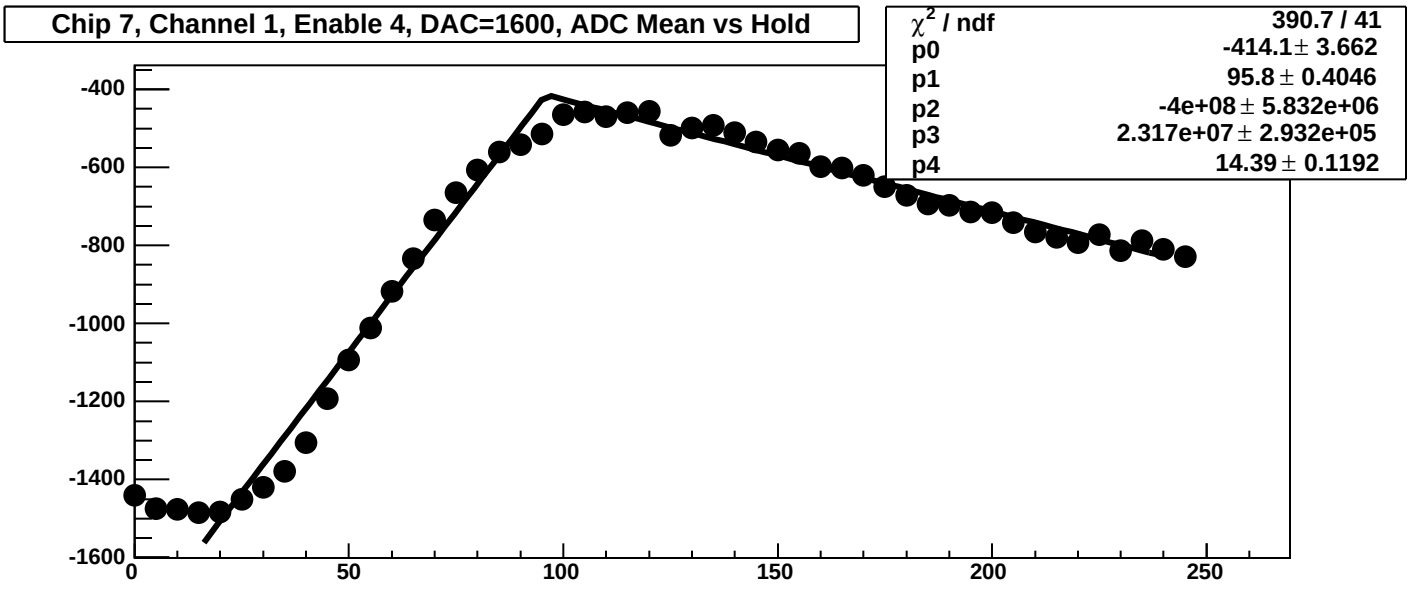


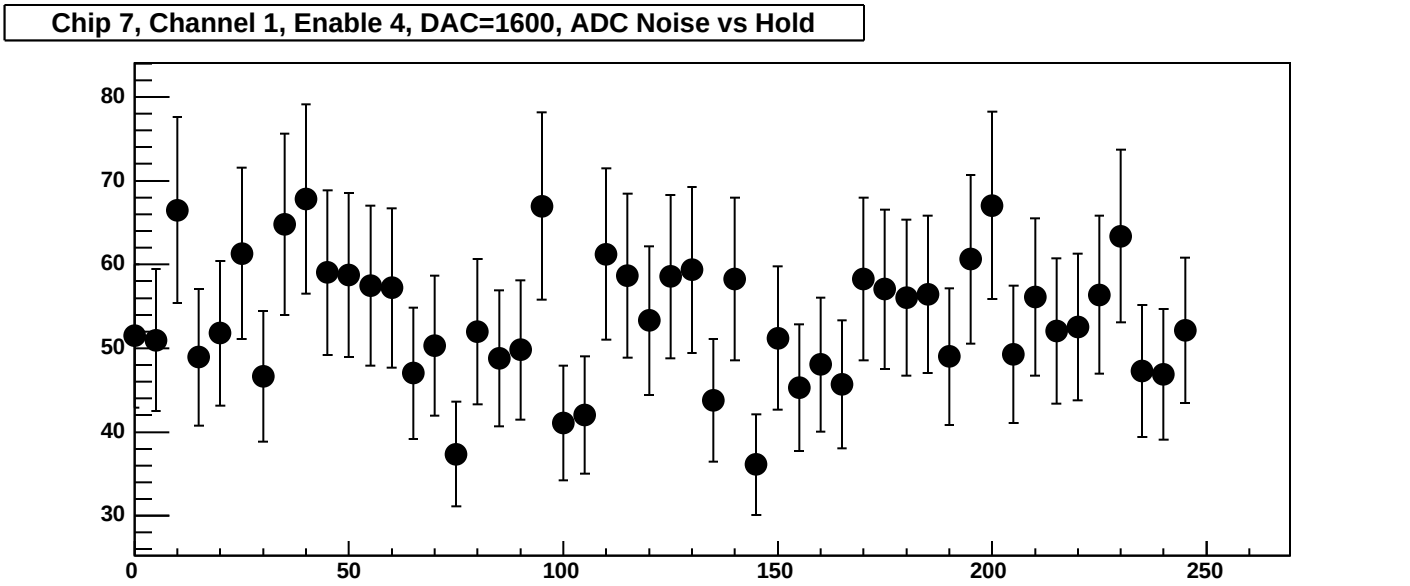
Chip 7, Channel 1, Enable 3, DAC=1600, ADC Noise vs Hold

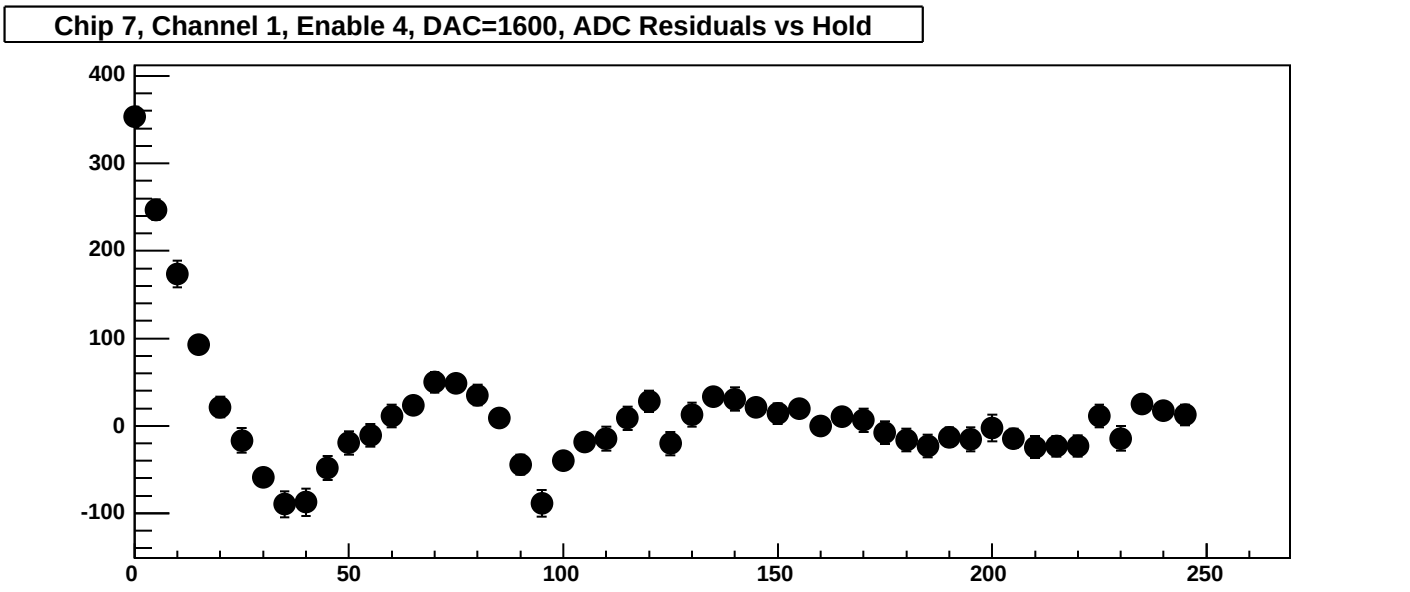


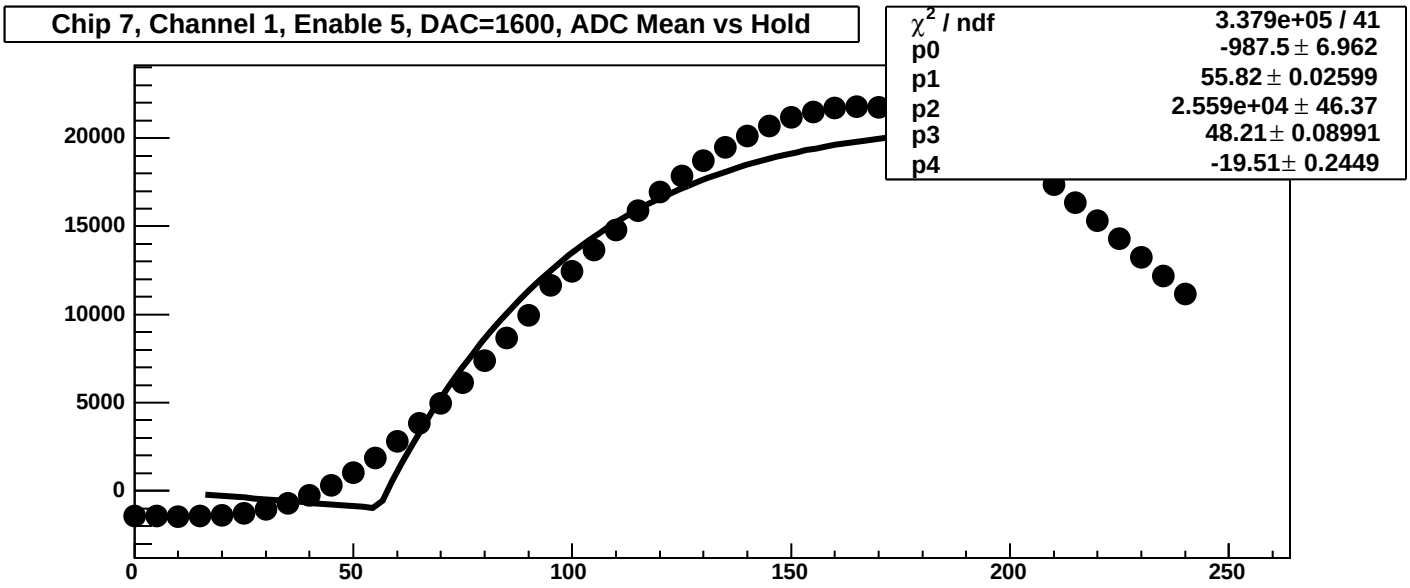
Chip 7, Channel 1, Enable 3, DAC=1600, ADC Residuals vs Hold

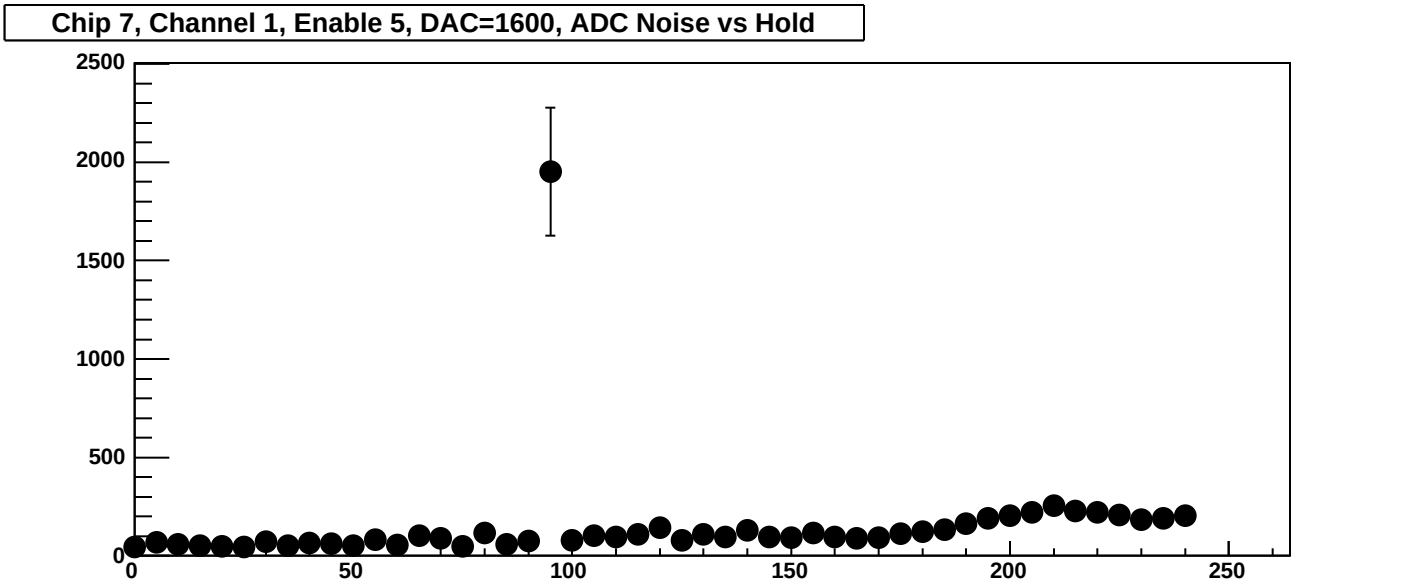


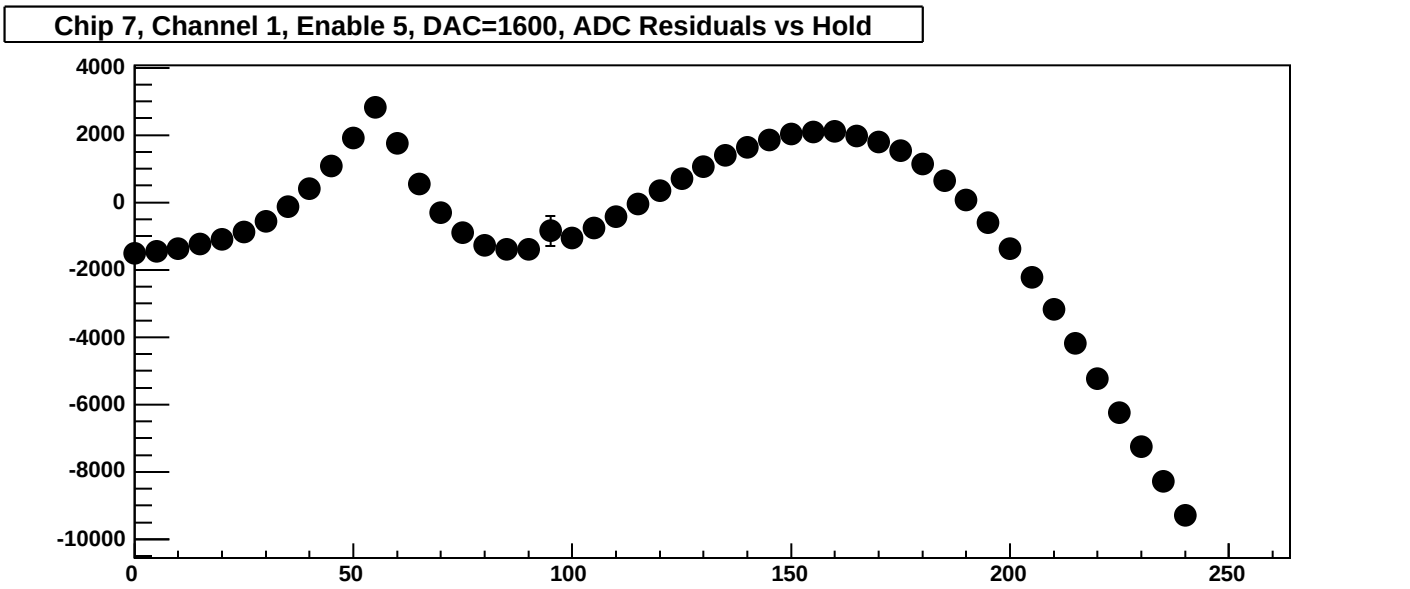

Chip 7, Channel 1, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 390.7 / 41
p0 -414.1 ± 3.662
p1 95.8 ± 0.4046
p2 -4e+08 ± 5.832e+06
p3 2.317e+07 ± 2.932e+05
p4 14.39 ± 0.1192
-400
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250


Chip 7, Channel 1, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

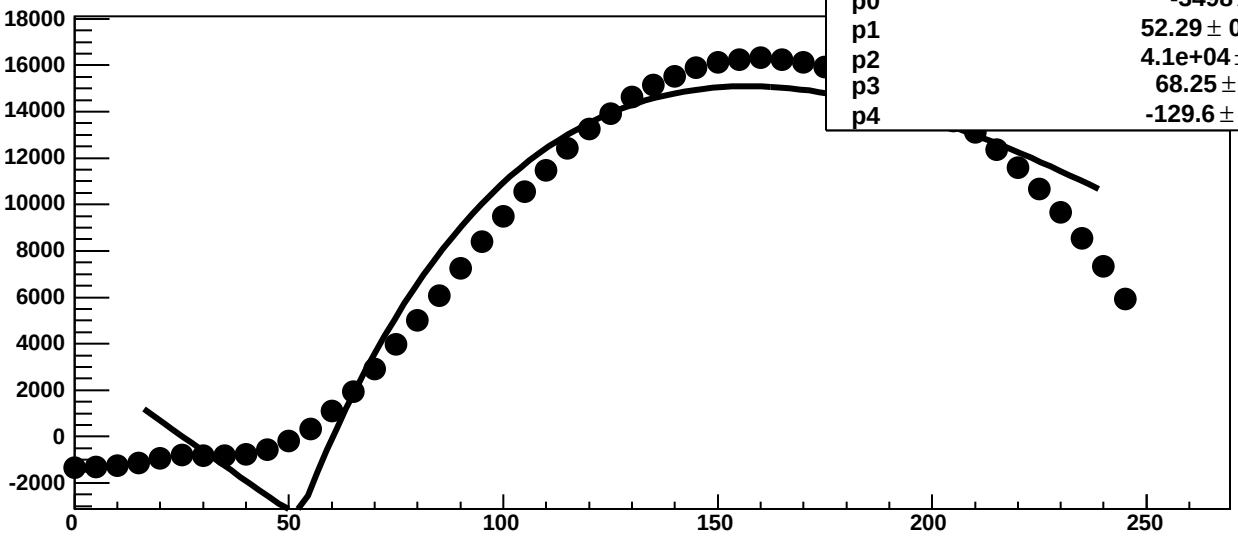

Chip 7, Channel 1, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 7, Channel 1, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.379e+05 / 41
p0 -987.5 ± 6.962
p1 55.82 ± 0.02599
p2 2.559e+04 ± 46.37
p3 48.21 ± 0.08991
p4 -19.51 ± 0.2449
20000
15000
10000
5000
0
0
50
100
150
200
250

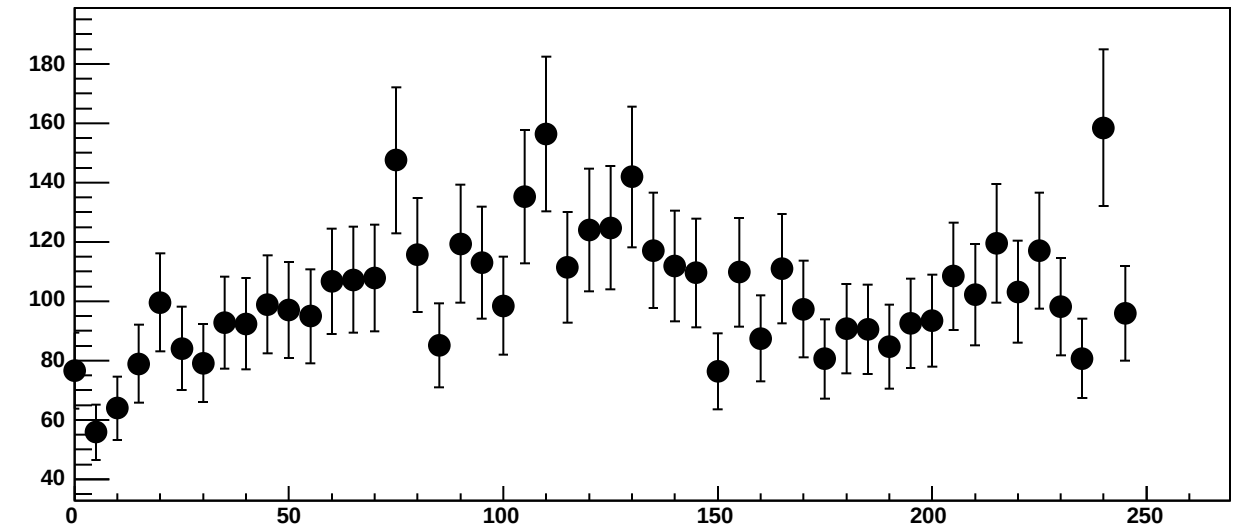

Chip 7, Channel 1, Enable 5, DAC=1600, ADC Noise vs Hold
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 7, Channel 1, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

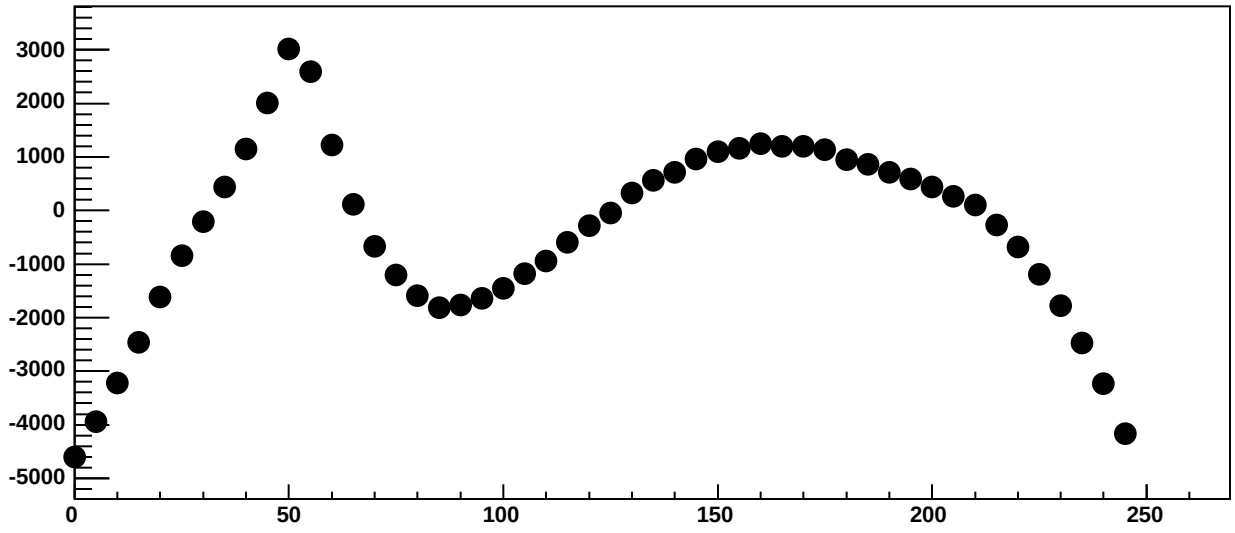
Chip 7, Channel 2, Enable 0, DAC=1600, ADC Mean vs Hold



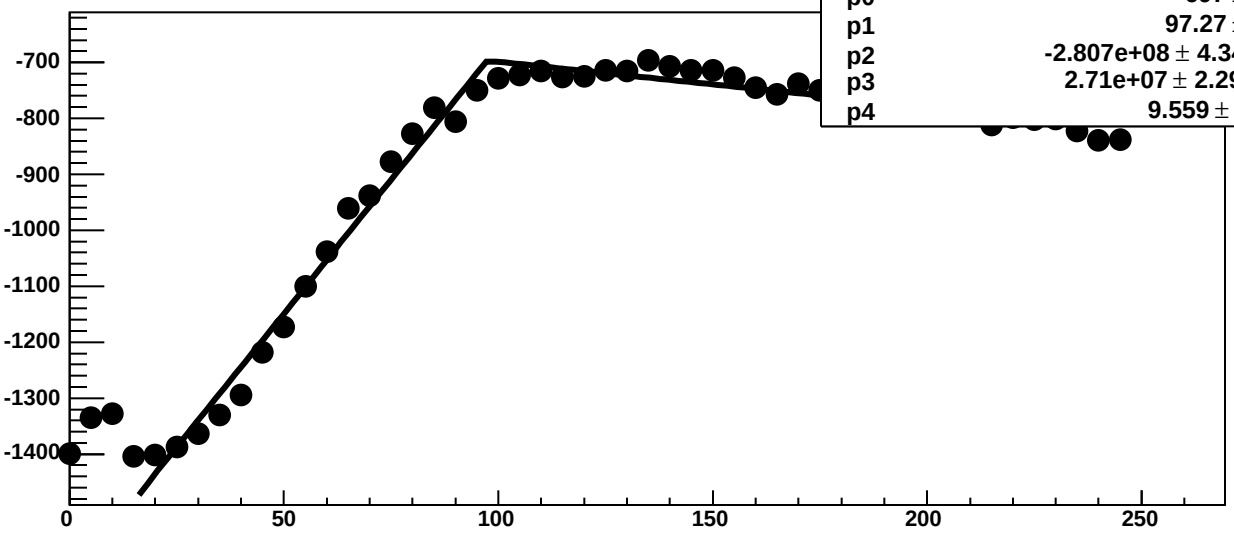
Chip 7, Channel 2, Enable 0, DAC=1600, ADC Noise vs Hold



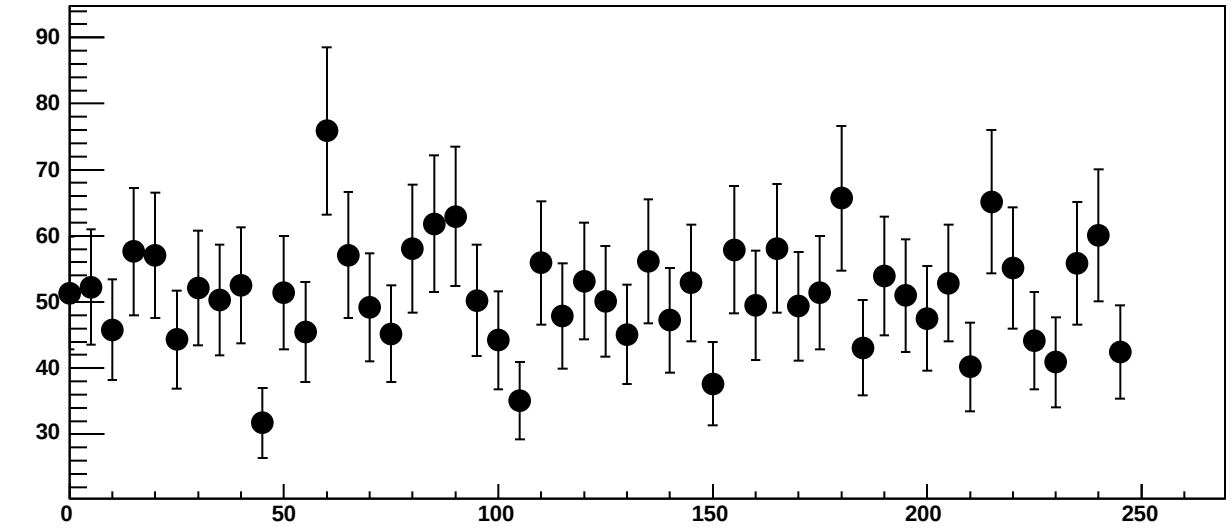
Chip 7, Channel 2, Enable 0, DAC=1600, ADC Residuals vs Hold



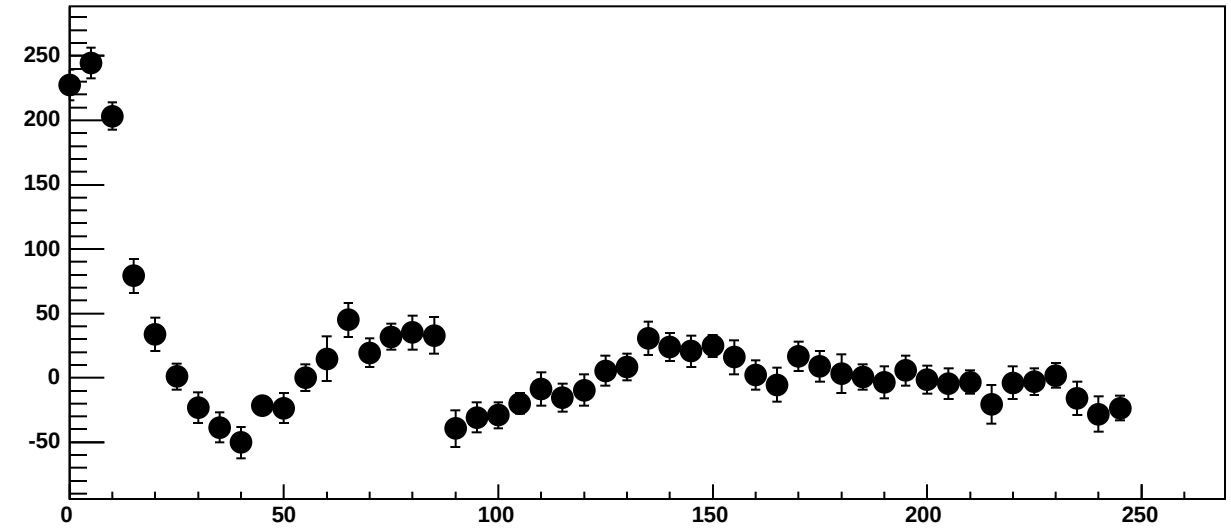
Chip 7, Channel 2, Enable 1, DAC=1600, ADC Mean vs Hold

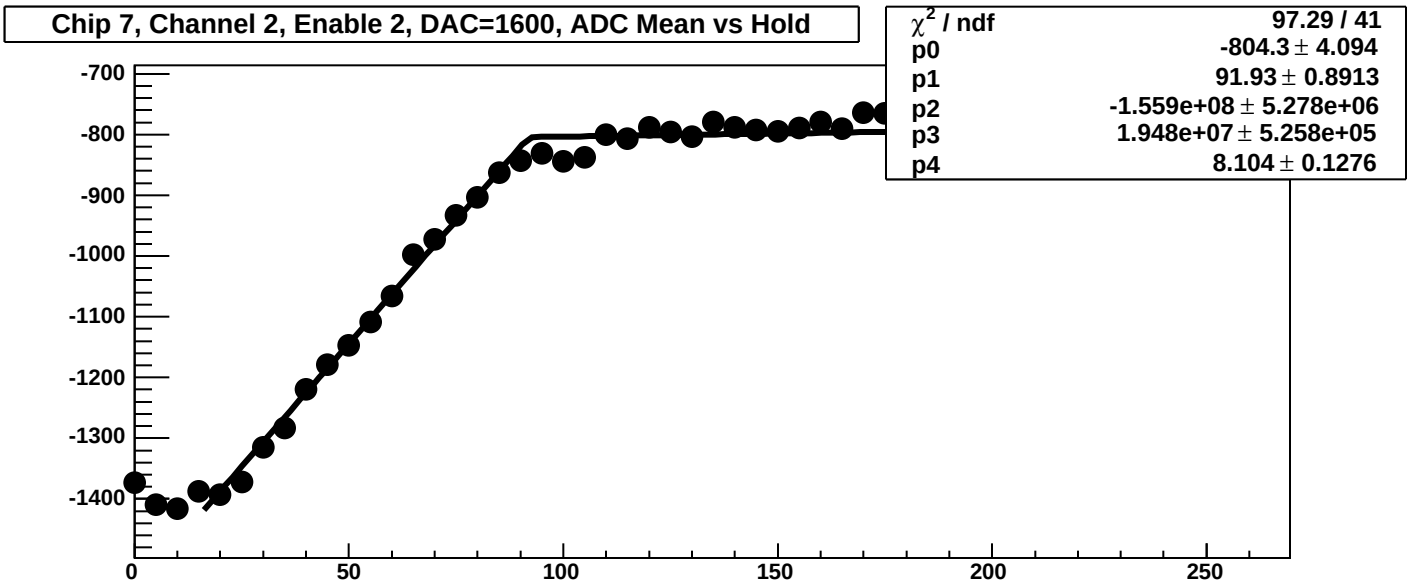


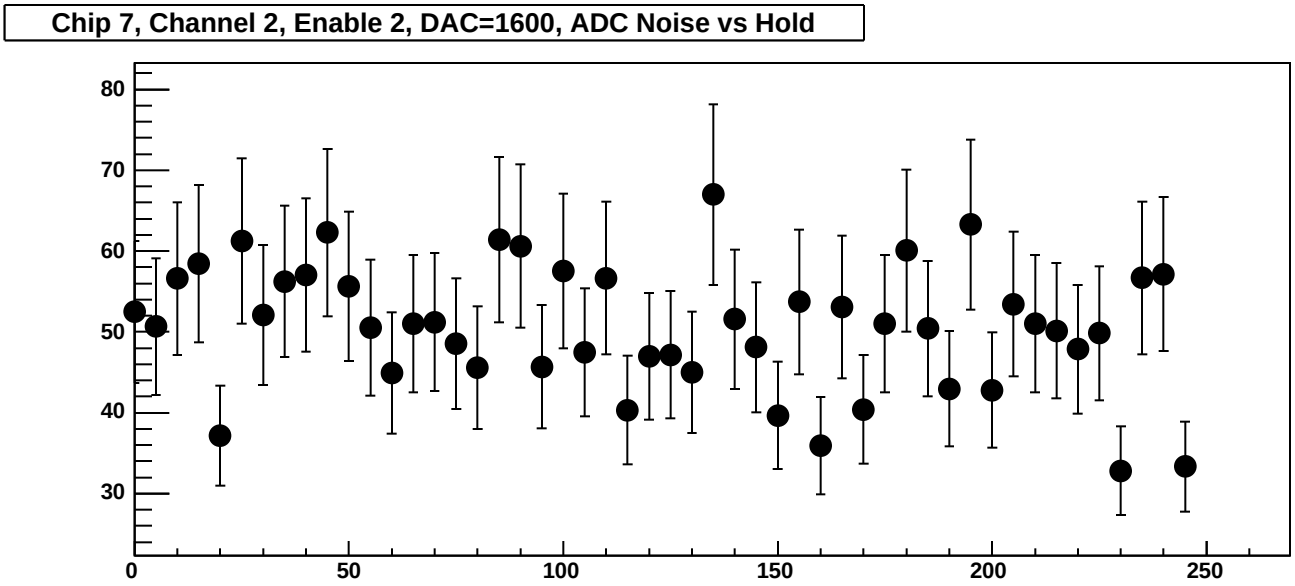
Chip 7, Channel 2, Enable 1, DAC=1600, ADC Noise vs Hold

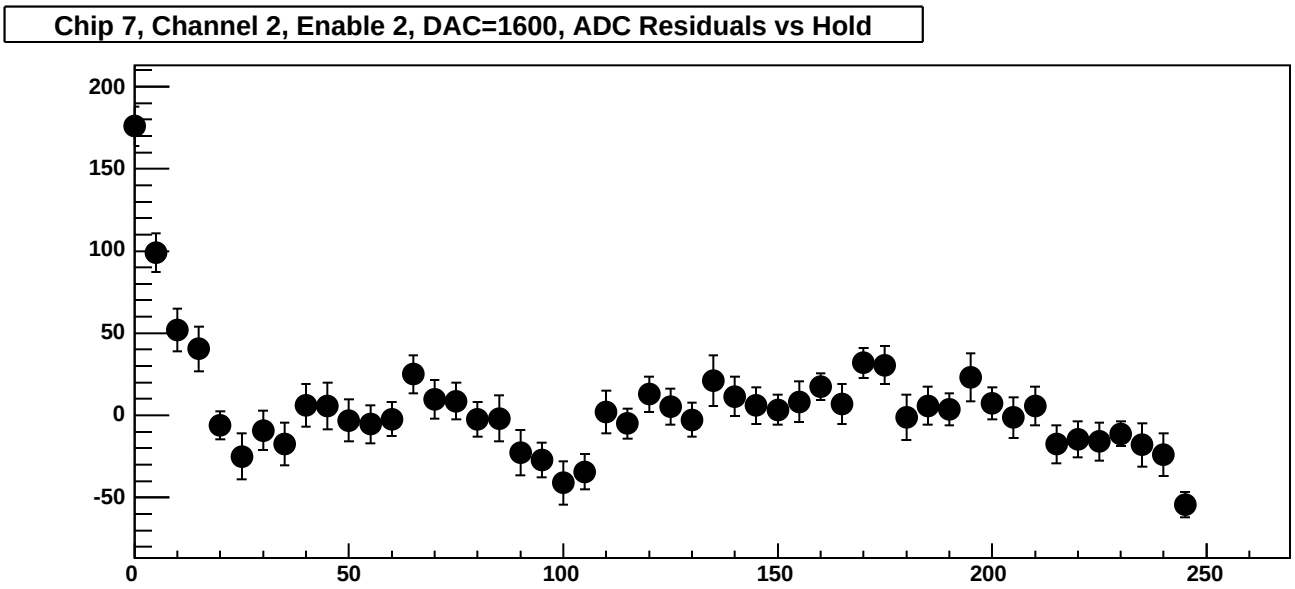


Chip 7, Channel 2, Enable 1, DAC=1600, ADC Residuals vs Hold

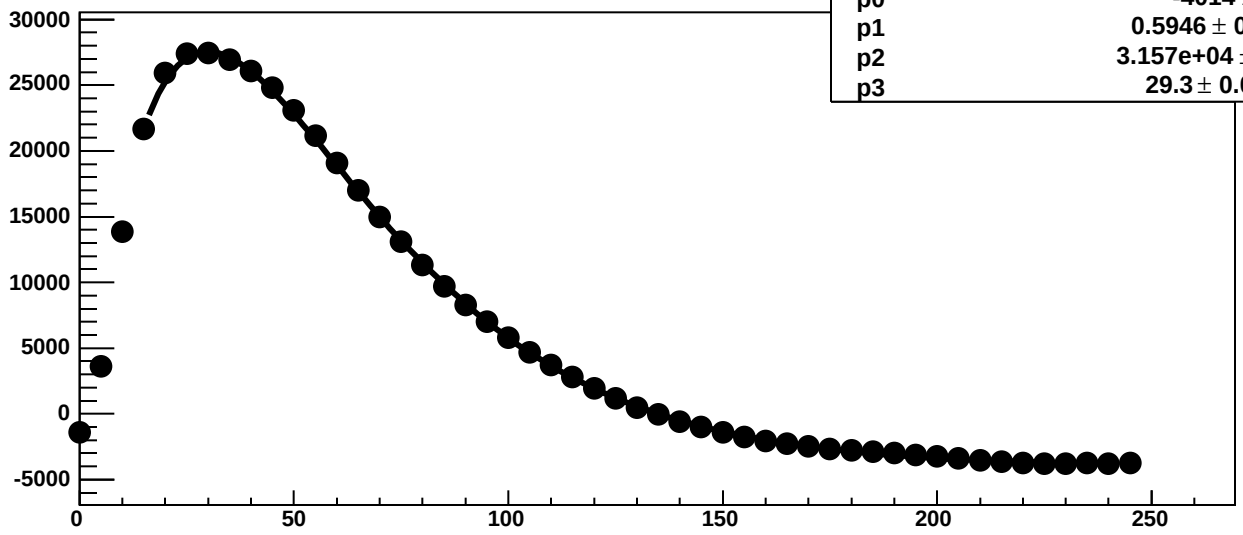



Chip 7, Channel 2, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 97.29 / 41
p0 -804.3 ± 4.094
p1 91.93 ± 0.8913
p2 -1.559e+08 ± 5.278e+06
p3 1.948e+07 ± 5.258e+05
p4 8.104 ± 0.1276
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
50
100
150
200
250

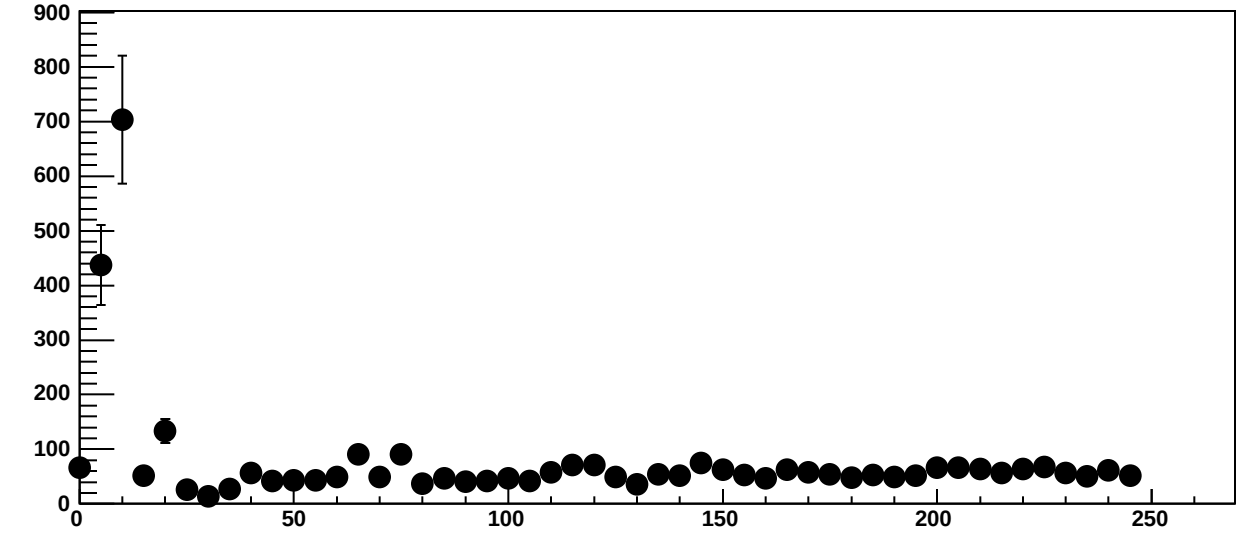

Chip 7, Channel 2, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 7, Channel 2, Enable 2, DAC=1600, ADC Residuals vs Hold
200
150
100
50
0
-50
0
50
100
150
200
250

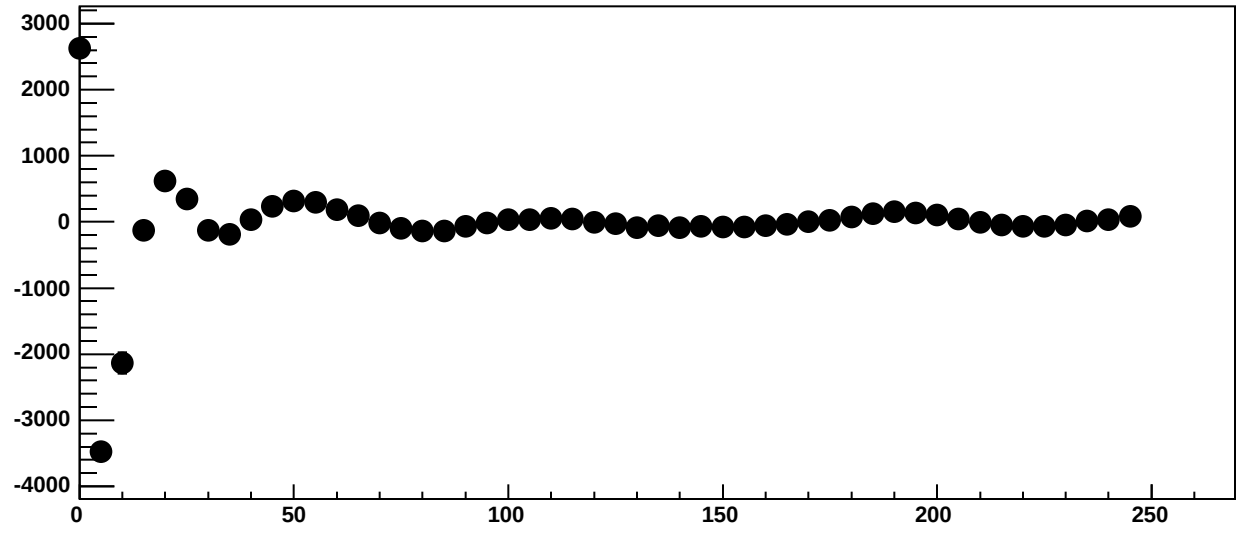
Chip 7, Channel 2, Enable 3!, DAC=1600, ADC Mean vs Hold

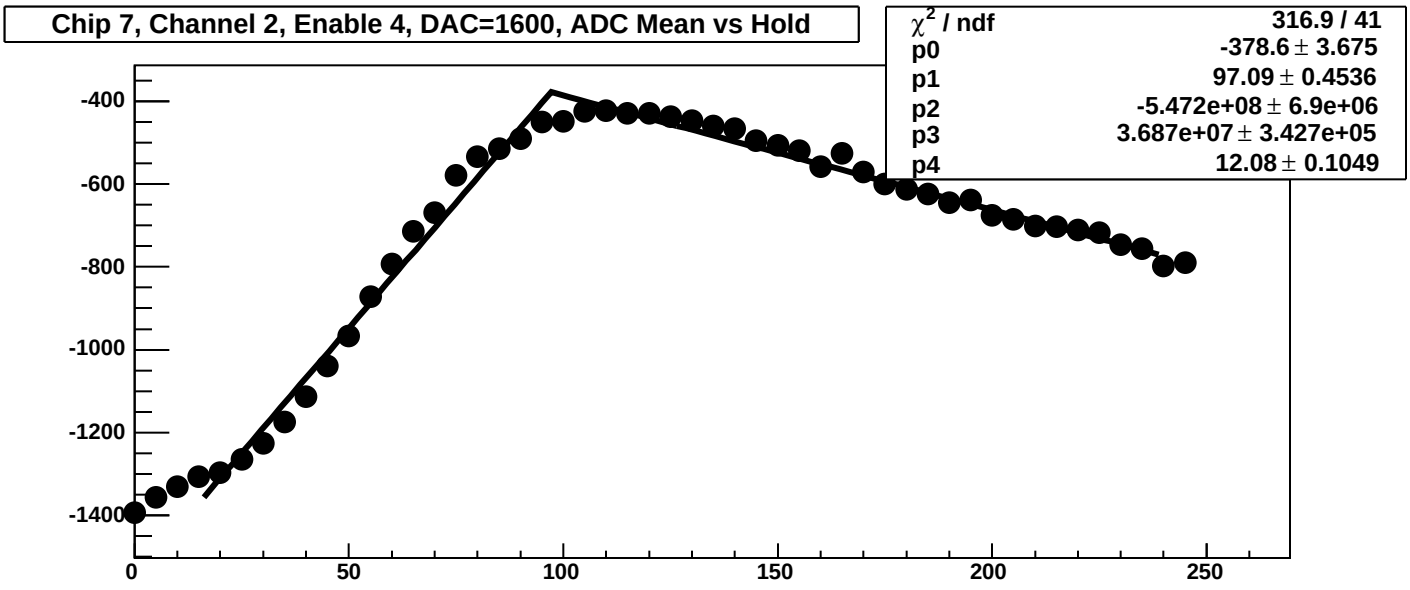


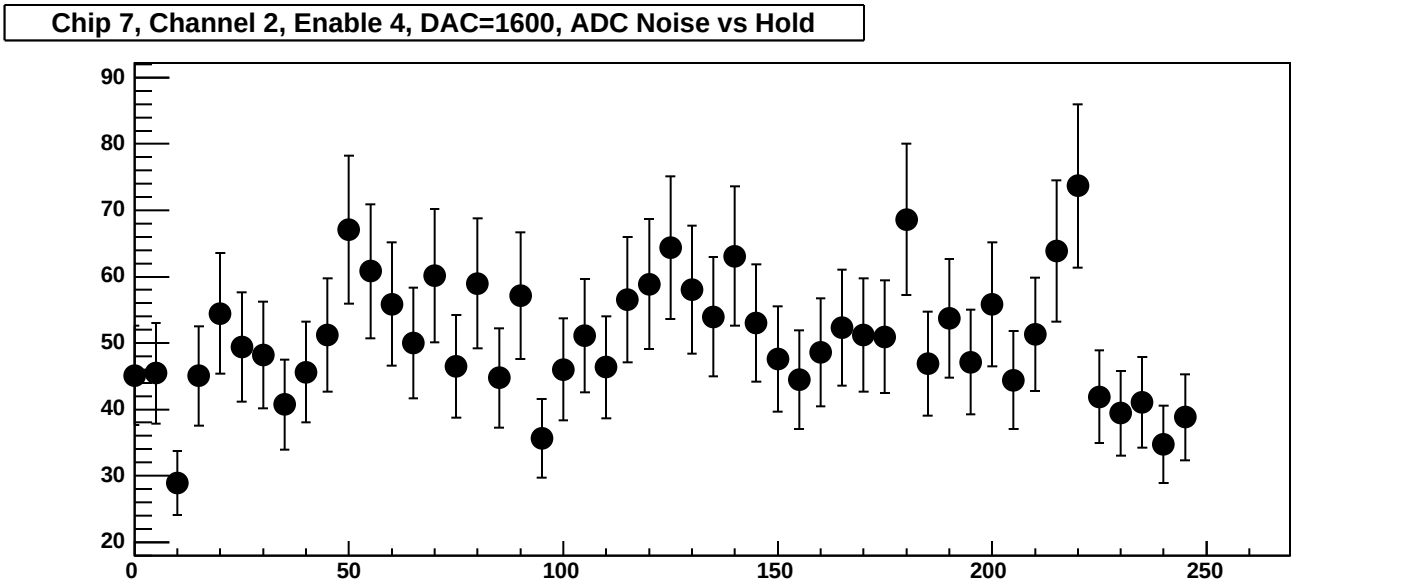
Chip 7, Channel 2, Enable 3!, DAC=1600, ADC Noise vs Hold

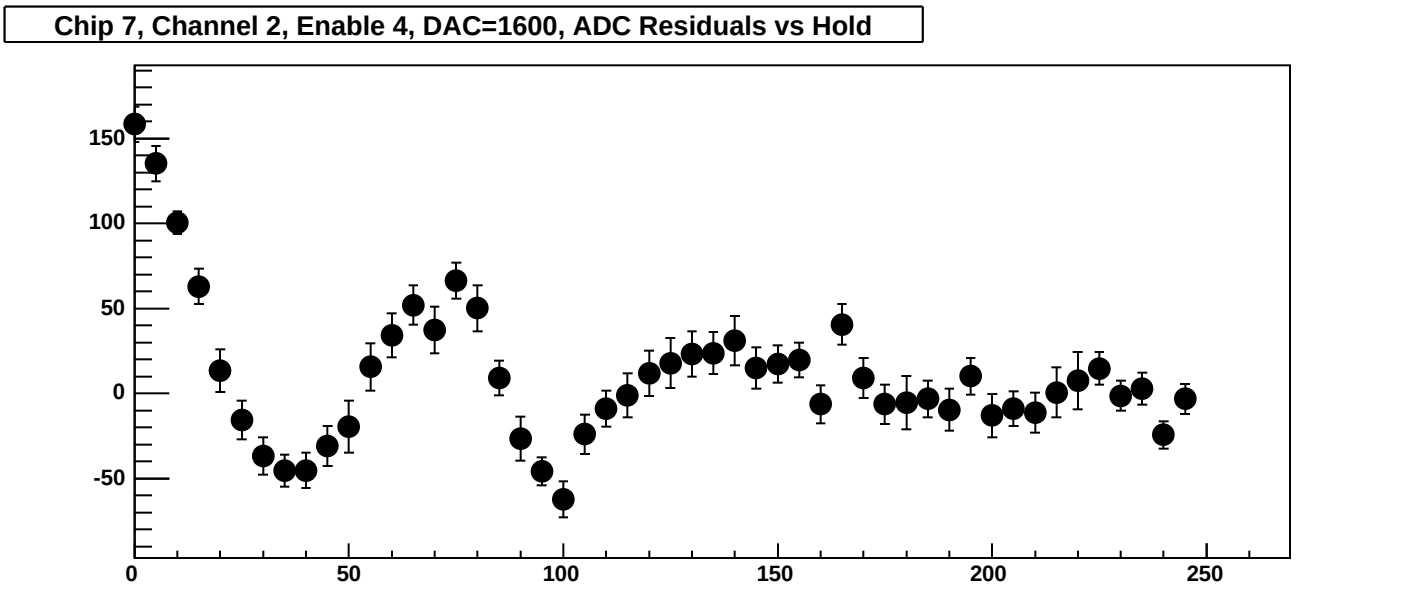


Chip 7, Channel 2, Enable 3!, DAC=1600, ADC Residuals vs Hold

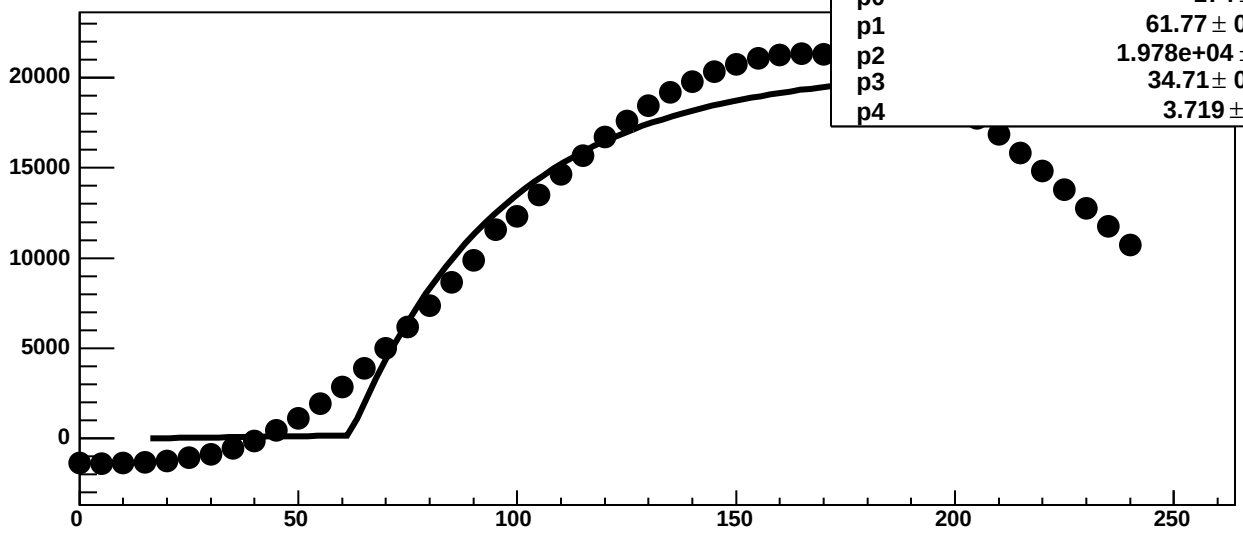



Chip 7, Channel 2, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 316.9 / 41
p0 -378.6 ± 3.675
p1 97.09 ± 0.4536
p2 -5.472e+08 ± 6.9e+06
p3 3.687e+07 ± 3.427e+05
p4 12.08 ± 0.1049
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

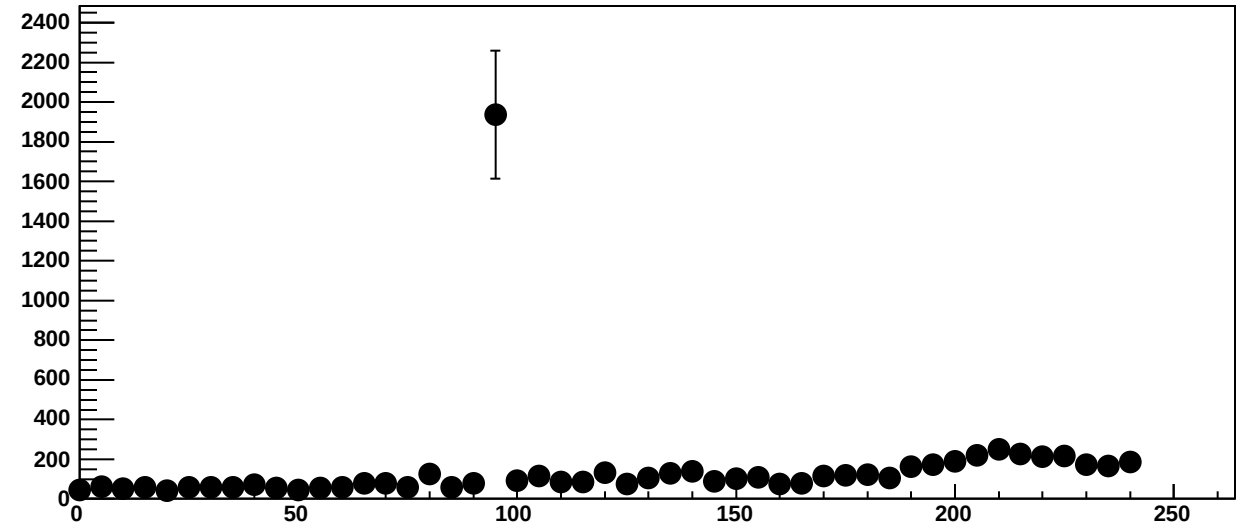

Chip 7, Channel 2, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 7, Channel 2, Enable 4, DAC=1600, ADC Residuals vs Hold
150
100
50
0
-50
0
50
100
150
200
250

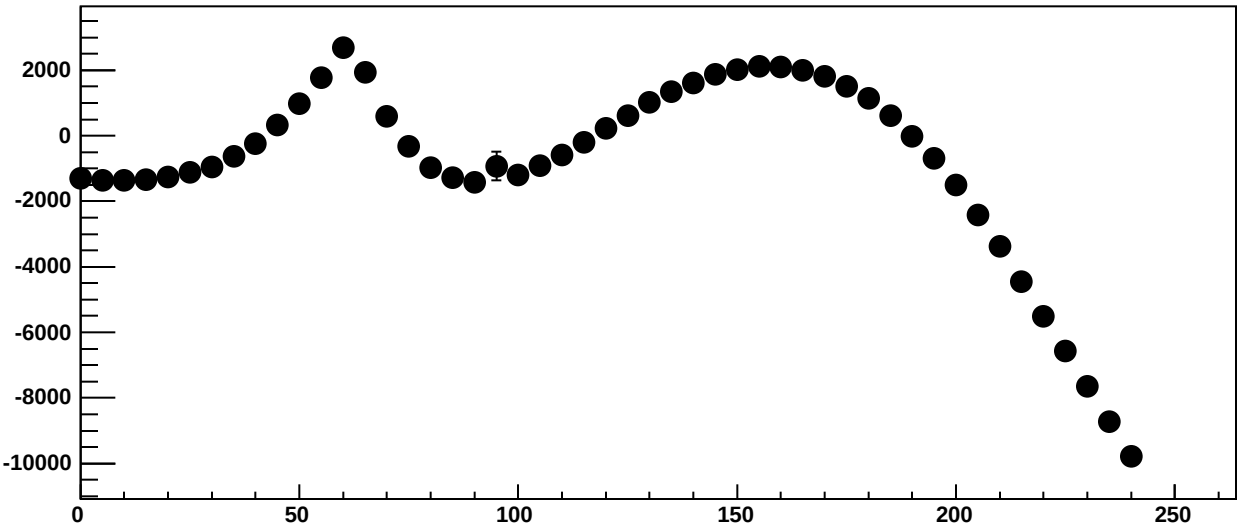
Chip 7, Channel 2, Enable 5, DAC=1600, ADC Mean vs Hold



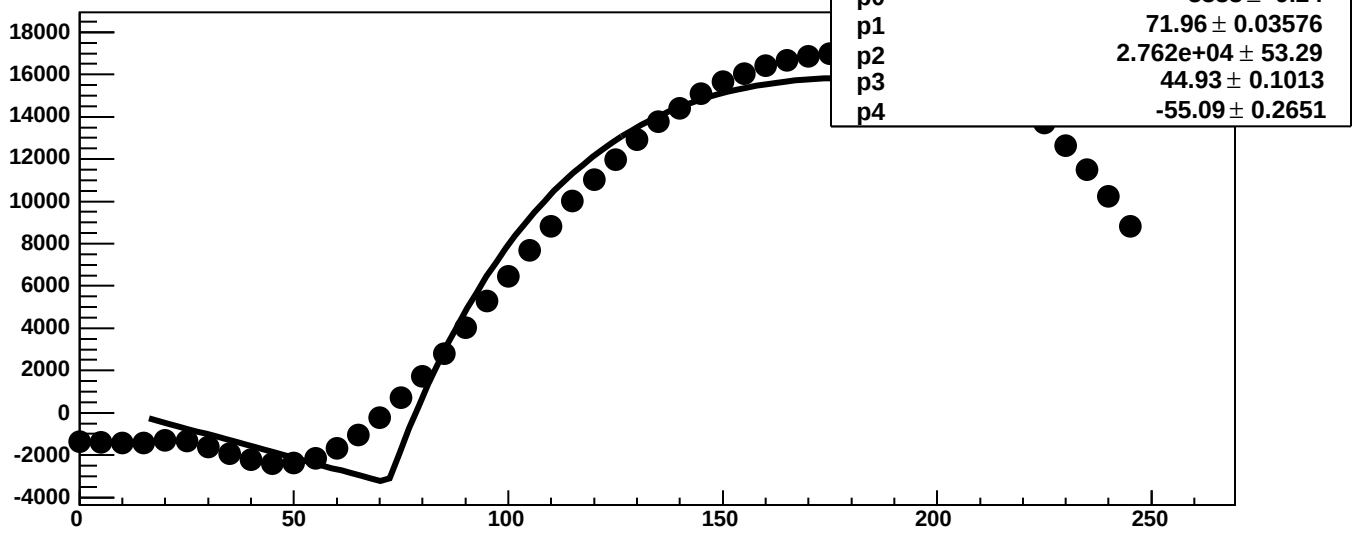
Chip 7, Channel 2, Enable 5, DAC=1600, ADC Noise vs Hold



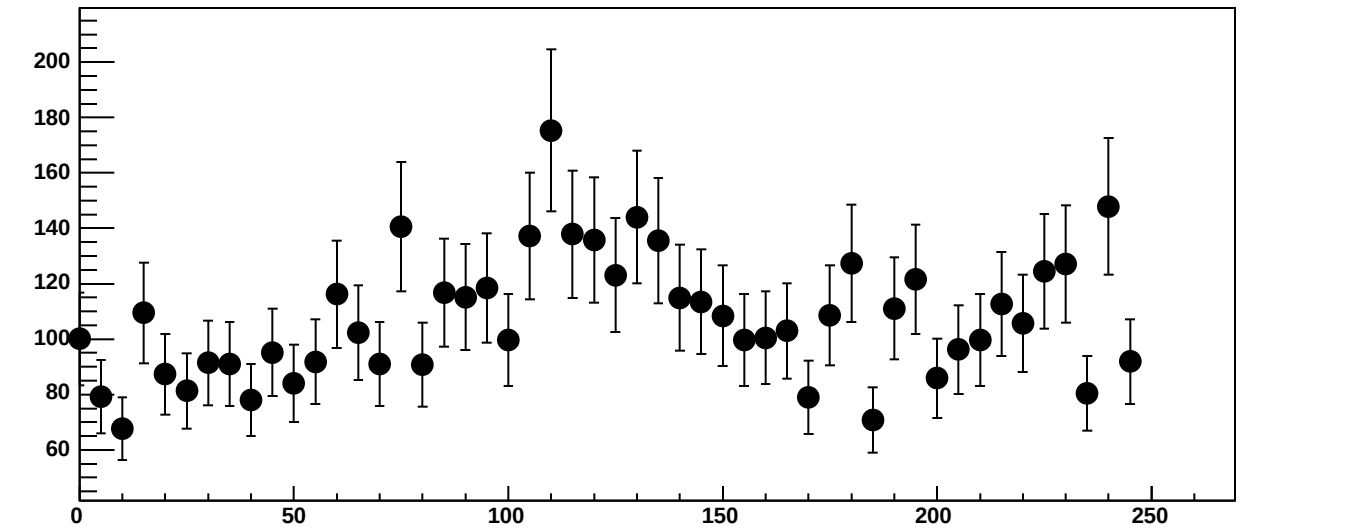
Chip 7, Channel 2, Enable 5, DAC=1600, ADC Residuals vs Hold



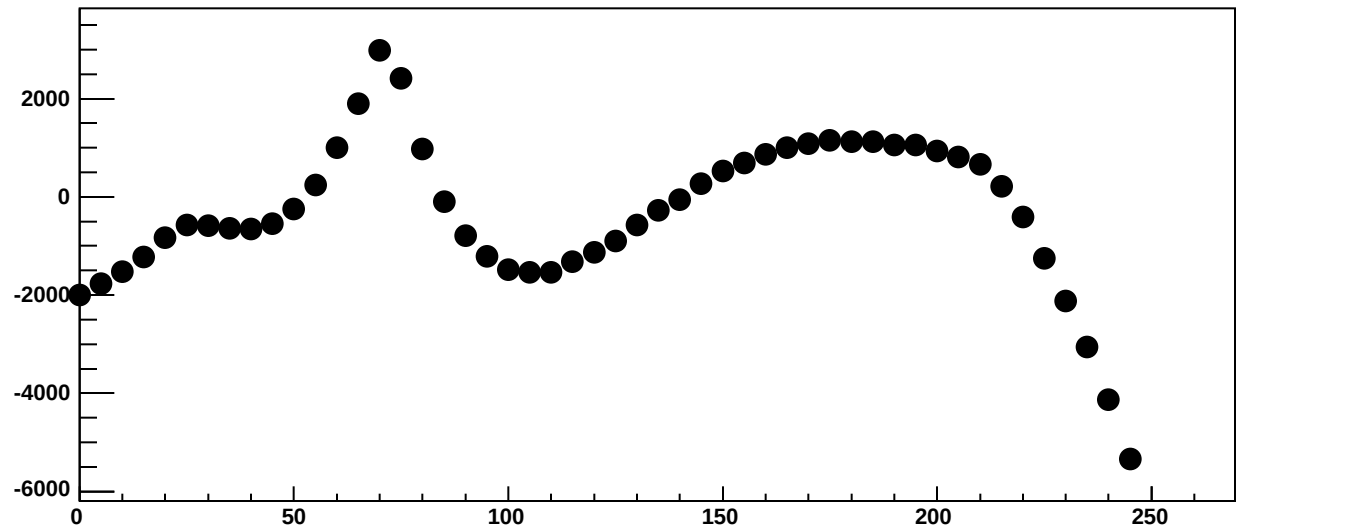
Chip 7, Channel 3, Enable 0, DAC=1600, ADC Mean vs Hold

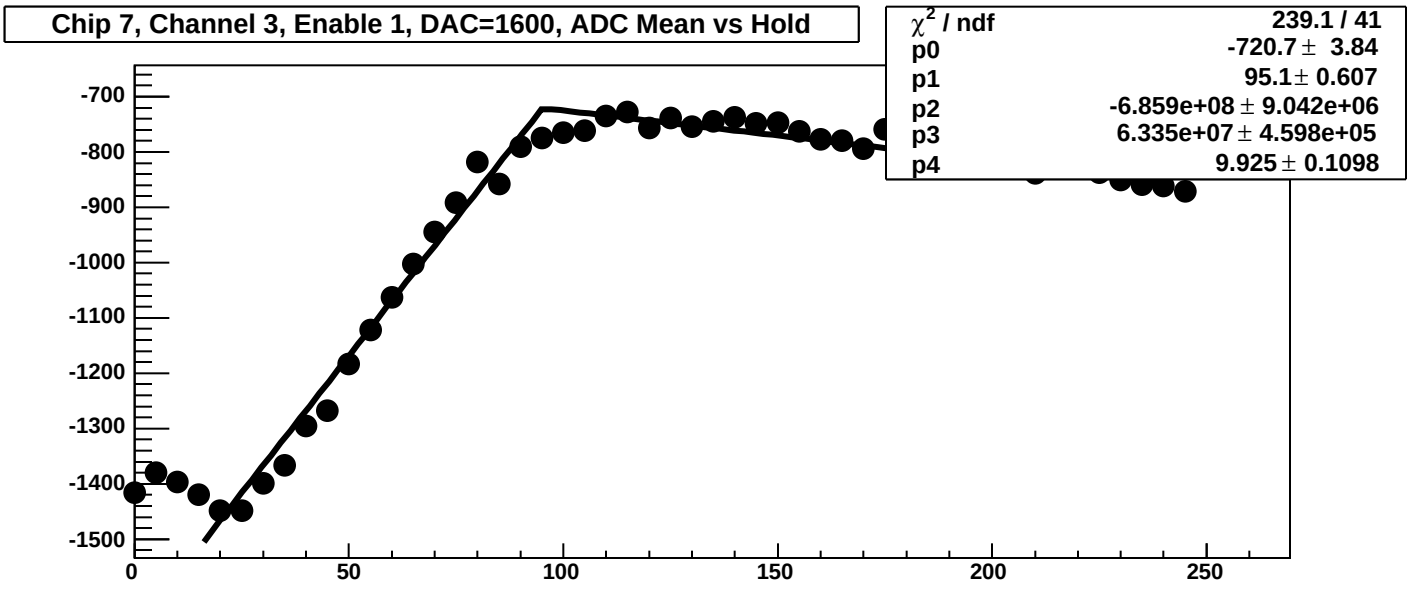


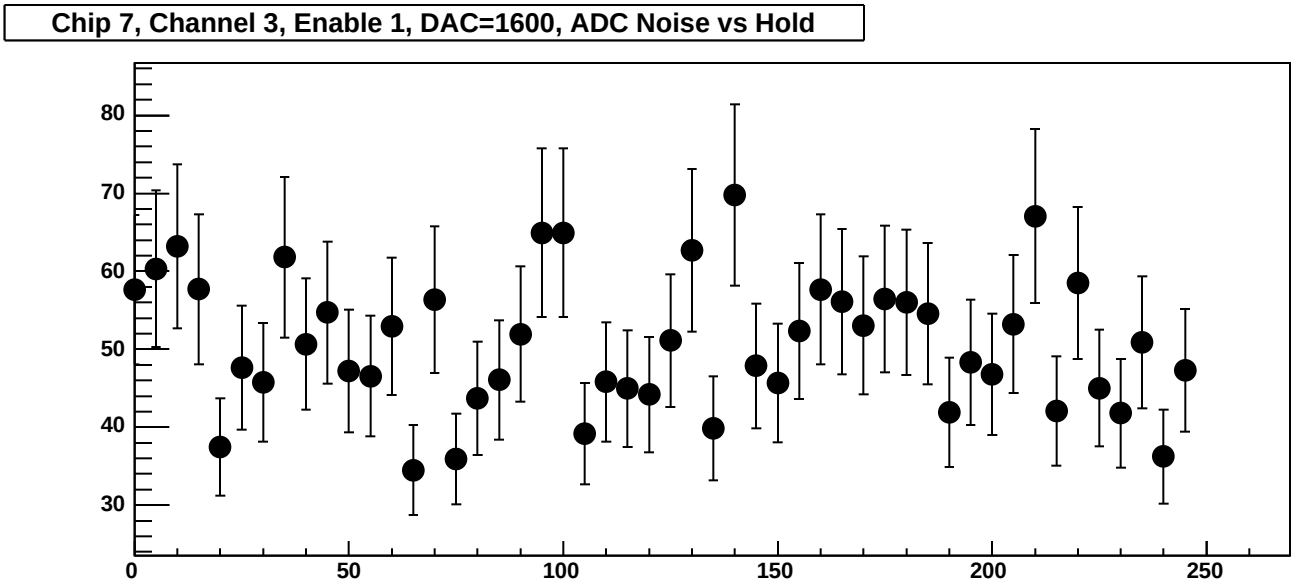
Chip 7, Channel 3, Enable 0, DAC=1600, ADC Noise vs Hold

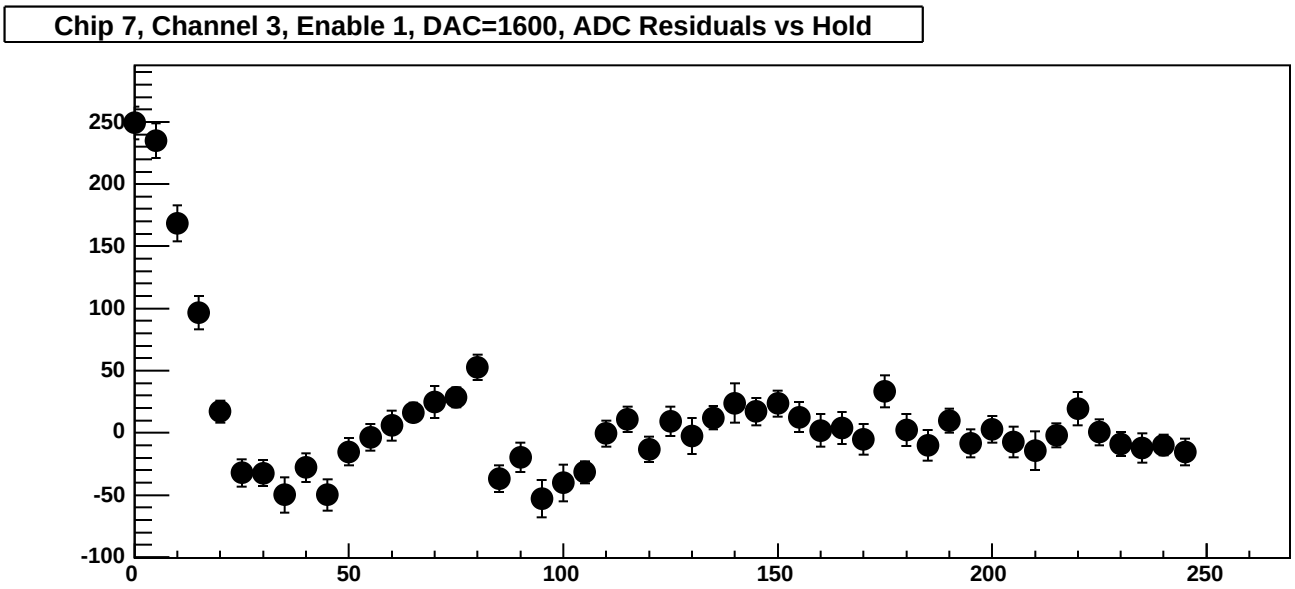


Chip 7, Channel 3, Enable 0, DAC=1600, ADC Residuals vs Hold

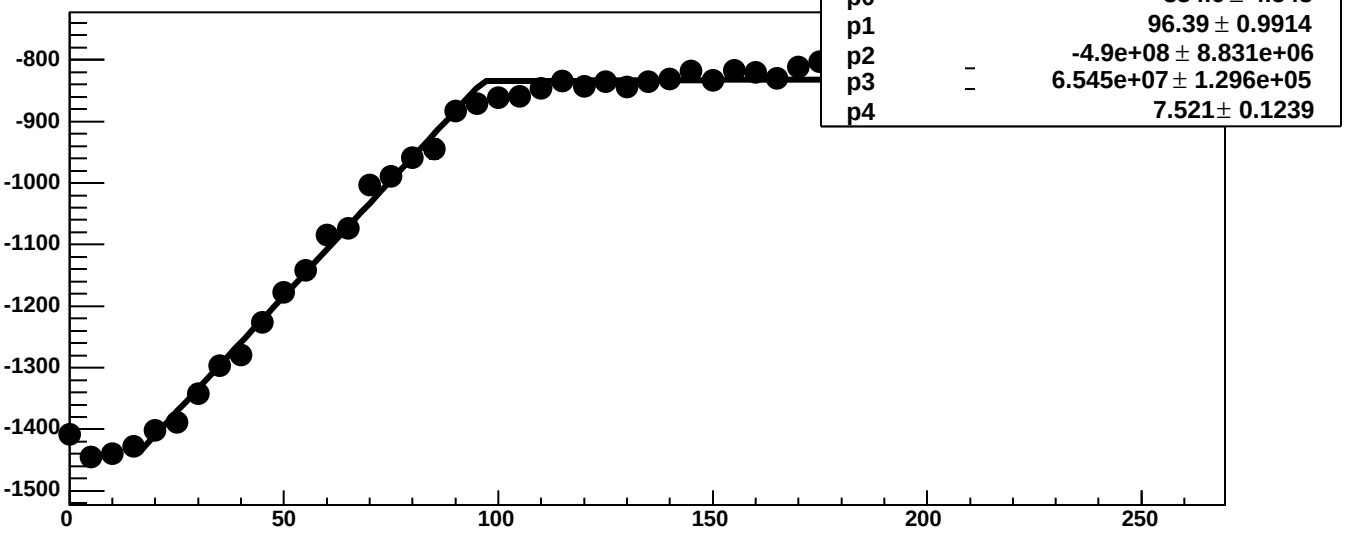



Chip 7, Channel 3, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 239.1 / 41
p0 -720.7 ± 3.84
p1 95.1 ± 0.607
p2 -6.859e+08 ± 9.042e+06
p3 6.335e+07 ± 4.598e+05
p4 9.925 ± 0.1098
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250

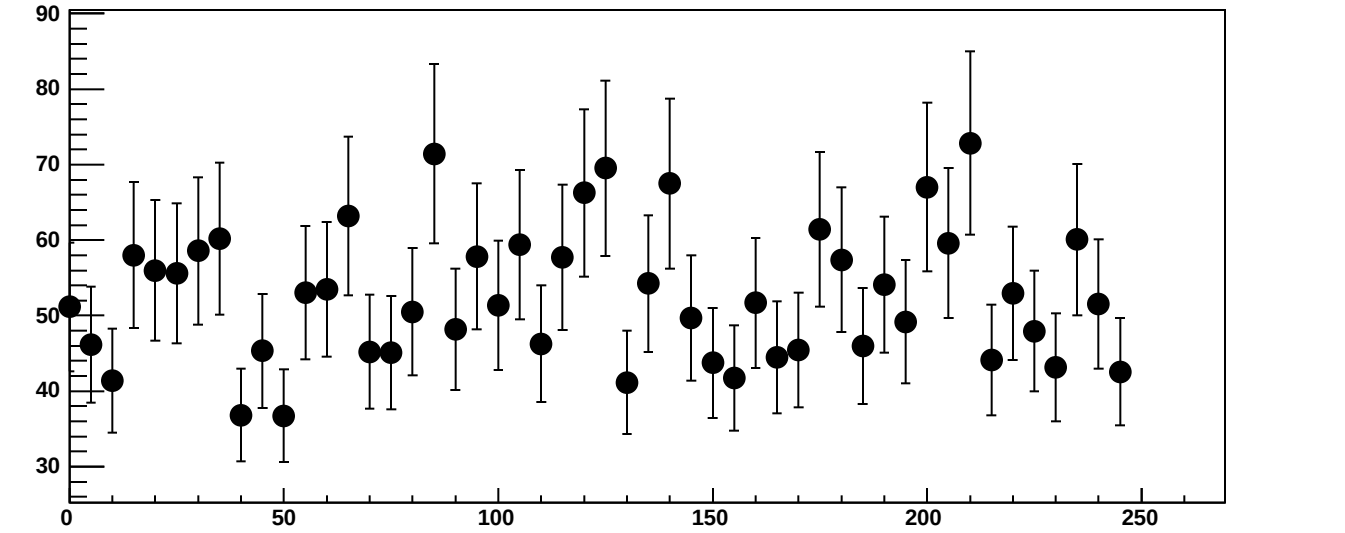

Chip 7, Channel 3, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 7, Channel 3, Enable 1, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

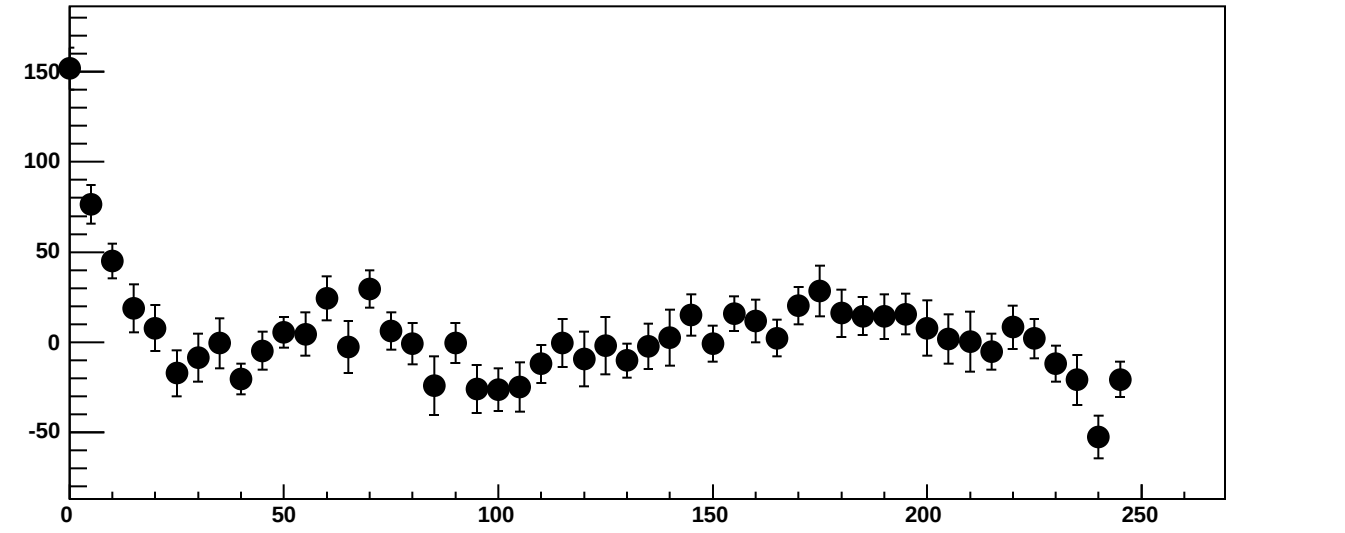
Chip 7, Channel 3, Enable 2, DAC=1600, ADC Mean vs Hold



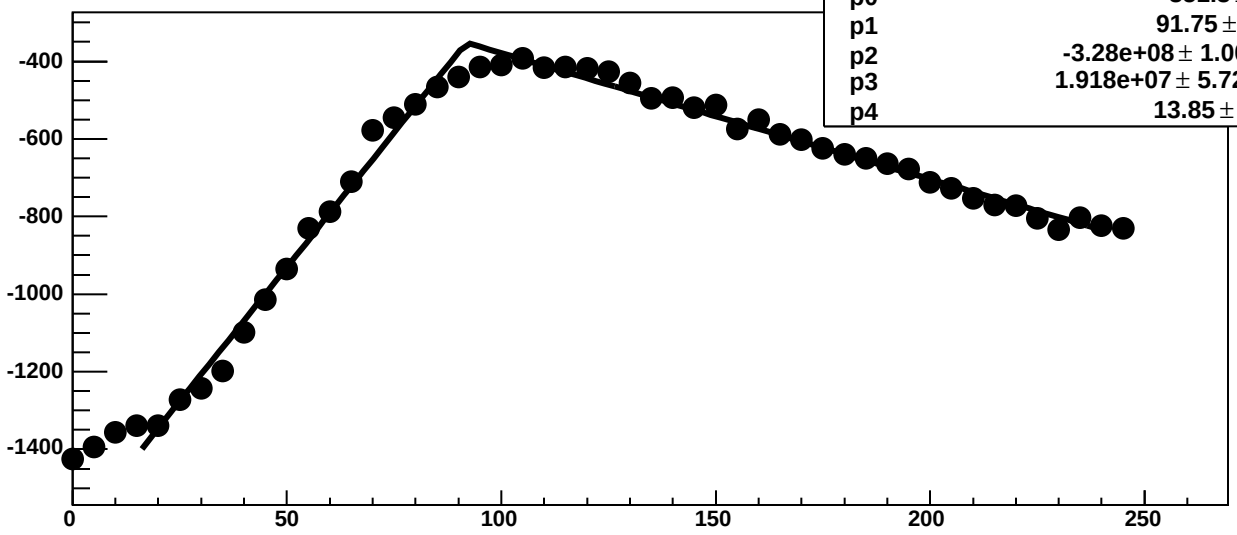
Chip 7, Channel 3, Enable 2, DAC=1600, ADC Noise vs Hold



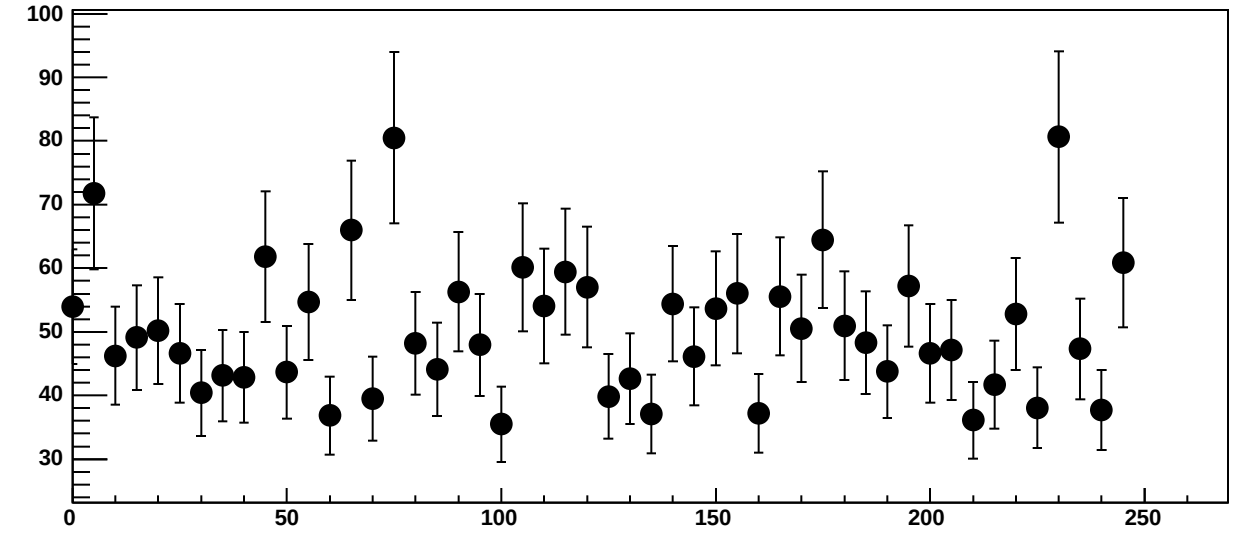
Chip 7, Channel 3, Enable 2, DAC=1600, ADC Residuals vs Hold



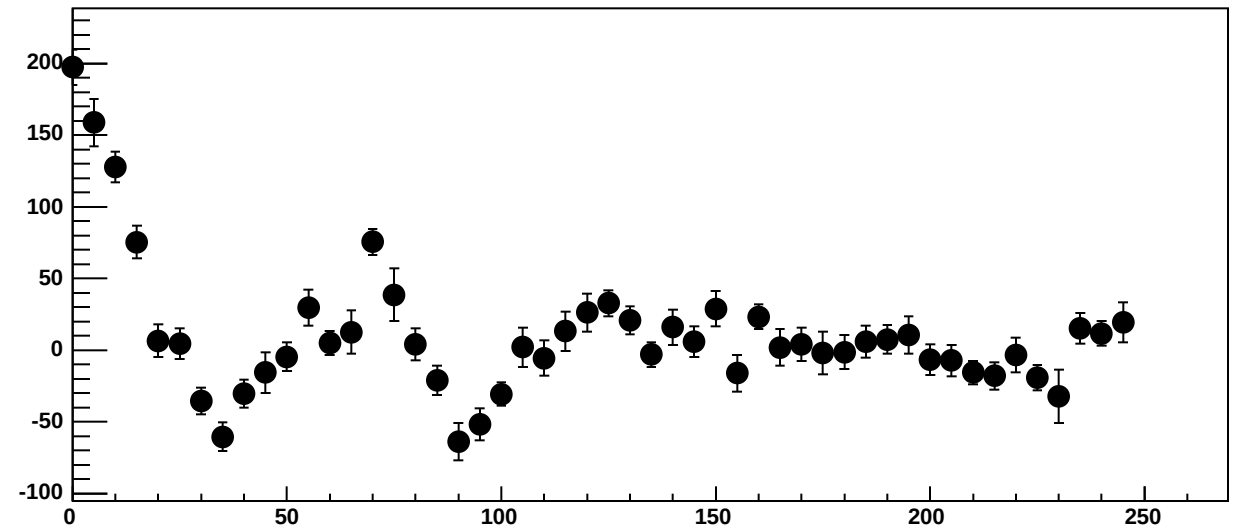
Chip 7, Channel 3, Enable 3, DAC=1600, ADC Mean vs Hold



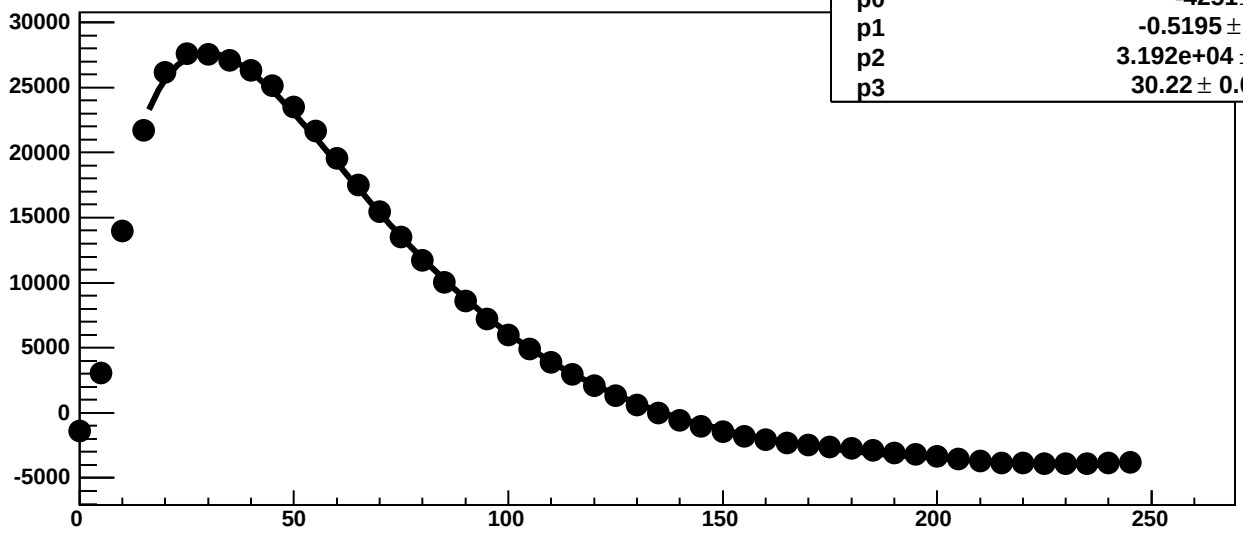
Chip 7, Channel 3, Enable 3, DAC=1600, ADC Noise vs Hold



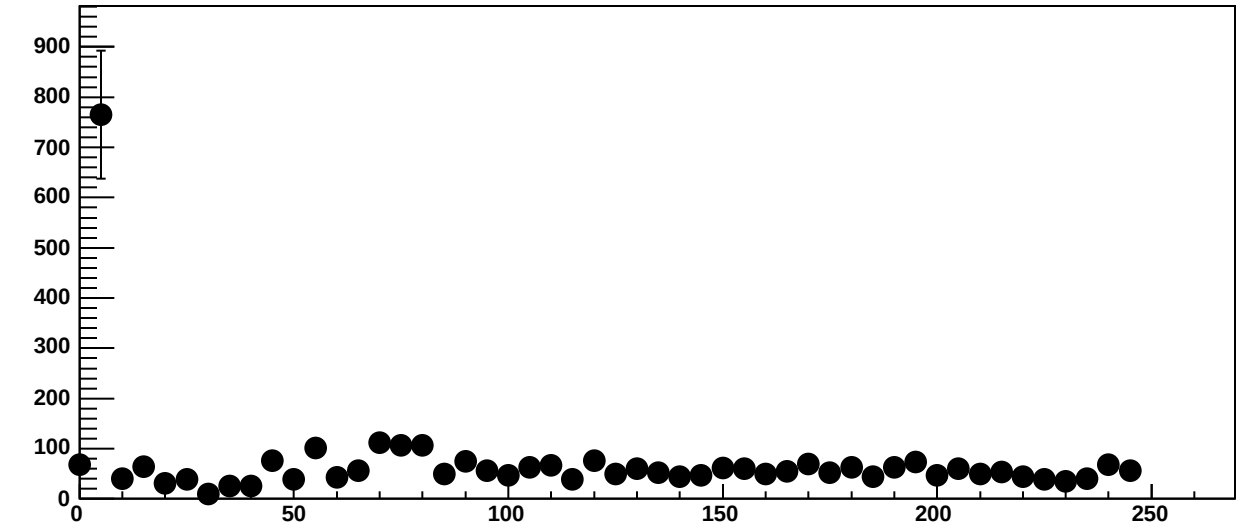
Chip 7, Channel 3, Enable 3, DAC=1600, ADC Residuals vs Hold



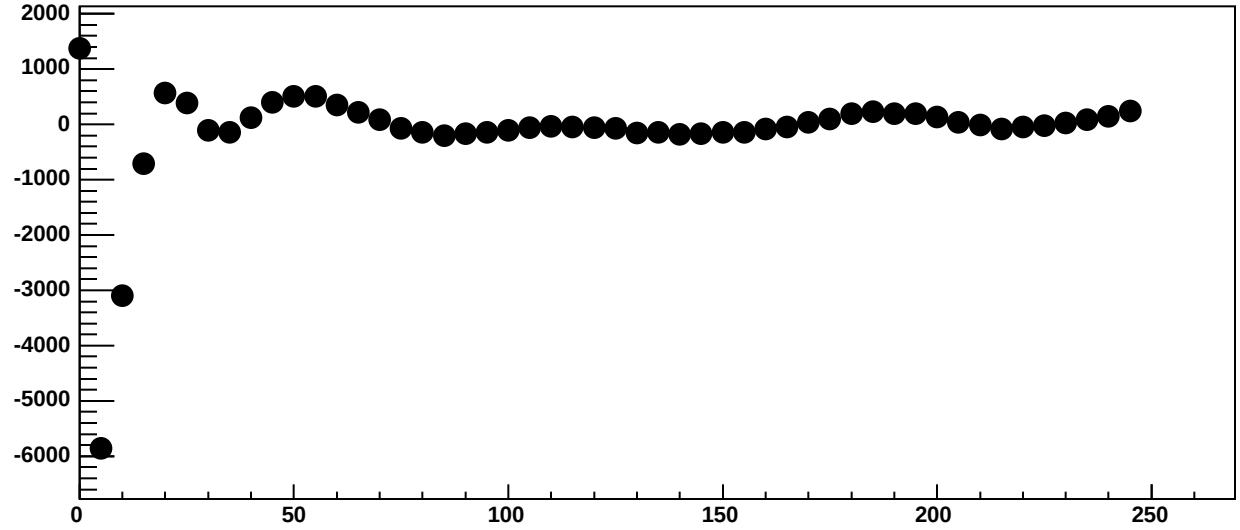
Chip 7, Channel 3, Enable 4!, DAC=1600, ADC Mean vs Hold



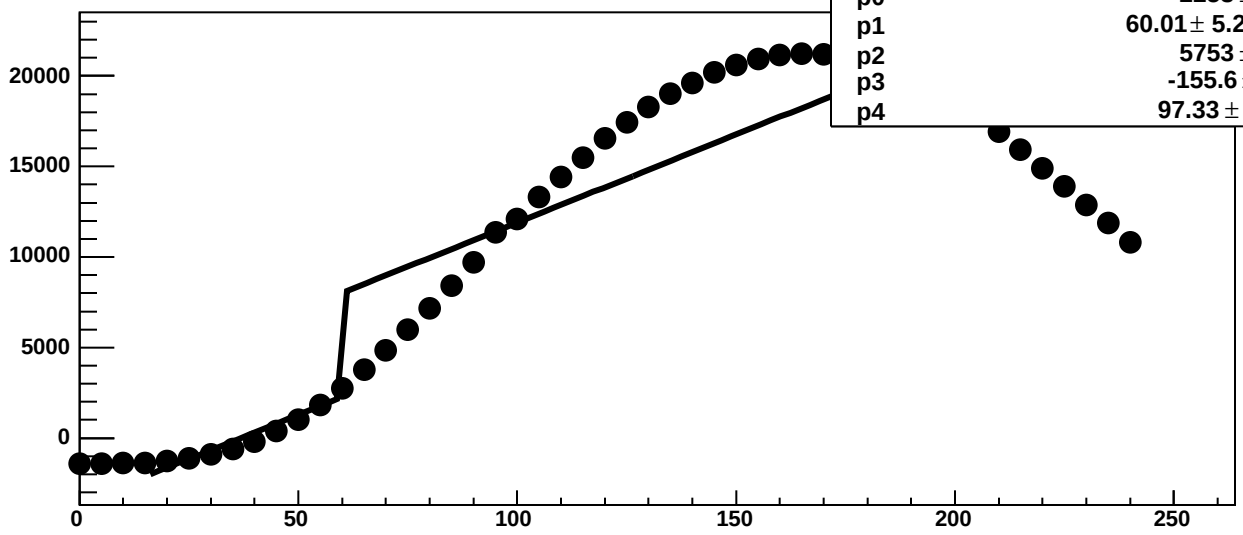
Chip 7, Channel 3, Enable 4!, DAC=1600, ADC Noise vs Hold



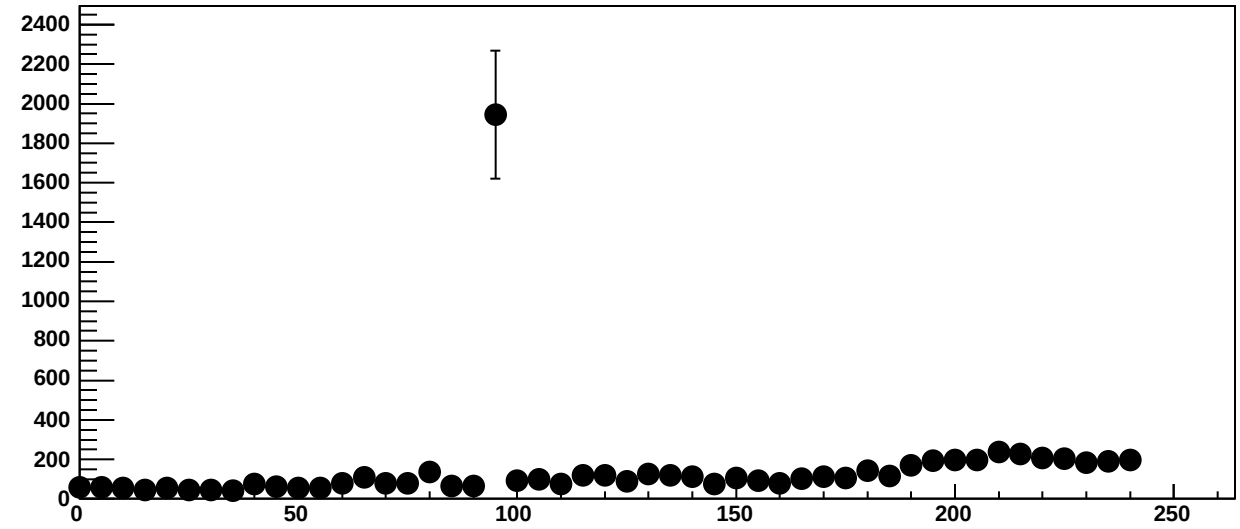
Chip 7, Channel 3, Enable 4!, DAC=1600, ADC Residuals vs Hold



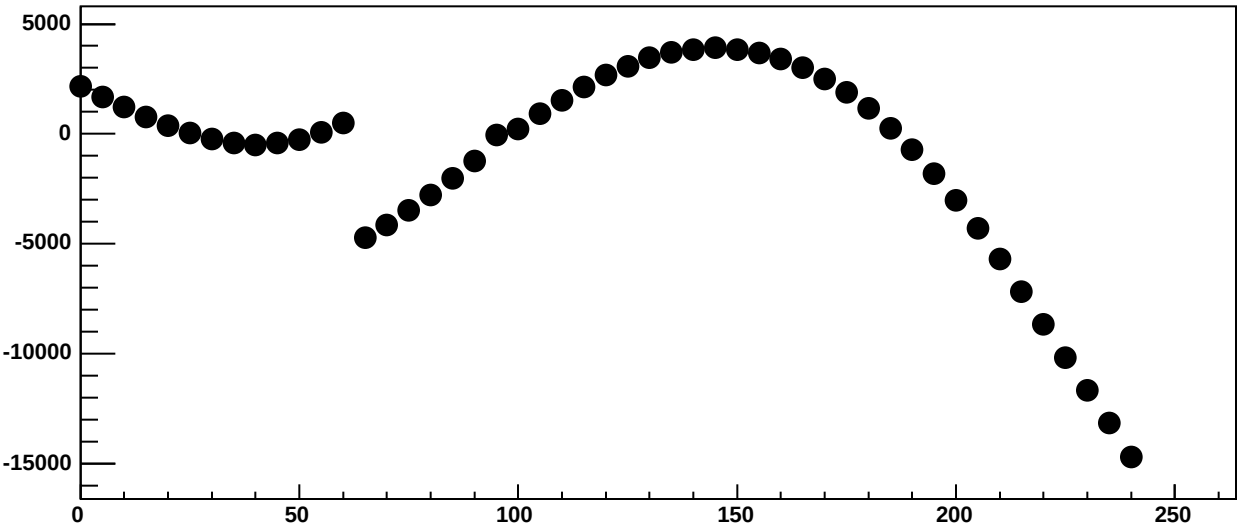
Chip 7, Channel 3, Enable 5, DAC=1600, ADC Mean vs Hold



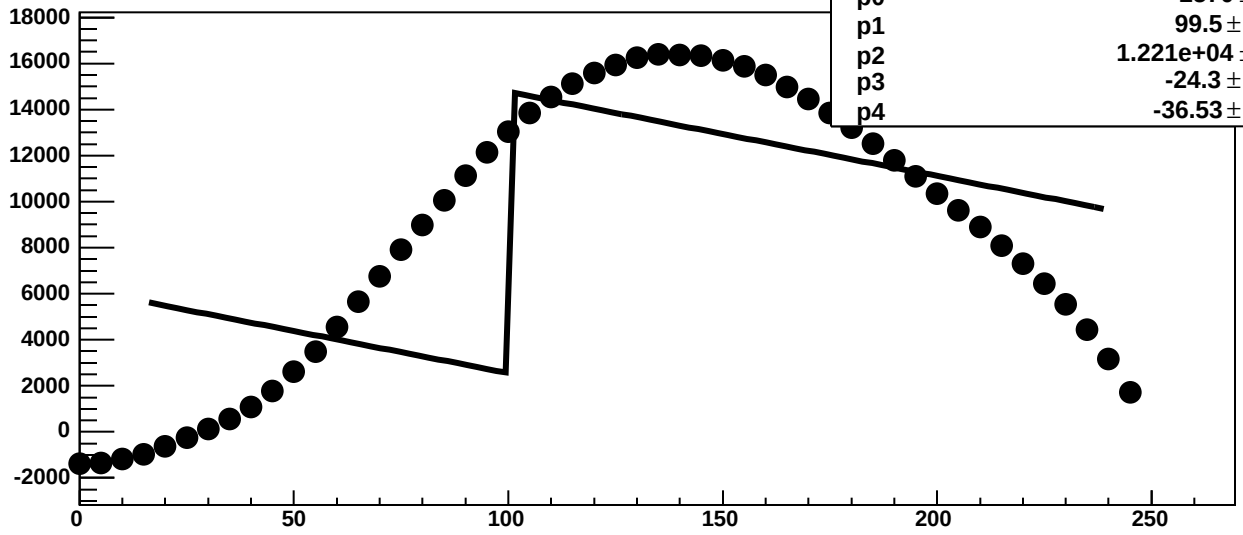
Chip 7, Channel 3, Enable 5, DAC=1600, ADC Noise vs Hold



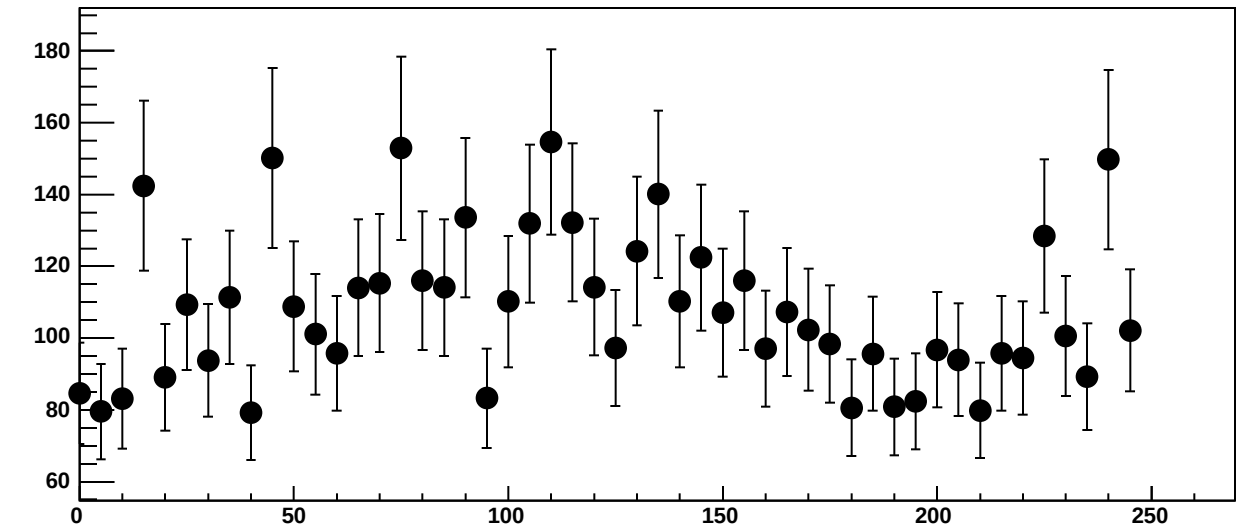
Chip 7, Channel 3, Enable 5, DAC=1600, ADC Residuals vs Hold



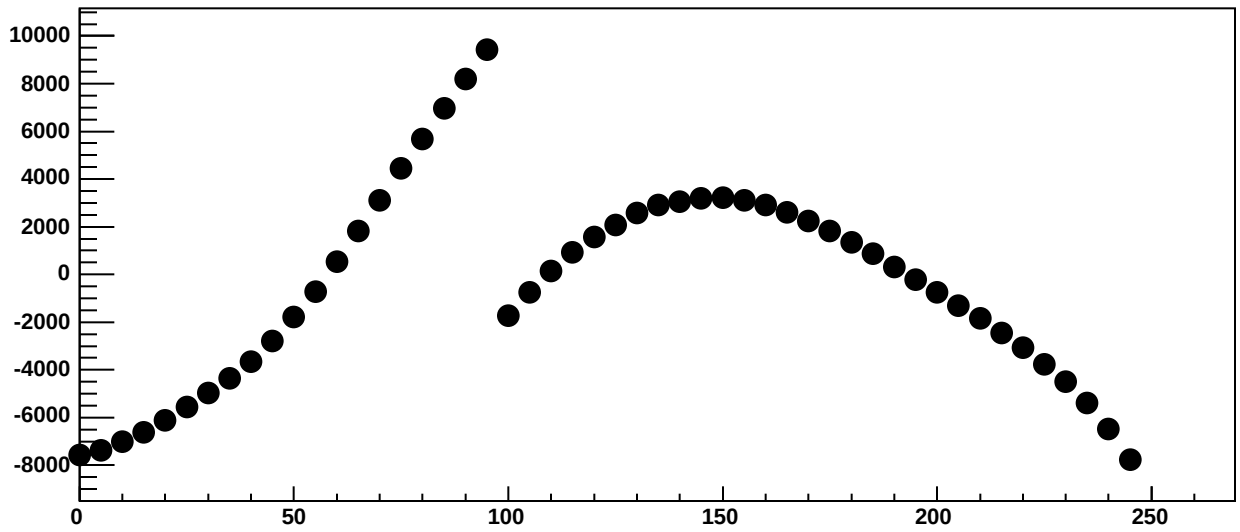
Chip 7, Channel 4, Enable 0, DAC=1600, ADC Mean vs Hold



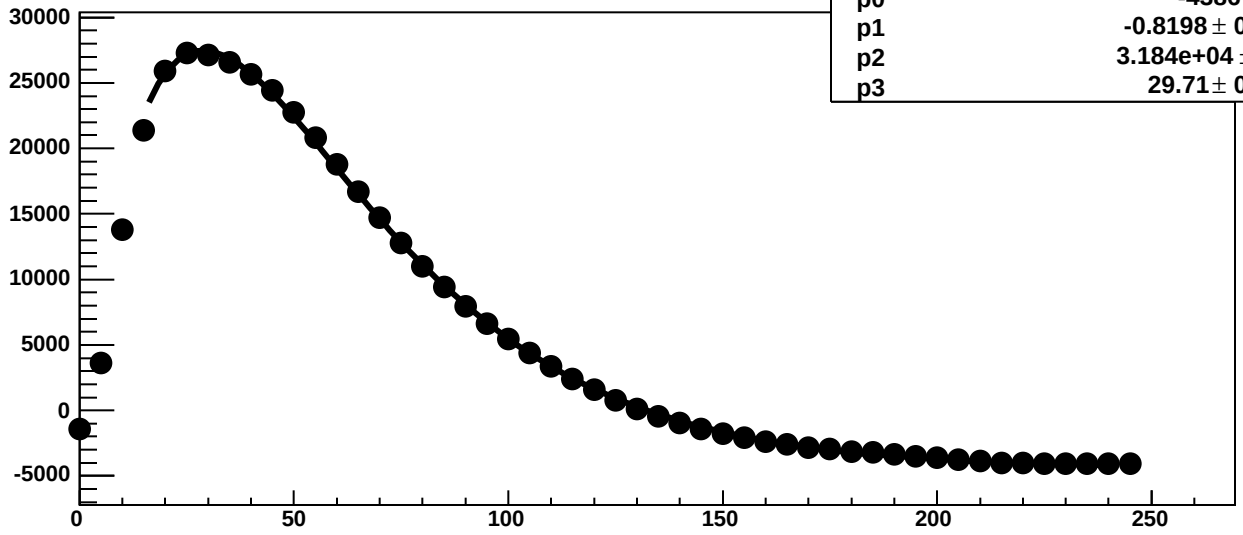
Chip 7, Channel 4, Enable 0, DAC=1600, ADC Noise vs Hold



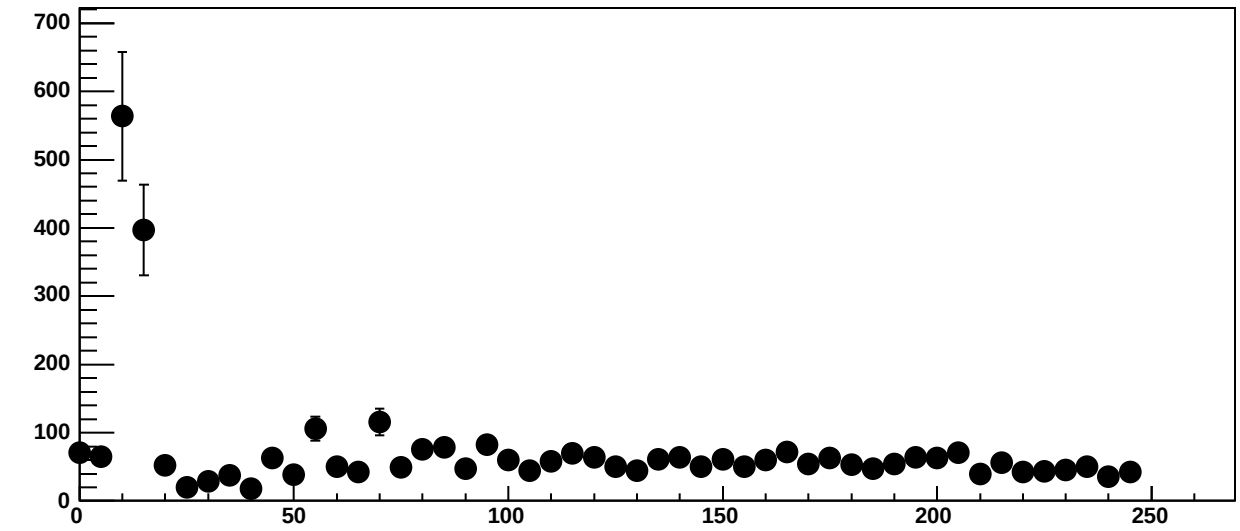
Chip 7, Channel 4, Enable 0, DAC=1600, ADC Residuals vs Hold



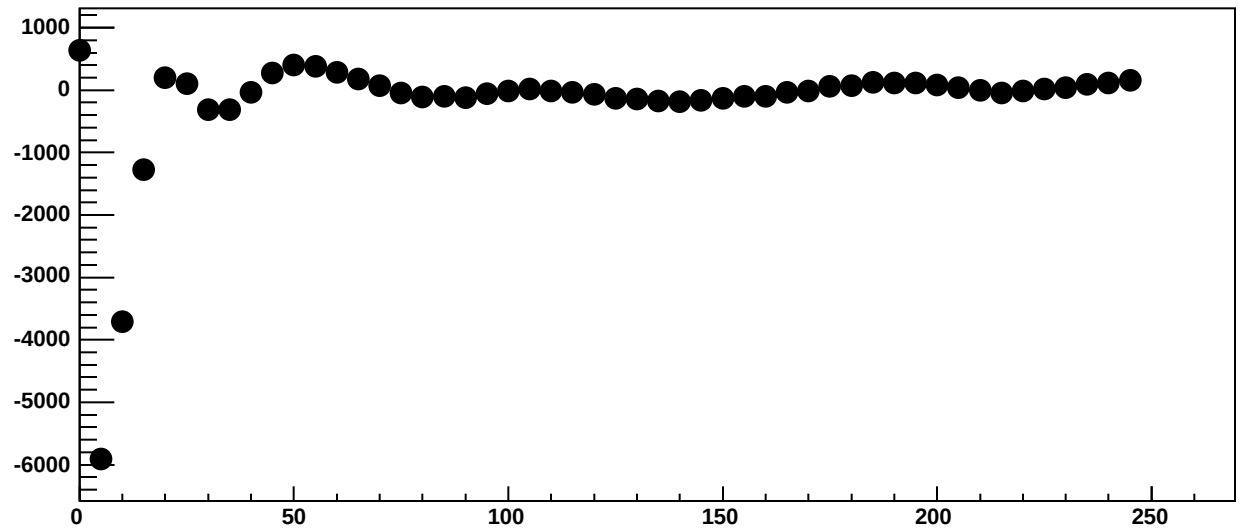
Chip 7, Channel 4, Enable 1!, DAC=1600, ADC Mean vs Hold



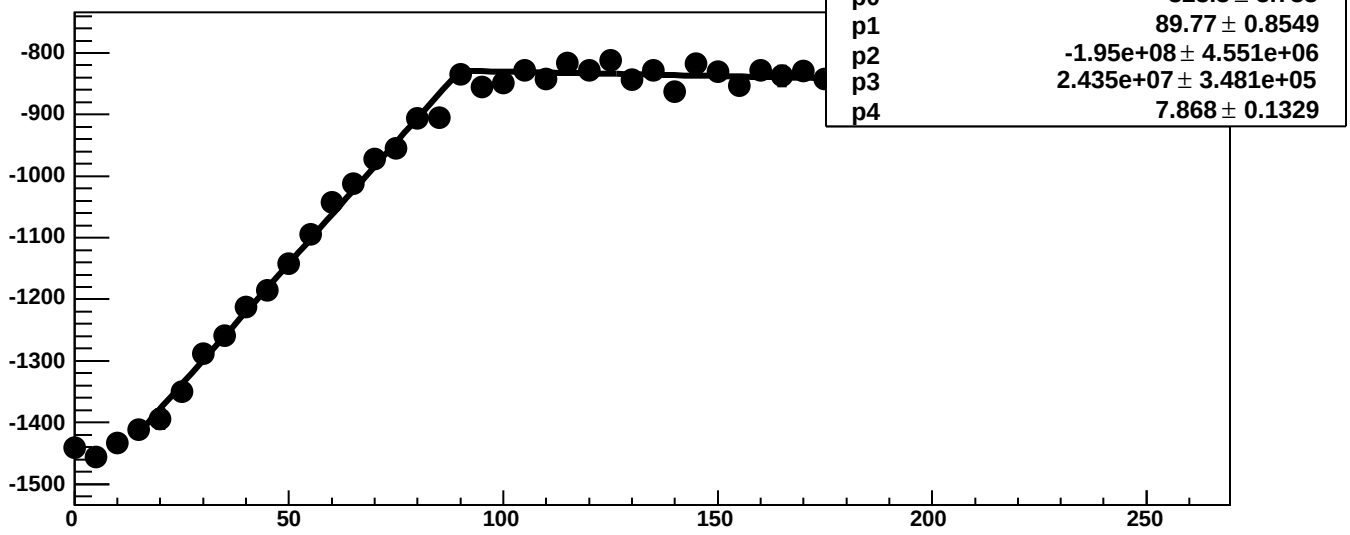
Chip 7, Channel 4, Enable 1!, DAC=1600, ADC Noise vs Hold



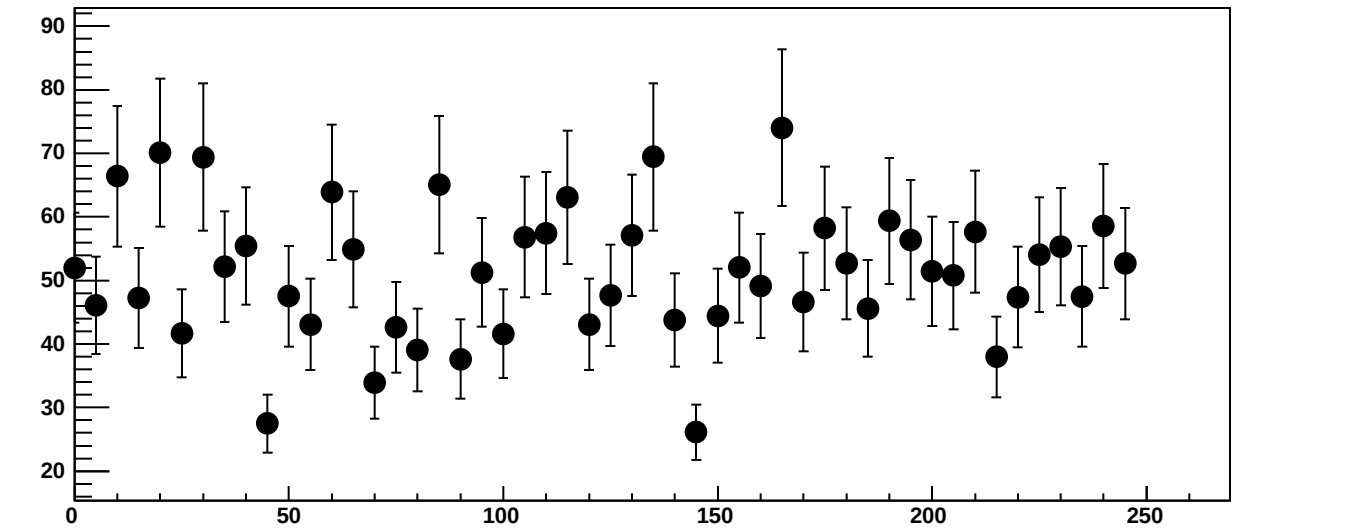
Chip 7, Channel 4, Enable 1!, DAC=1600, ADC Residuals vs Hold



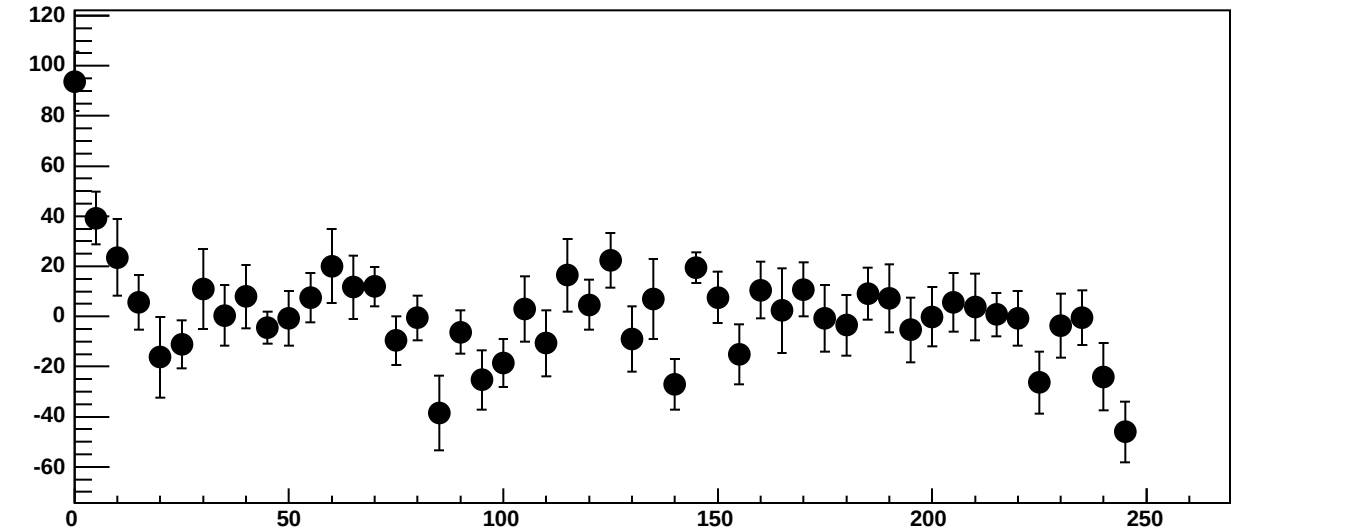
Chip 7, Channel 4, Enable 2, DAC=1600, ADC Mean vs Hold



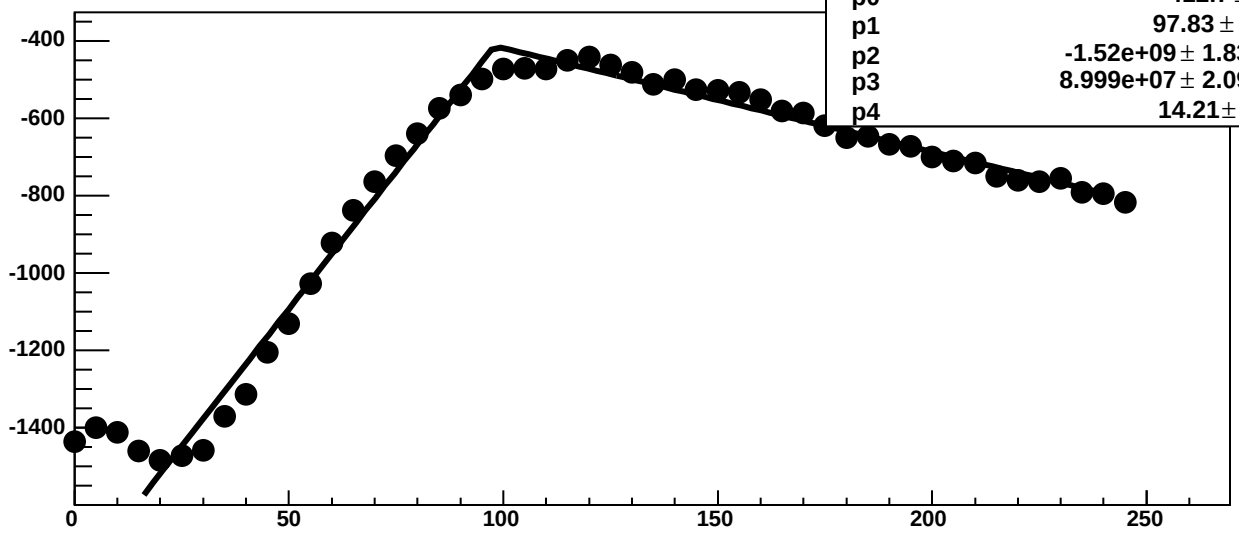
Chip 7, Channel 4, Enable 2, DAC=1600, ADC Noise vs Hold



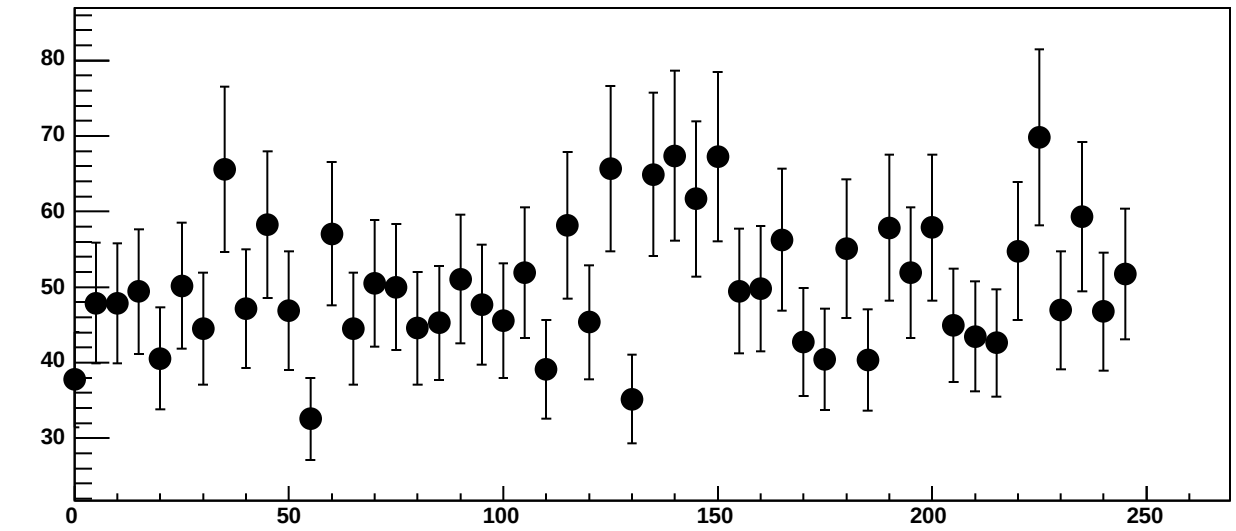
Chip 7, Channel 4, Enable 2, DAC=1600, ADC Residuals vs Hold



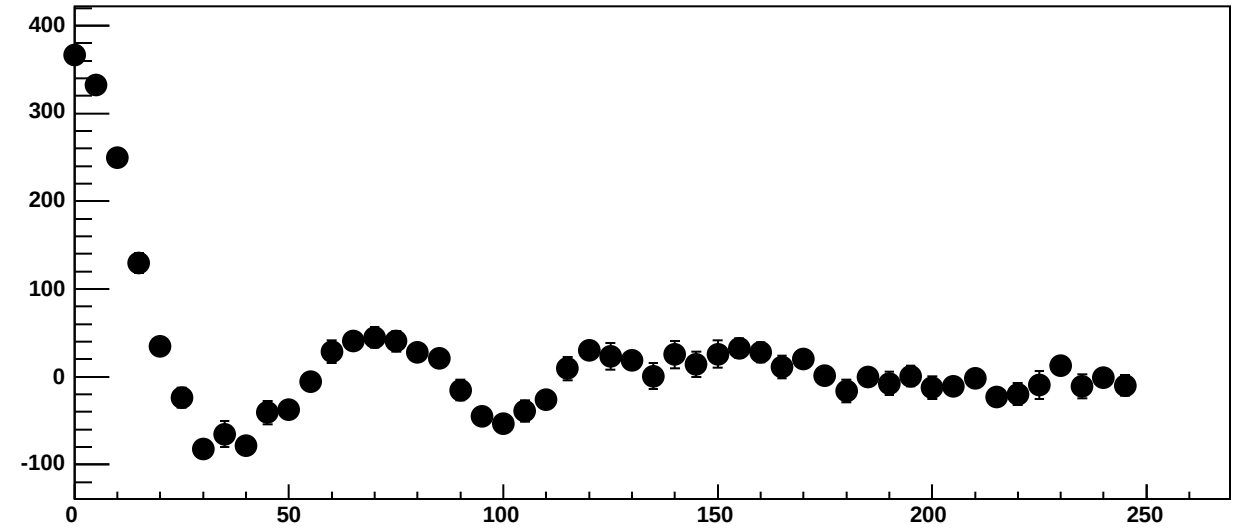
Chip 7, Channel 4, Enable 3, DAC=1600, ADC Mean vs Hold

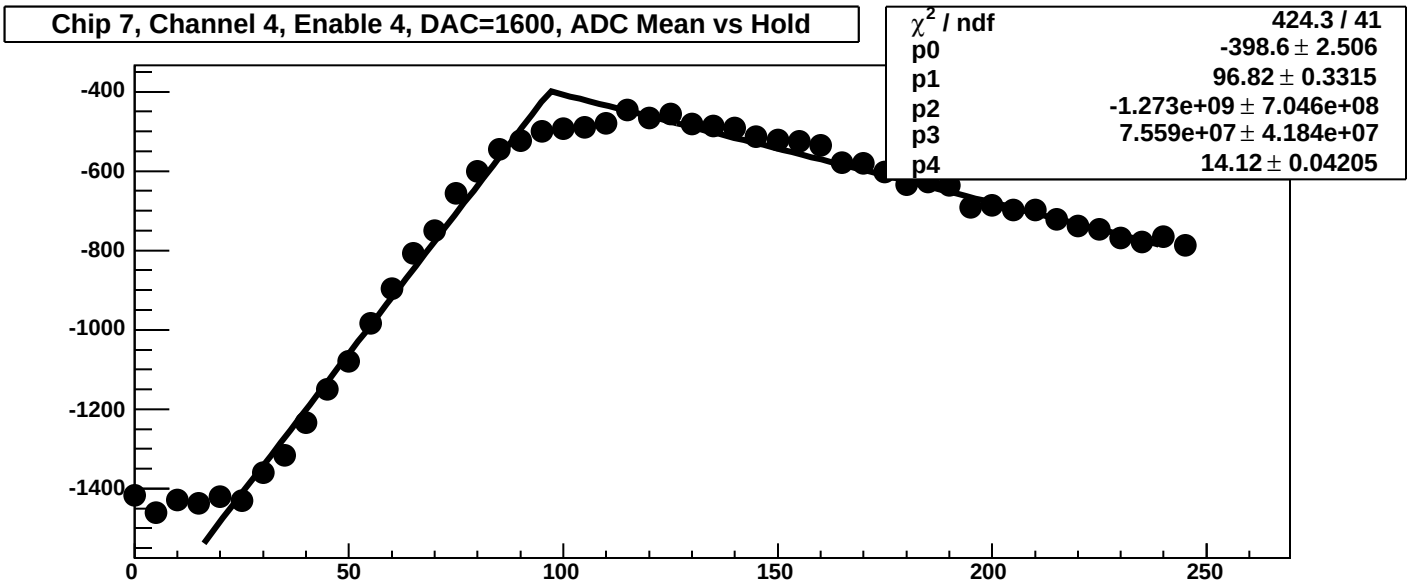


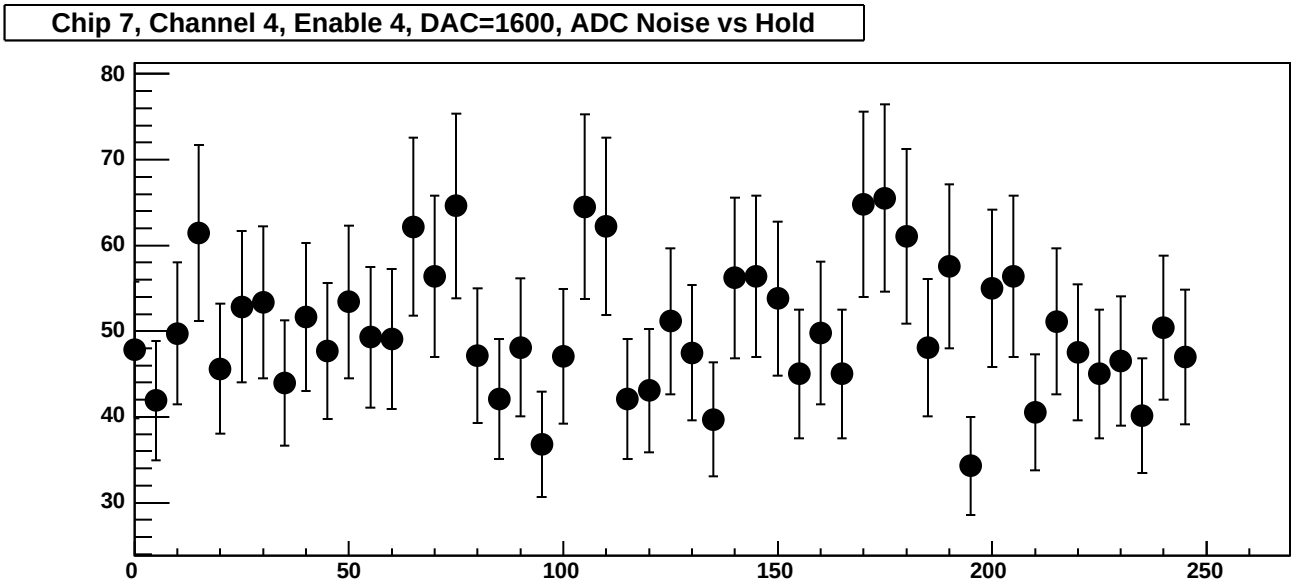
Chip 7, Channel 4, Enable 3, DAC=1600, ADC Noise vs Hold

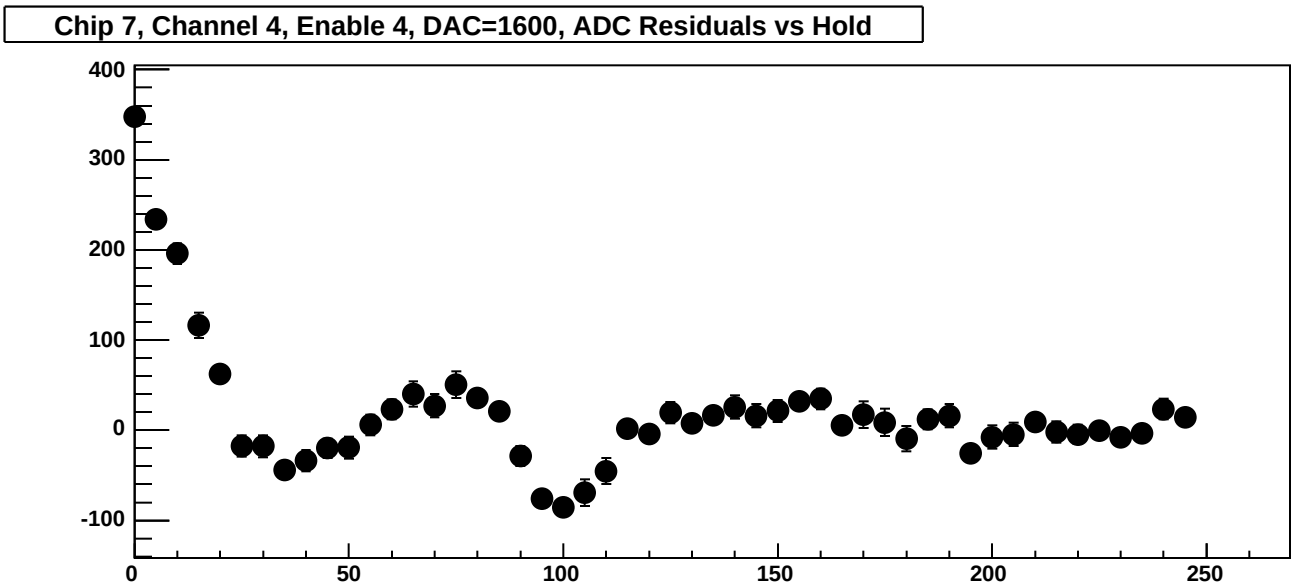


Chip 7, Channel 4, Enable 3, DAC=1600, ADC Residuals vs Hold

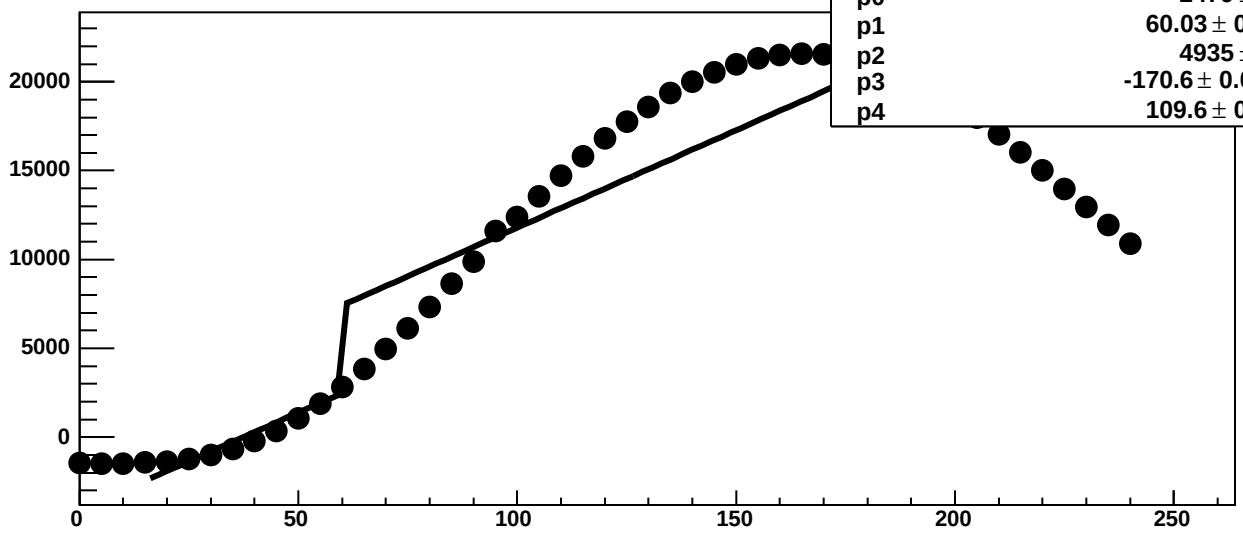



Chip 7, Channel 4, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 424.3 / 41
p0 -398.6 ± 2.506
p1 96.82 ± 0.3315
p2 -1.273e+09 ± 7.046e+08
p3 7.559e+07 ± 4.184e+07
p4 14.12 ± 0.04205
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

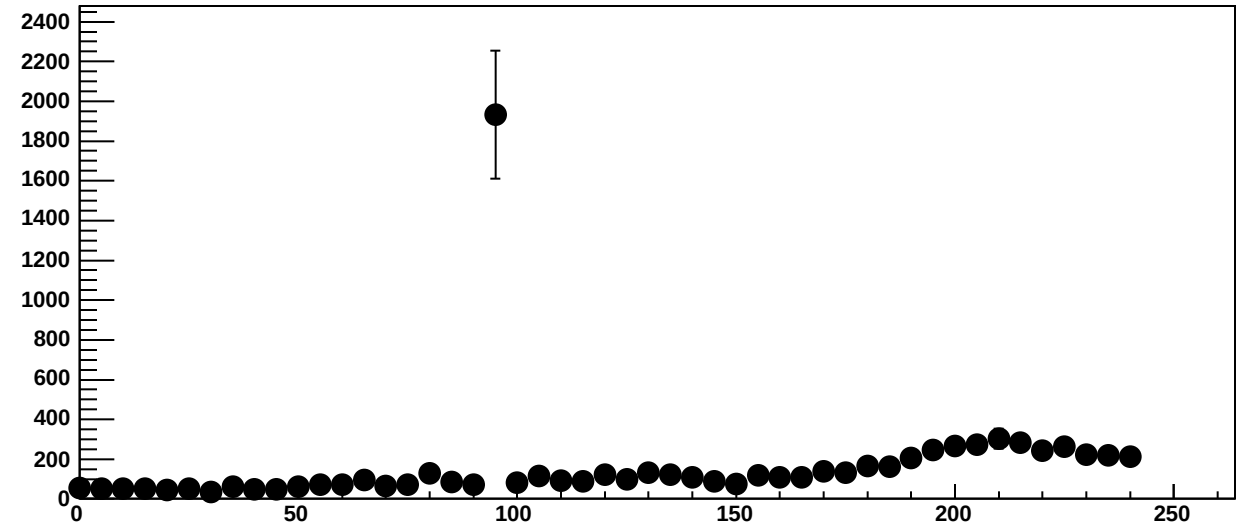

Chip 7, Channel 4, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 7, Channel 4, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

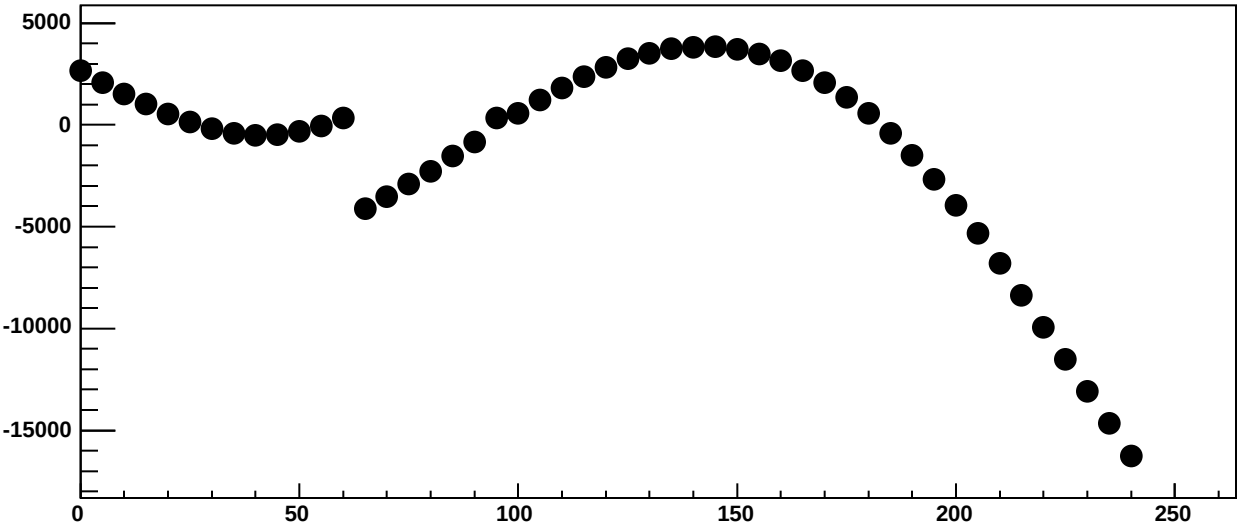
Chip 7, Channel 4, Enable 5, DAC=1600, ADC Mean vs Hold



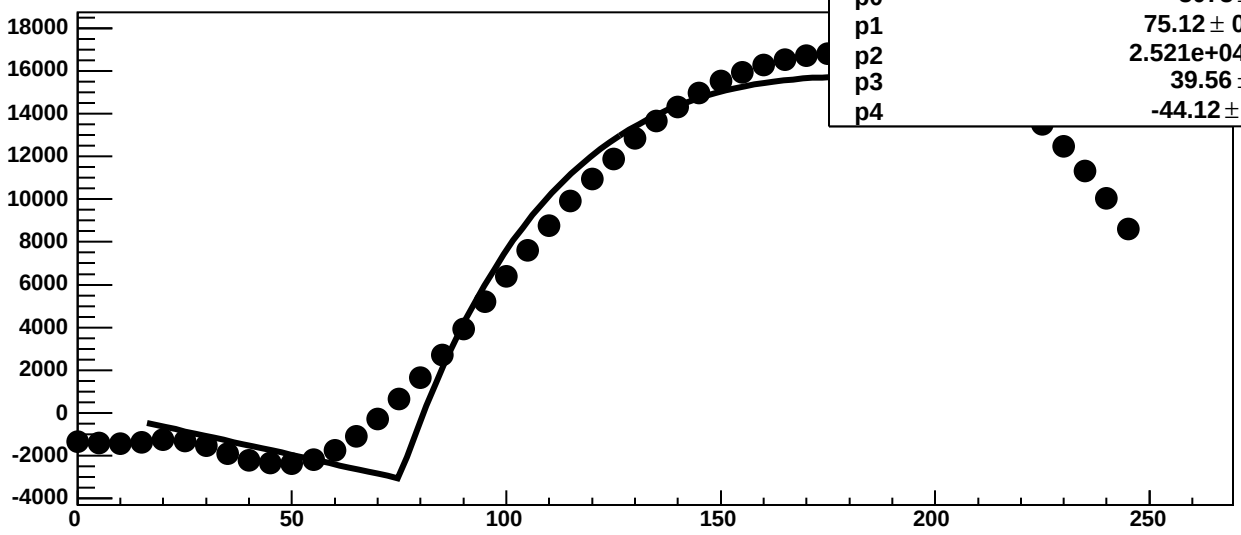
Chip 7, Channel 4, Enable 5, DAC=1600, ADC Noise vs Hold



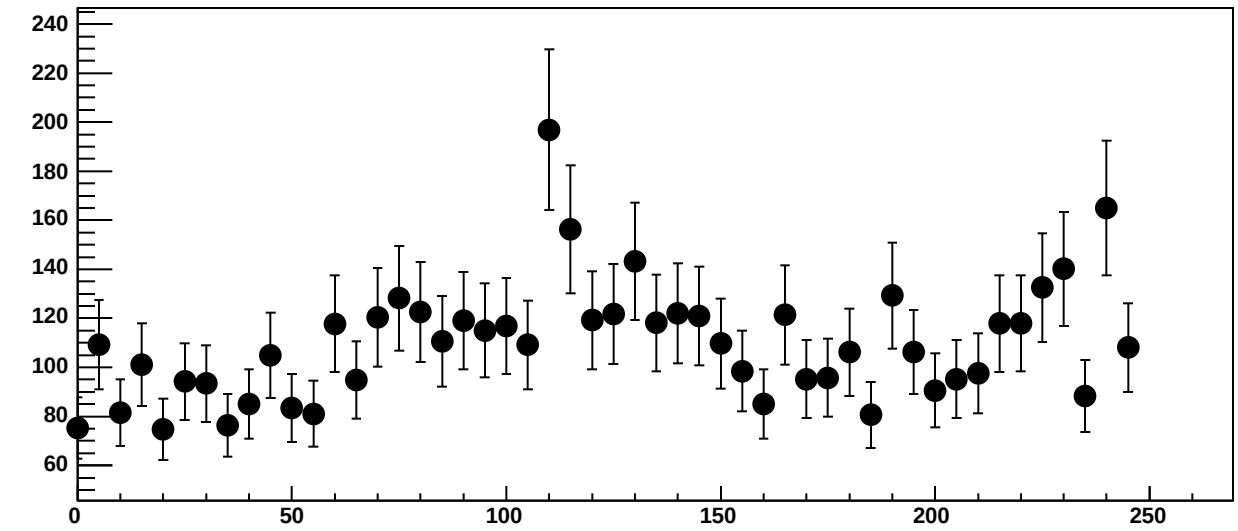
Chip 7, Channel 4, Enable 5, DAC=1600, ADC Residuals vs Hold



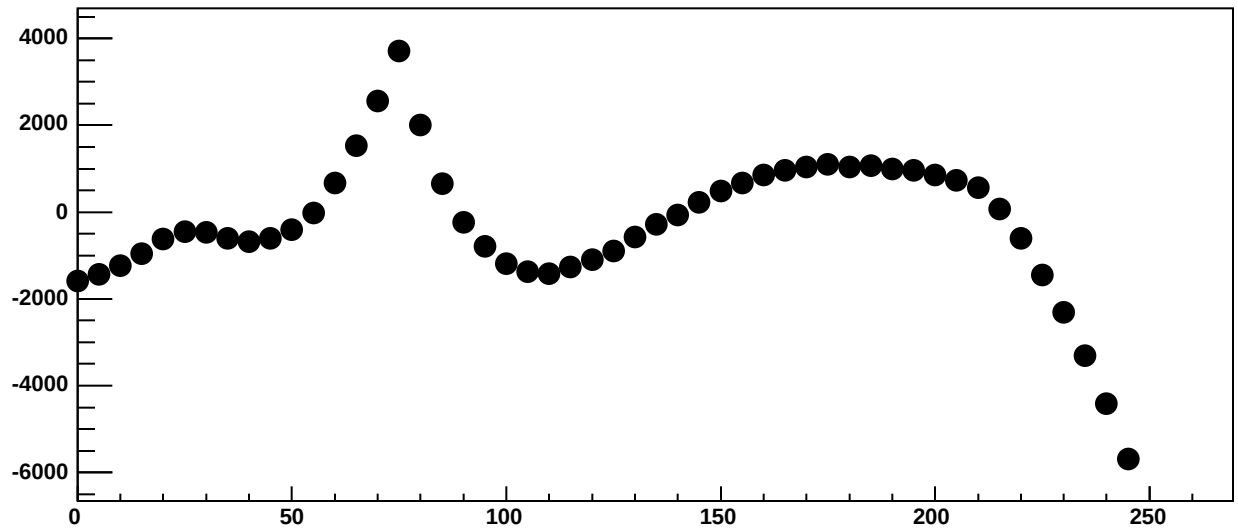
Chip 7, Channel 5, Enable 0, DAC=1600, ADC Mean vs Hold

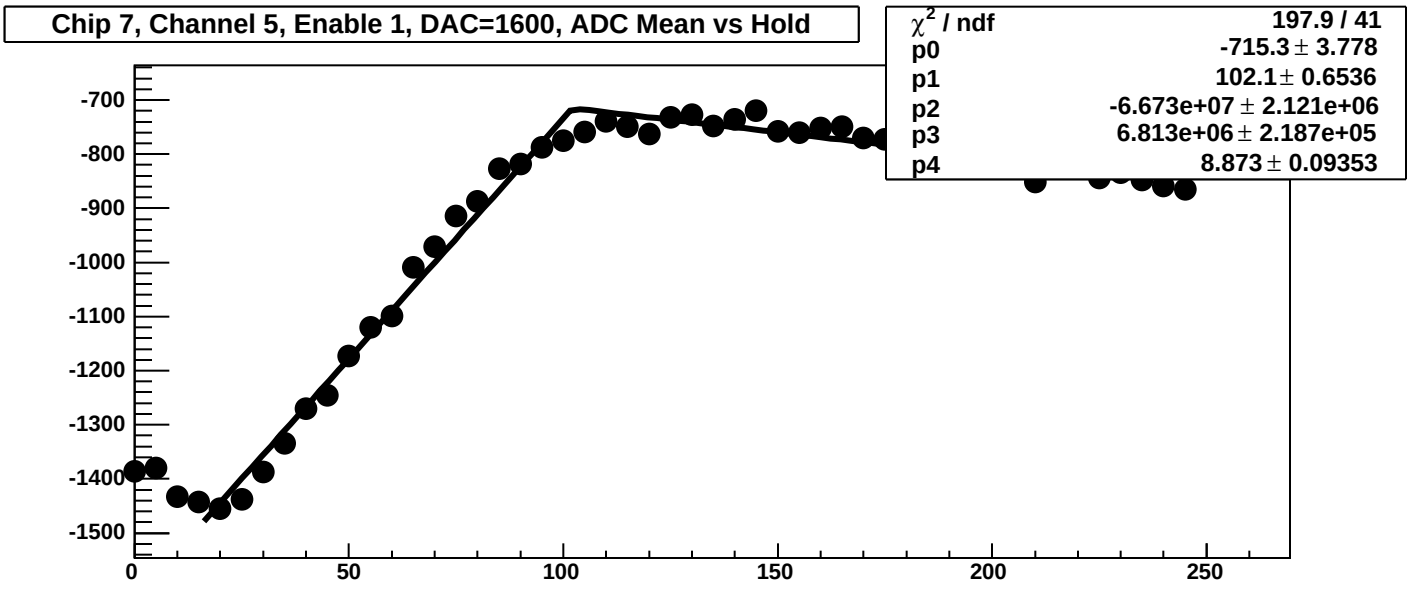


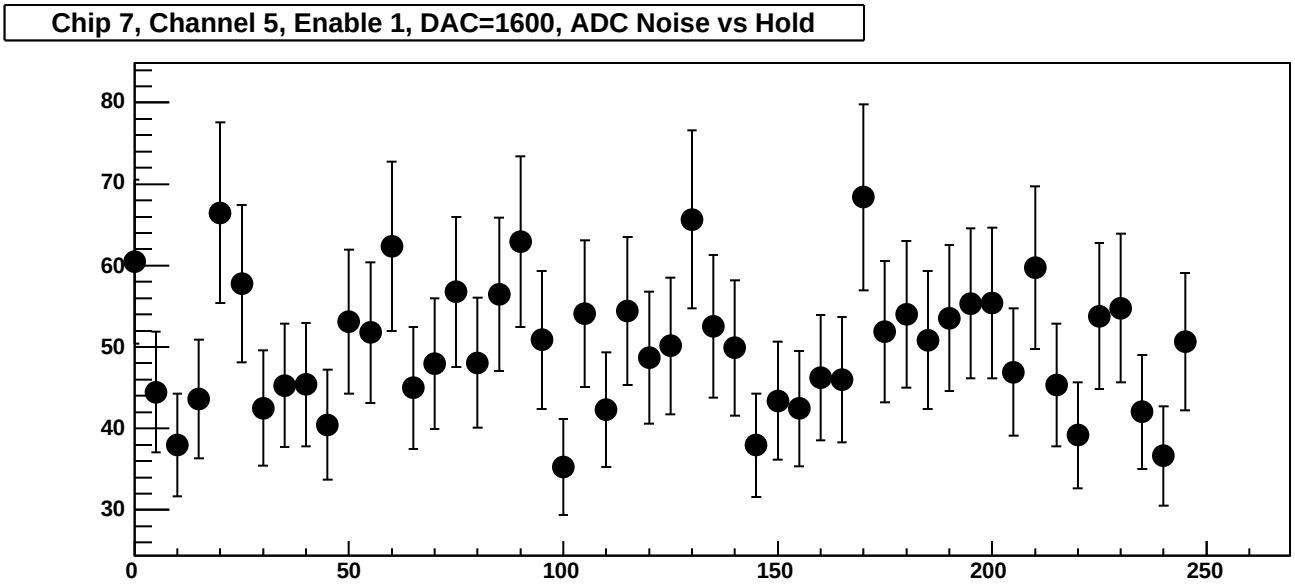
Chip 7, Channel 5, Enable 0, DAC=1600, ADC Noise vs Hold

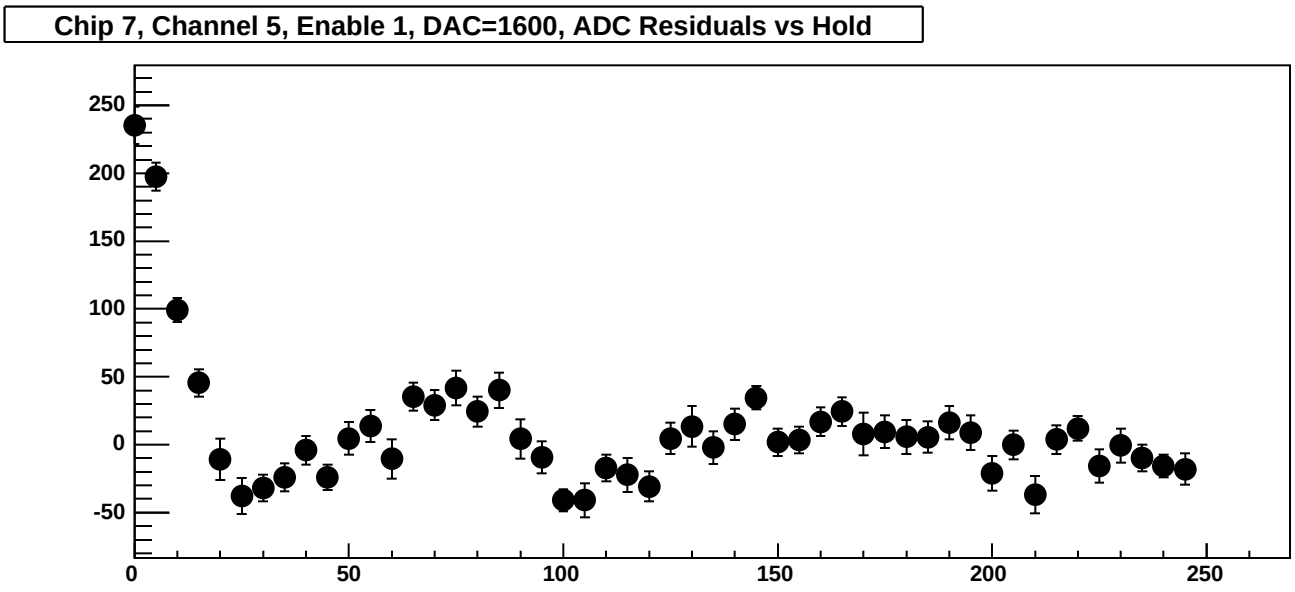


Chip 7, Channel 5, Enable 0, DAC=1600, ADC Residuals vs Hold

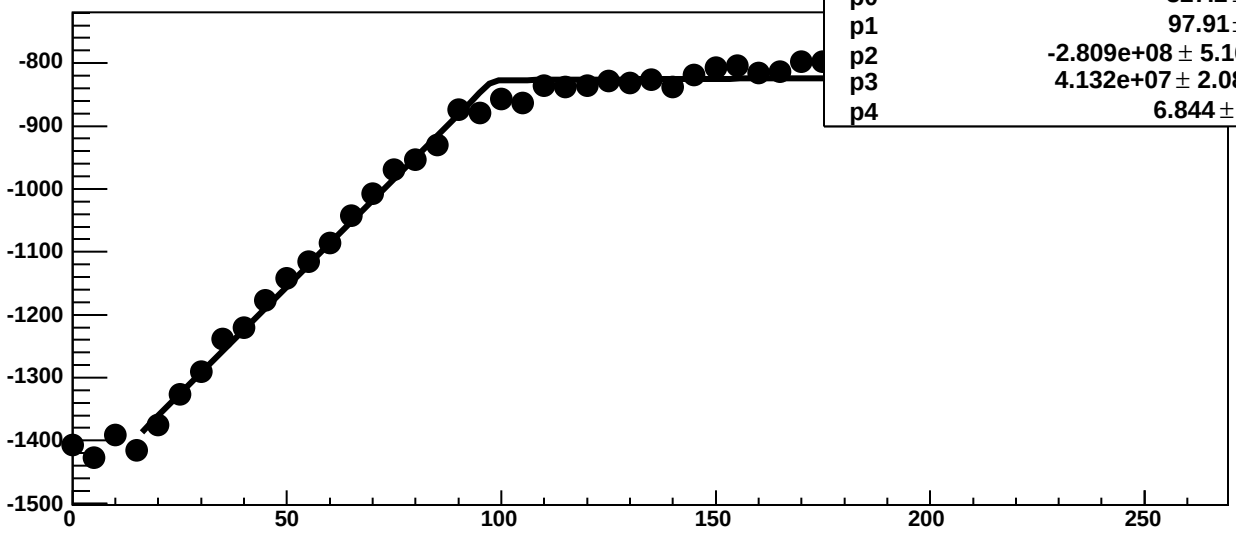



Chip 7, Channel 5, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 197.9 / 41
p0 -715.3 ± 3.778
p1 102.1 ± 0.6536
p2 -6.673e+07 ± 2.121e+06
p3 6.813e+06 ± 2.187e+05
p4 8.873 ± 0.09353
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250

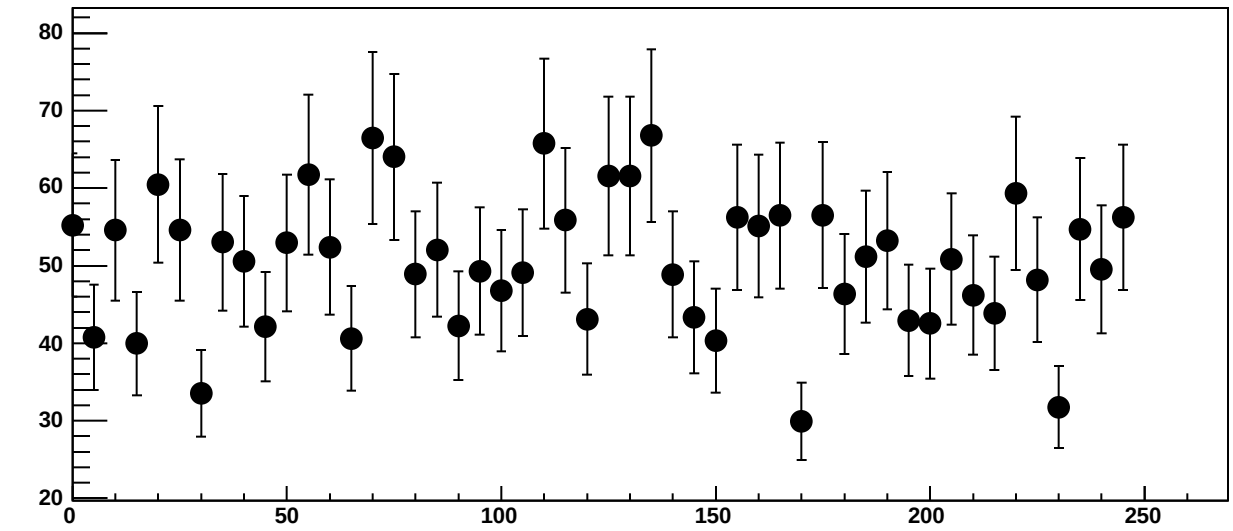

Chip 7, Channel 5, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 7, Channel 5, Enable 1, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
0
50
100
150
200
250

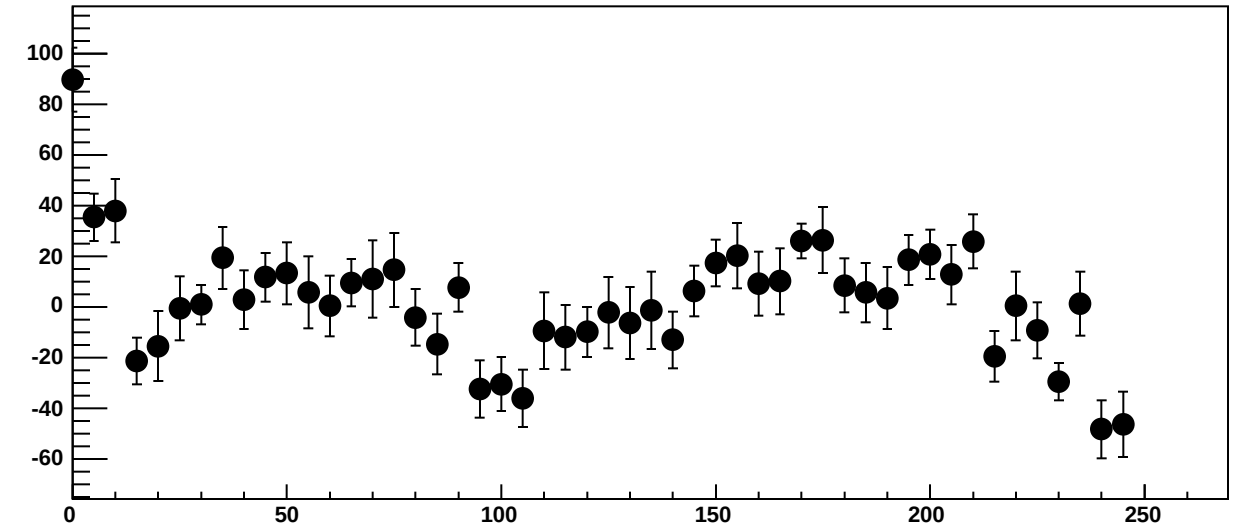
Chip 7, Channel 5, Enable 2, DAC=1600, ADC Mean vs Hold



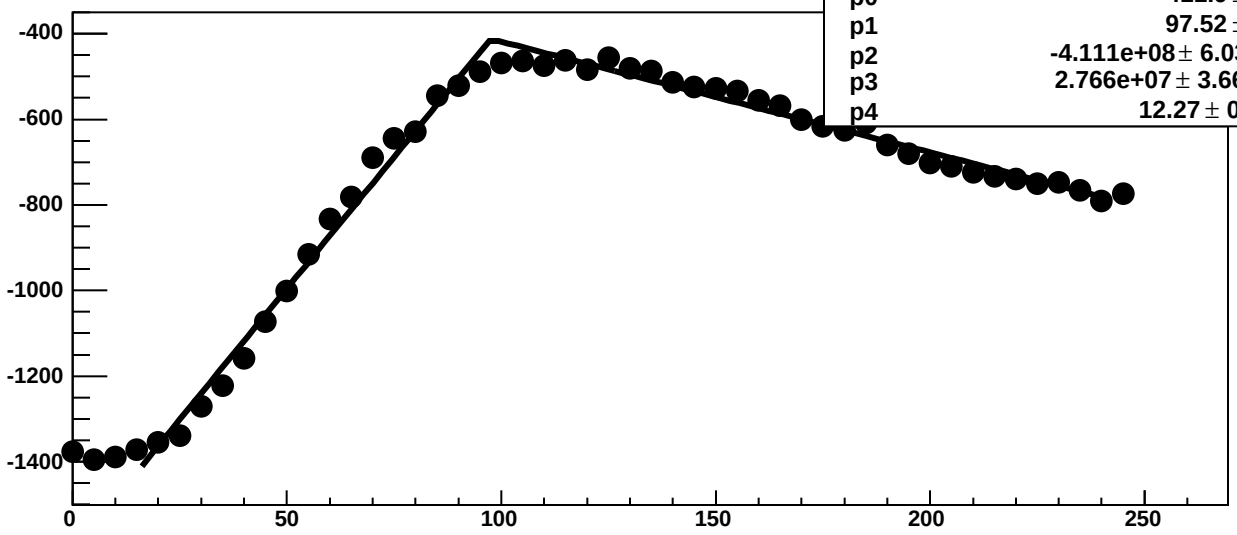
Chip 7, Channel 5, Enable 2, DAC=1600, ADC Noise vs Hold



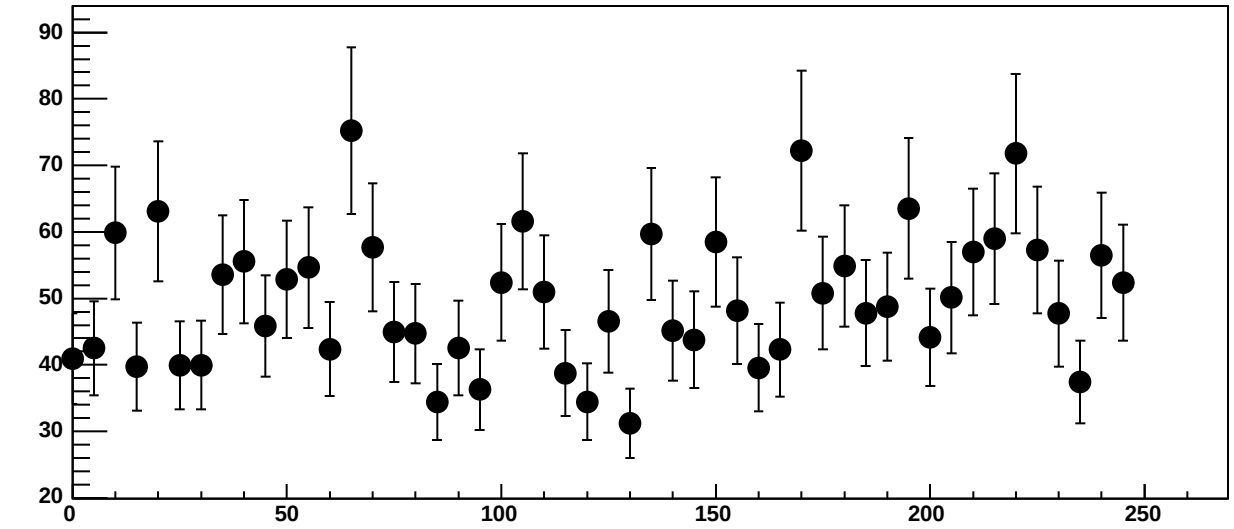
Chip 7, Channel 5, Enable 2, DAC=1600, ADC Residuals vs Hold



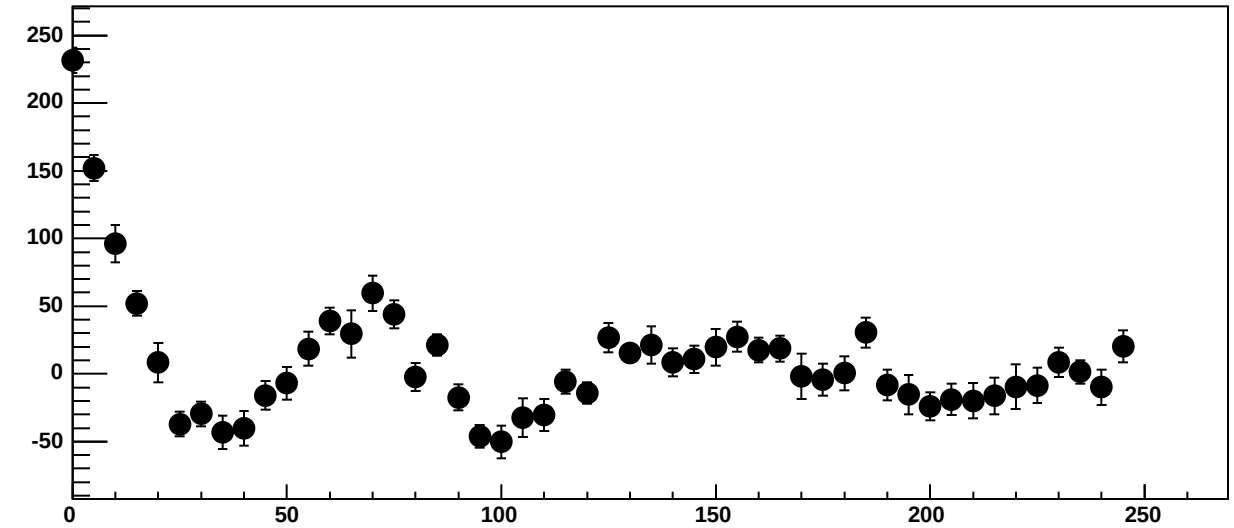
Chip 7, Channel 5, Enable 3, DAC=1600, ADC Mean vs Hold



Chip 7, Channel 5, Enable 3, DAC=1600, ADC Noise vs Hold

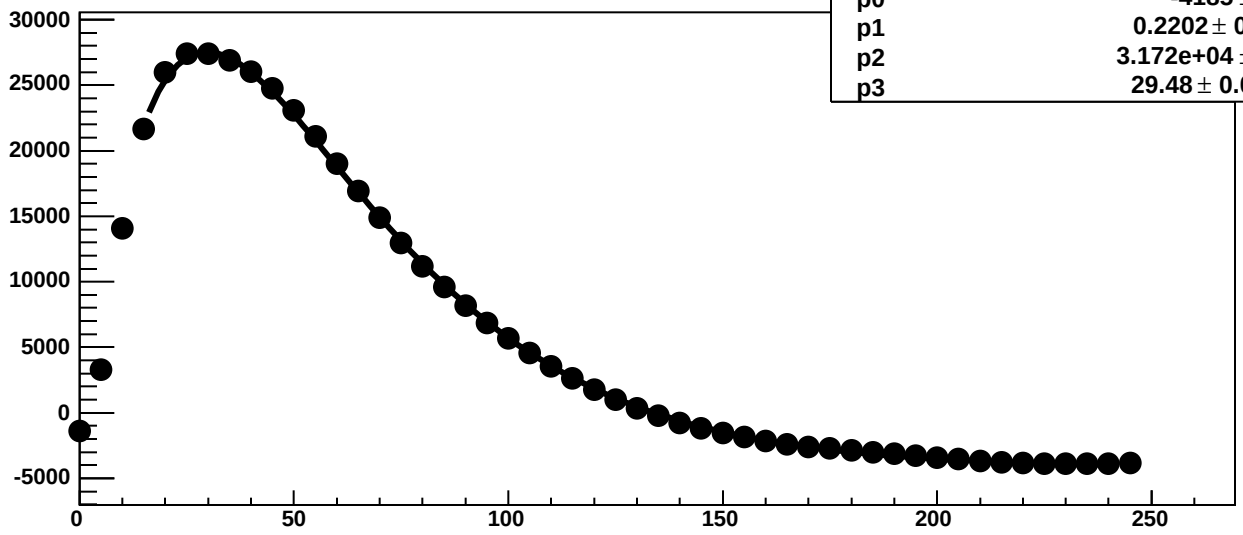


Chip 7, Channel 5, Enable 3, DAC=1600, ADC Residuals vs Hold

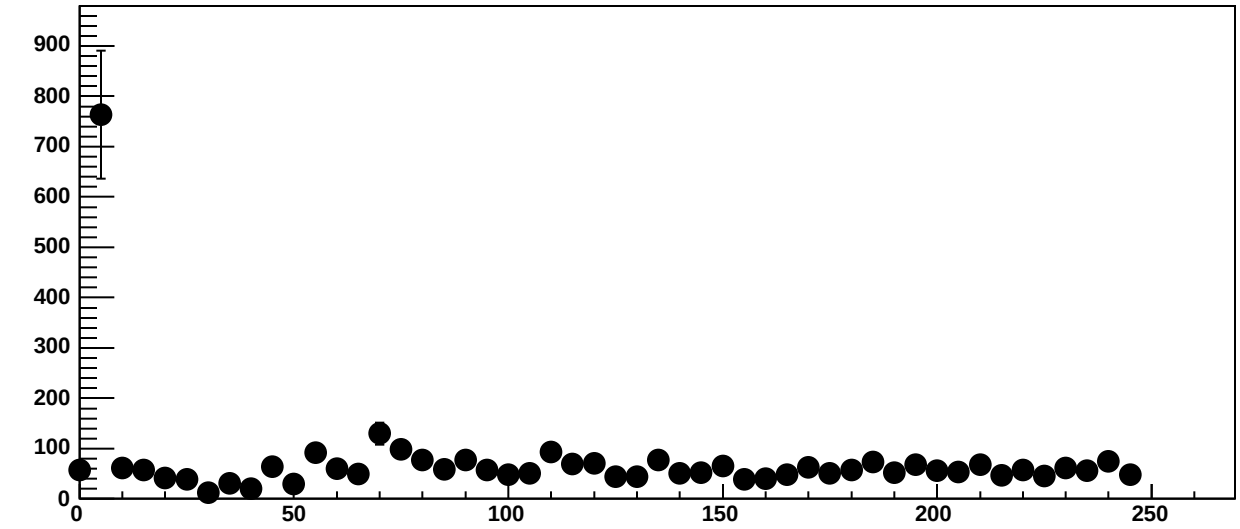


Chip 7, Channel 5, Enable 4!, DAC=1600, ADC Mean vs Hold

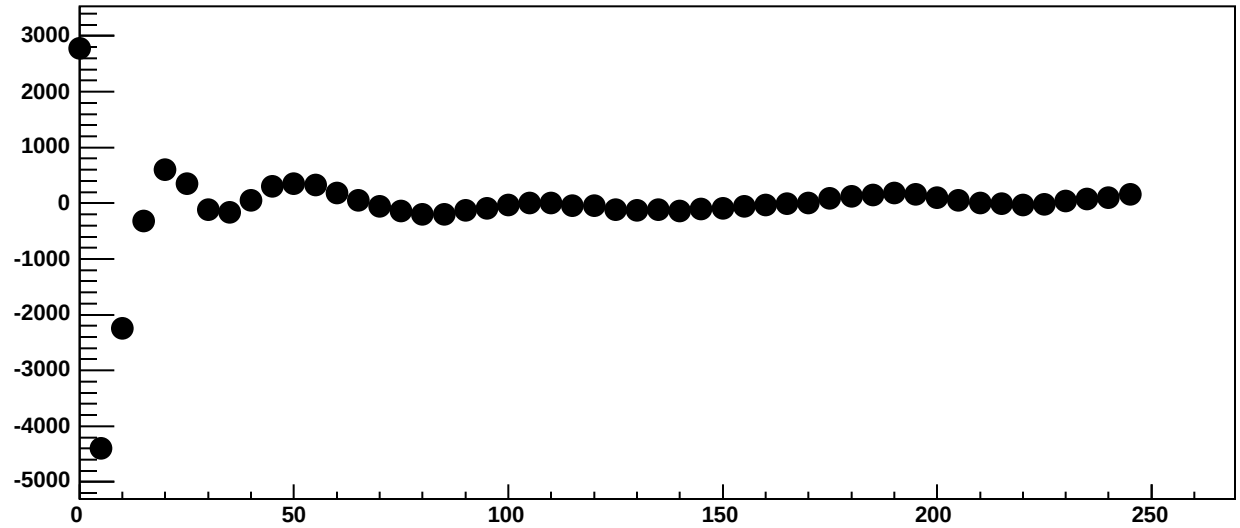
χ^2 / ndf	1.354e+04 / 42
p0	-4185 ± 3.615
p1	0.2202 ± 0.01442
p2	3.172e+04 ± 3.944
p3	29.48 ± 0.009578



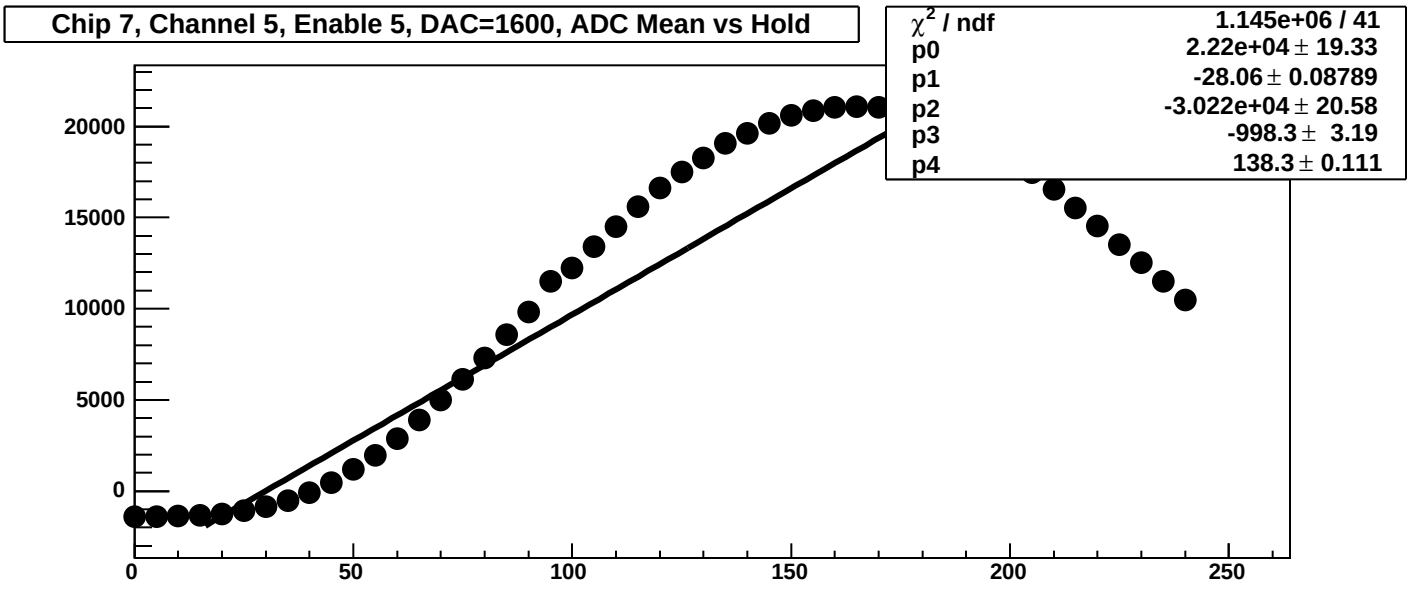
Chip 7, Channel 5, Enable 4!, DAC=1600, ADC Noise vs Hold



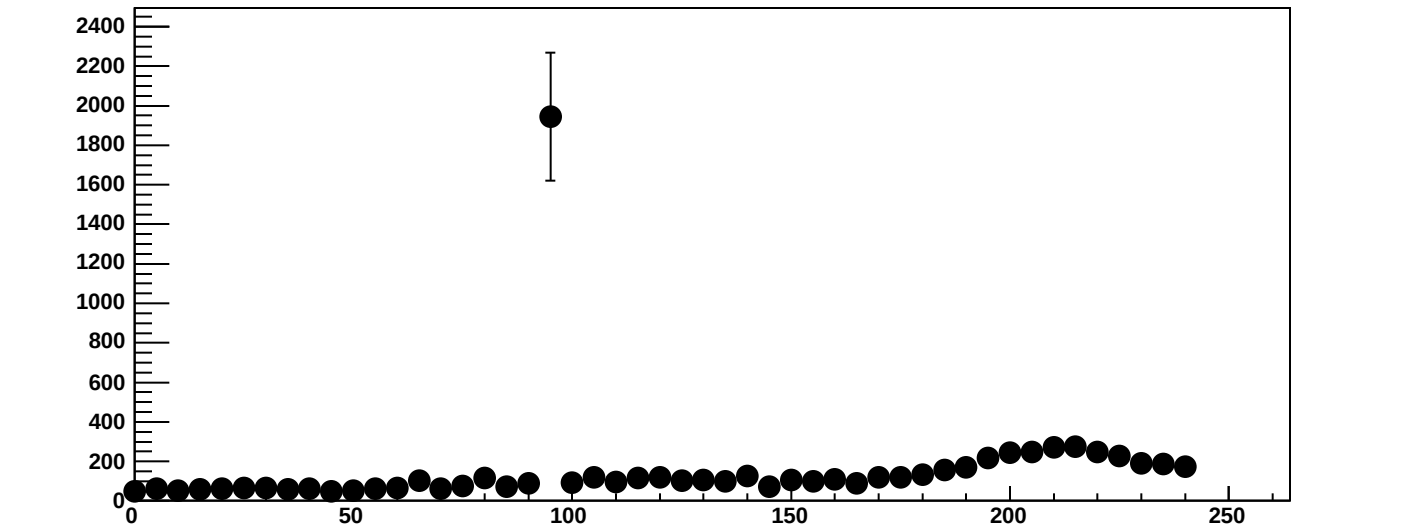
Chip 7, Channel 5, Enable 4!, DAC=1600, ADC Residuals vs Hold



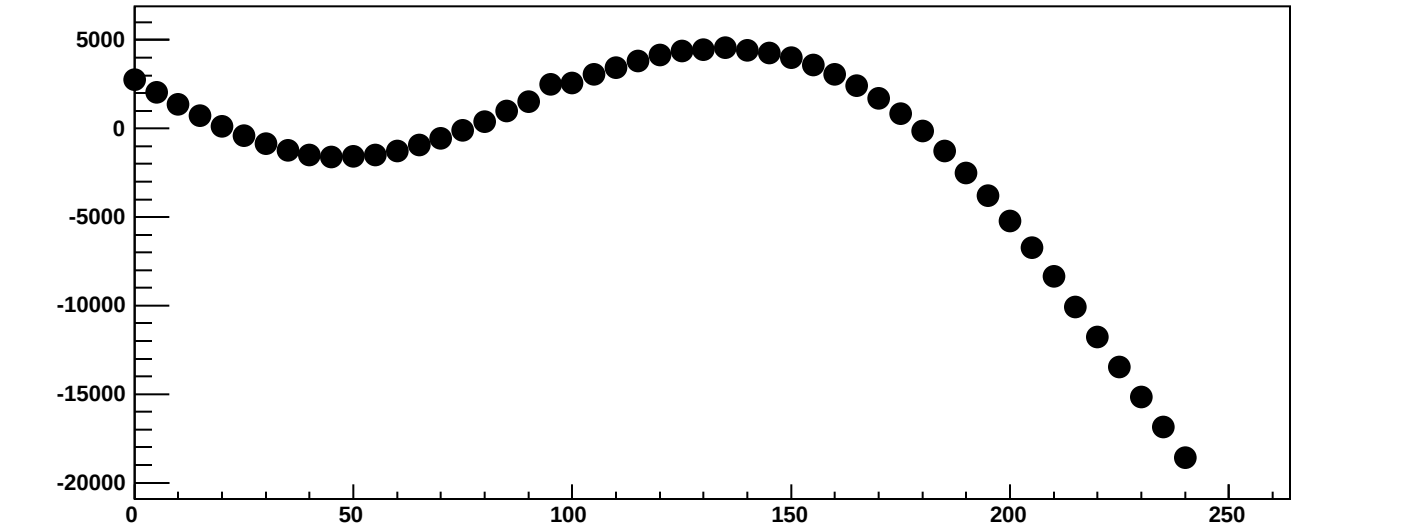
Chip 7, Channel 5, Enable 5, DAC=1600, ADC Mean vs Hold

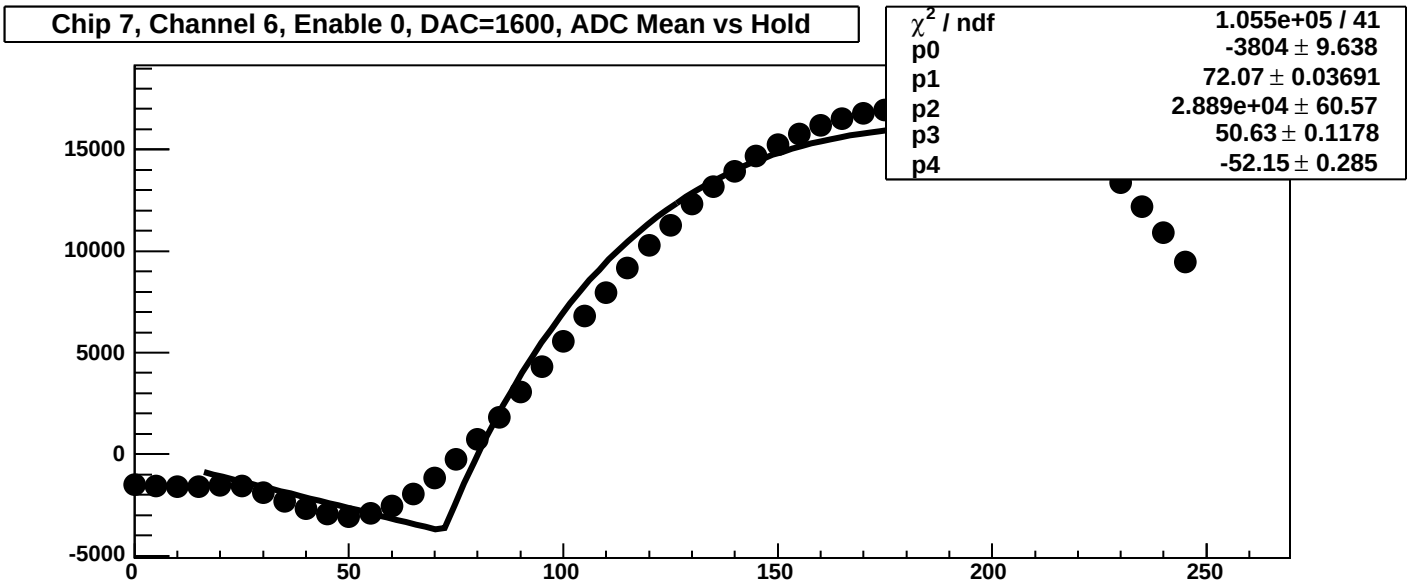


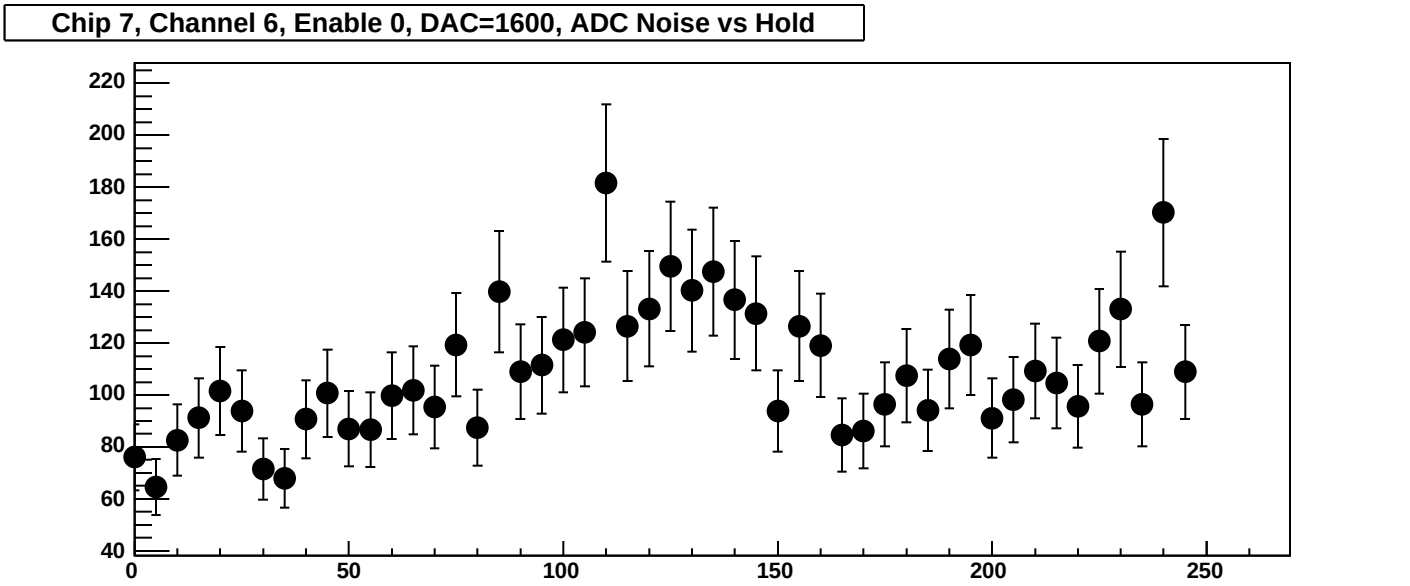
Chip 7, Channel 5, Enable 5, DAC=1600, ADC Noise vs Hold

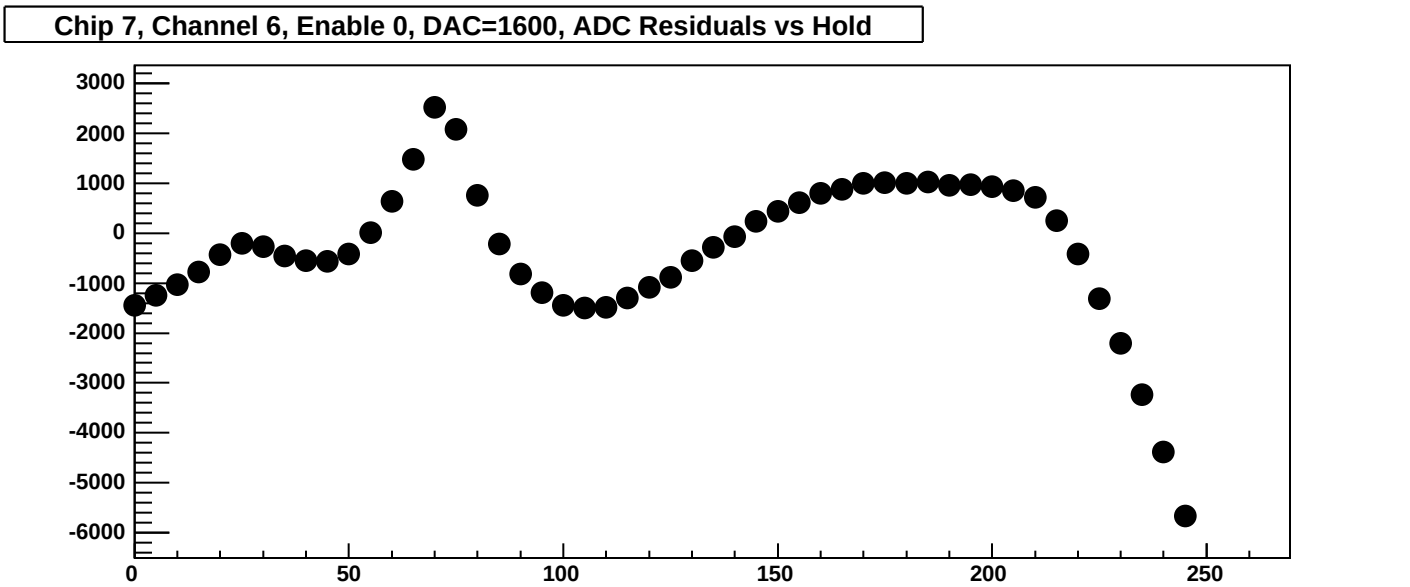


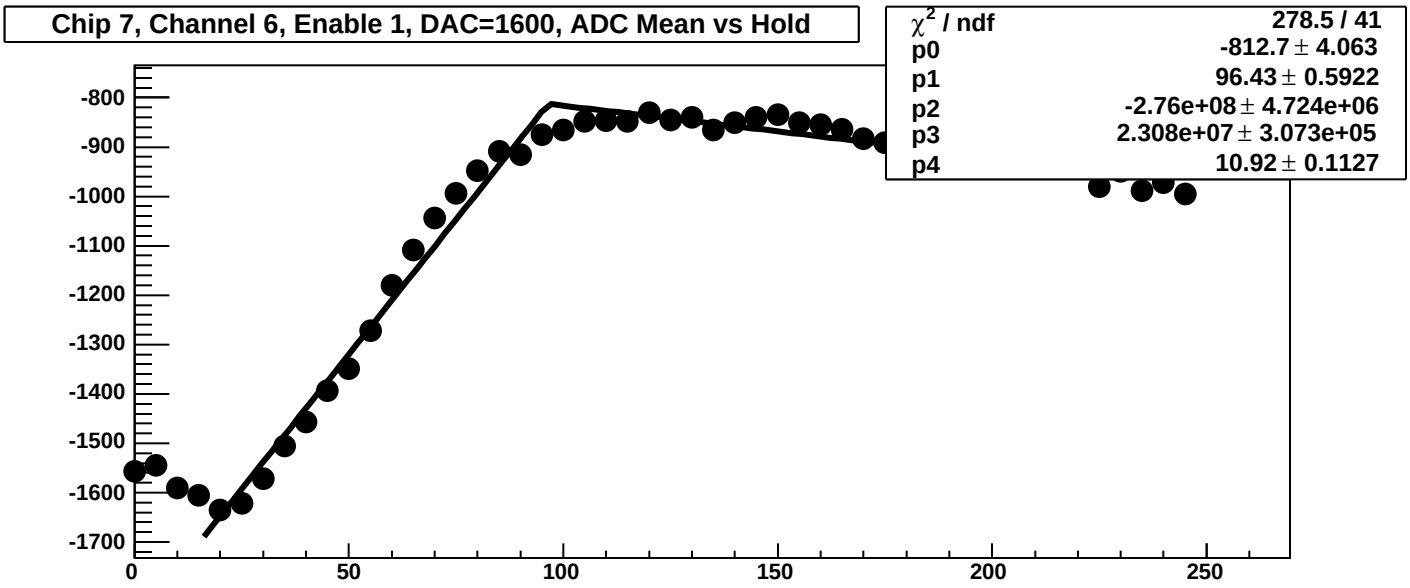
Chip 7, Channel 5, Enable 5, DAC=1600, ADC Residuals vs Hold

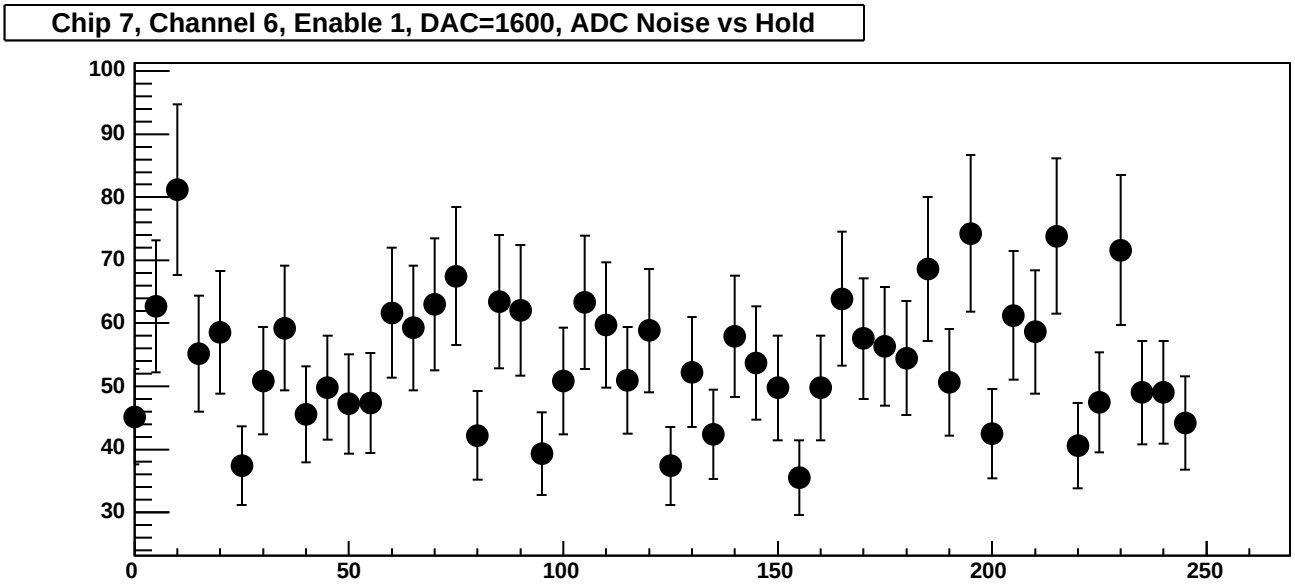


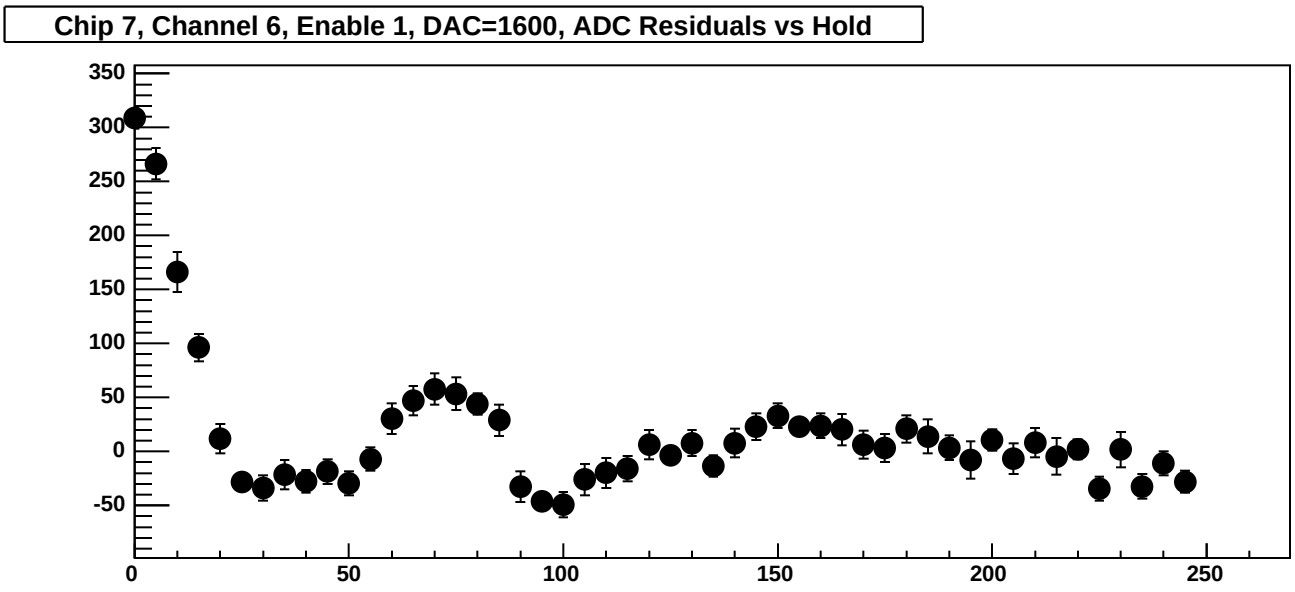

Chip 7, Channel 6, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.055e+05 / 41
p0 -3804 ± 9.638
p1 72.07 ± 0.03691
p2 2.889e+04 ± 60.57
p3 50.63 ± 0.1178
p4 -52.15 ± 0.285
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 7, Channel 6, Enable 0, DAC=1600, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250

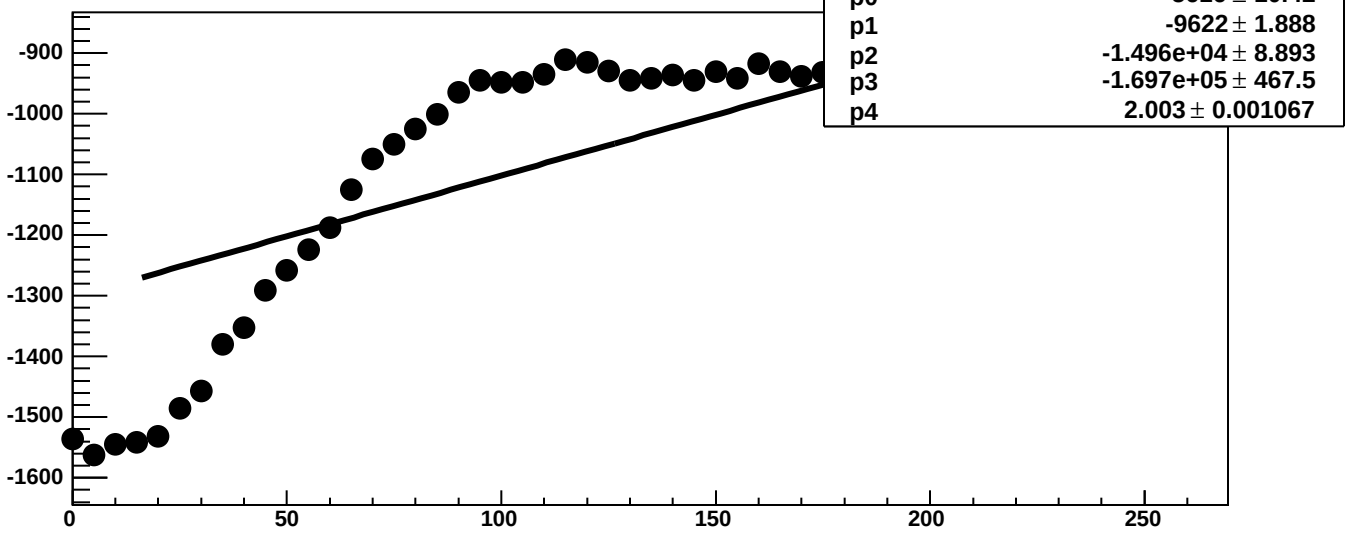

Chip 7, Channel 6, Enable 0, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
50
100
150
200
250


Chip 7, Channel 6, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 278.5 / 41
p0 -812.7 ± 4.063
p1 96.43 ± 0.5922
p2 -2.76e+08 ± 4.724e+06
p3 2.308e+07 ± 3.073e+05
p4 10.92 ± 0.1127
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
50
100
150
200
250

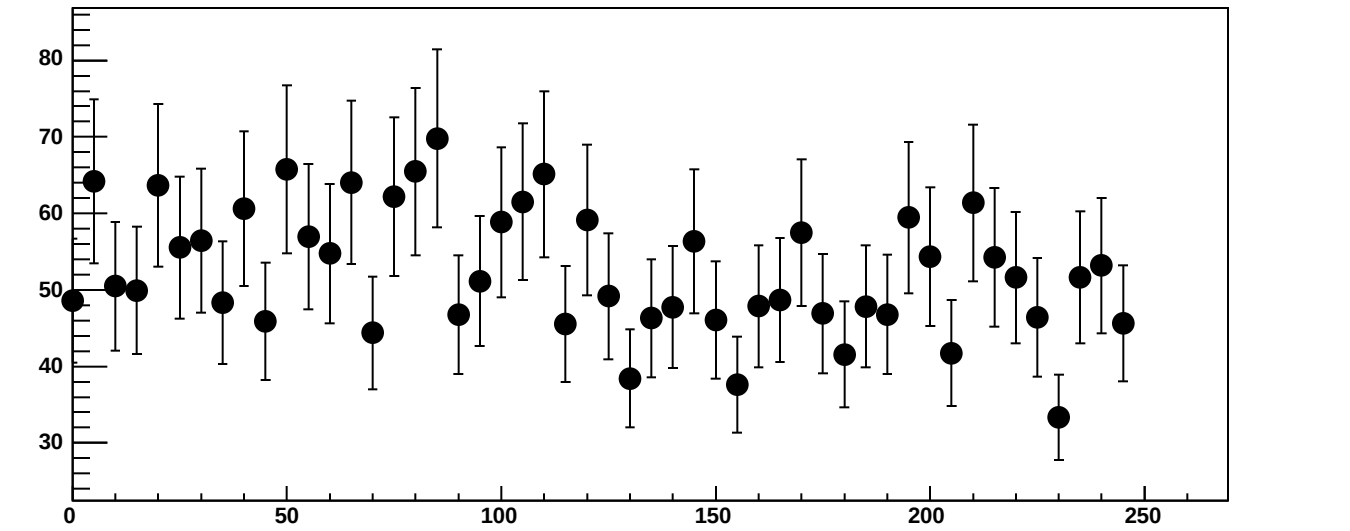

Chip 7, Channel 6, Enable 1, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 7, Channel 6, Enable 1, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
0
50
100
150
200
250

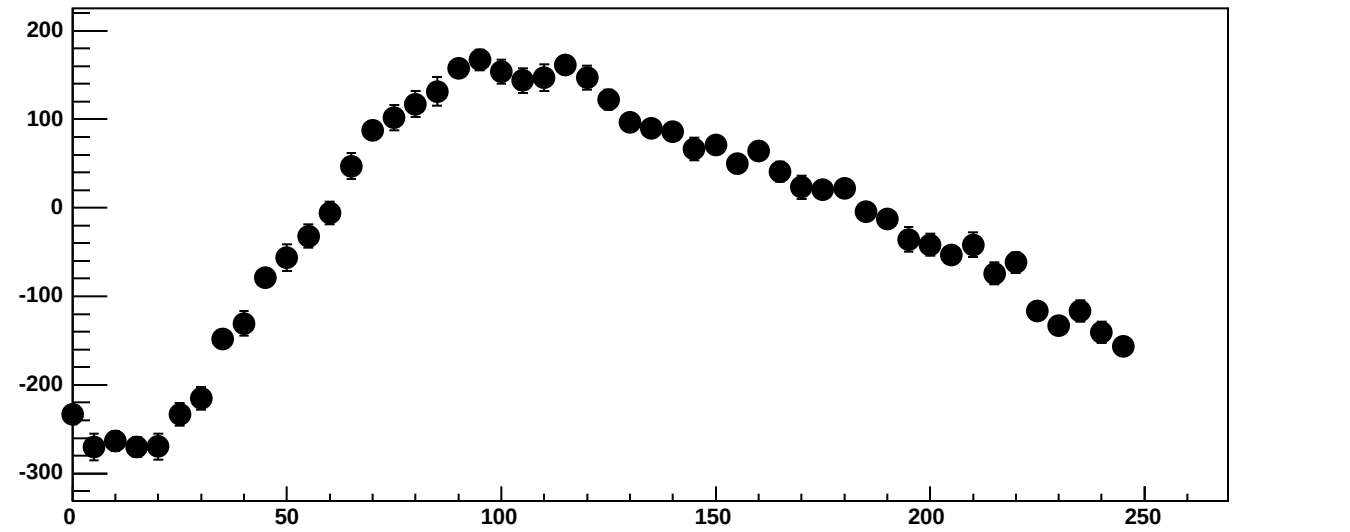
Chip 7, Channel 6, Enable 2, DAC=1600, ADC Mean vs Hold



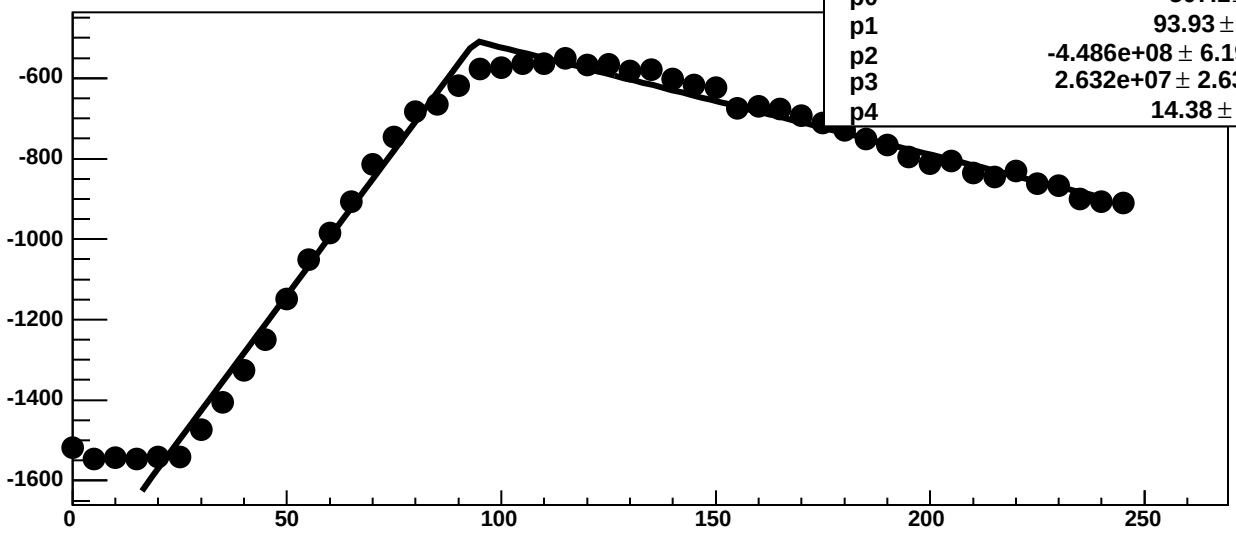
Chip 7, Channel 6, Enable 2, DAC=1600, ADC Noise vs Hold



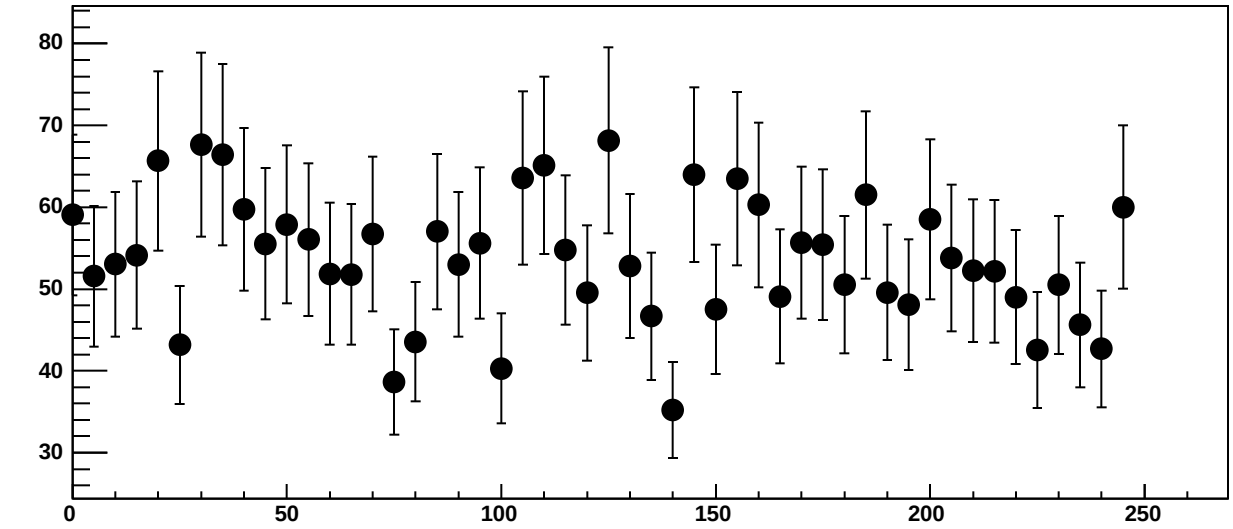
Chip 7, Channel 6, Enable 2, DAC=1600, ADC Residuals vs Hold



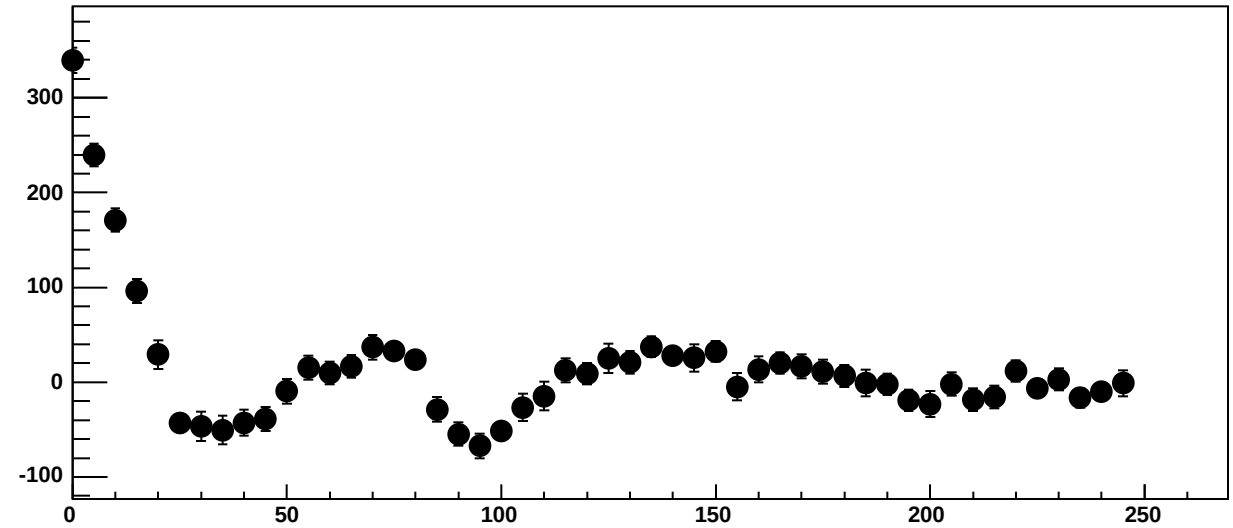
Chip 7, Channel 6, Enable 3, DAC=1600, ADC Mean vs Hold



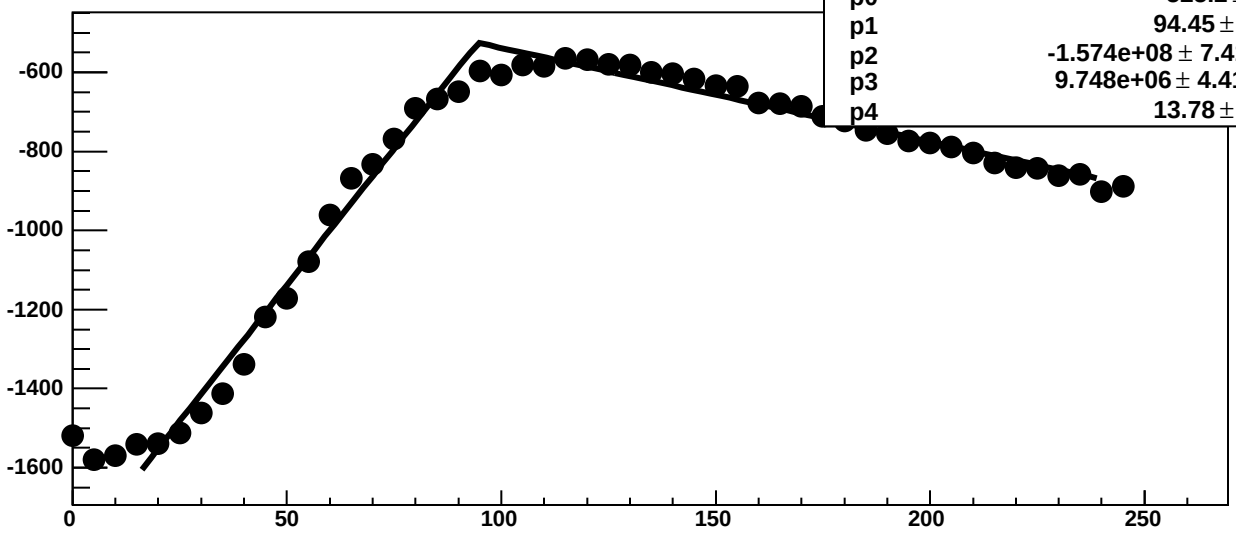
Chip 7, Channel 6, Enable 3, DAC=1600, ADC Noise vs Hold



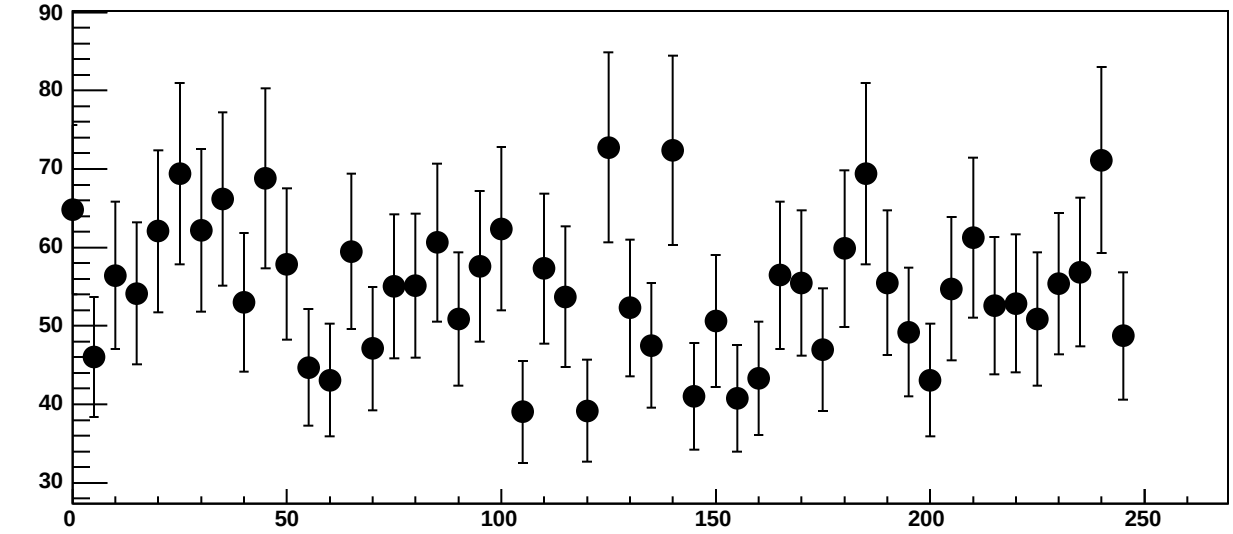
Chip 7, Channel 6, Enable 3, DAC=1600, ADC Residuals vs Hold



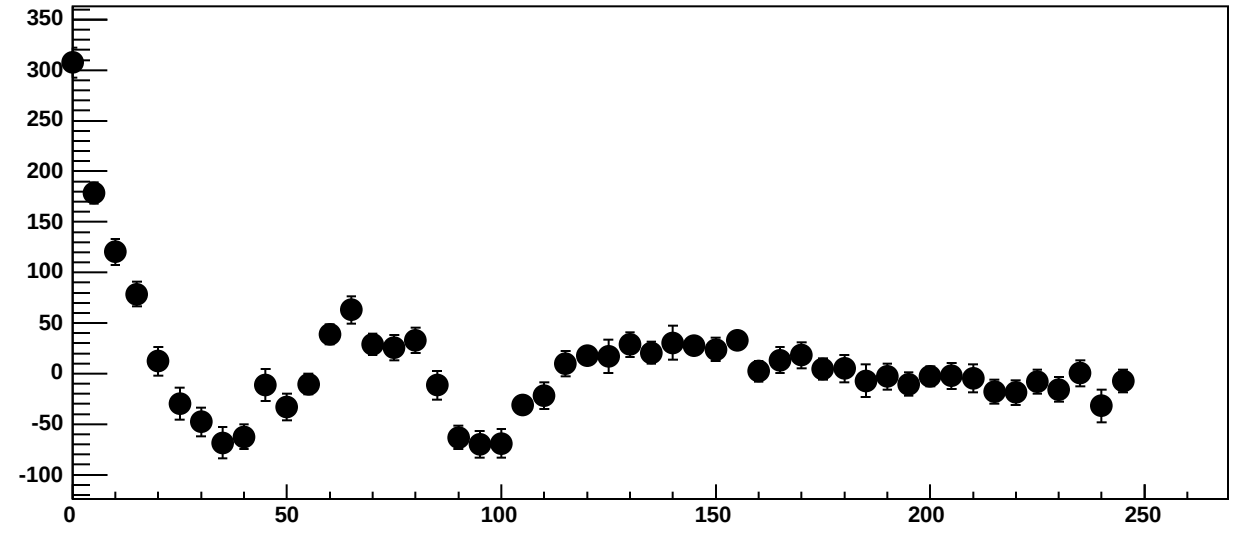
Chip 7, Channel 6, Enable 4, DAC=1600, ADC Mean vs Hold

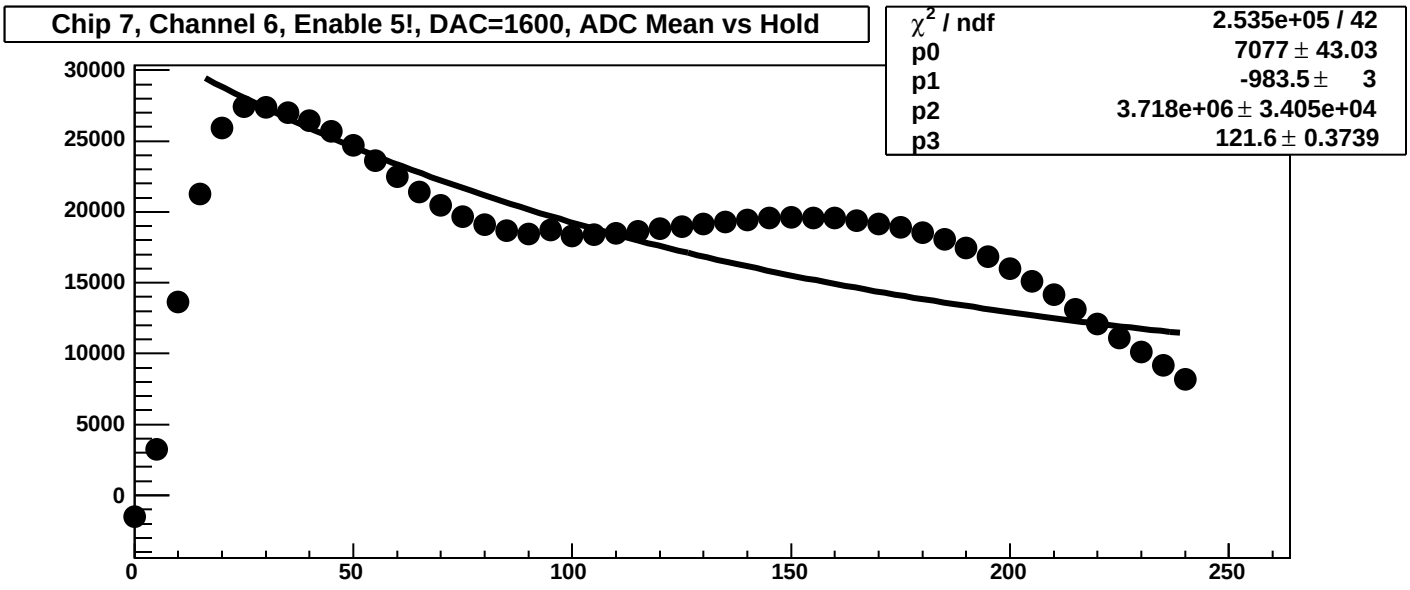


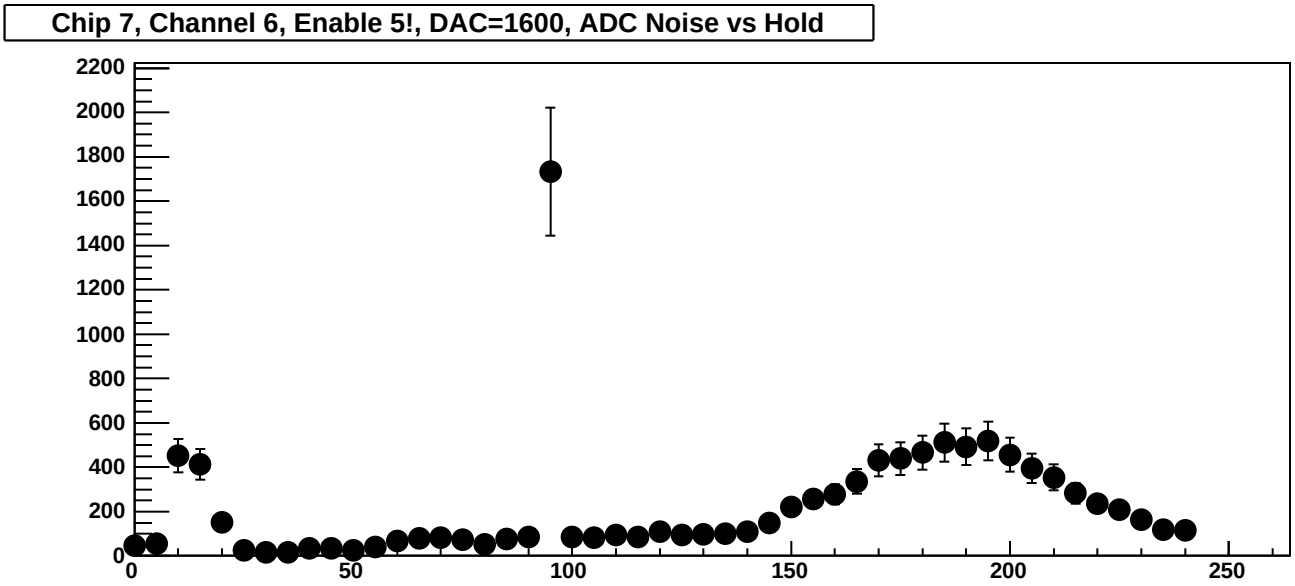
Chip 7, Channel 6, Enable 4, DAC=1600, ADC Noise vs Hold

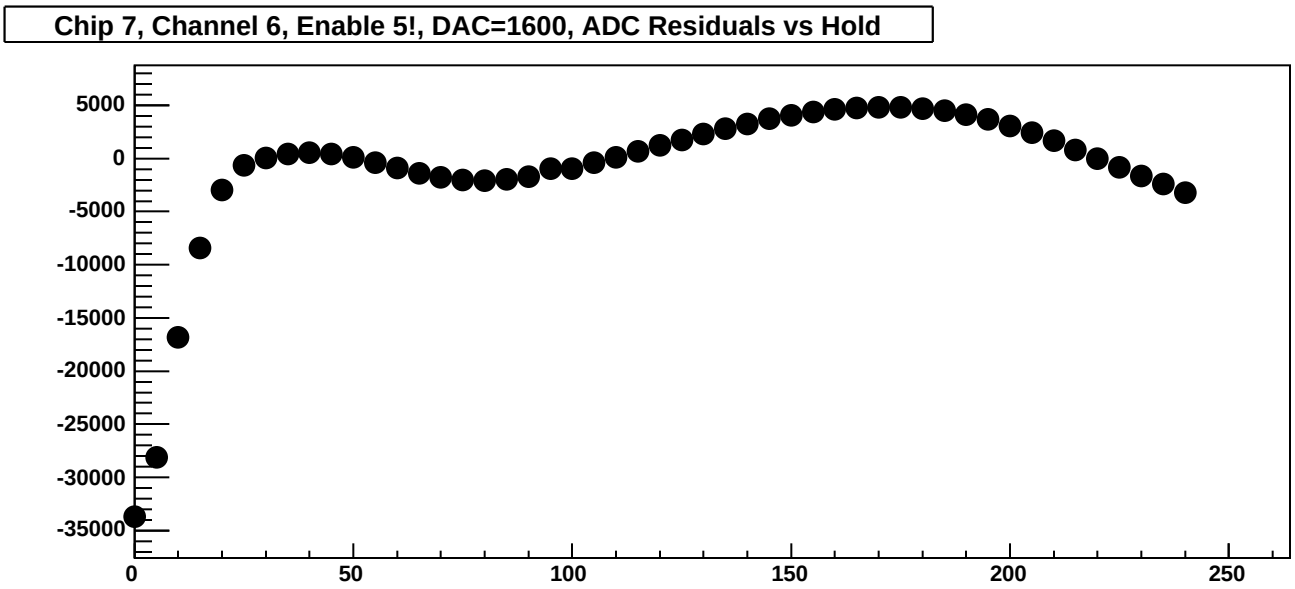


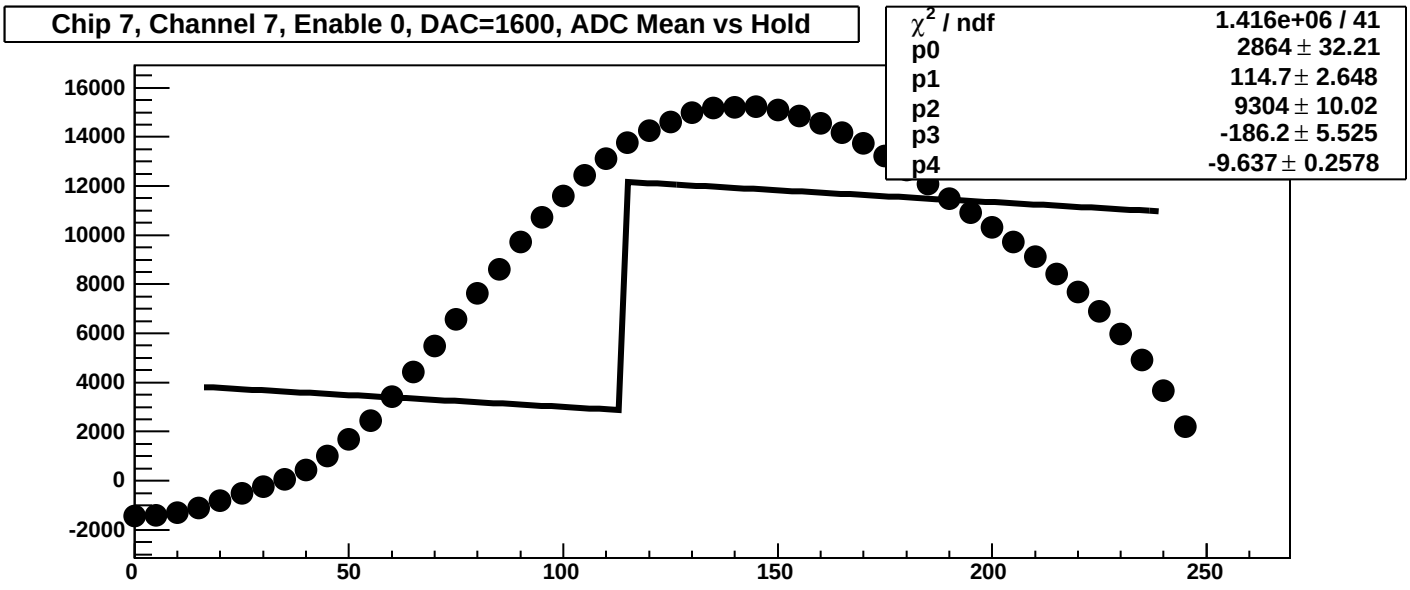
Chip 7, Channel 6, Enable 4, DAC=1600, ADC Residuals vs Hold

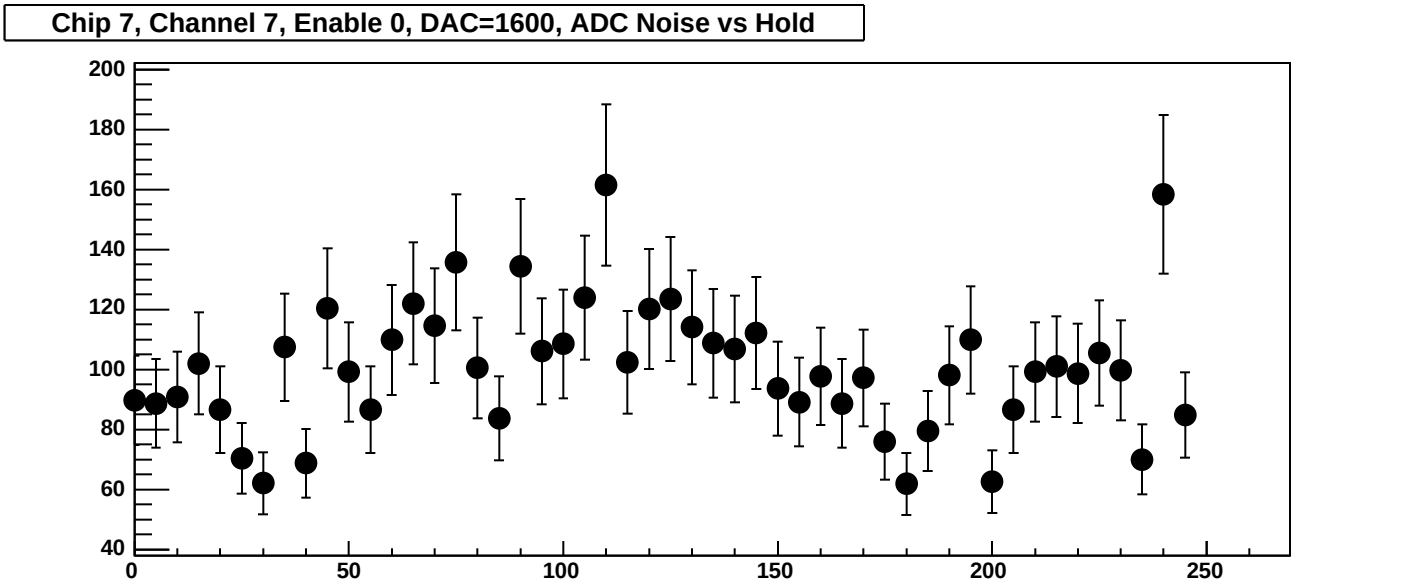


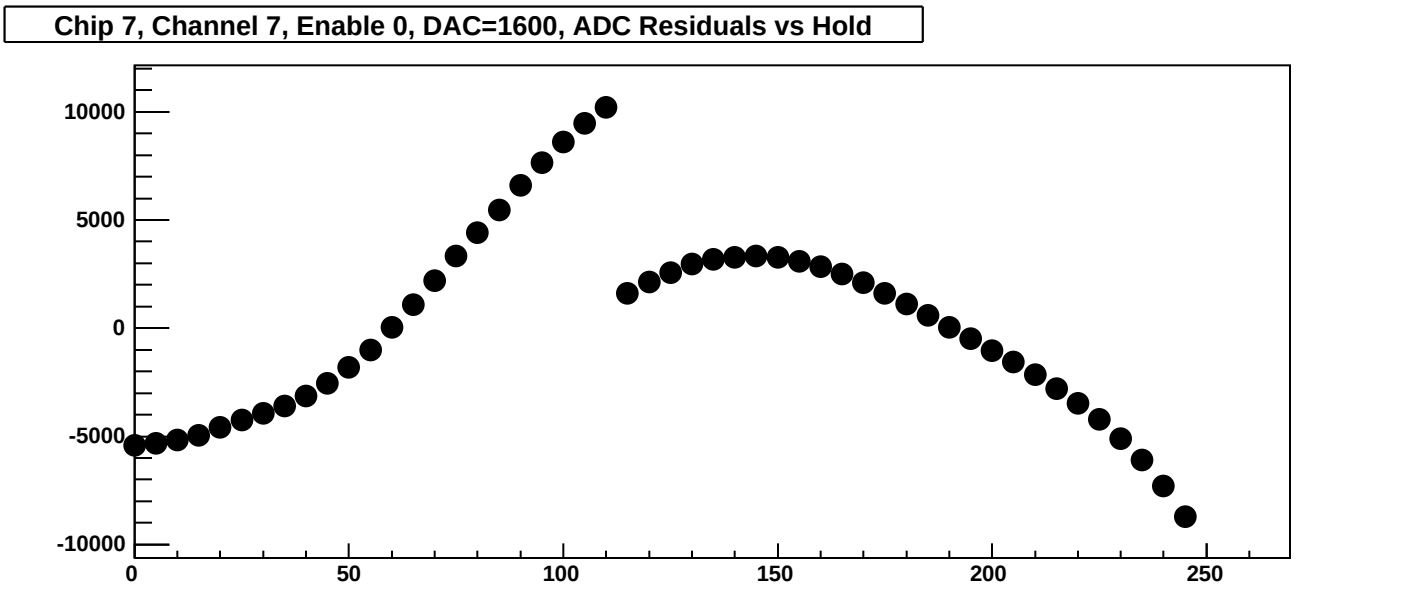

Chip 7, Channel 6, Enable 5!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.535e+05 / 42
p0 7077 ± 43.03
p1 -983.5 ± 3
p2 3.718e+06 ± 3.405e+04
p3 121.6 ± 0.3739
30000
25000
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 7, Channel 6, Enable 5!, DAC=1600, ADC Noise vs Hold
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

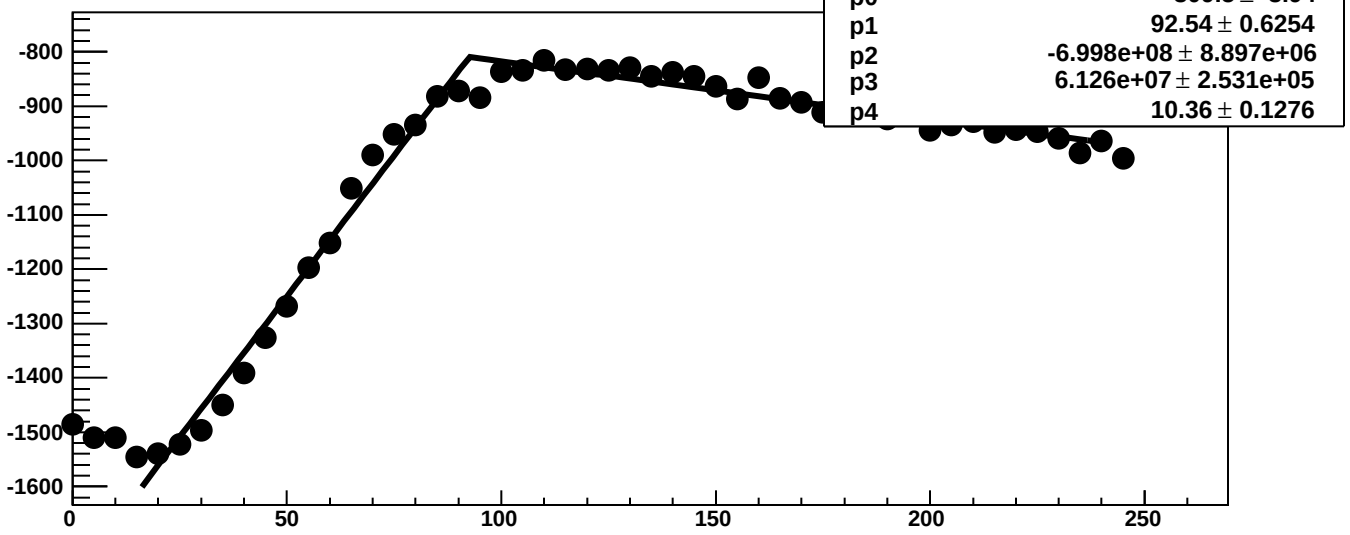

Chip 7, Channel 6, Enable 5!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
-35000
0
50
100
150
200
250


Chip 7, Channel 7, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.416e+06 / 41
p0 2864 ± 32.21
p1 114.7 ± 2.648
p2 9304 ± 10.02
p3 -186.2 ± 5.525
p4 -9.637 ± 0.2578
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

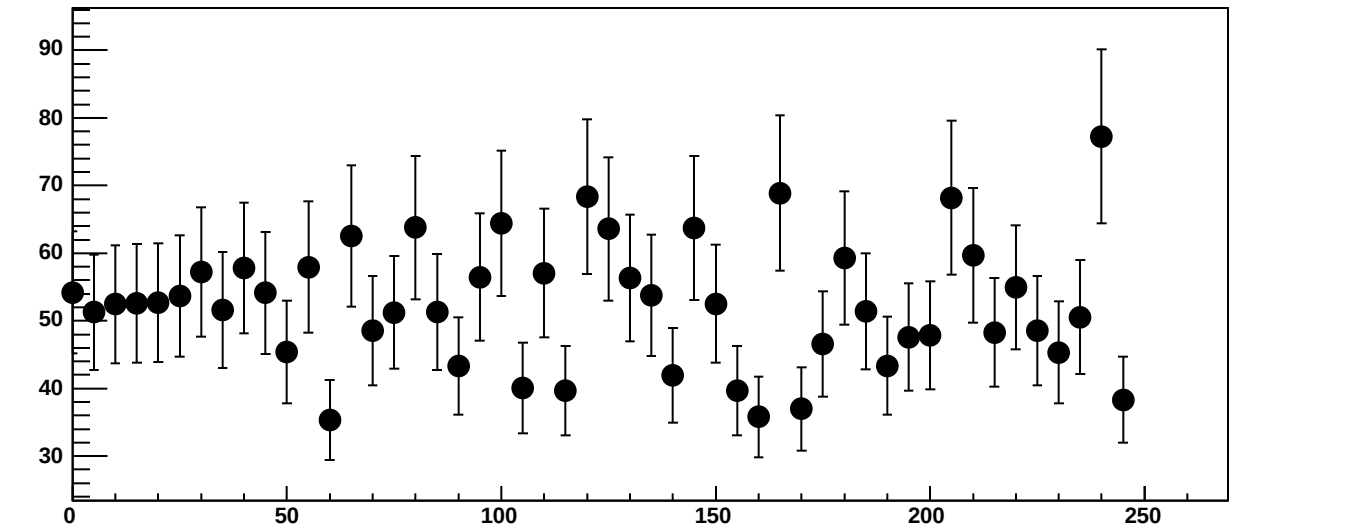

Chip 7, Channel 7, Enable 0, DAC=1600, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250


Chip 7, Channel 7, Enable 0, DAC=1600, ADC Residuals vs Hold
10000
5000
0
-5000
-10000
0
50
100
150
200
250

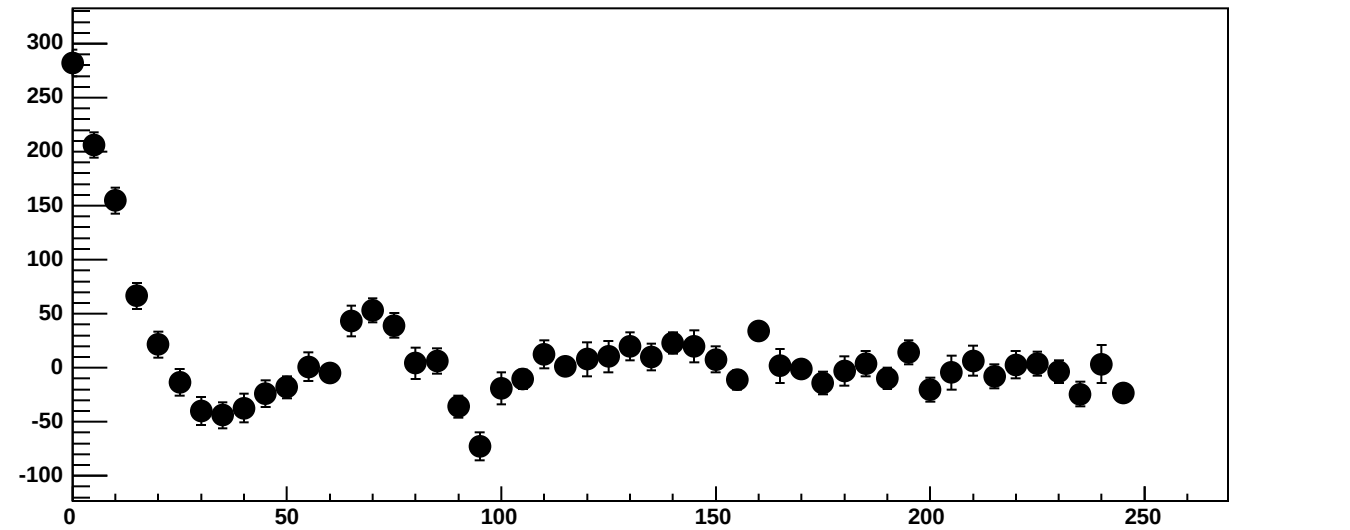
Chip 7, Channel 7, Enable 1, DAC=1600, ADC Mean vs Hold

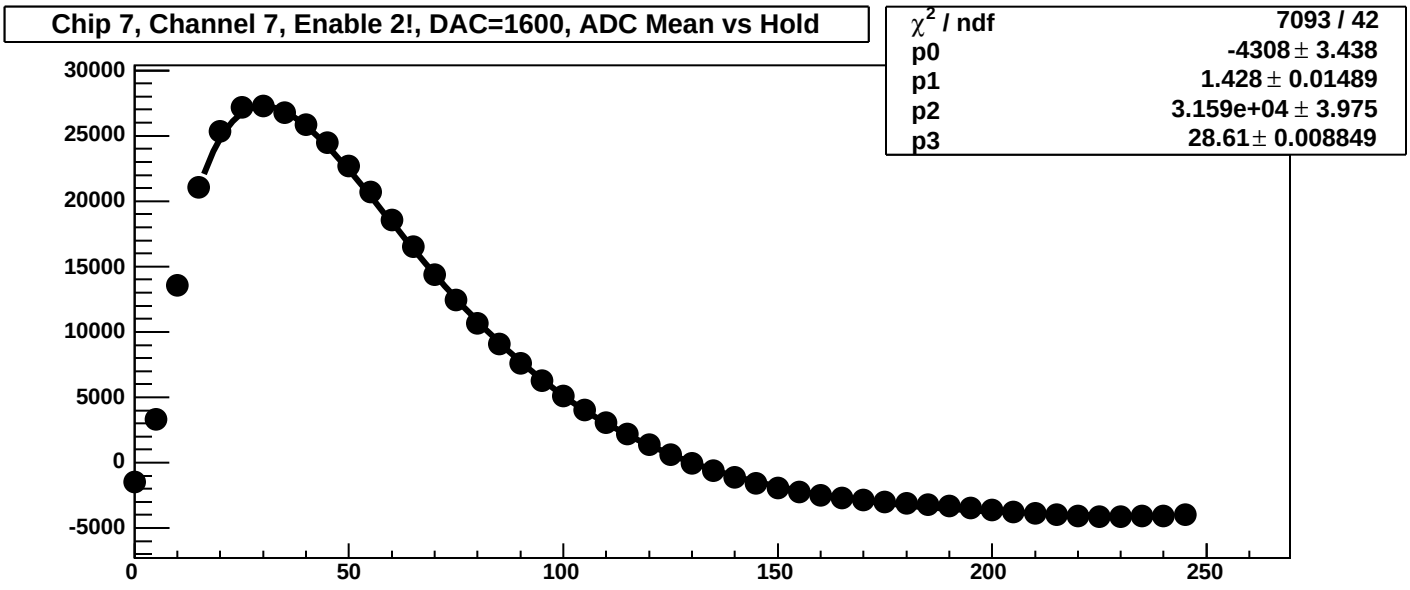


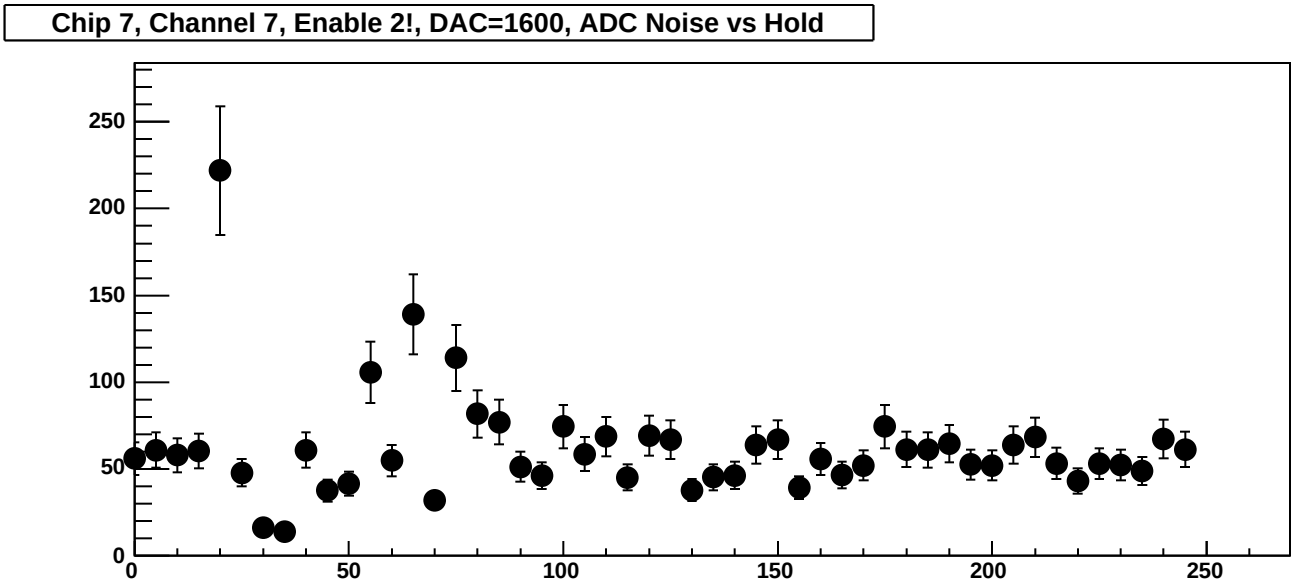
Chip 7, Channel 7, Enable 1, DAC=1600, ADC Noise vs Hold

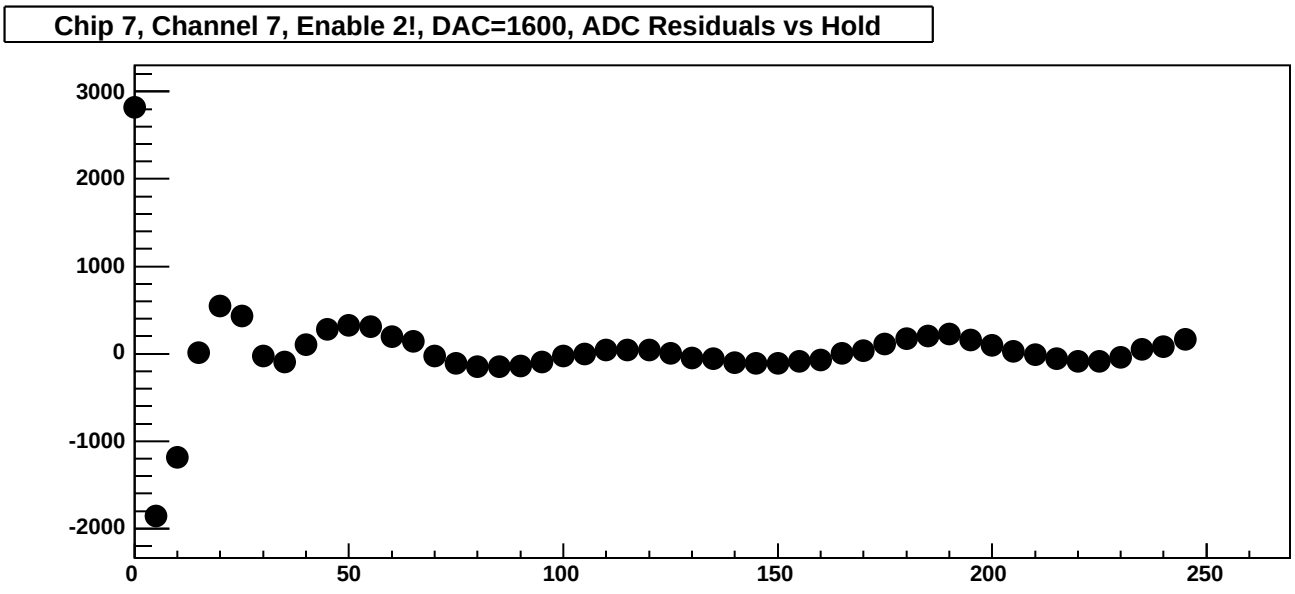


Chip 7, Channel 7, Enable 1, DAC=1600, ADC Residuals vs Hold

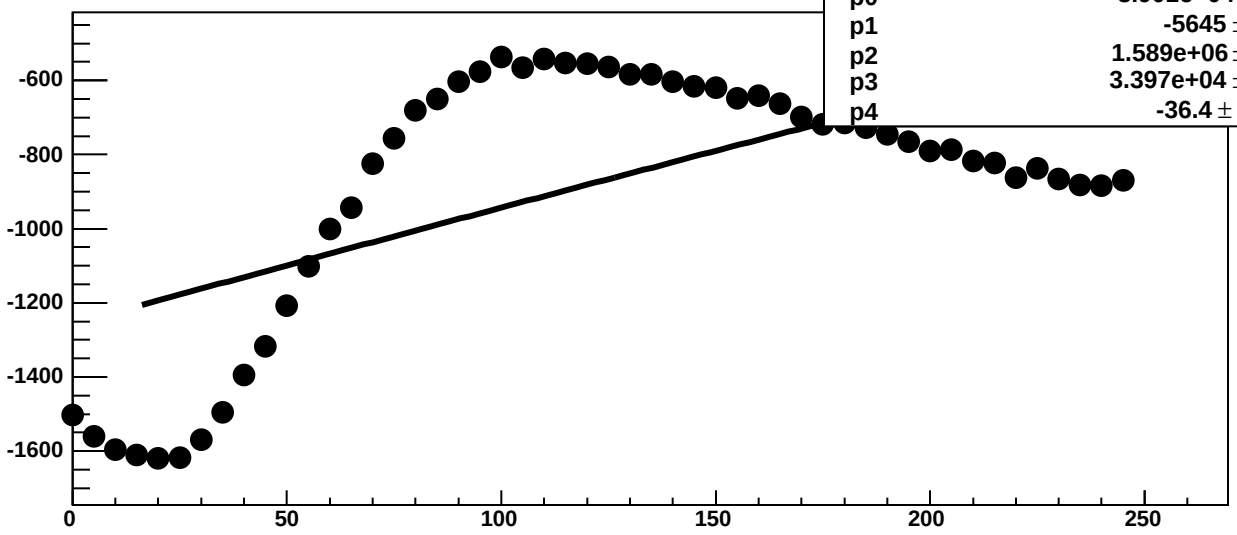



Chip 7, Channel 7, Enable 2!, DAC=1600, ADC Mean vs Hold
χ² / ndf 7093 / 42
p0 -4308 ± 3.438
p1 1.428 ± 0.01489
p2 3.159e+04 ± 3.975
p3 28.61 ± 0.008849
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

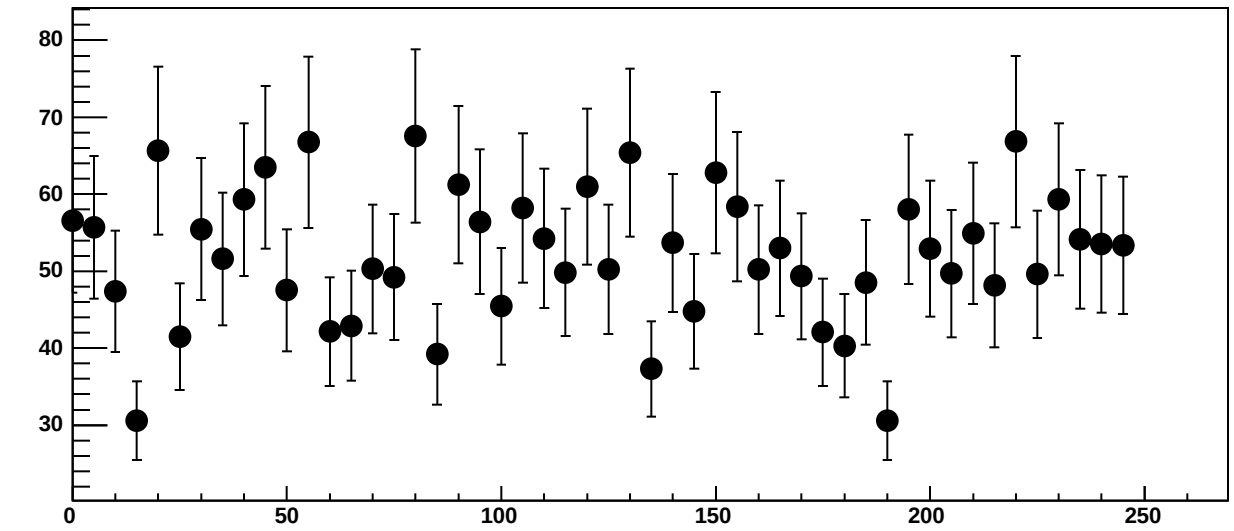

Chip 7, Channel 7, Enable 2!, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250


Chip 7, Channel 7, Enable 2!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
0
50
100
150
200
250

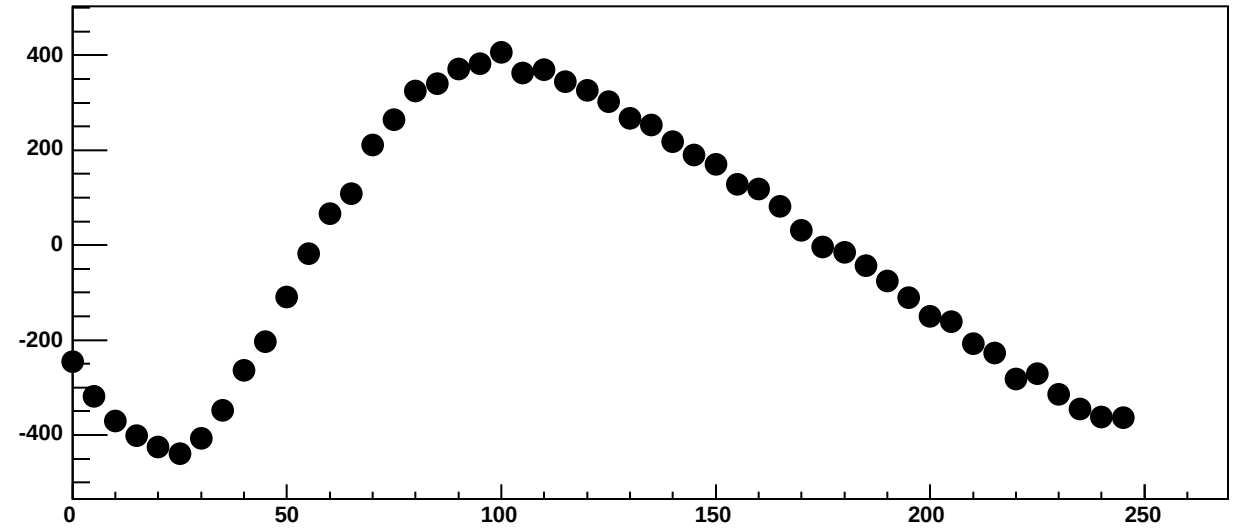
Chip 7, Channel 7, Enable 3, DAC=1600, ADC Mean vs Hold

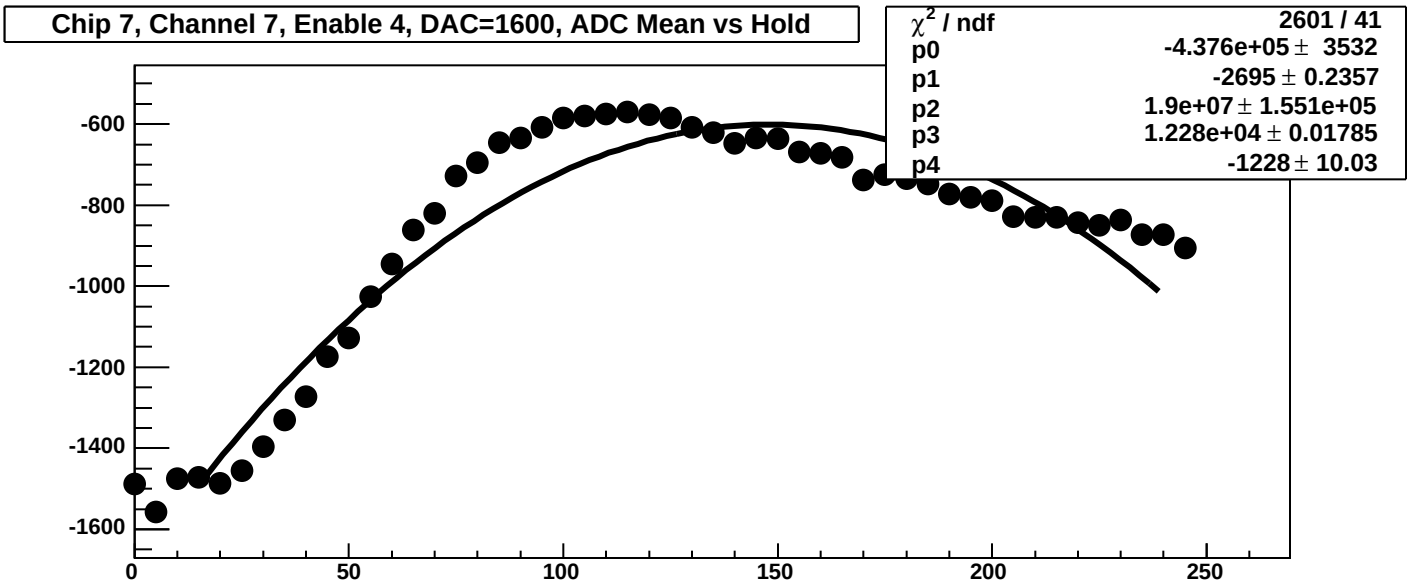


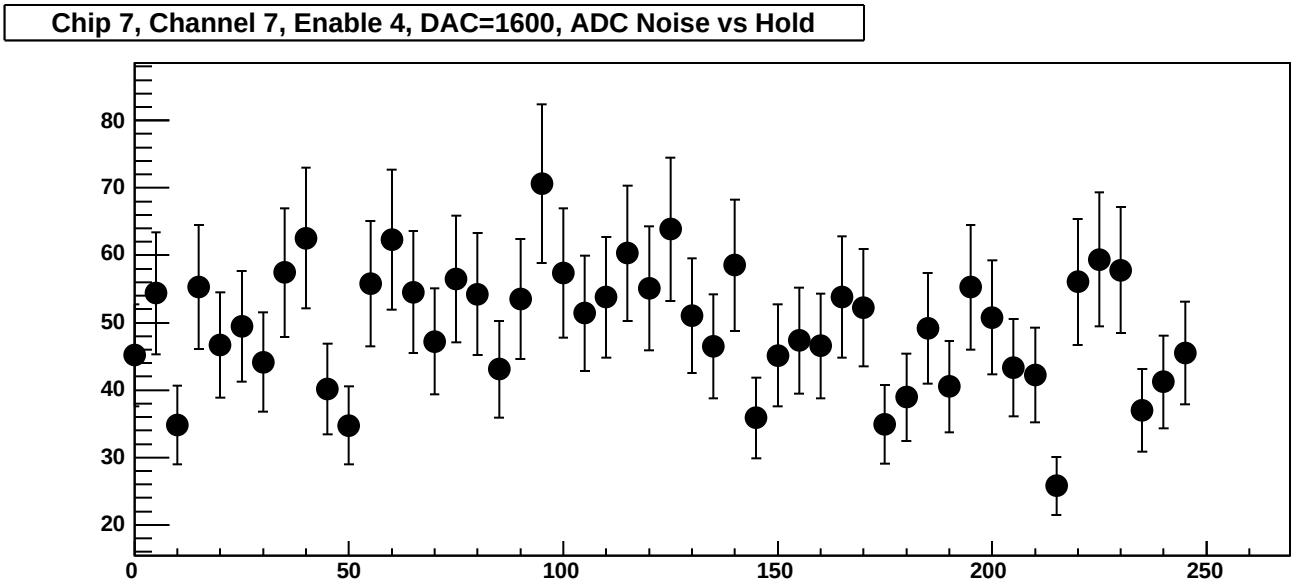
Chip 7, Channel 7, Enable 3, DAC=1600, ADC Noise vs Hold

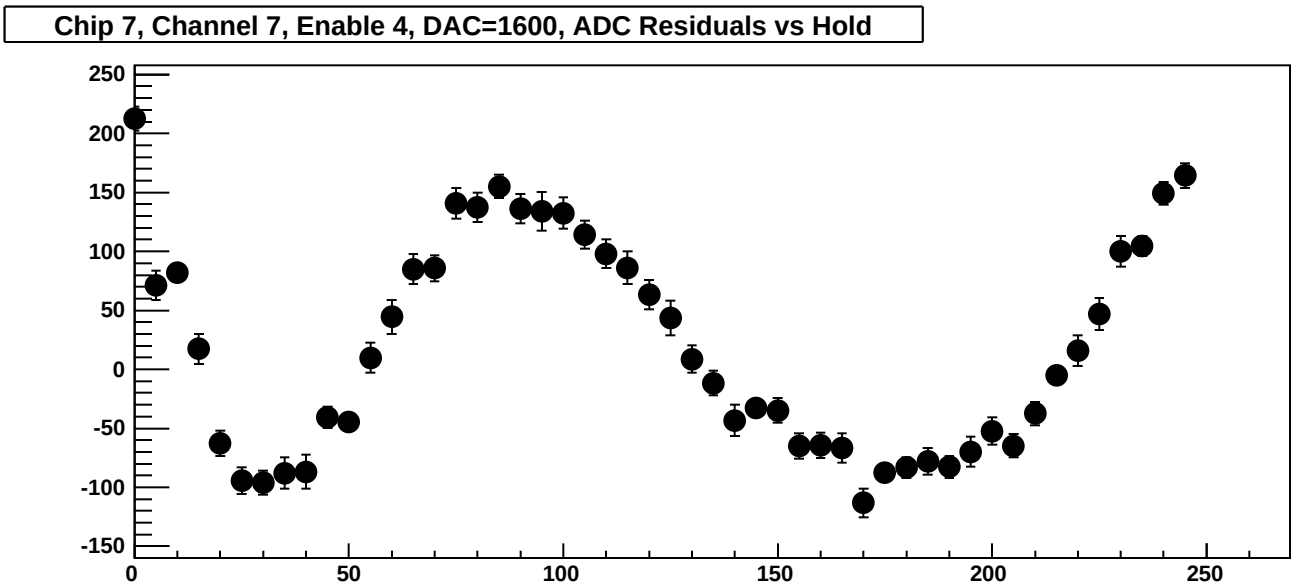


Chip 7, Channel 7, Enable 3, DAC=1600, ADC Residuals vs Hold

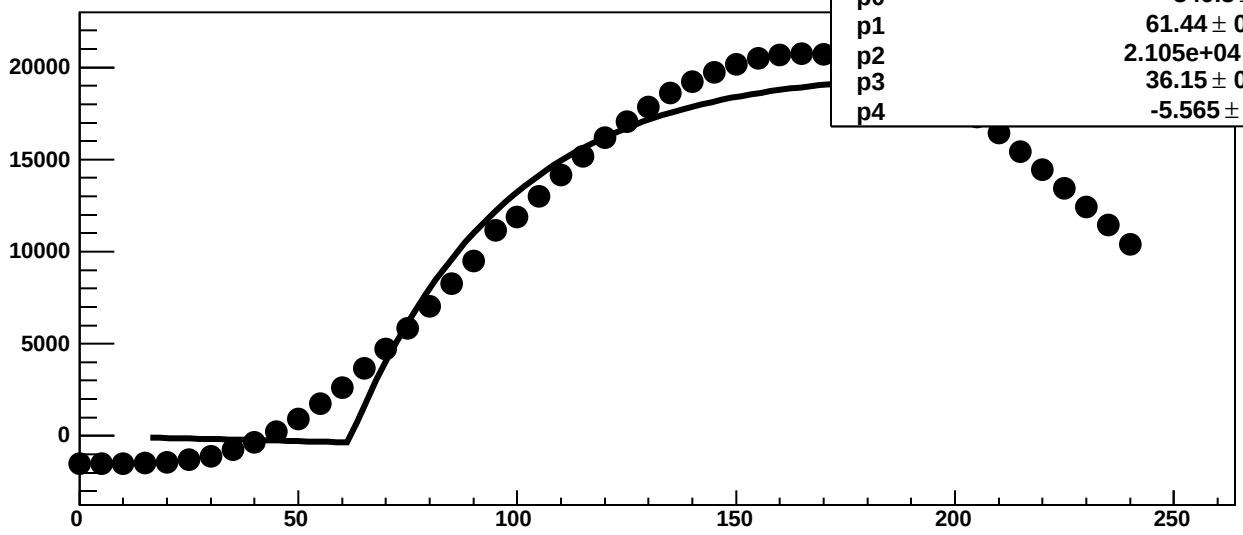



Chip 7, Channel 7, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 2601 / 41
p0 -4.376e+05 ± 3532
p1 -2695 ± 0.2357
p2 1.9e+07 ± 1.551e+05
p3 1.228e+04 ± 0.01785
p4 -1228 ± 10.03
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250

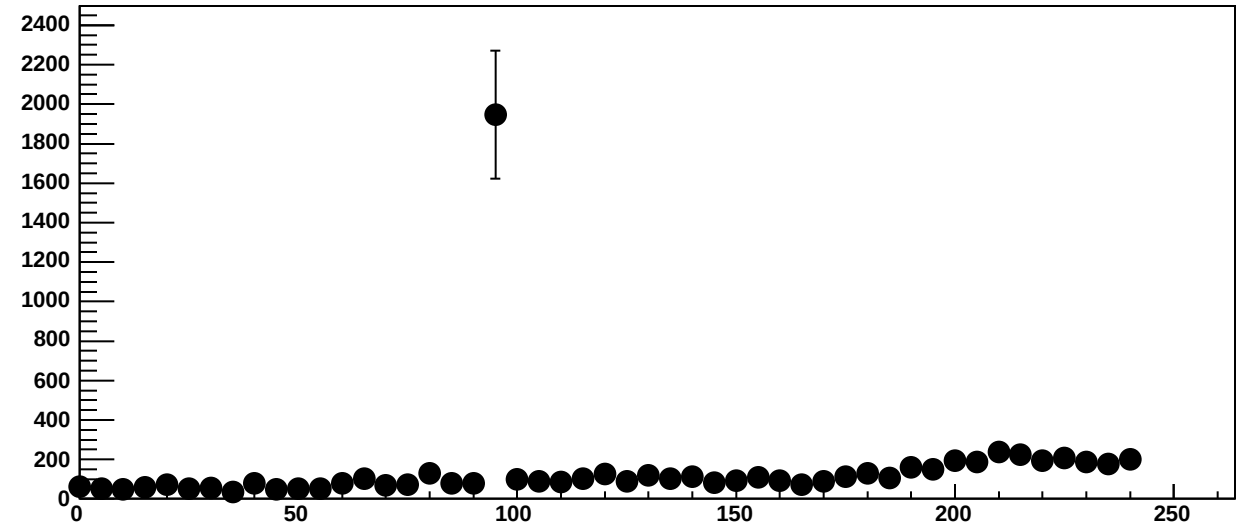

Chip 7, Channel 7, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 7, Channel 7, Enable 4, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
-100
-150
0
50
100
150
200
250

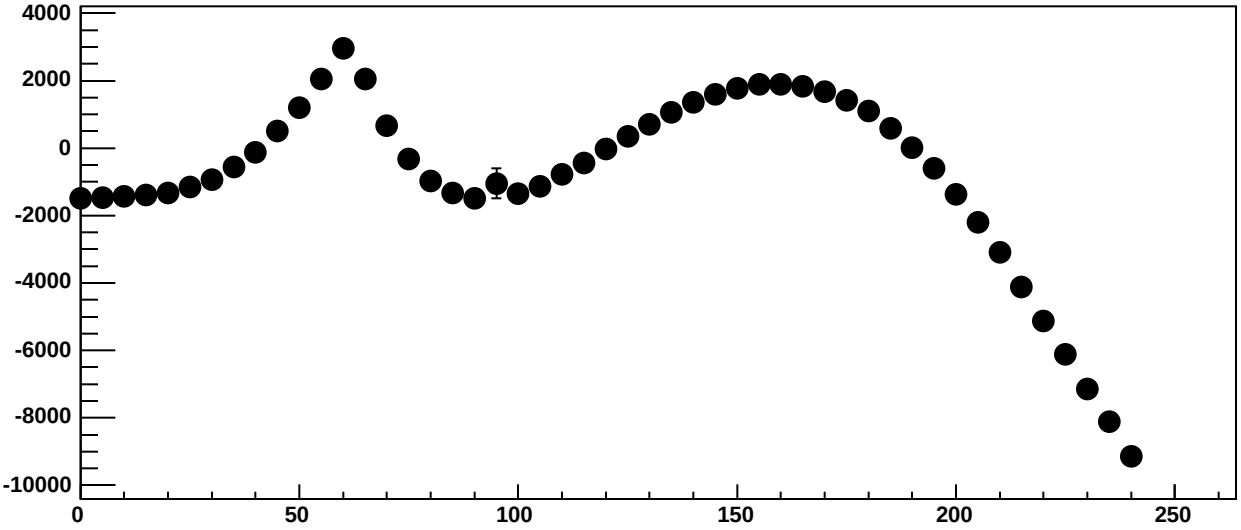
Chip 7, Channel 7, Enable 5, DAC=1600, ADC Mean vs Hold



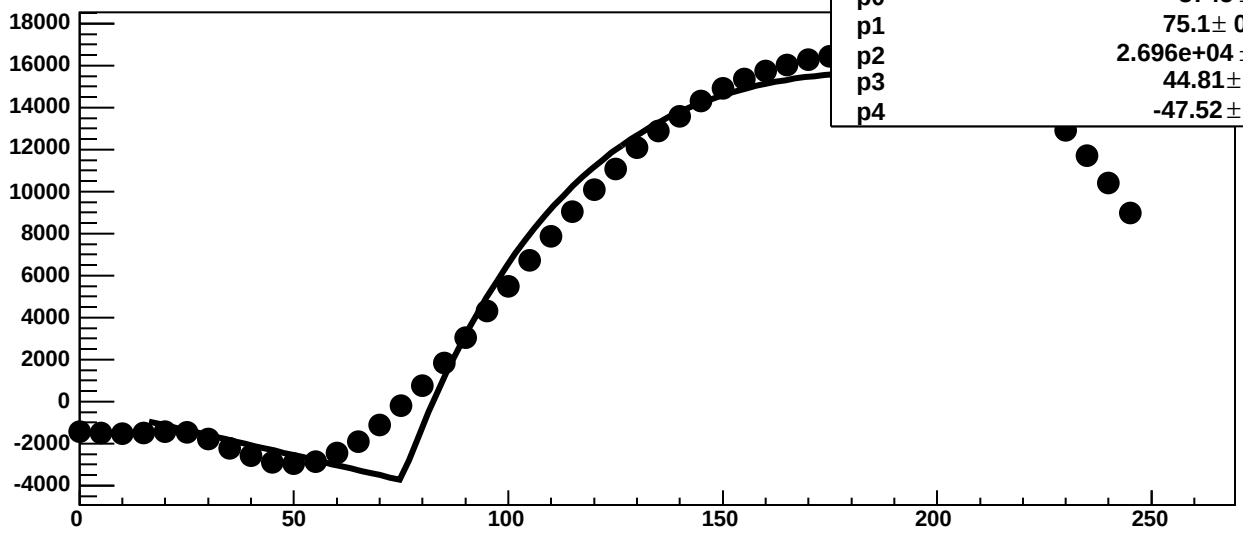
Chip 7, Channel 7, Enable 5, DAC=1600, ADC Noise vs Hold



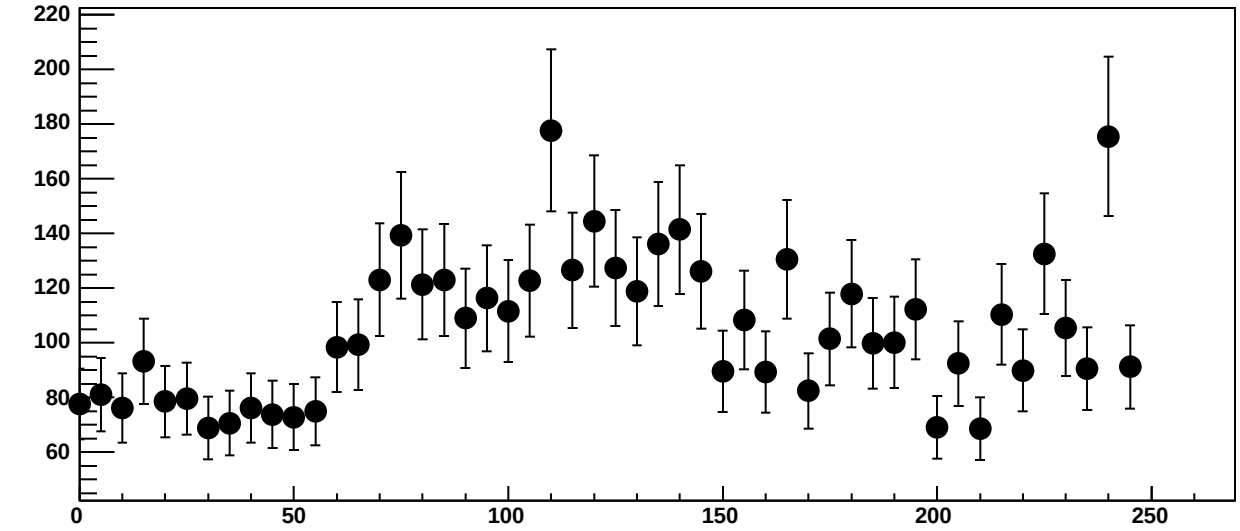
Chip 7, Channel 7, Enable 5, DAC=1600, ADC Residuals vs Hold



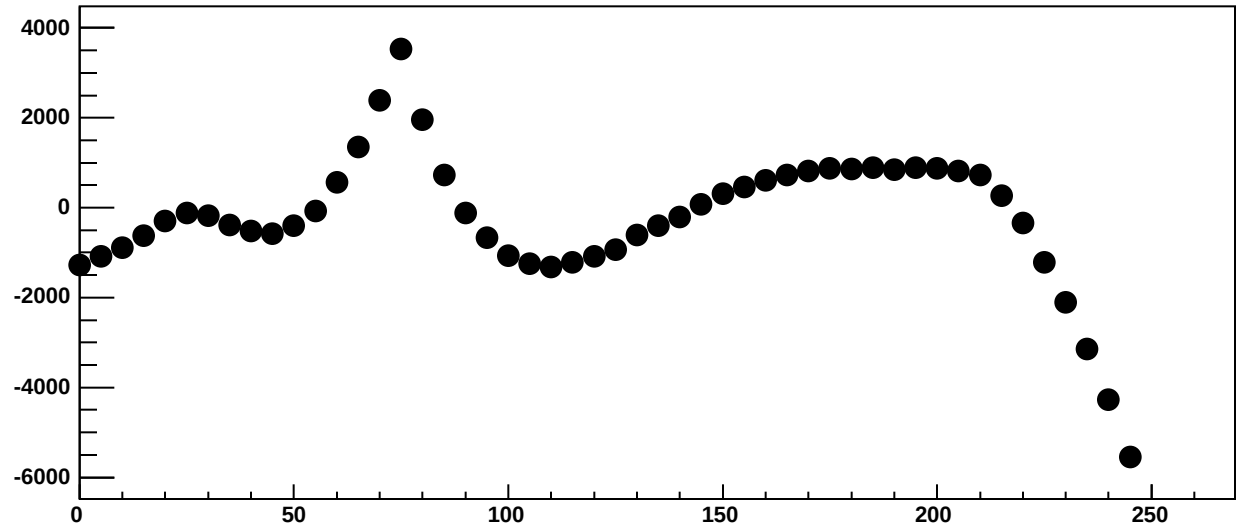
Chip 7, Channel 8, Enable 0, DAC=1600, ADC Mean vs Hold



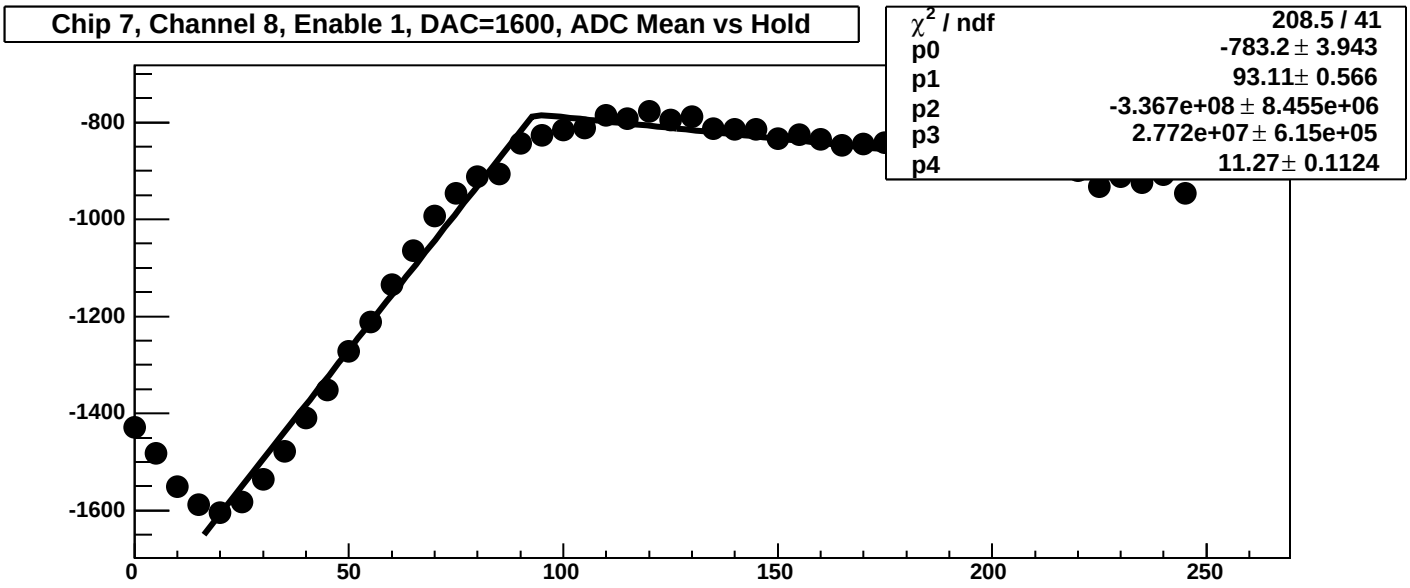
Chip 7, Channel 8, Enable 0, DAC=1600, ADC Noise vs Hold



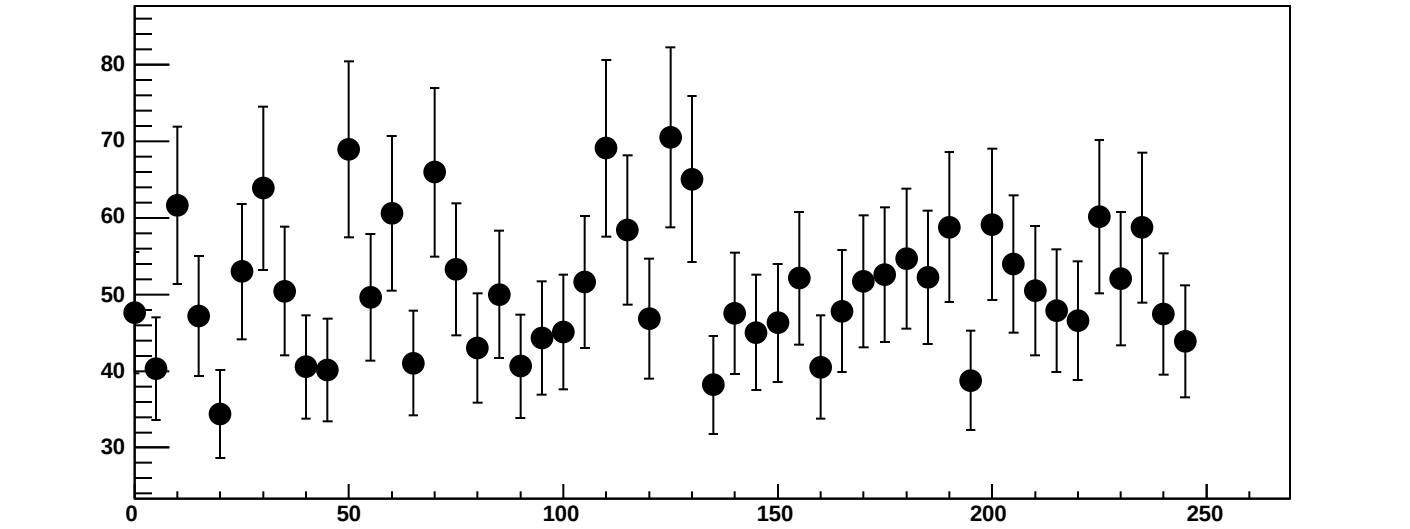
Chip 7, Channel 8, Enable 0, DAC=1600, ADC Residuals vs Hold



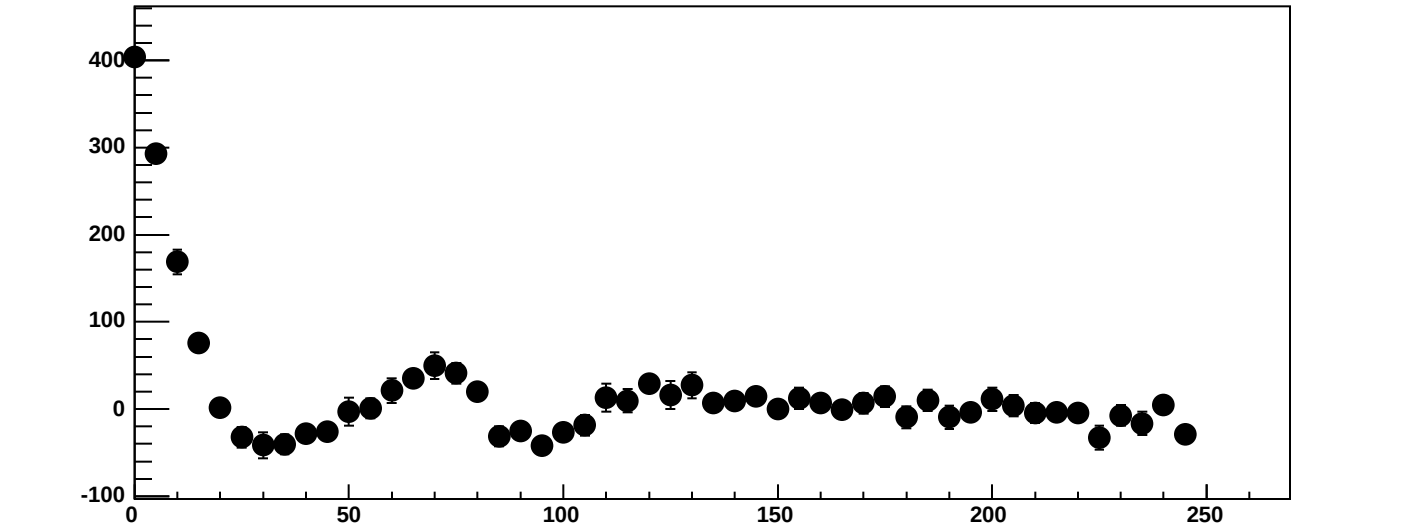
Chip 7, Channel 8, Enable 1, DAC=1600, ADC Mean vs Hold



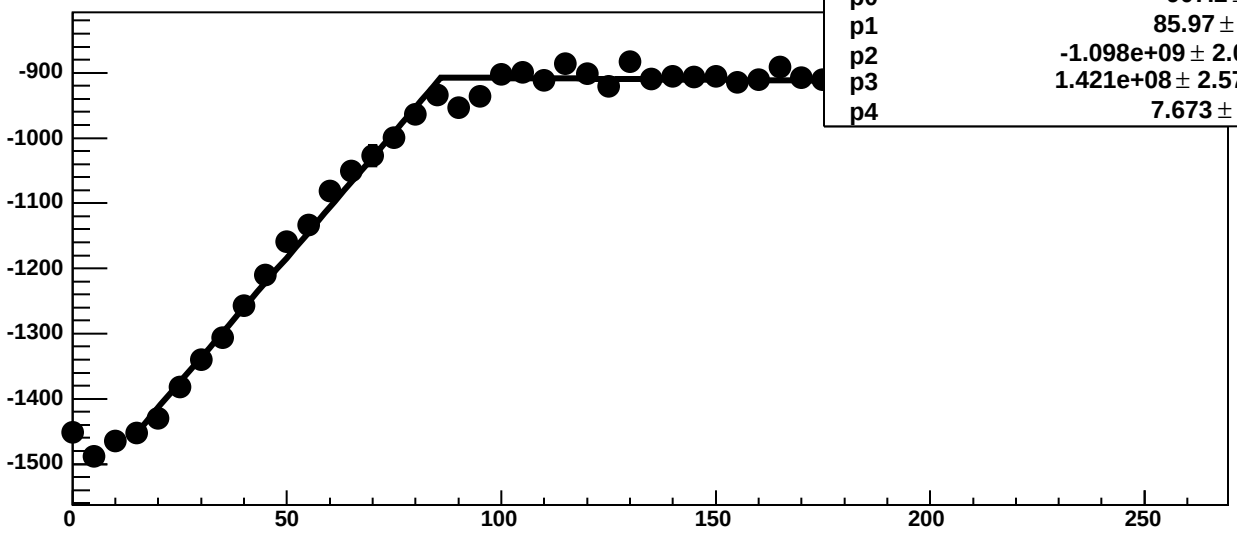
Chip 7, Channel 8, Enable 1, DAC=1600, ADC Noise vs Hold



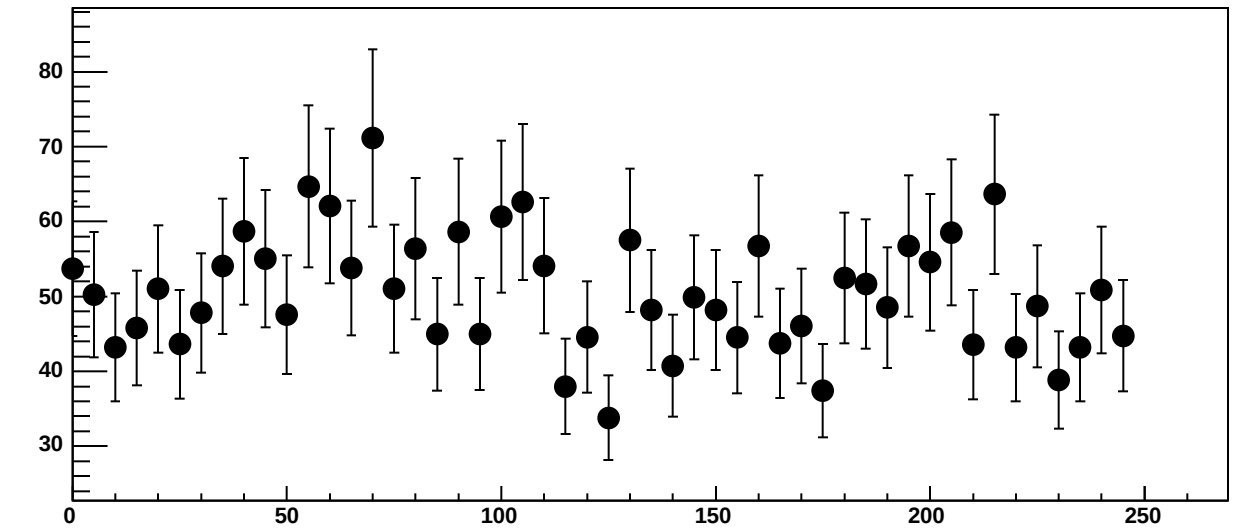
Chip 7, Channel 8, Enable 1, DAC=1600, ADC Residuals vs Hold



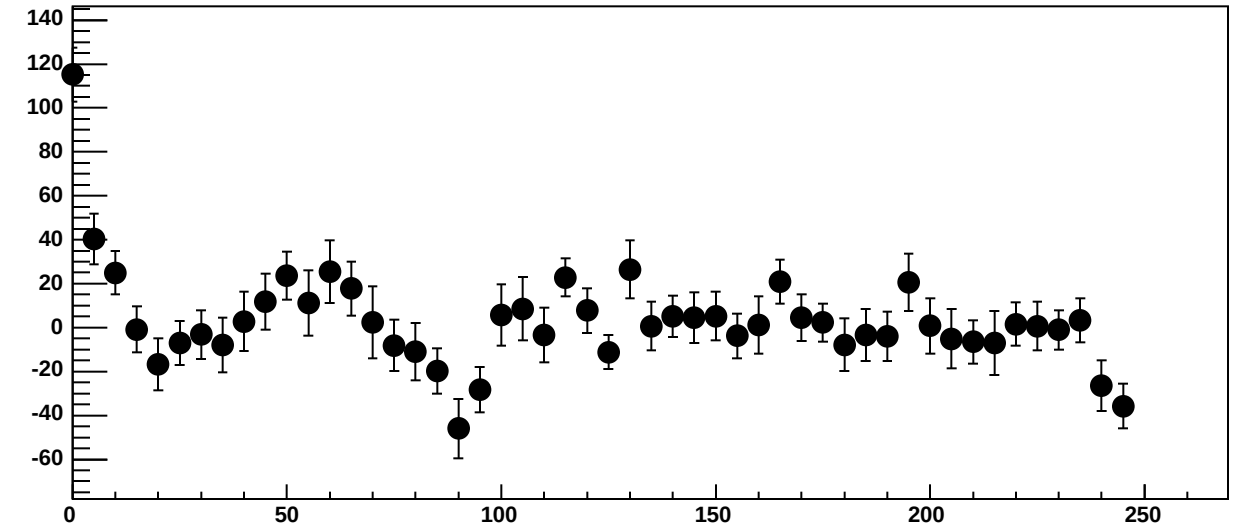
Chip 7, Channel 8, Enable 2, DAC=1600, ADC Mean vs Hold



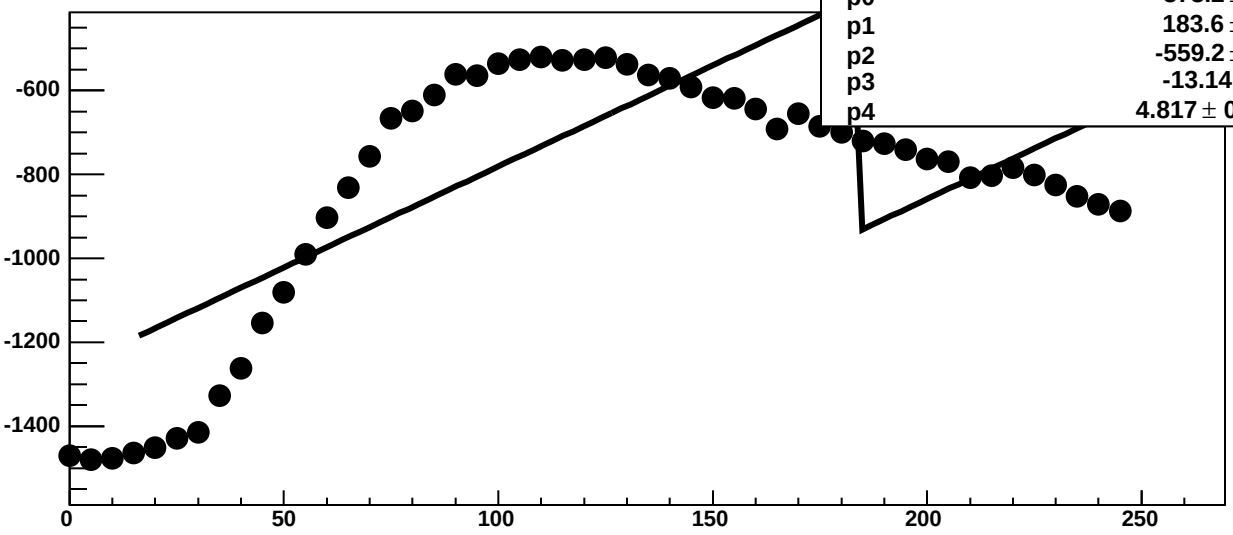
Chip 7, Channel 8, Enable 2, DAC=1600, ADC Noise vs Hold



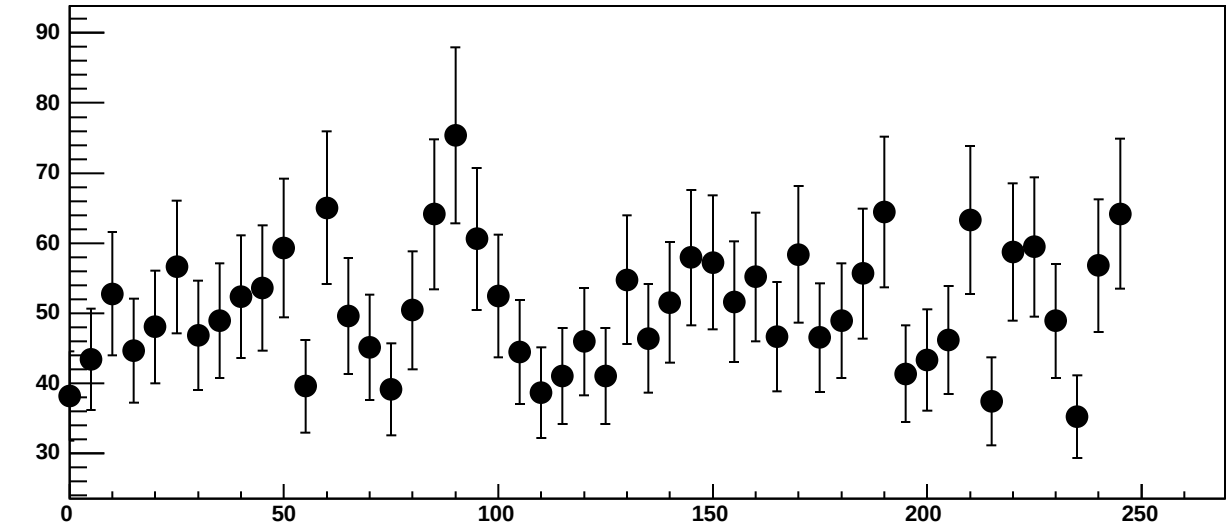
Chip 7, Channel 8, Enable 2, DAC=1600, ADC Residuals vs Hold



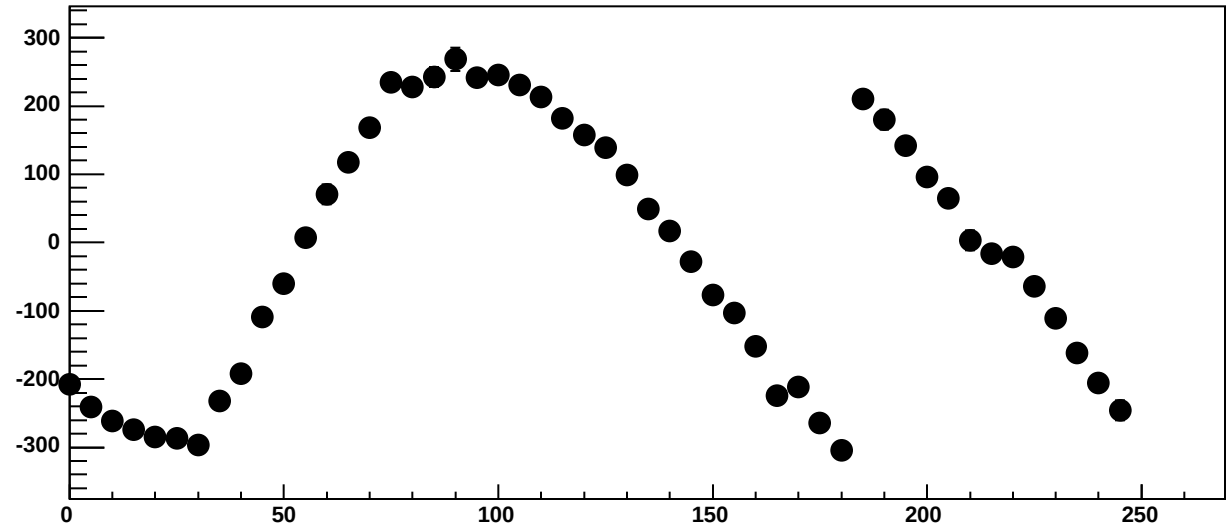
Chip 7, Channel 8, Enable 3, DAC=1600, ADC Mean vs Hold



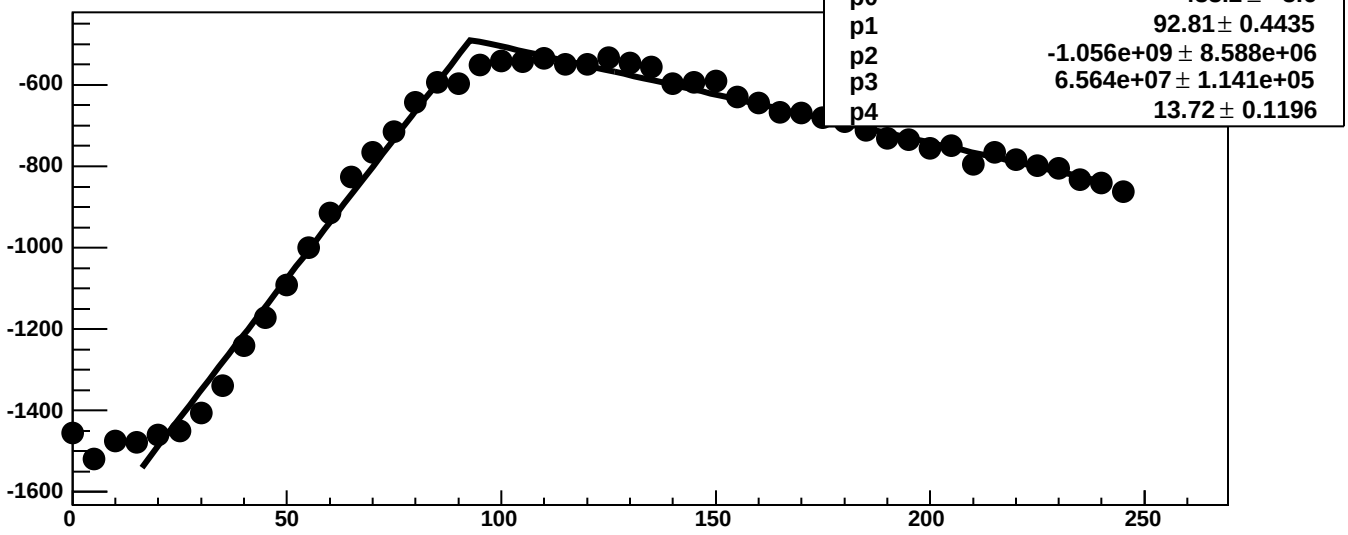
Chip 7, Channel 8, Enable 3, DAC=1600, ADC Noise vs Hold



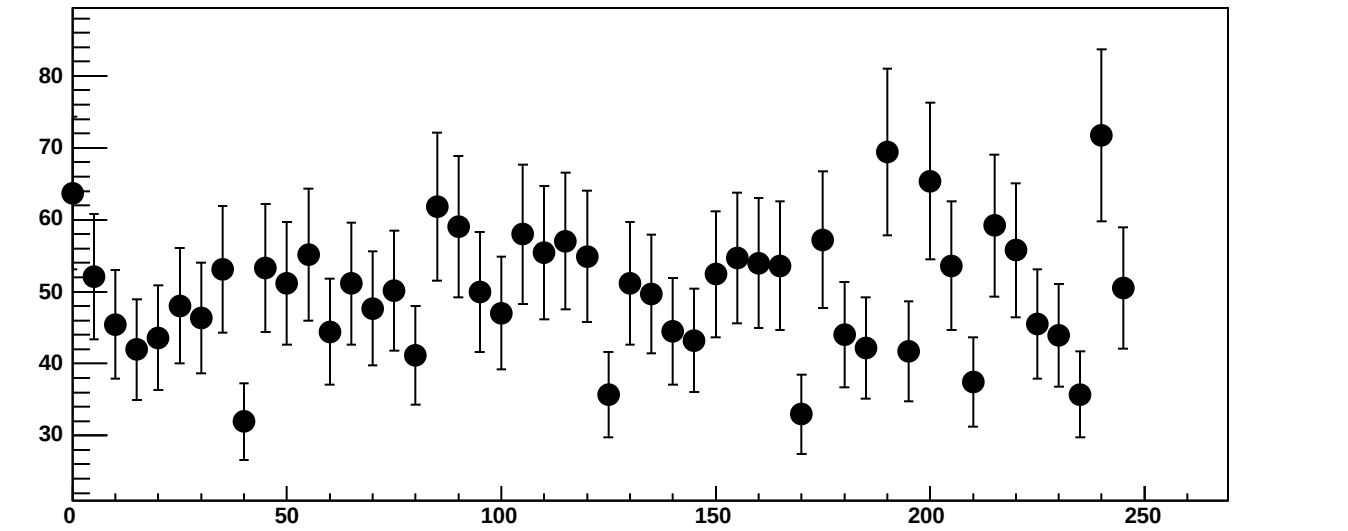
Chip 7, Channel 8, Enable 3, DAC=1600, ADC Residuals vs Hold



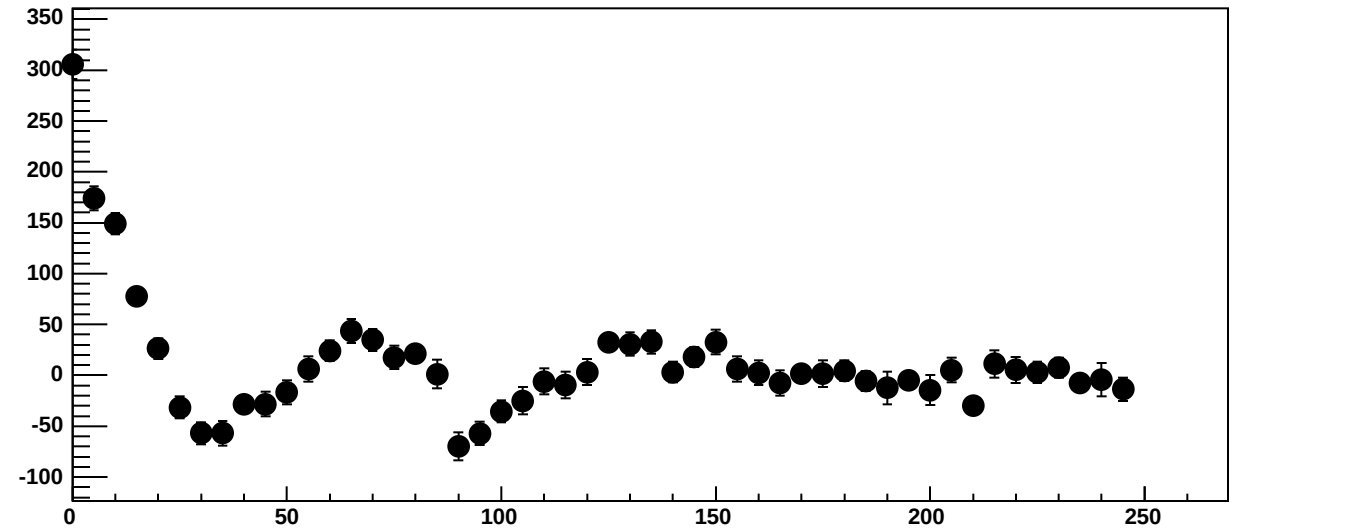
Chip 7, Channel 8, Enable 4, DAC=1600, ADC Mean vs Hold

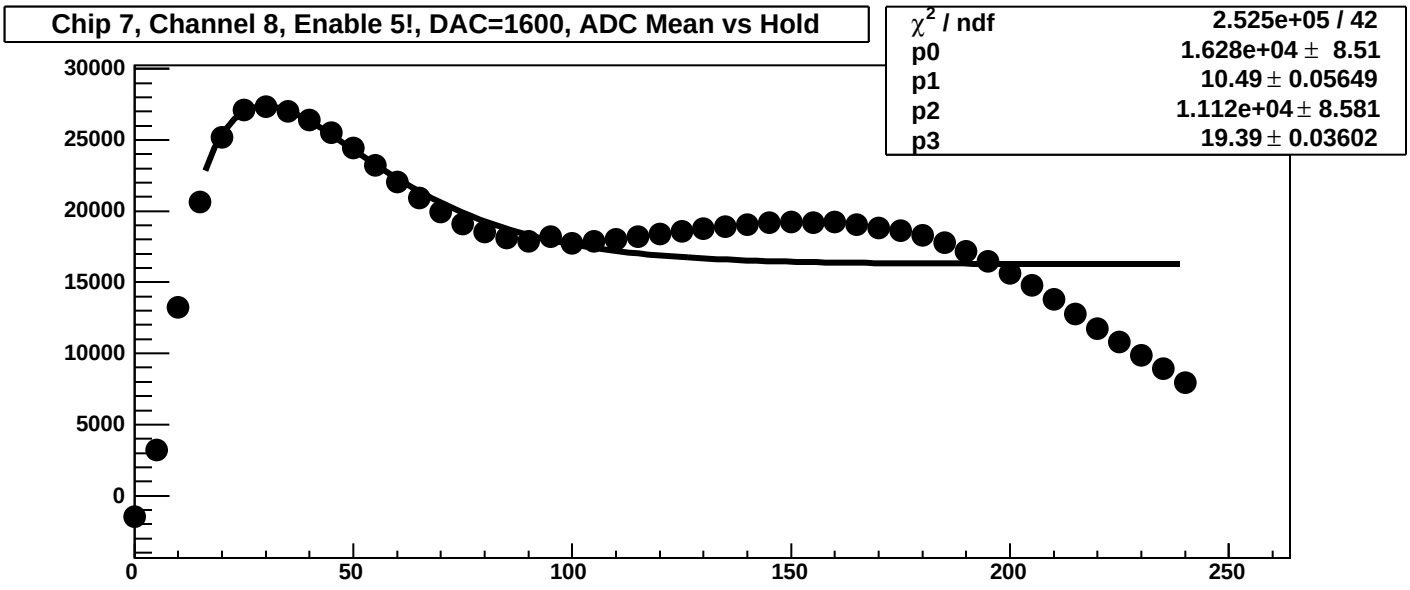


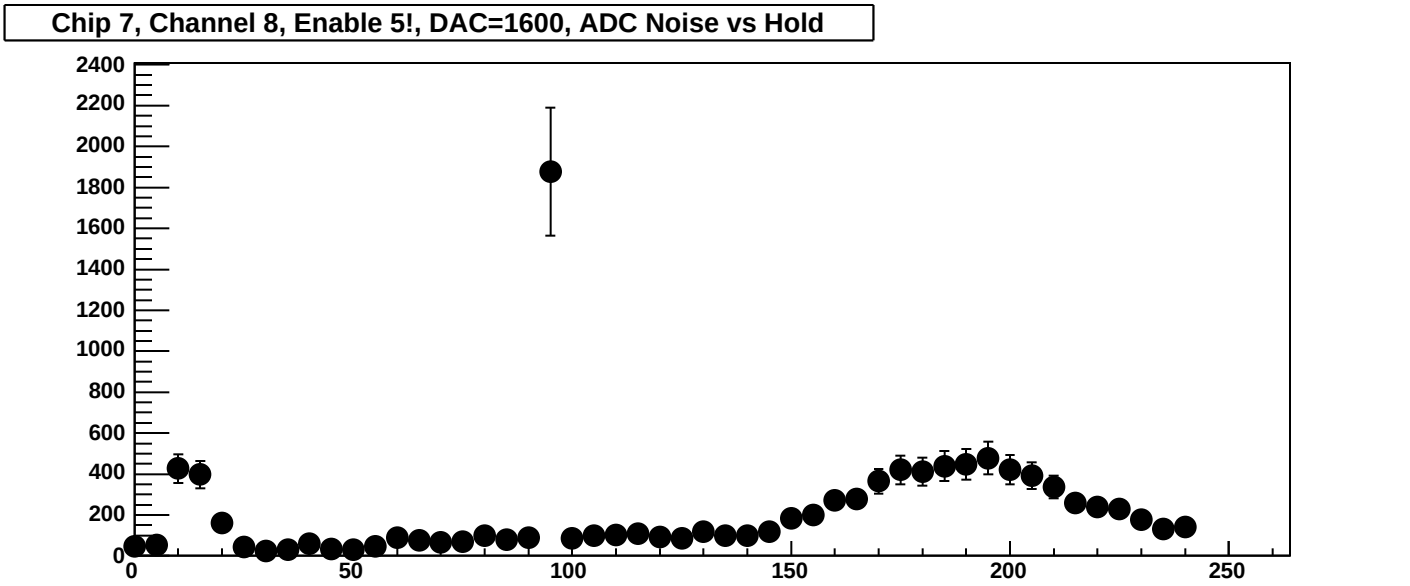
Chip 7, Channel 8, Enable 4, DAC=1600, ADC Noise vs Hold

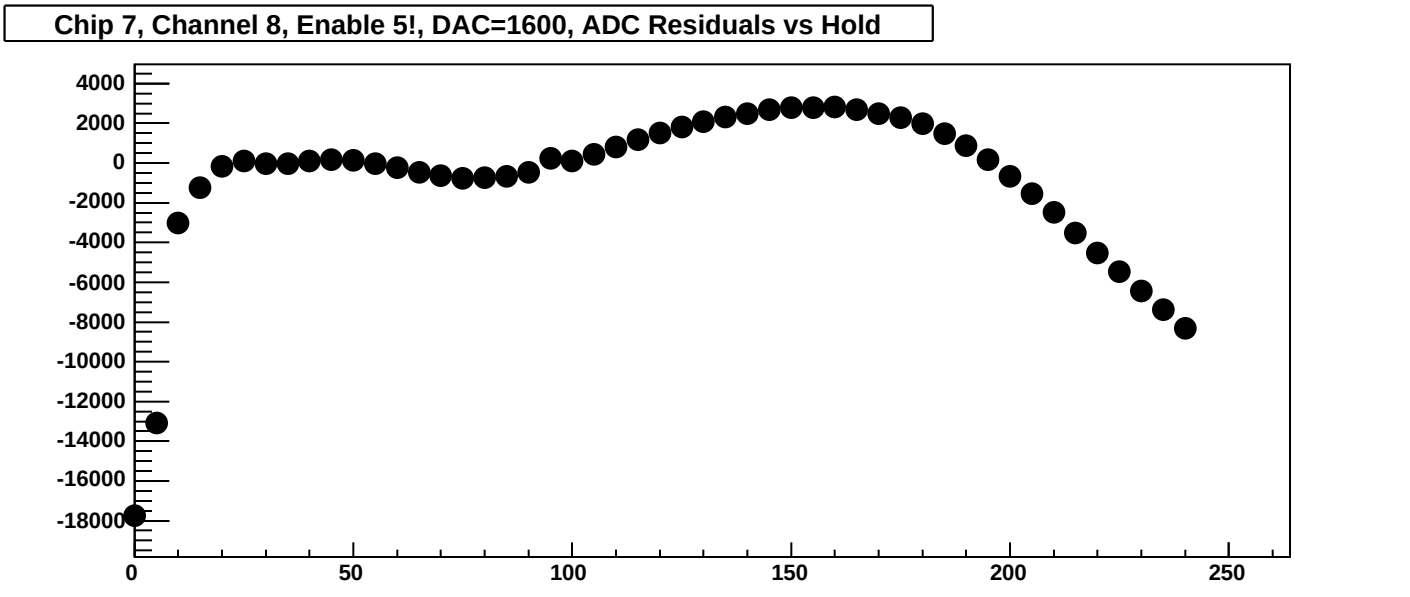


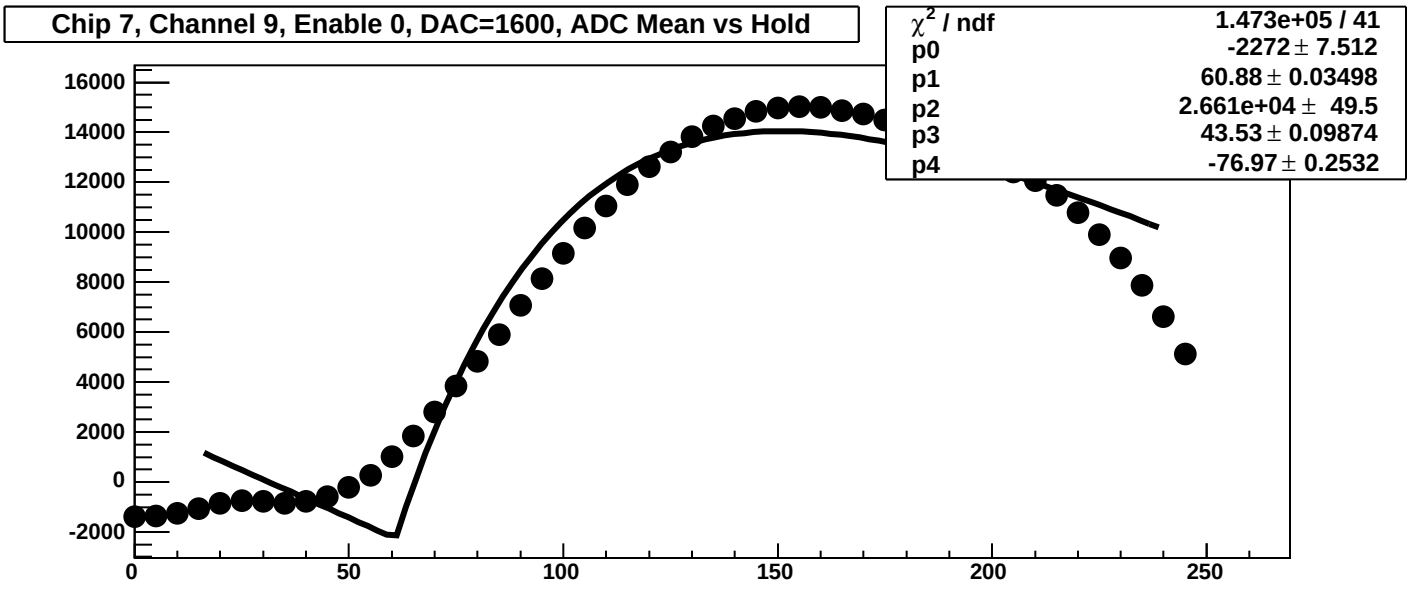
Chip 7, Channel 8, Enable 4, DAC=1600, ADC Residuals vs Hold

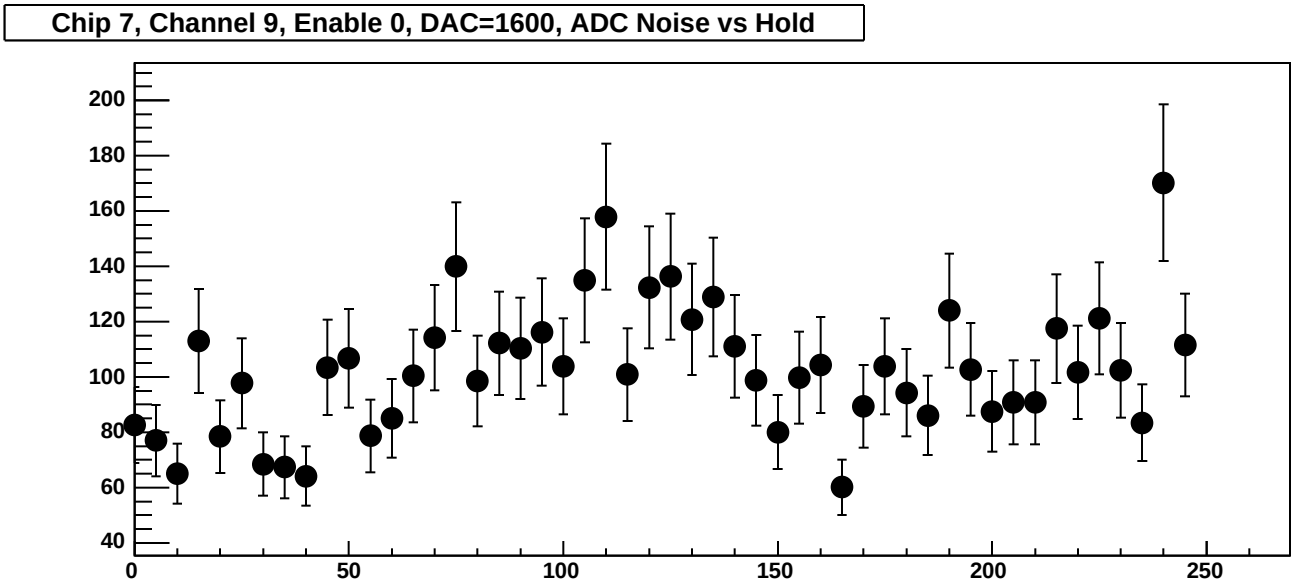


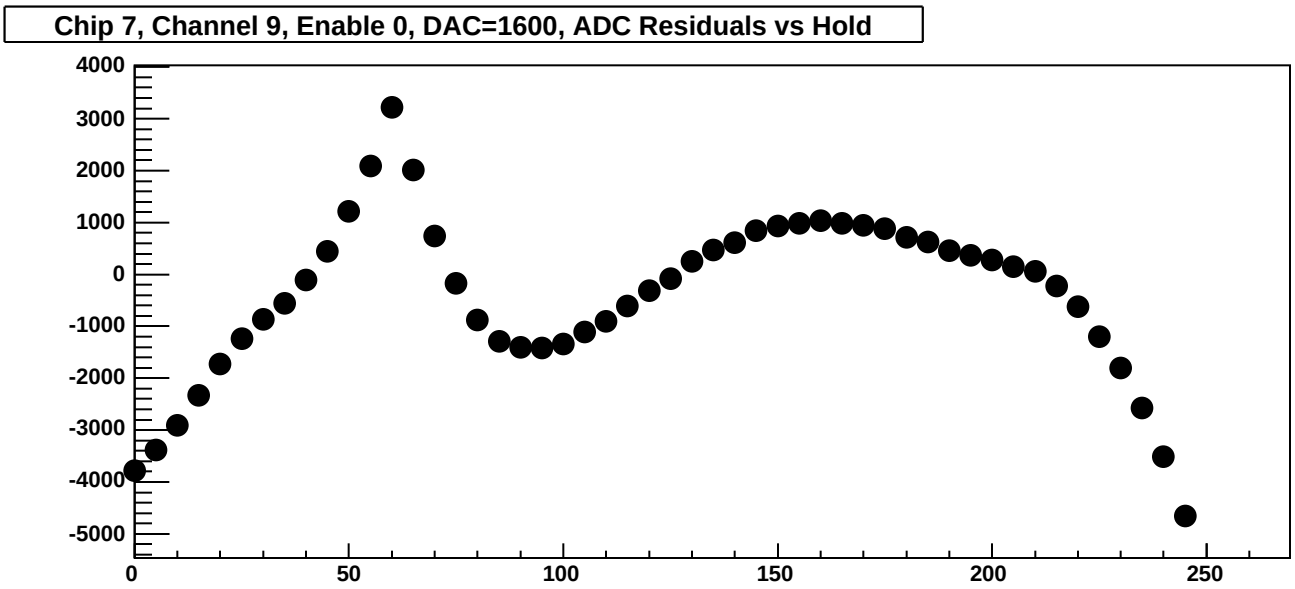

Chip 7, Channel 8, Enable 5!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.525e+05 / 42
p0 1.628e+04 ± 8.51
p1 10.49 ± 0.05649
p2 1.112e+04 ± 8.581
p3 19.39 ± 0.03602
30000
25000
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 7, Channel 8, Enable 5!, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

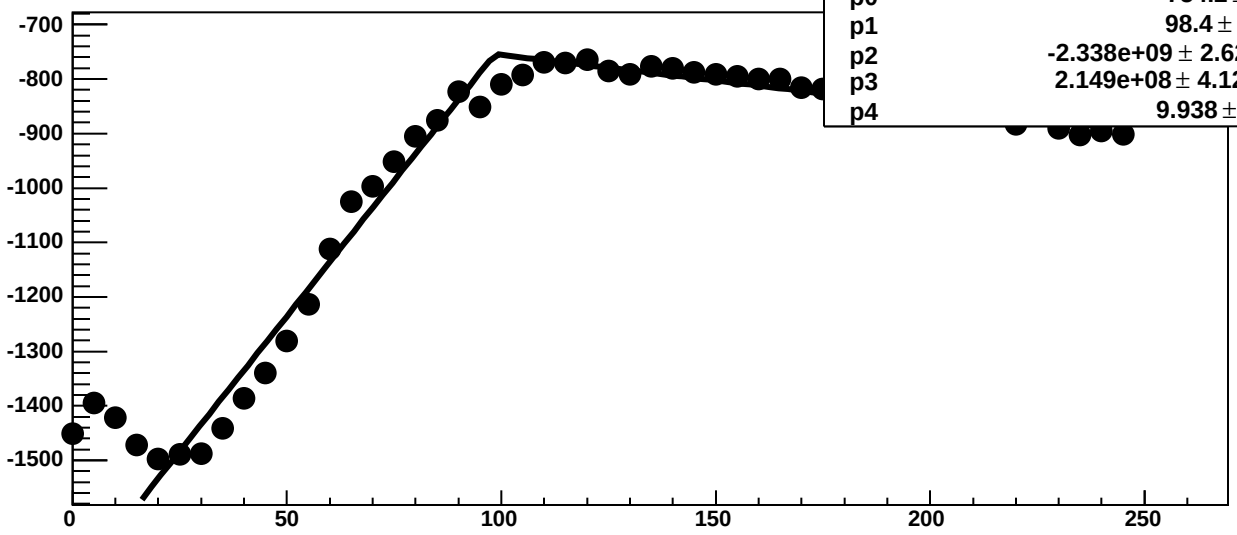

Chip 7, Channel 8, Enable 5!, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
-18000
0
50
100
150
200
250


Chip 7, Channel 9, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.473e+05 / 41
p0 -2272 ± 7.512
p1 60.88 ± 0.03498
p2 2.661e+04 ± 49.5
p3 43.53 ± 0.09874
p4 -76.97 ± 0.2532
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

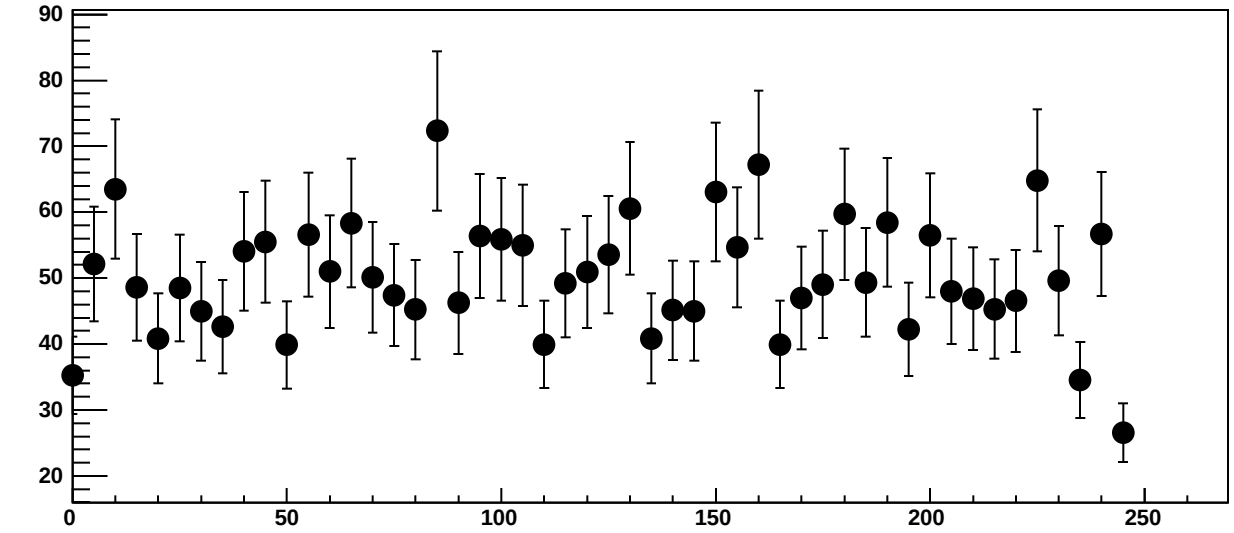

Chip 7, Channel 9, Enable 0, DAC=1600, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250


Chip 7, Channel 9, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

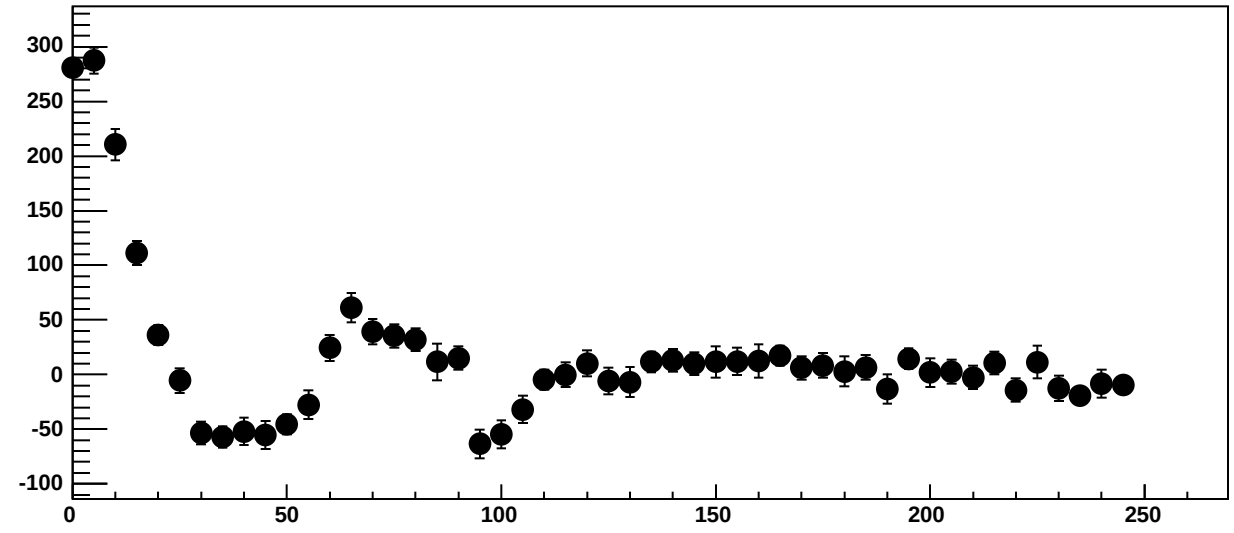
Chip 7, Channel 9, Enable 1, DAC=1600, ADC Mean vs Hold



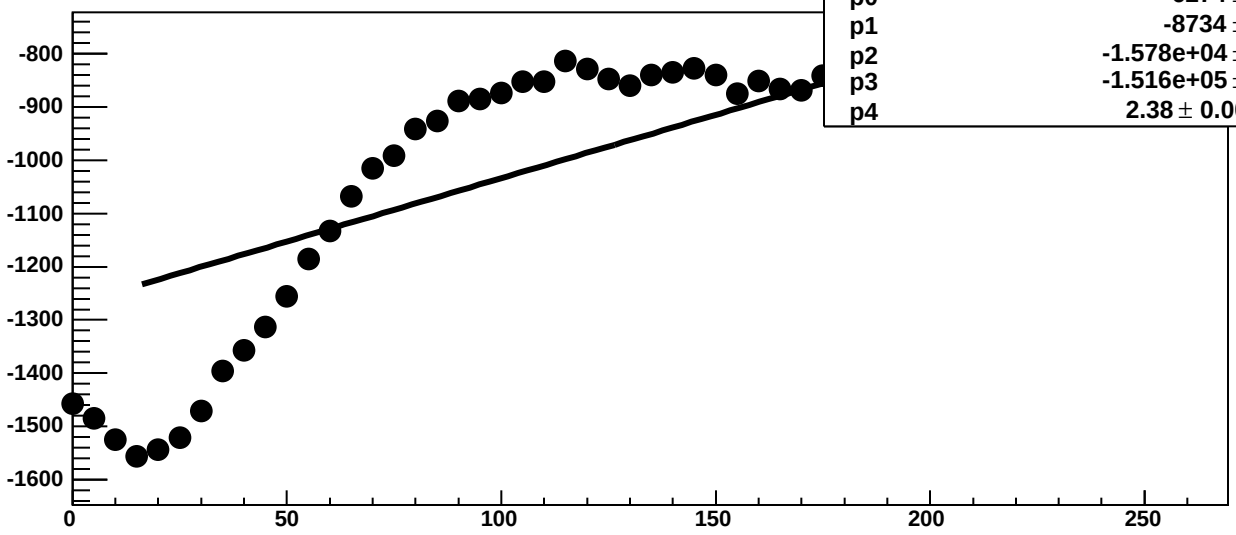
Chip 7, Channel 9, Enable 1, DAC=1600, ADC Noise vs Hold



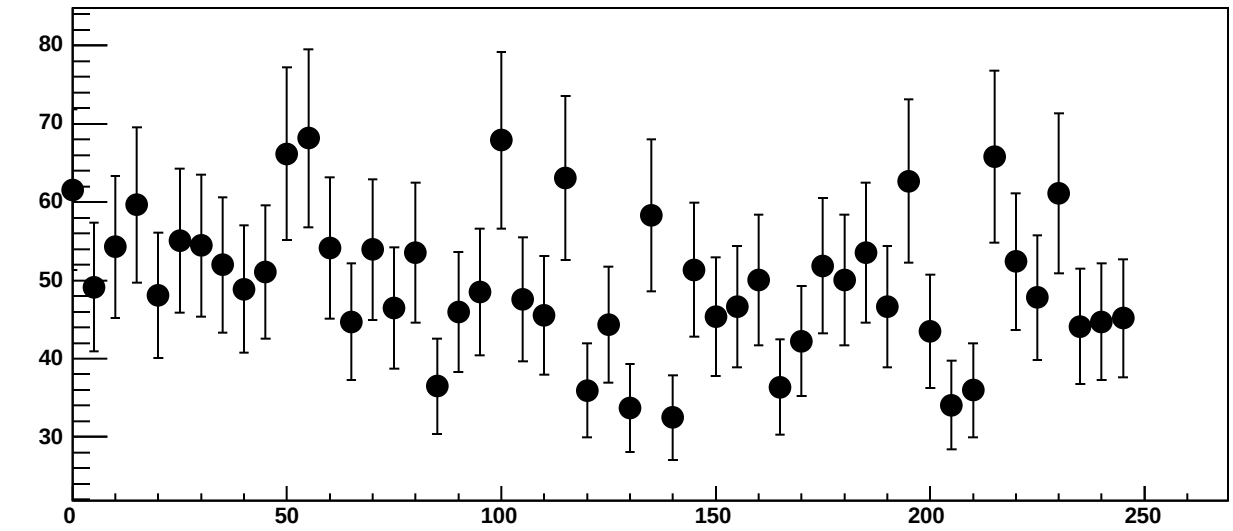
Chip 7, Channel 9, Enable 1, DAC=1600, ADC Residuals vs Hold



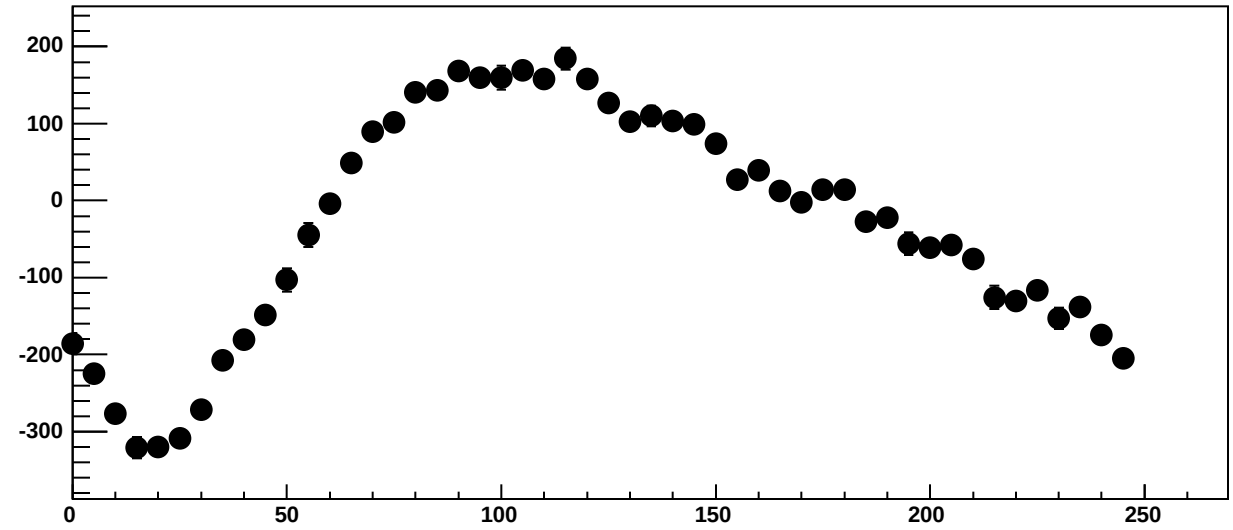
Chip 7, Channel 9, Enable 2, DAC=1600, ADC Mean vs Hold



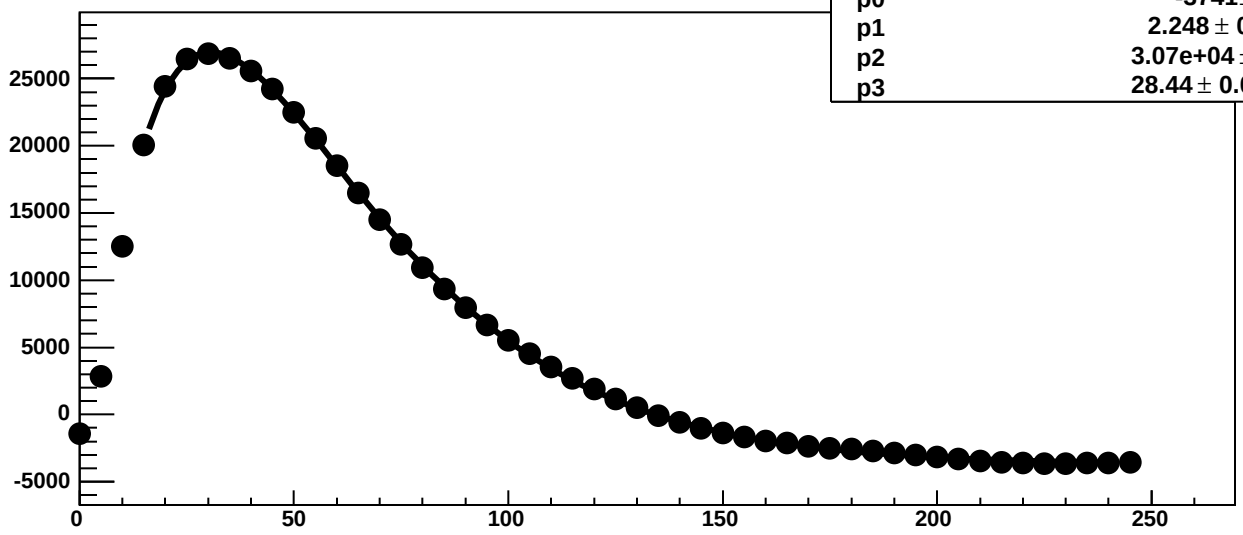
Chip 7, Channel 9, Enable 2, DAC=1600, ADC Noise vs Hold



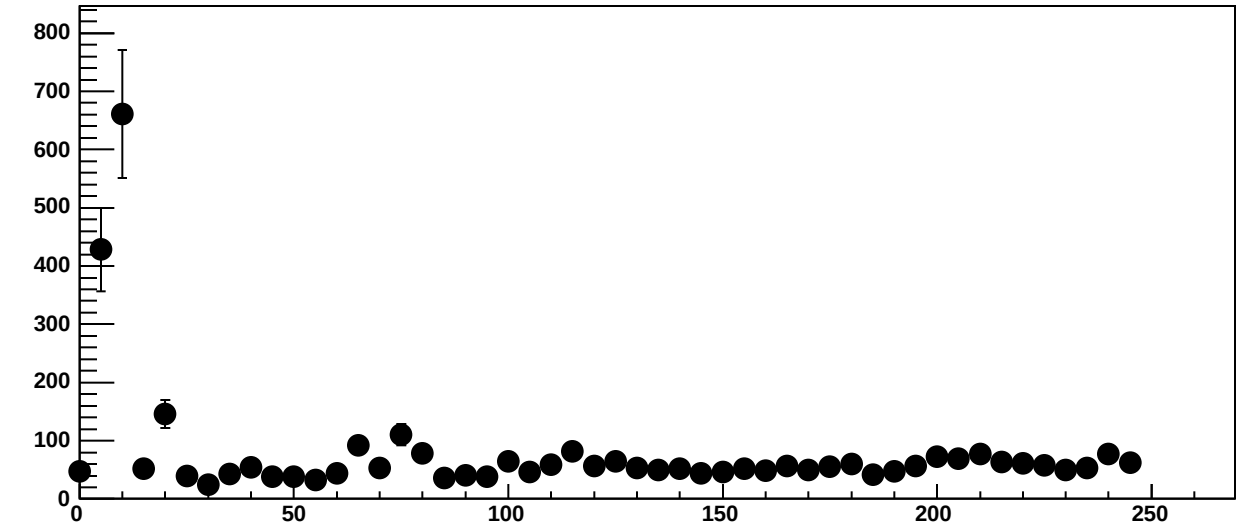
Chip 7, Channel 9, Enable 2, DAC=1600, ADC Residuals vs Hold



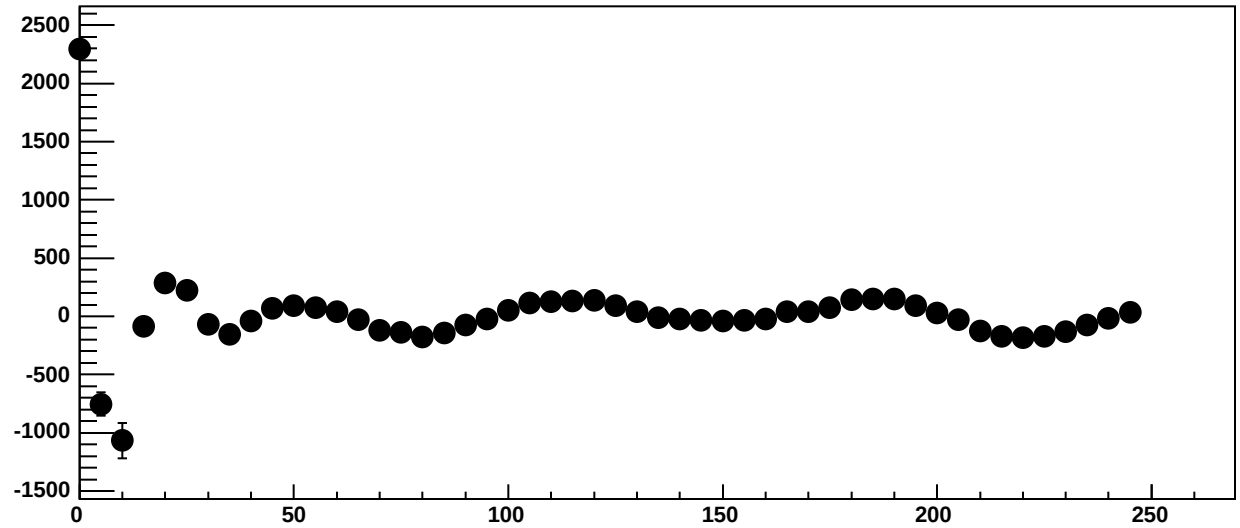
Chip 7, Channel 9, Enable 3!, DAC=1600, ADC Mean vs Hold

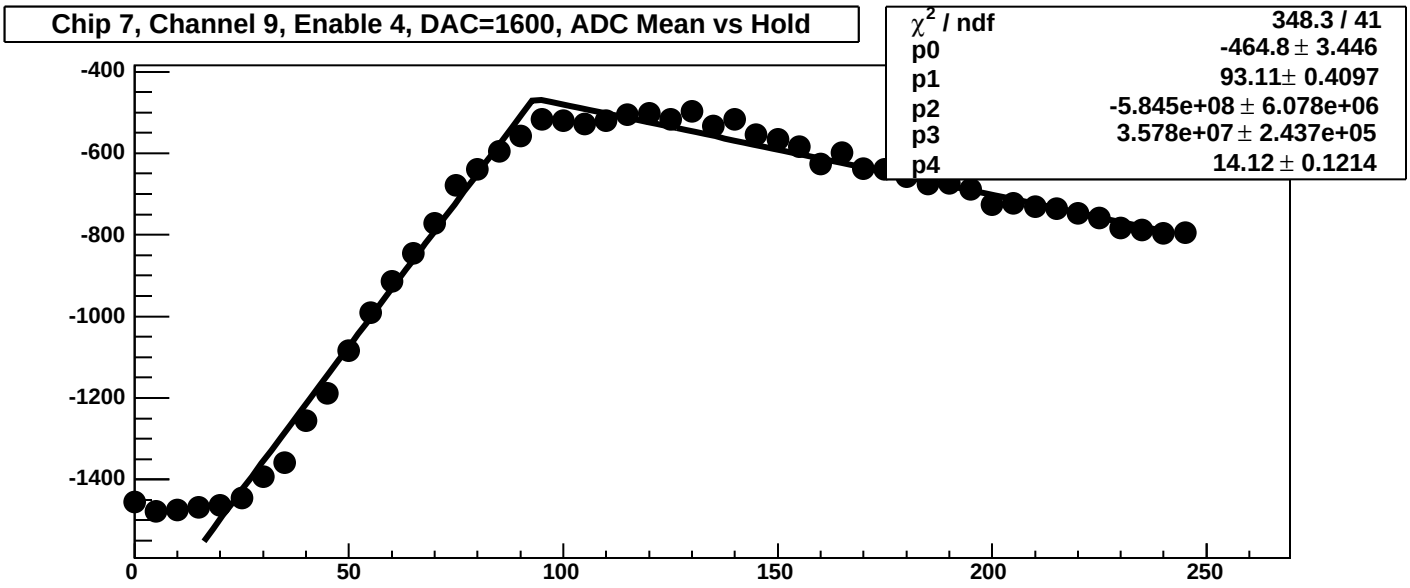


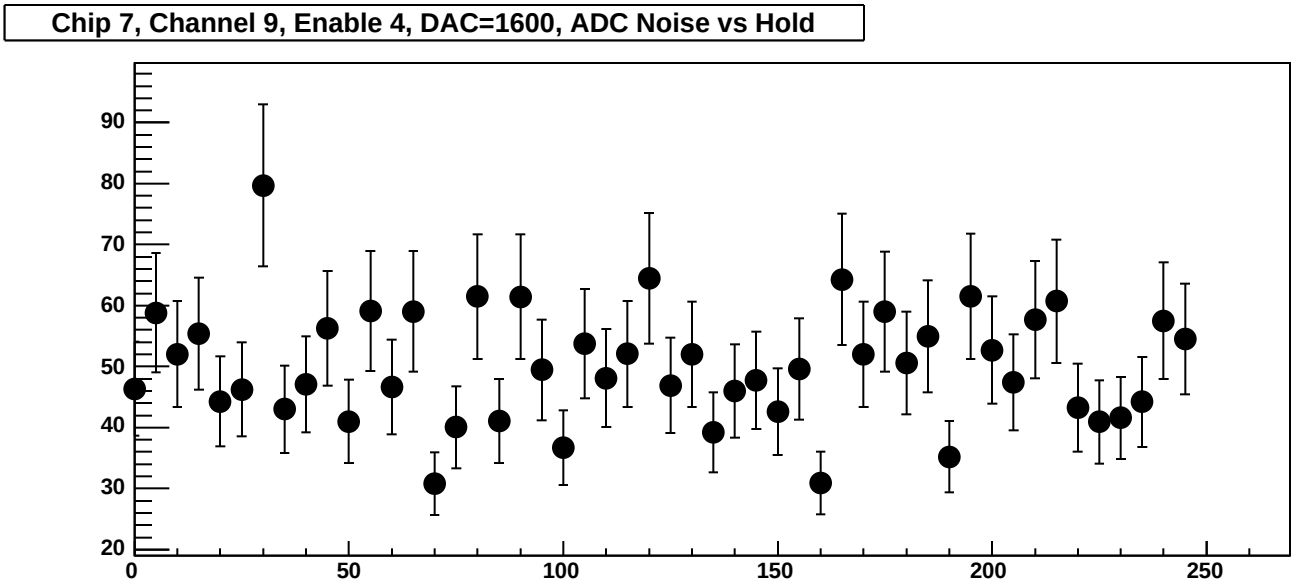
Chip 7, Channel 9, Enable 3!, DAC=1600, ADC Noise vs Hold

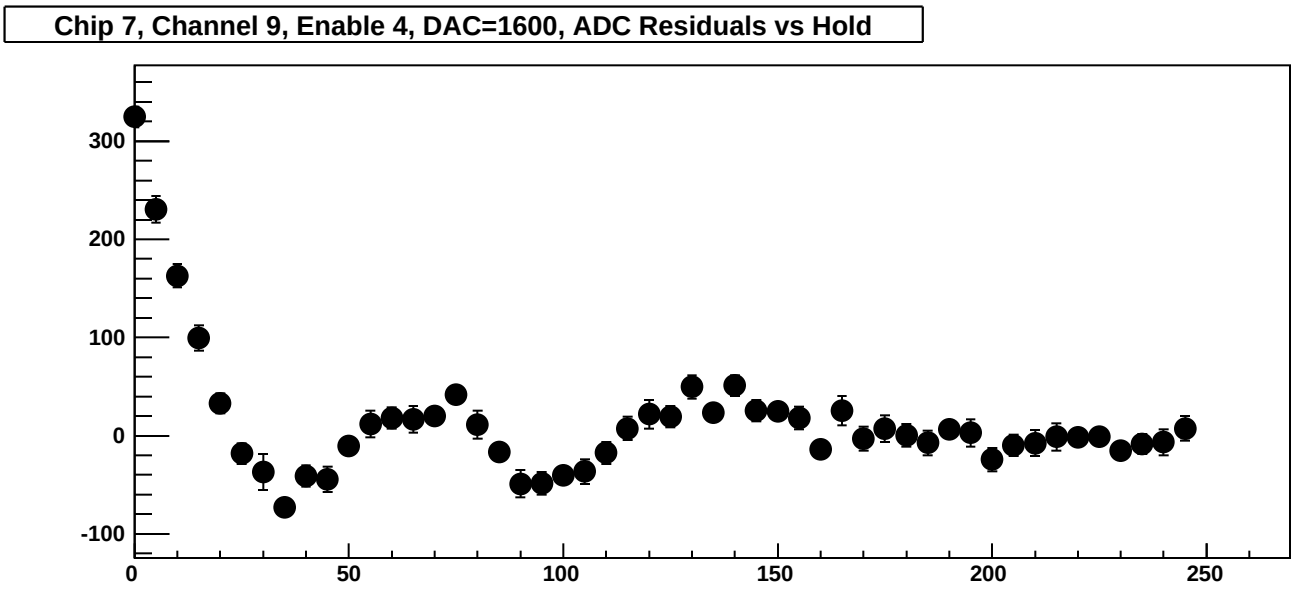


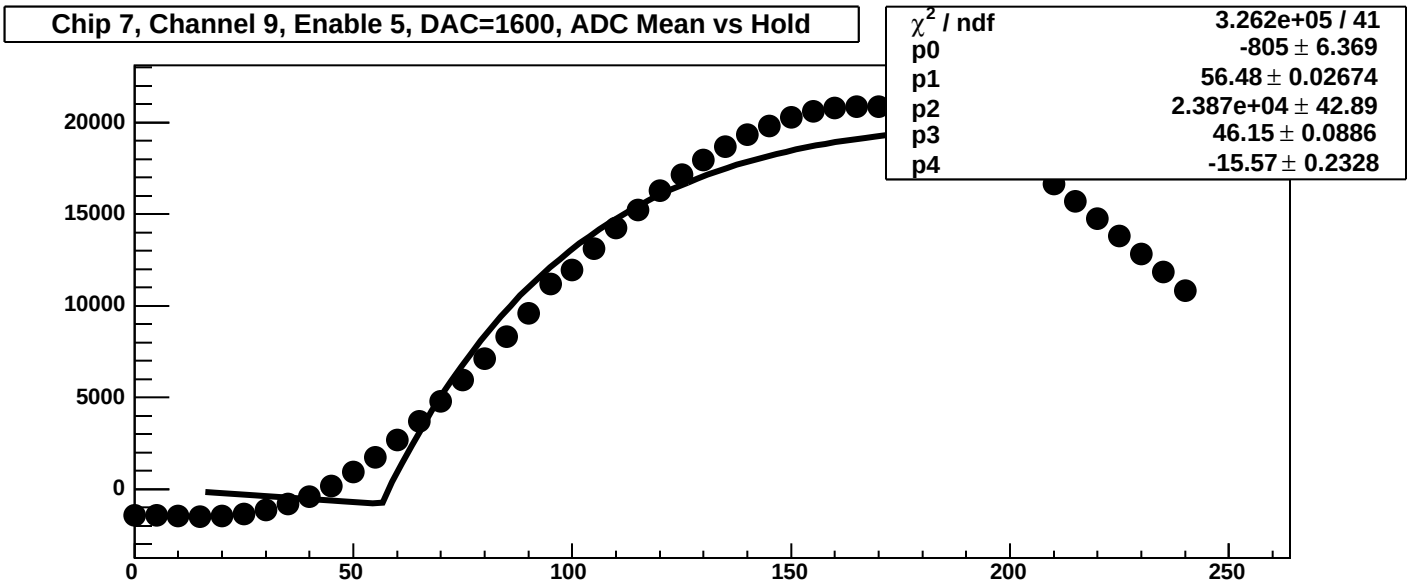
Chip 7, Channel 9, Enable 3!, DAC=1600, ADC Residuals vs Hold

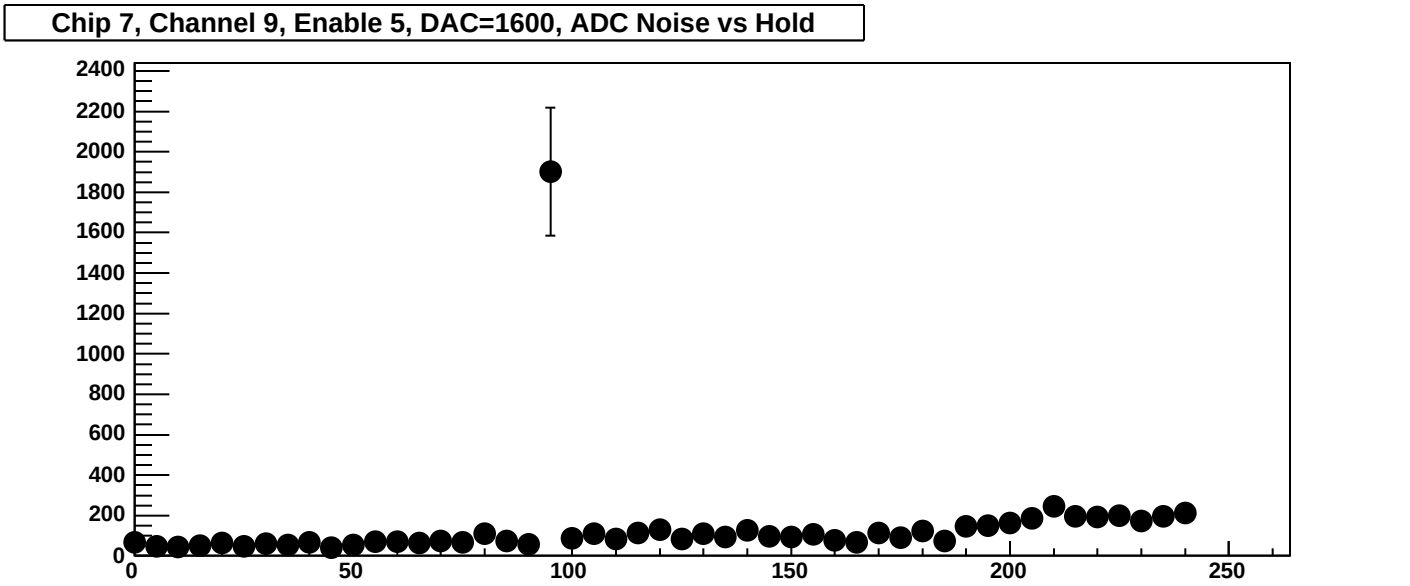


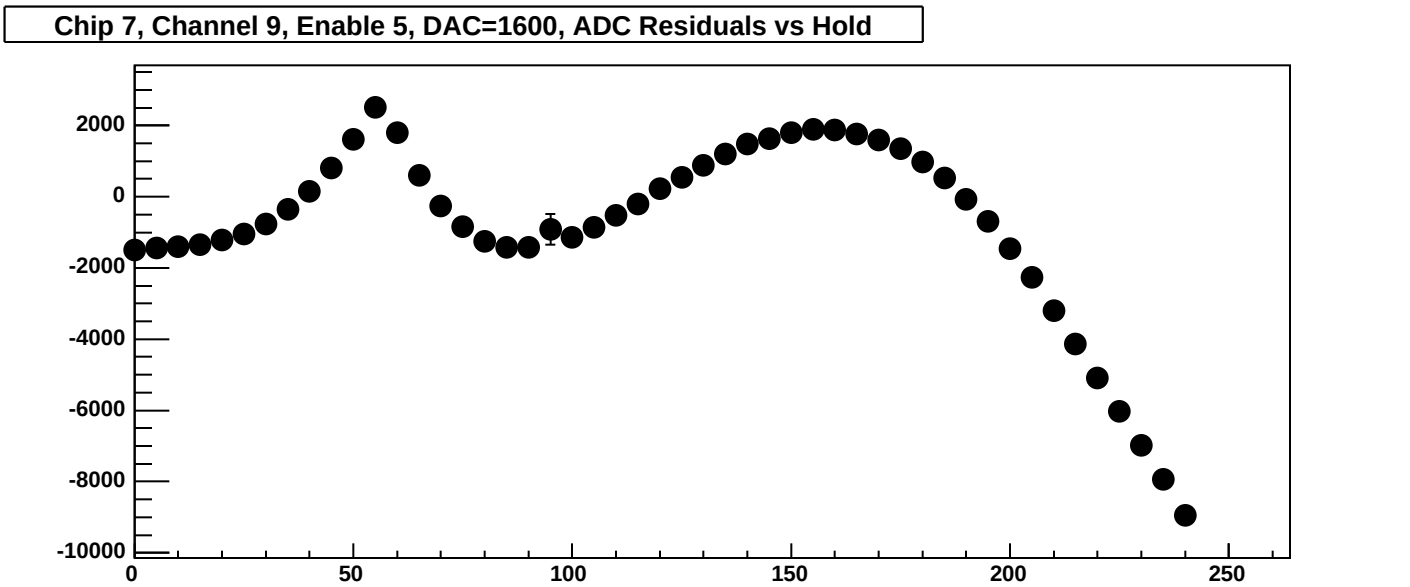

Chip 7, Channel 9, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 348.3 / 41
p0 -464.8 ± 3.446
p1 93.11 ± 0.4097
p2 -5.845e+08 ± 6.078e+06
p3 3.578e+07 ± 2.437e+05
p4 14.12 ± 0.1214
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

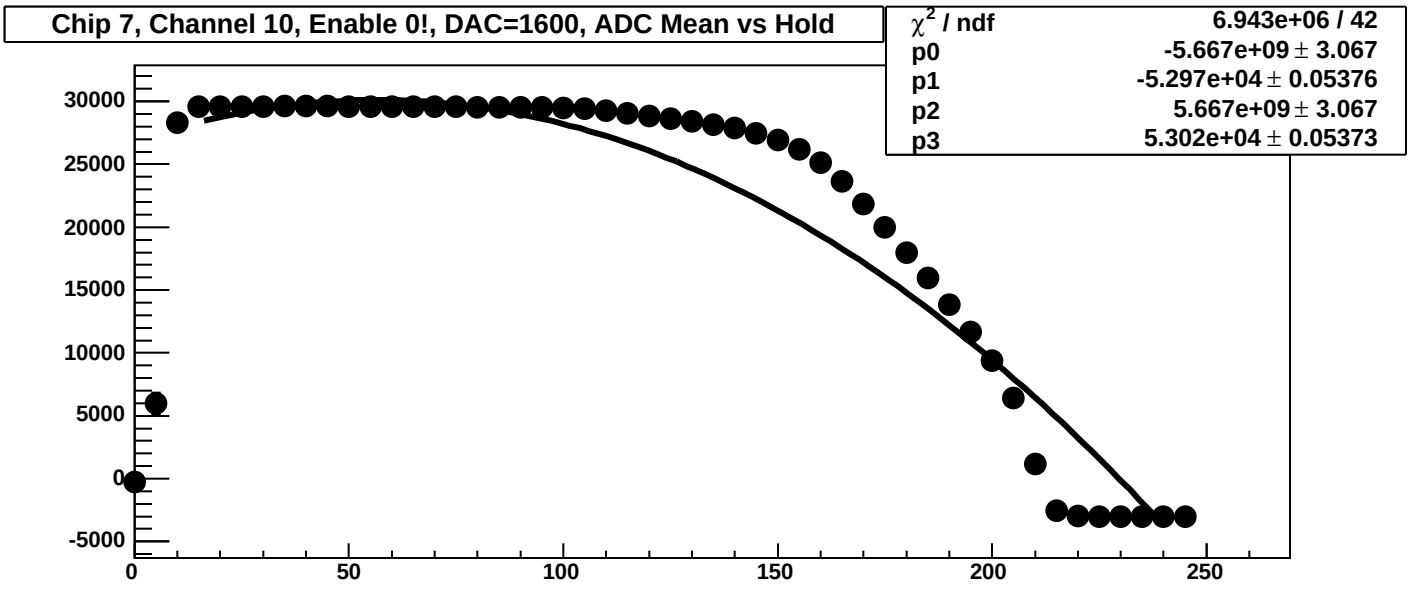

Chip 7, Channel 9, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
20
0
50
100
150
200
250

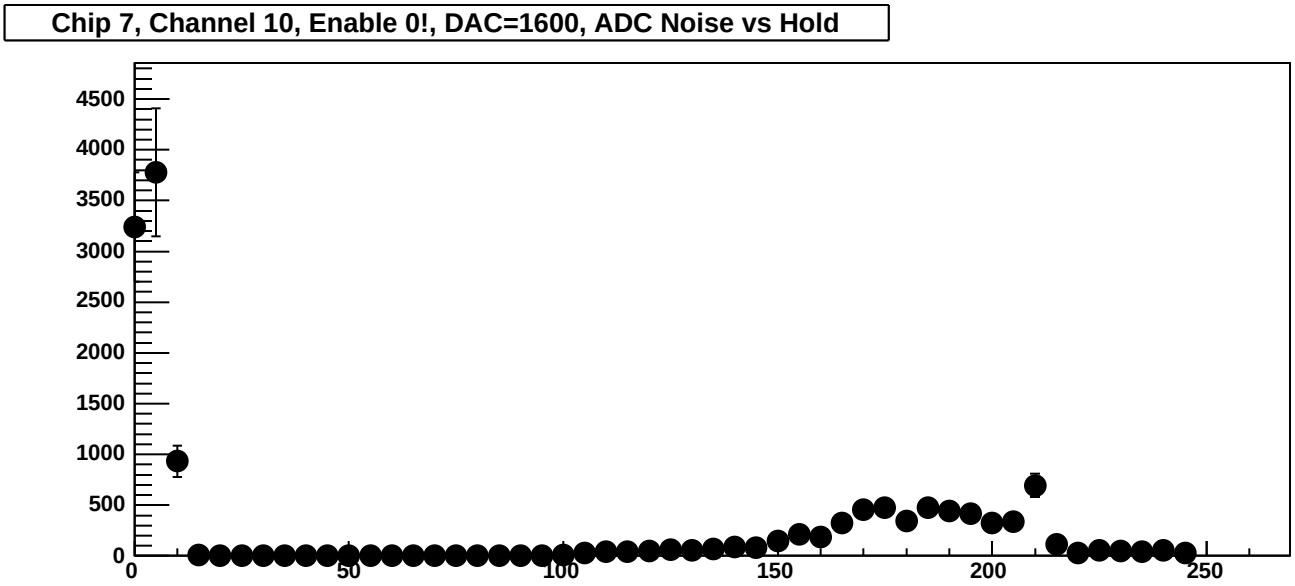

Chip 7, Channel 9, Enable 4, DAC=1600, ADC Residuals vs Hold
300
200
100
0
-100
0
50
100
150
200
250

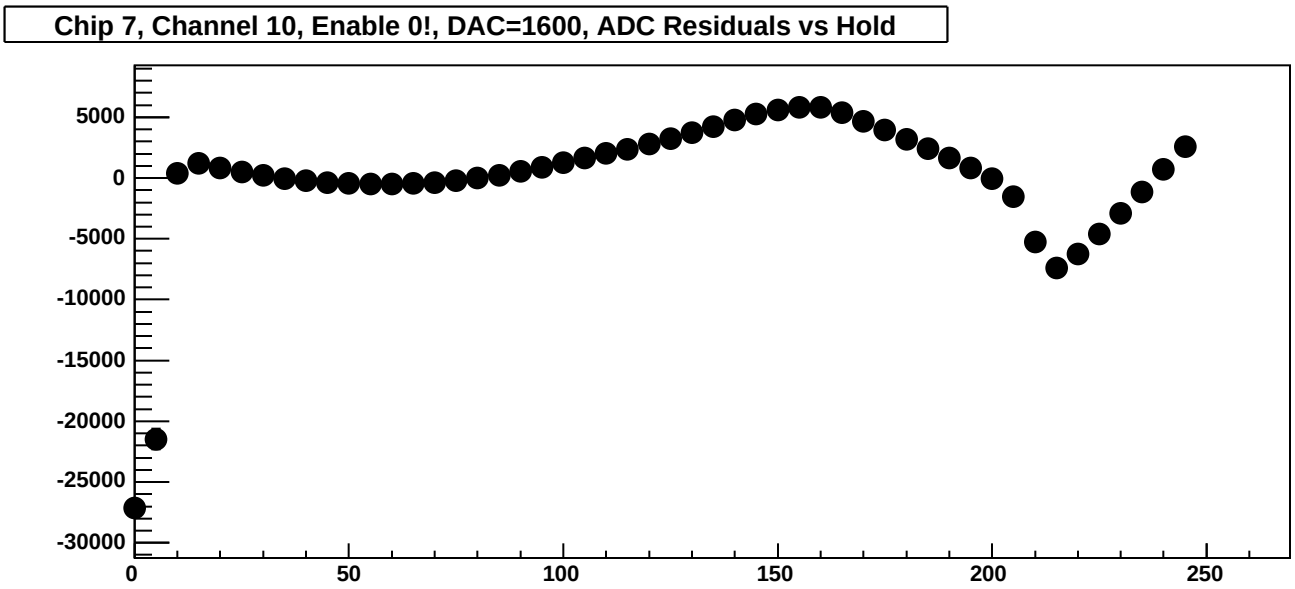

Chip 7, Channel 9, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.262e+05 / 41
p0 -805 ± 6.369
p1 56.48 ± 0.02674
p2 2.387e+04 ± 42.89
p3 46.15 ± 0.0886
p4 -15.57 ± 0.2328
20000
15000
10000
5000
0
0
50
100
150
200
250

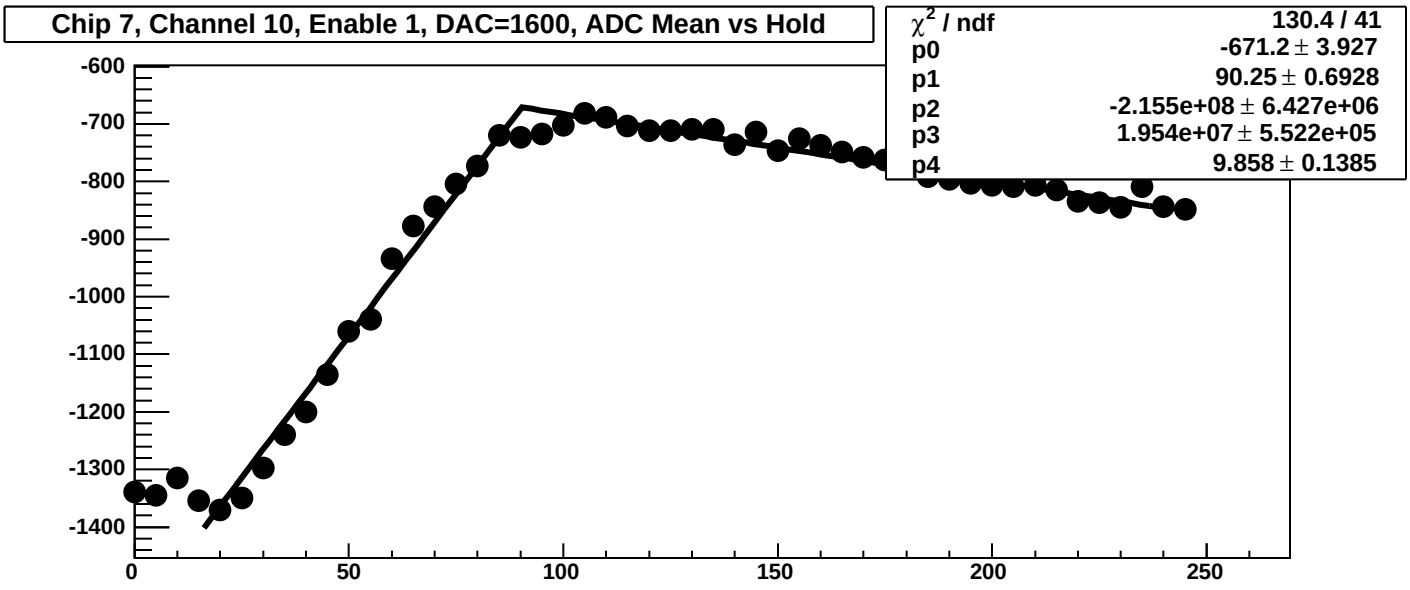

Chip 7, Channel 9, Enable 5, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

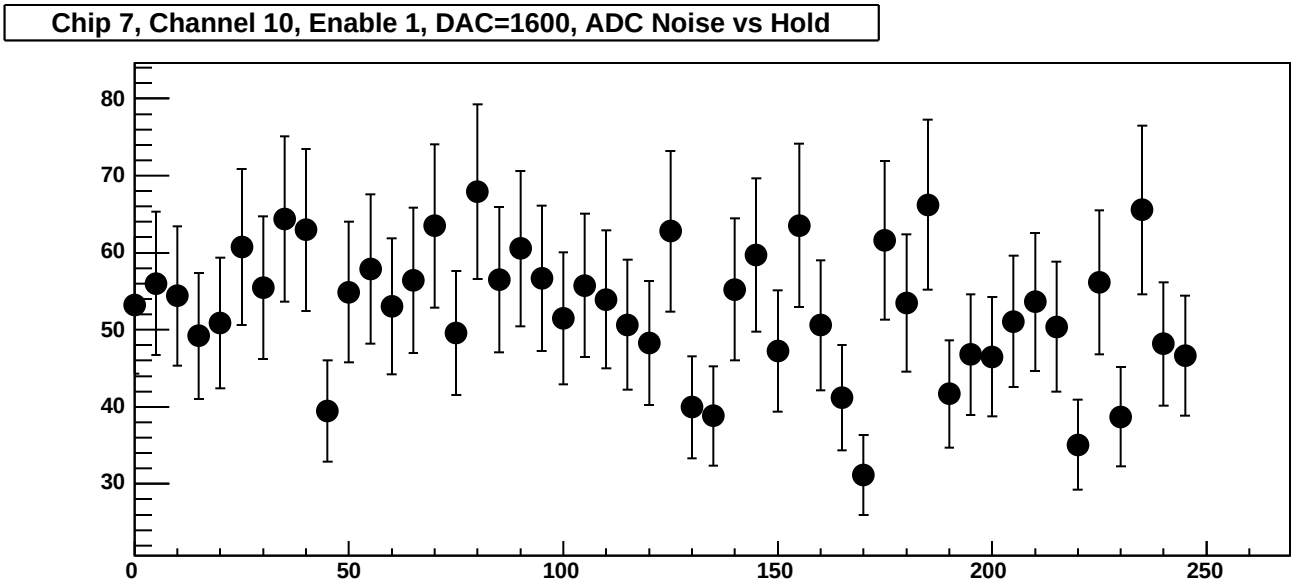

Chip 7, Channel 9, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

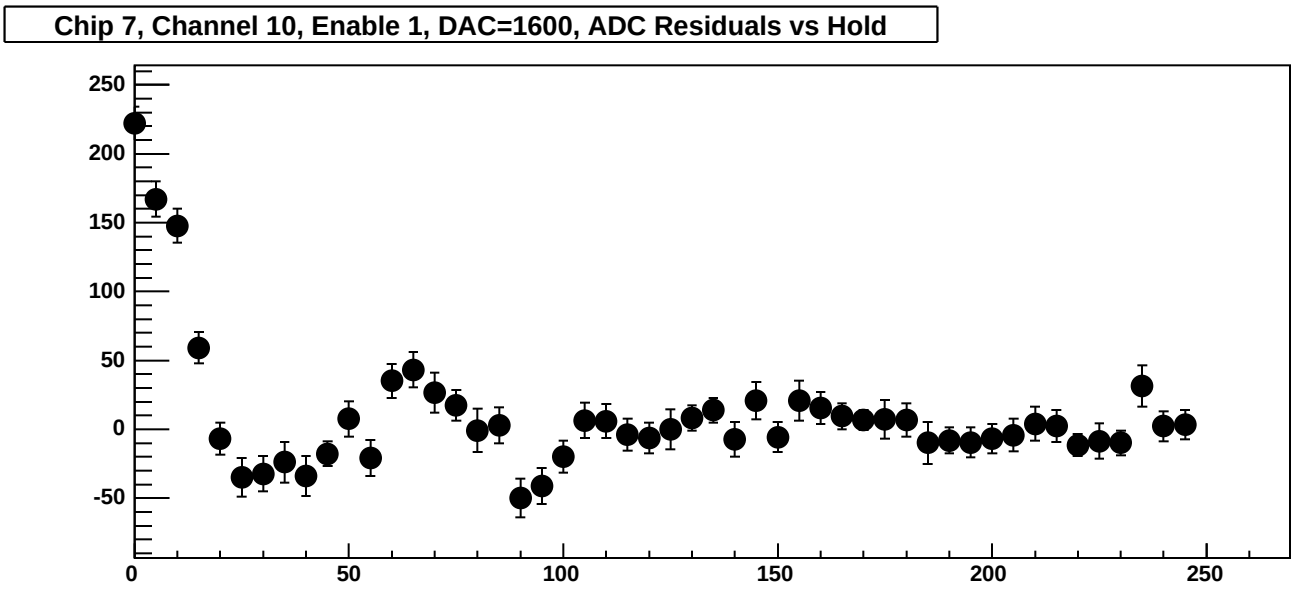

Chip 7, Channel 10, Enable 0!, DAC=1600, ADC Mean vs Hold
χ² / ndf 6.943e+06 / 42
p0 -5.667e+09 ± 3.067
p1 -5.297e+04 ± 0.05376
p2 5.667e+09 ± 3.067
p3 5.302e+04 ± 0.05373
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

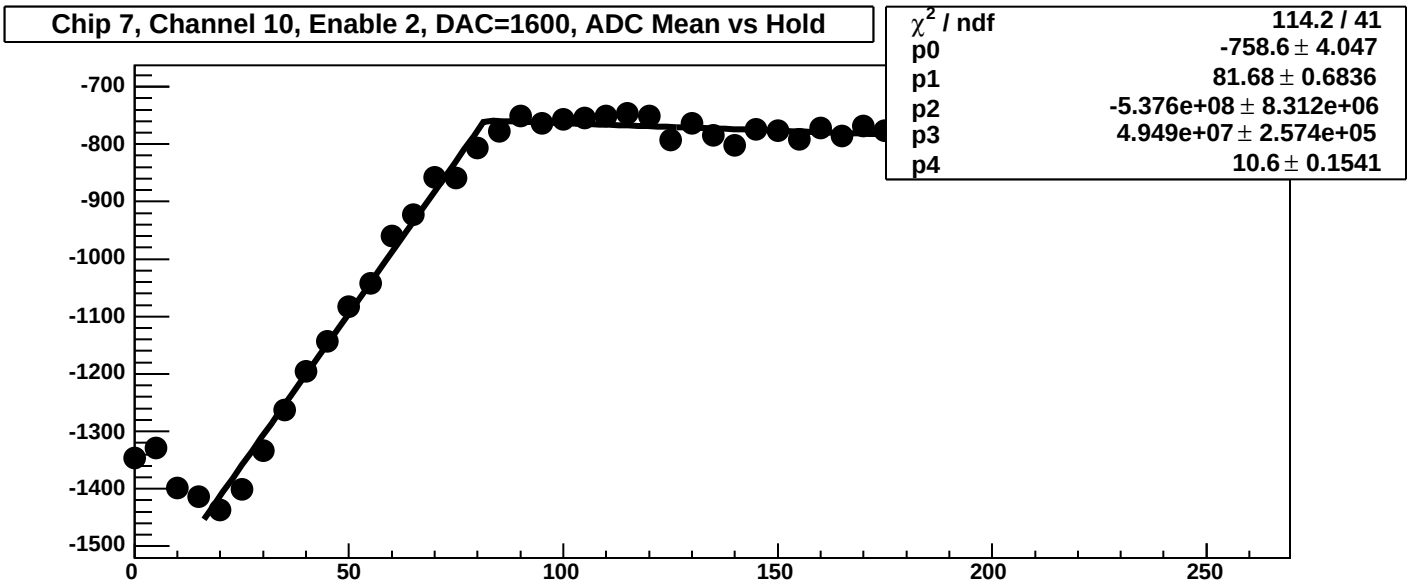

Chip 7, Channel 10, Enable 0!, DAC=1600, ADC Noise vs Hold
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
50
100
150
200
250

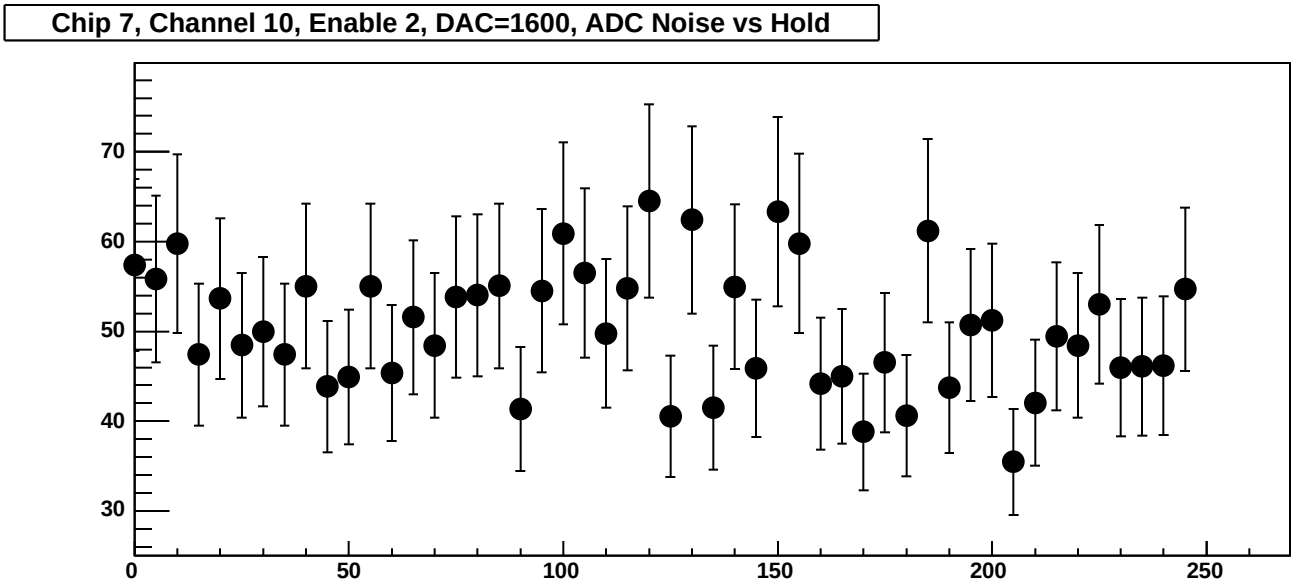

Chip 7, Channel 10, Enable 0!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
0
50
100
150
200
250

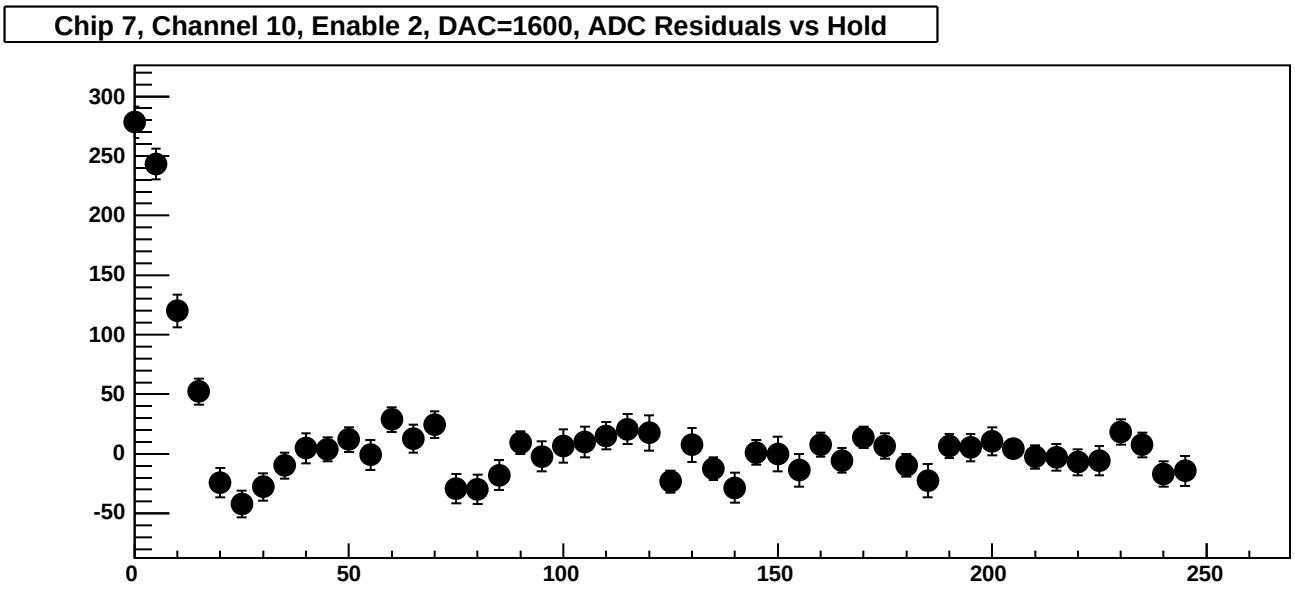

Chip 7, Channel 10, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 130.4 / 41
p0 -671.2 ± 3.927
p1 90.25 ± 0.6928
p2 -2.155e+08 ± 6.427e+06
p3 1.954e+07 ± 5.522e+05
p4 9.858 ± 0.1385
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
50
100
150
200
250


Chip 7, Channel 10, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

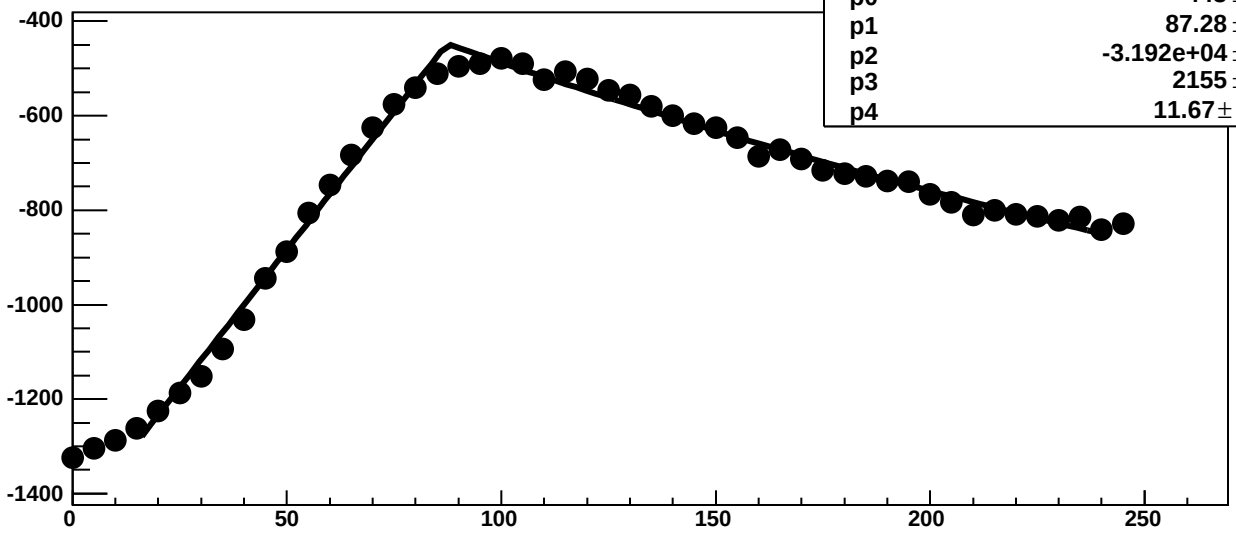

Chip 7, Channel 10, Enable 1, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
0
50
100
150
200
250


Chip 7, Channel 10, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 114.2 / 41
p0 -758.6 ± 4.047
p1 81.68 ± 0.6836
p2 -5.376e+08 ± 8.312e+06
p3 4.949e+07 ± 2.574e+05
p4 10.6 ± 0.1541
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250

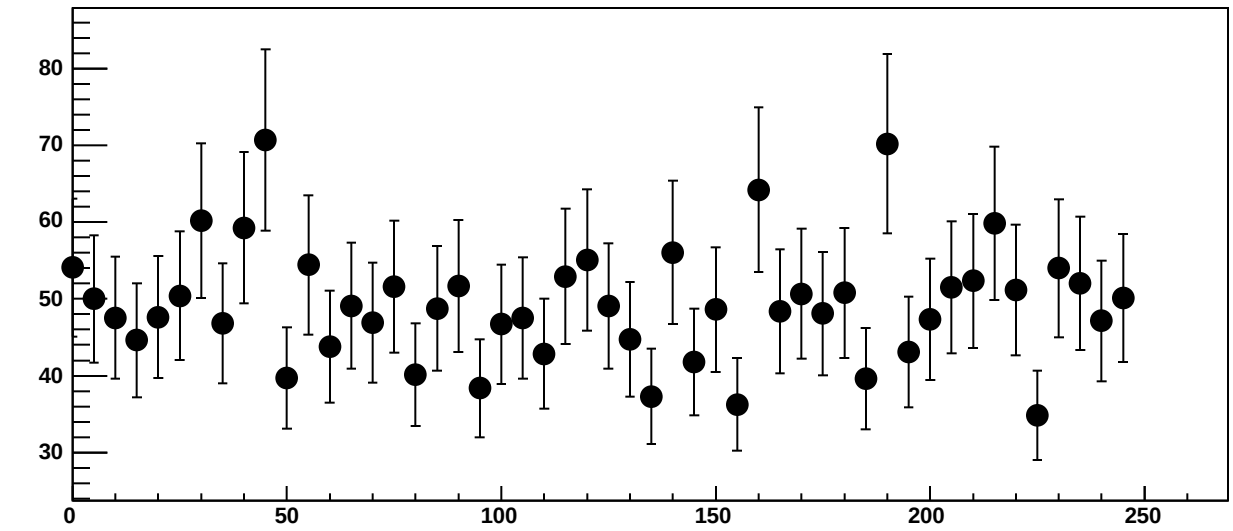

Chip 7, Channel 10, Enable 2, DAC=1600, ADC Noise vs Hold
70
60
50
40
30
0
50
100
150
200
250


Chip 7, Channel 10, Enable 2, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
0
50
100
150
200
250

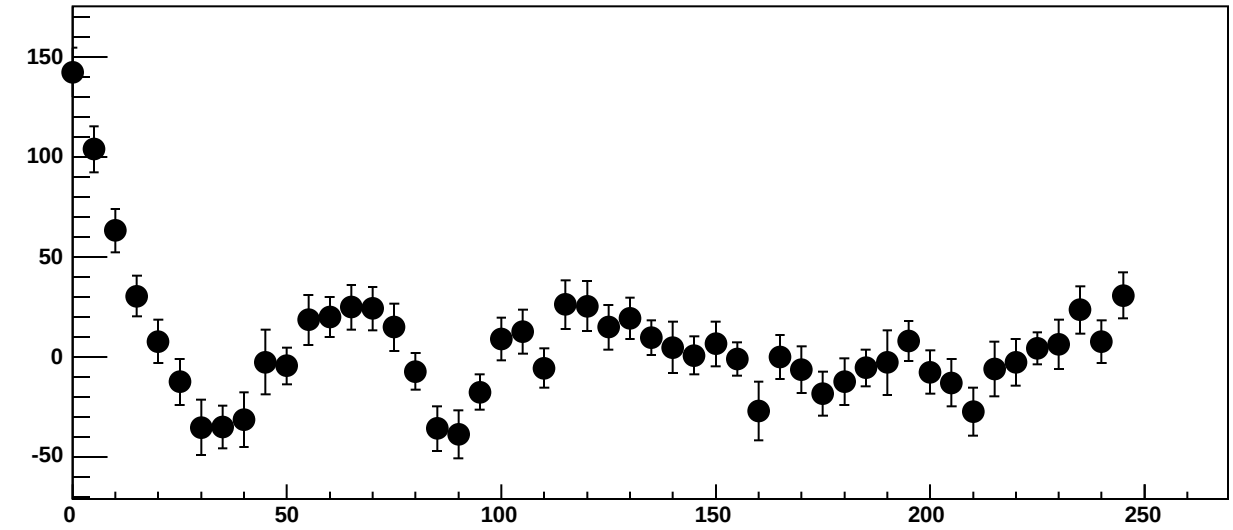
Chip 7, Channel 10, Enable 3, DAC=1600, ADC Mean vs Hold

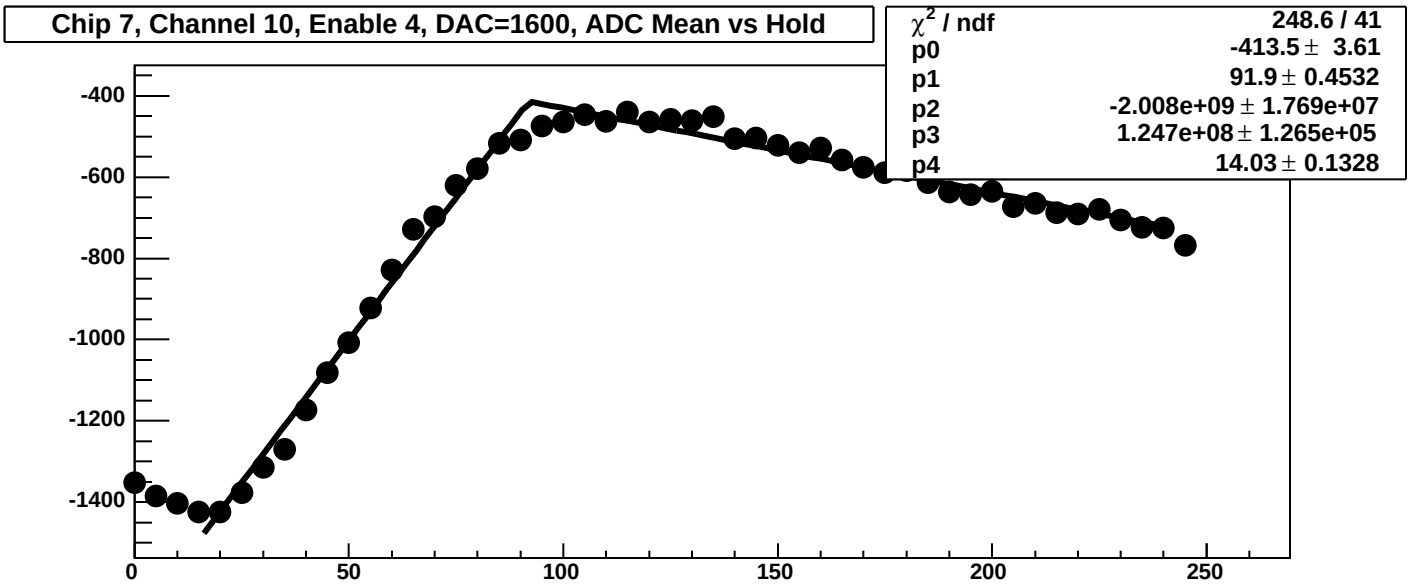


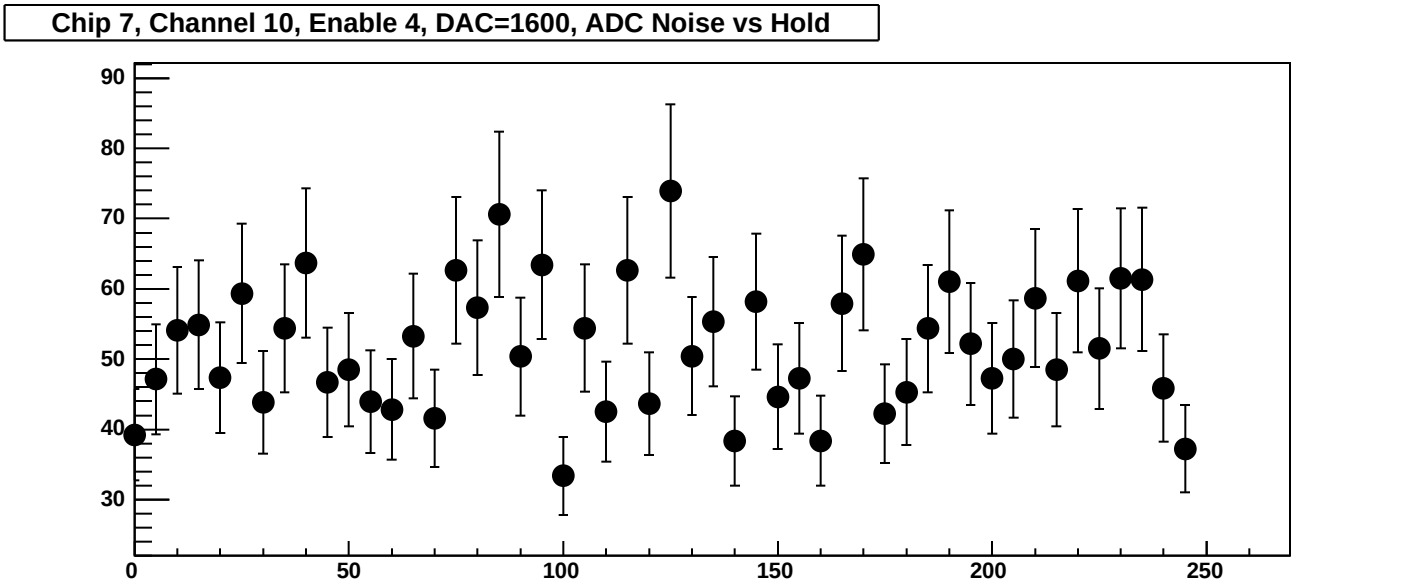
Chip 7, Channel 10, Enable 3, DAC=1600, ADC Noise vs Hold

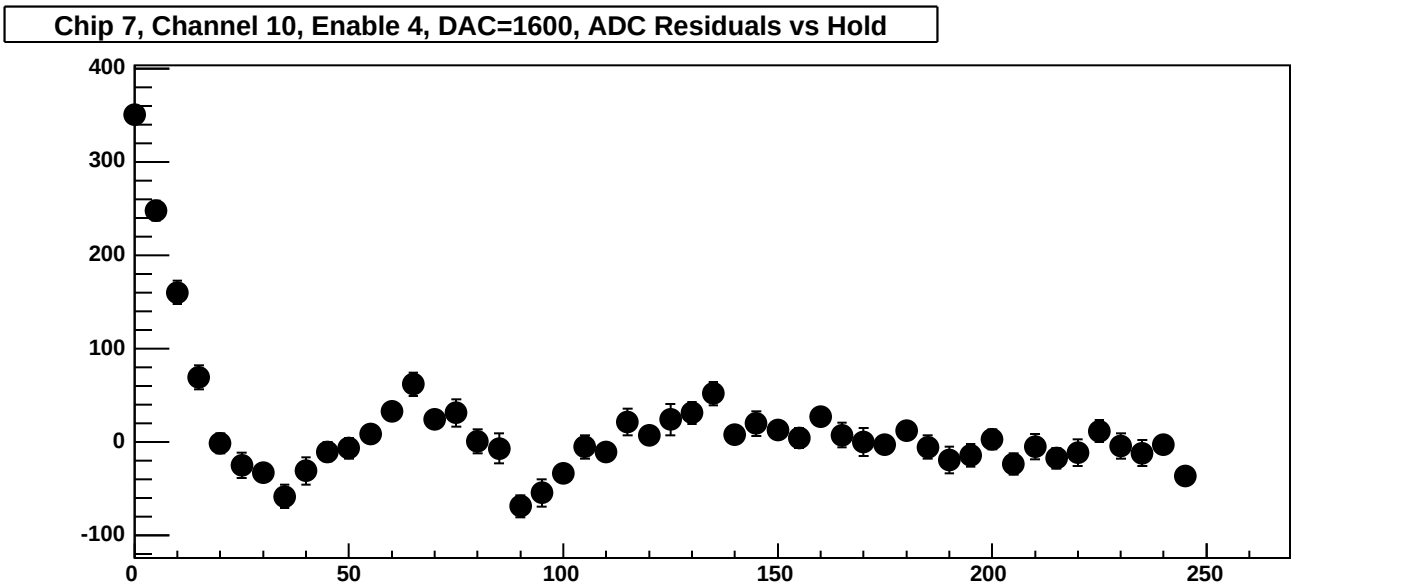


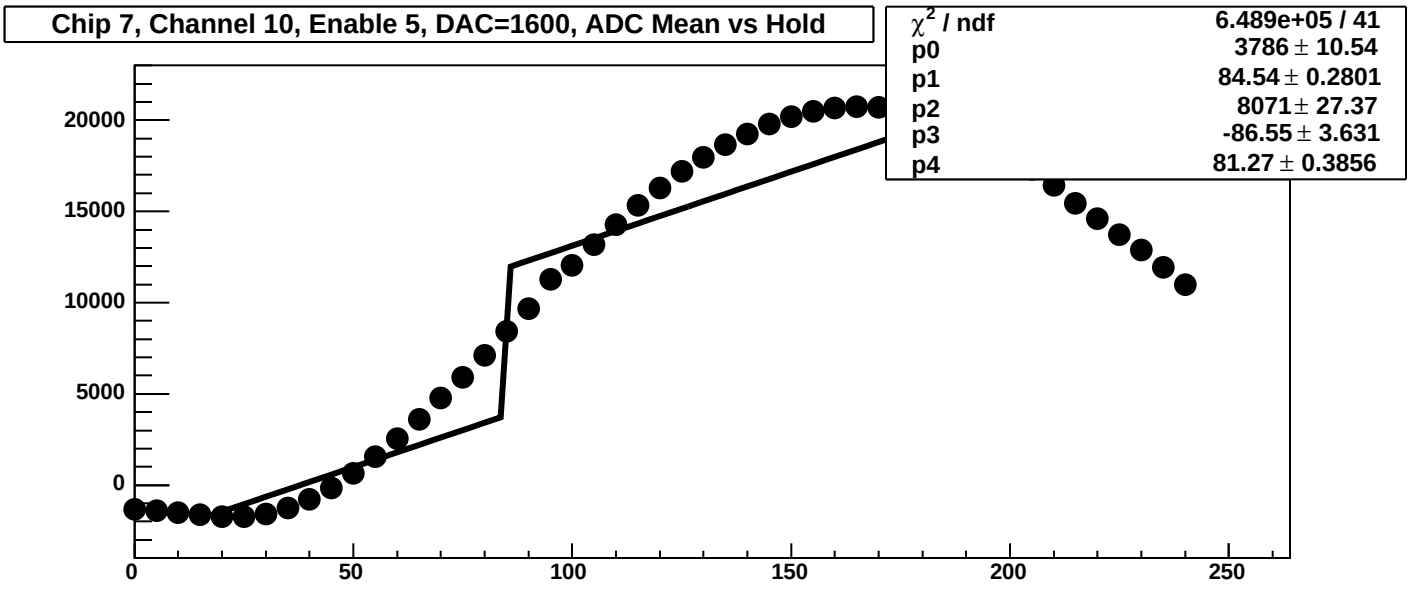
Chip 7, Channel 10, Enable 3, DAC=1600, ADC Residuals vs Hold

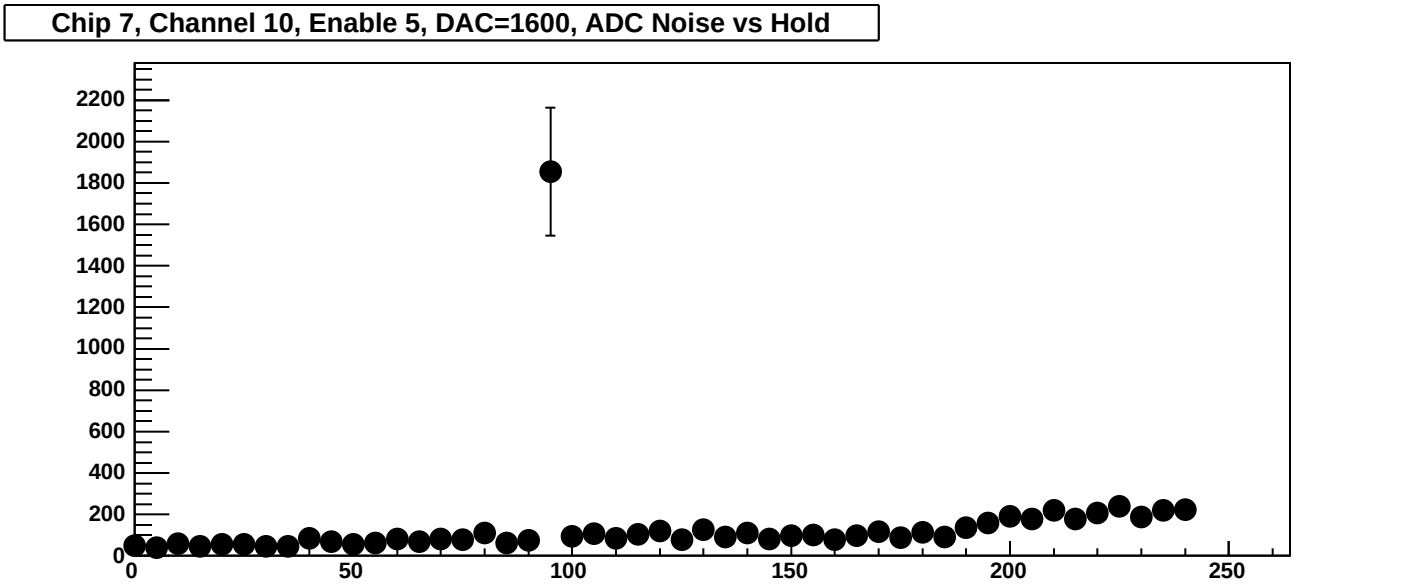


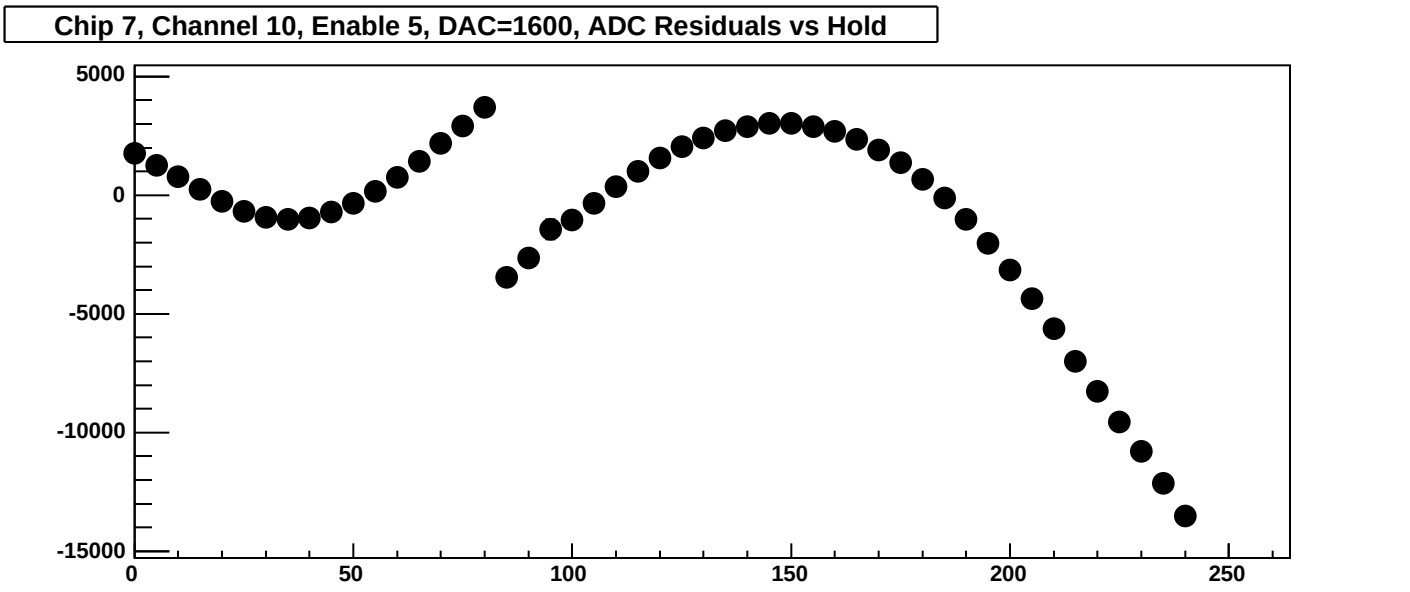

Chip 7, Channel 10, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 248.6 / 41
p0 -413.5 ± 3.61
p1 91.9 ± 0.4532
p2 -2.008e+09 ± 1.769e+07
p3 1.247e+08 ± 1.265e+05
p4 14.03 ± 0.1328
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250


Chip 7, Channel 10, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

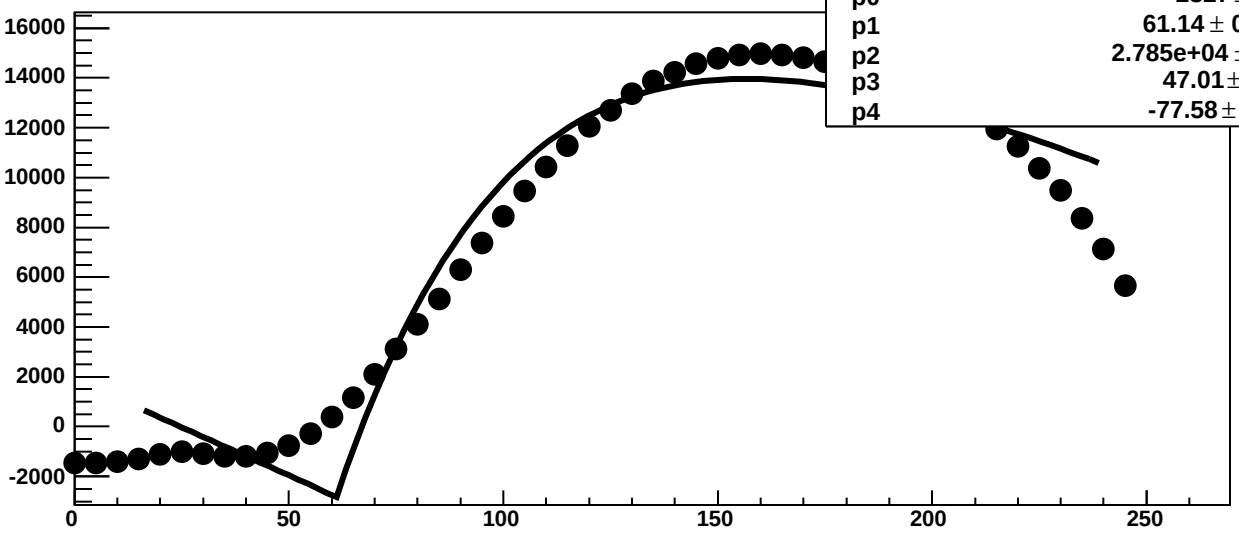

Chip 7, Channel 10, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 7, Channel 10, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 6.489e+05 / 41
p0 3786 ± 10.54
p1 84.54 ± 0.2801
p2 8071 ± 27.37
p3 -86.55 ± 3.631
p4 81.27 ± 0.3856
20000
15000
10000
5000
0
0
50
100
150
200
250

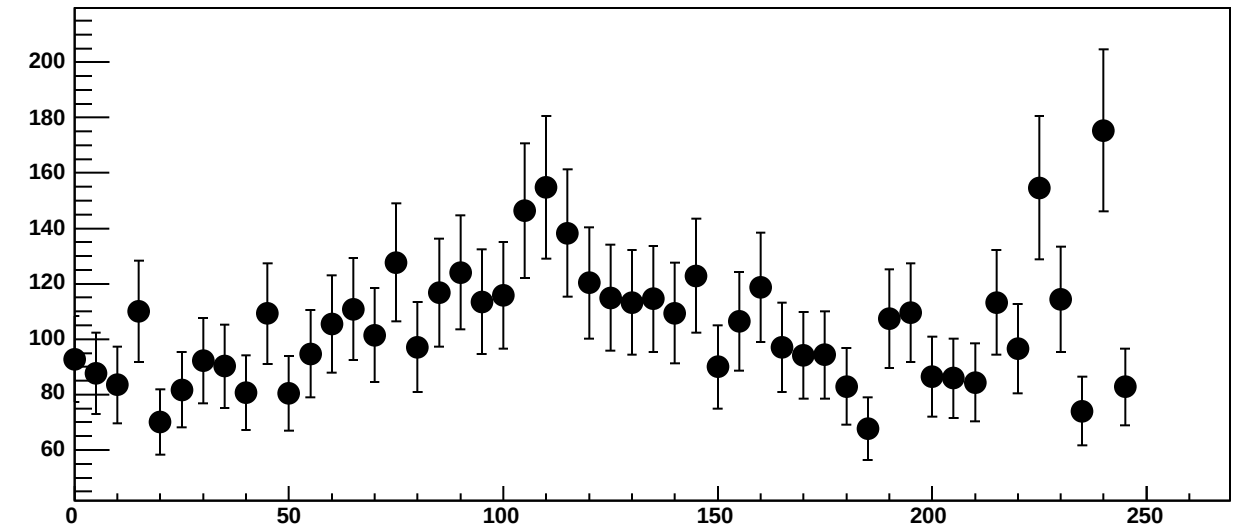

Chip 7, Channel 10, Enable 5, DAC=1600, ADC Noise vs Hold
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 7, Channel 10, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
0
50
100
150
200
250

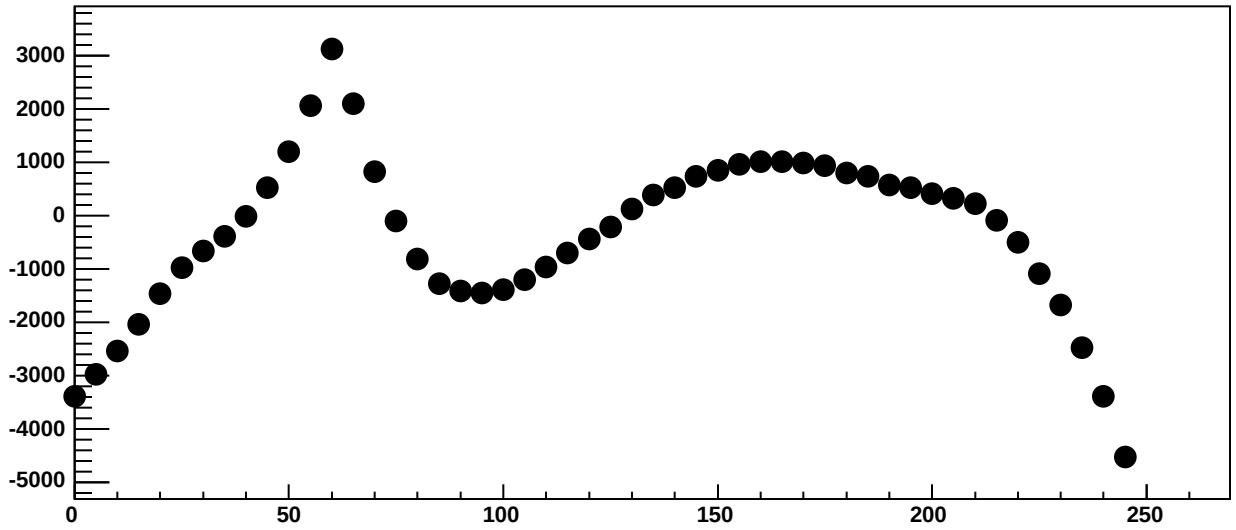
Chip 7, Channel 11, Enable 0, DAC=1600, ADC Mean vs Hold



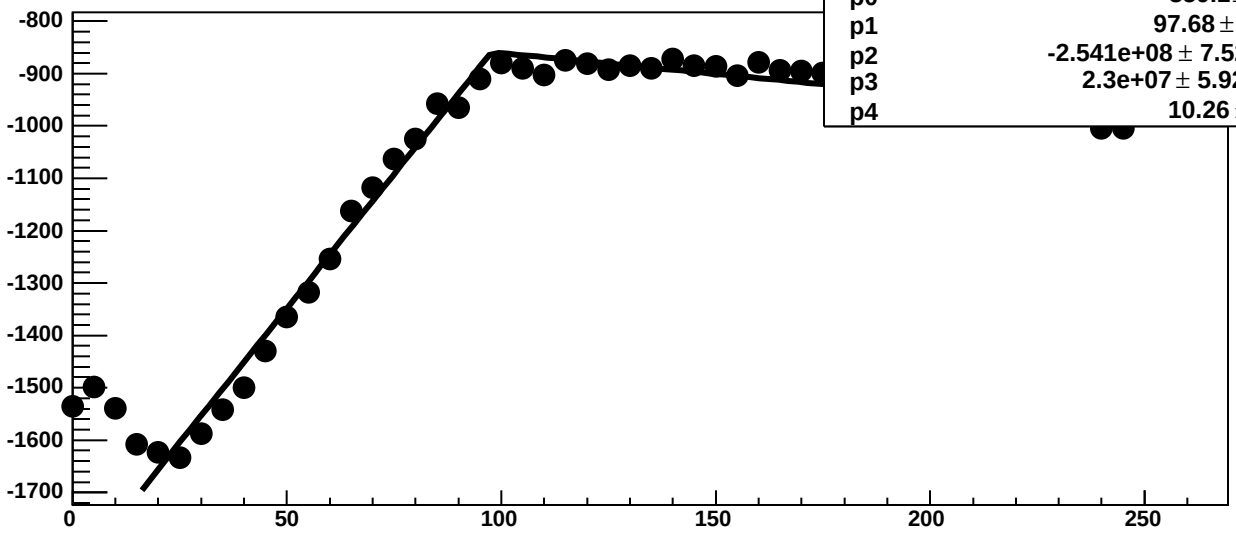
Chip 7, Channel 11, Enable 0, DAC=1600, ADC Noise vs Hold



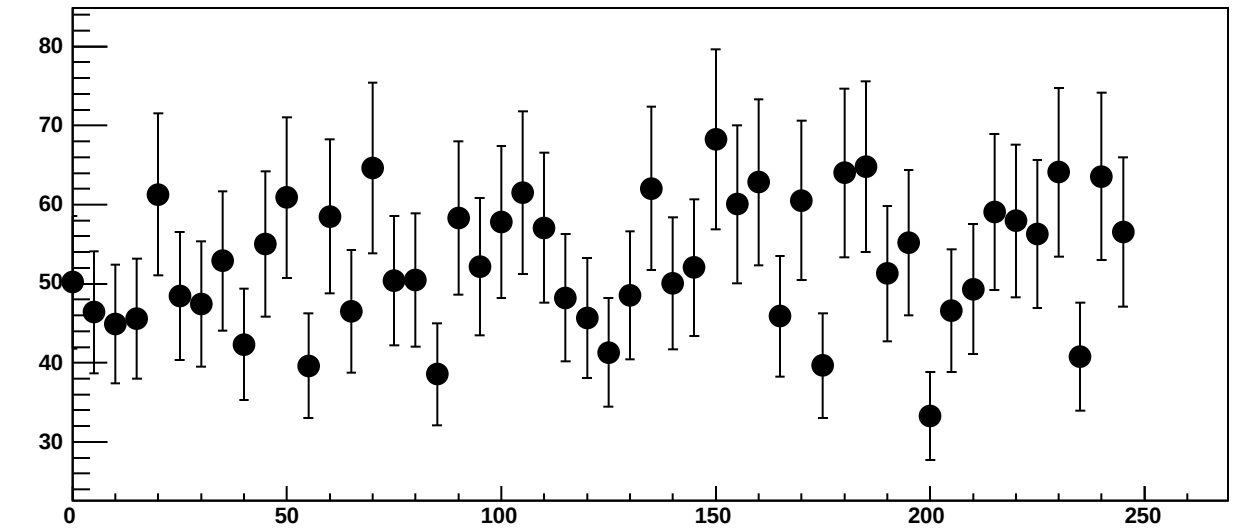
Chip 7, Channel 11, Enable 0, DAC=1600, ADC Residuals vs Hold



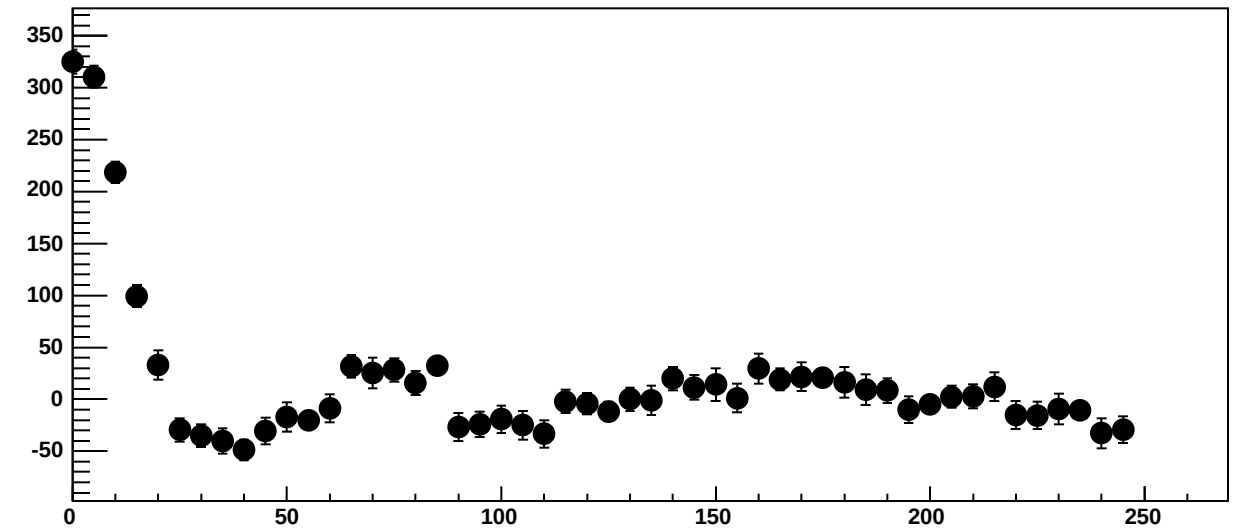
Chip 7, Channel 11, Enable 1, DAC=1600, ADC Mean vs Hold

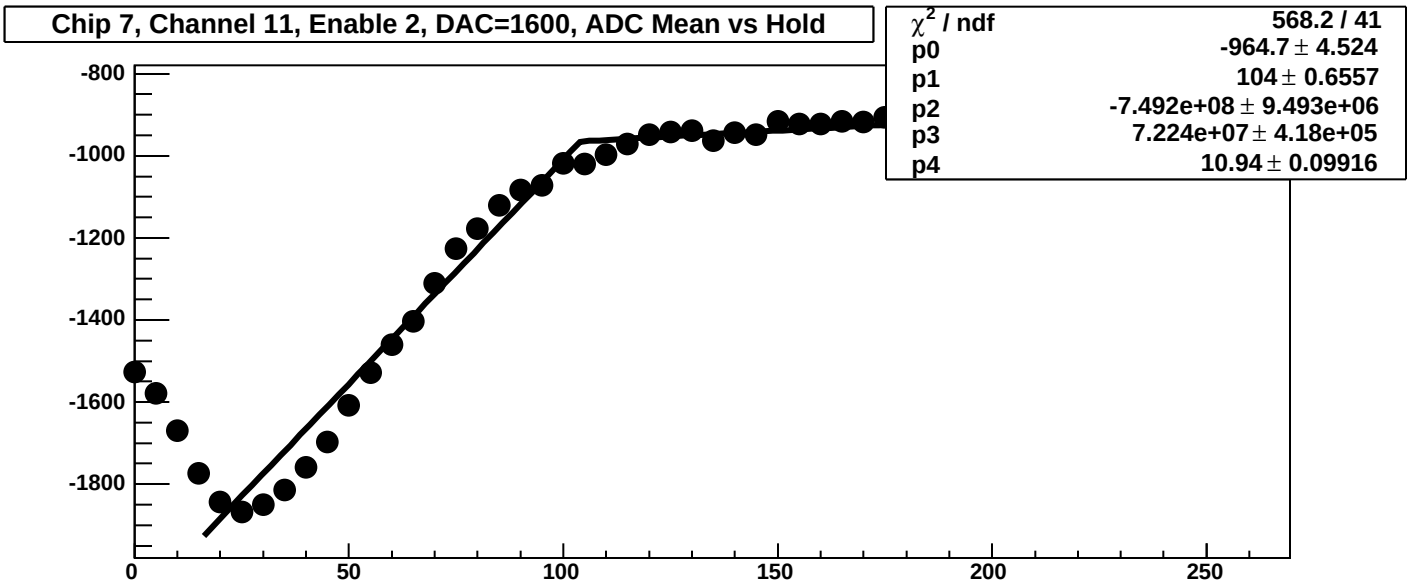


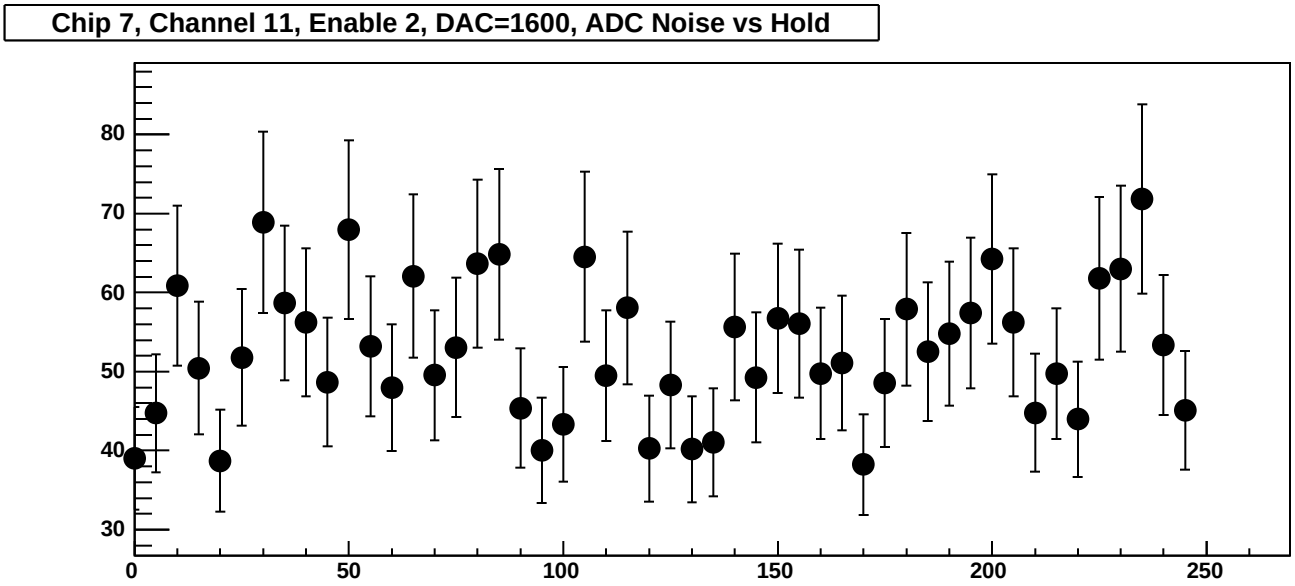
Chip 7, Channel 11, Enable 1, DAC=1600, ADC Noise vs Hold

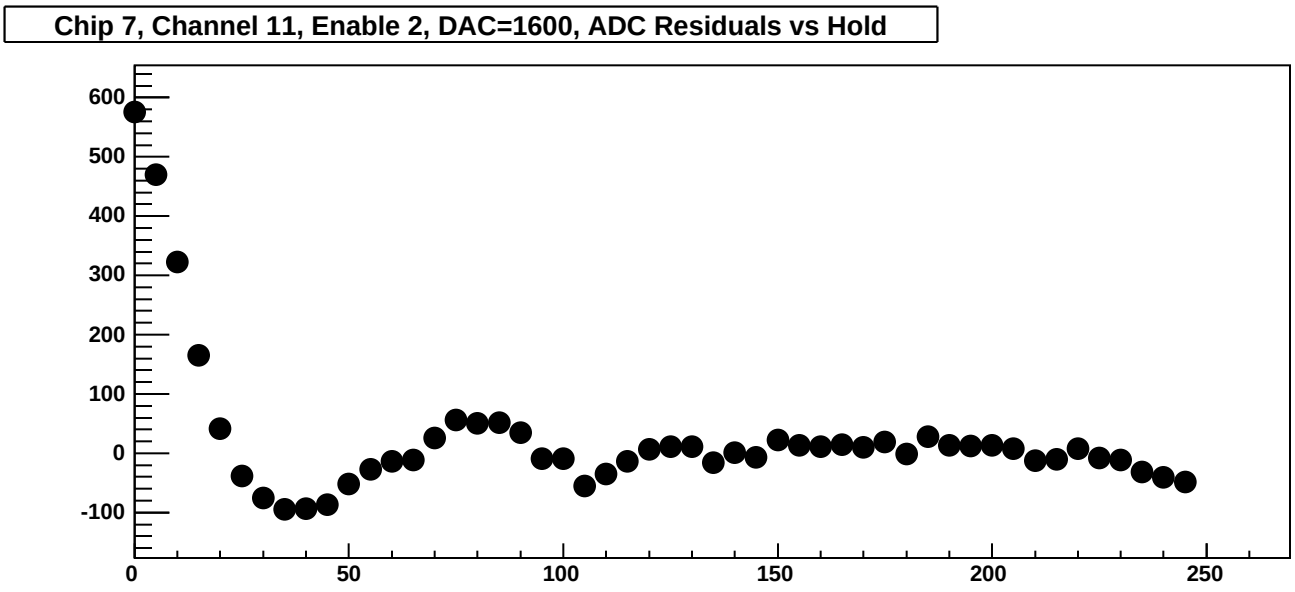


Chip 7, Channel 11, Enable 1, DAC=1600, ADC Residuals vs Hold

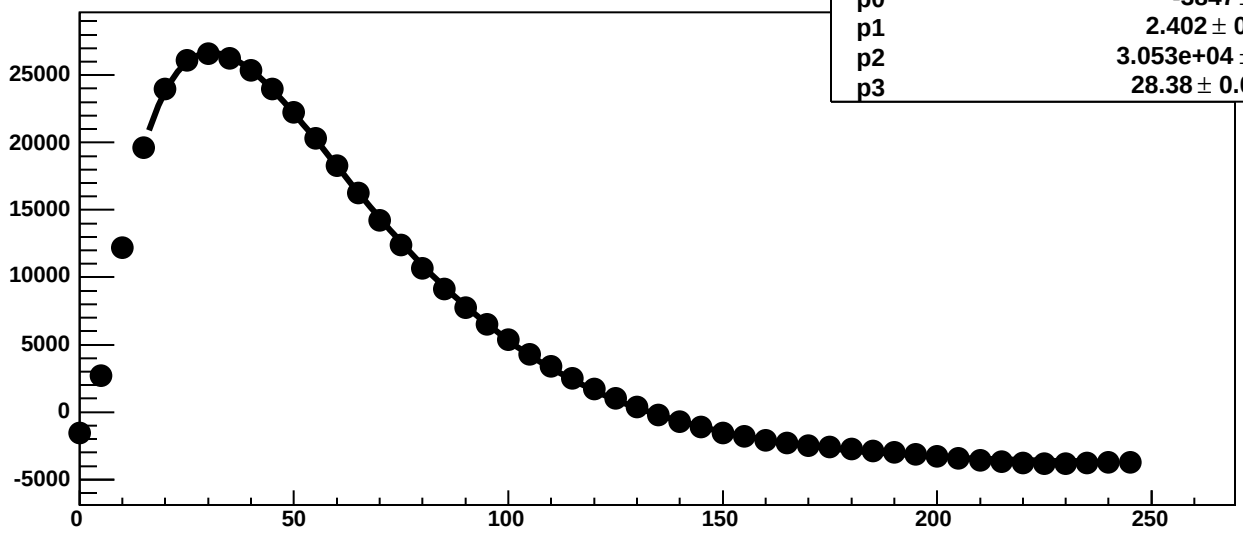



Chip 7, Channel 11, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 568.2 / 41
p0 -964.7 ± 4.524
p1 104 ± 0.6557
p2 -7.492e+08 ± 9.493e+06
p3 7.224e+07 ± 4.18e+05
p4 10.94 ± 0.09916
-800
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250

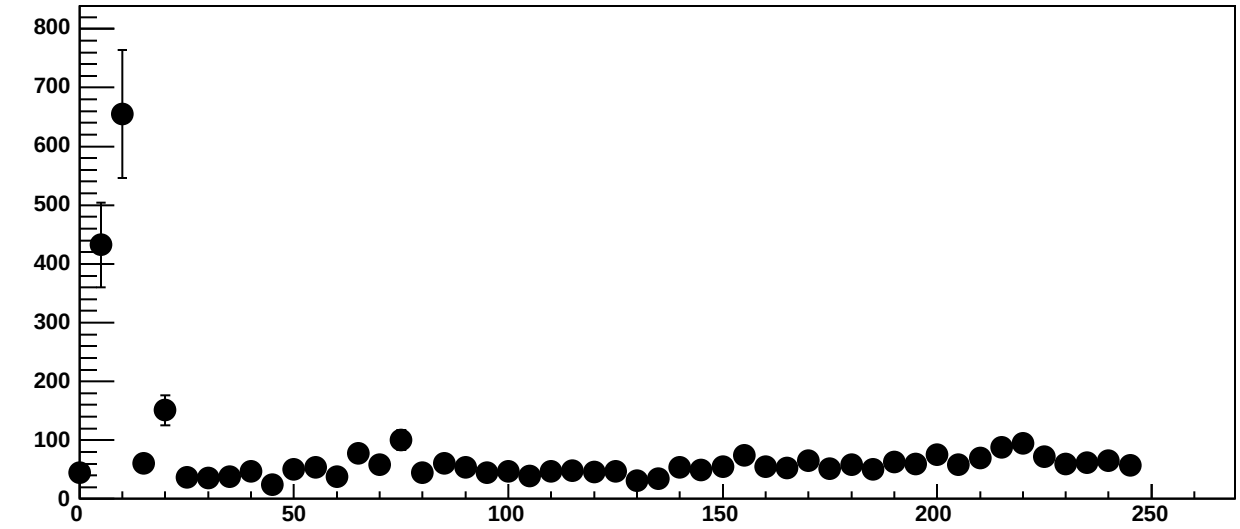

Chip 7, Channel 11, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 7, Channel 11, Enable 2, DAC=1600, ADC Residuals vs Hold
600
500
400
300
200
100
0
-100
0
50
100
150
200
250

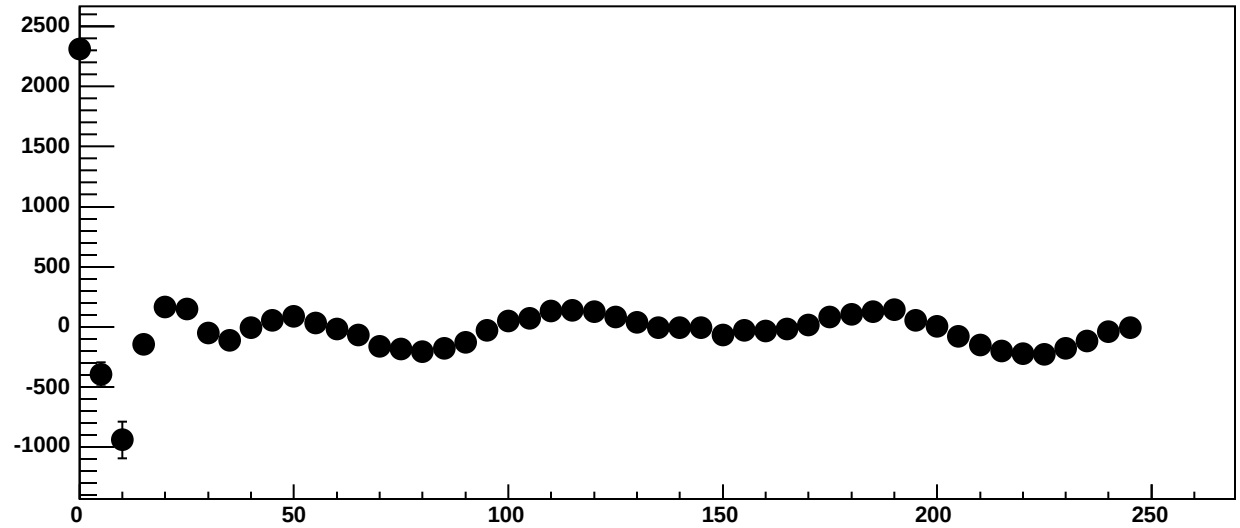
Chip 7, Channel 11, Enable 3!, DAC=1600, ADC Mean vs Hold

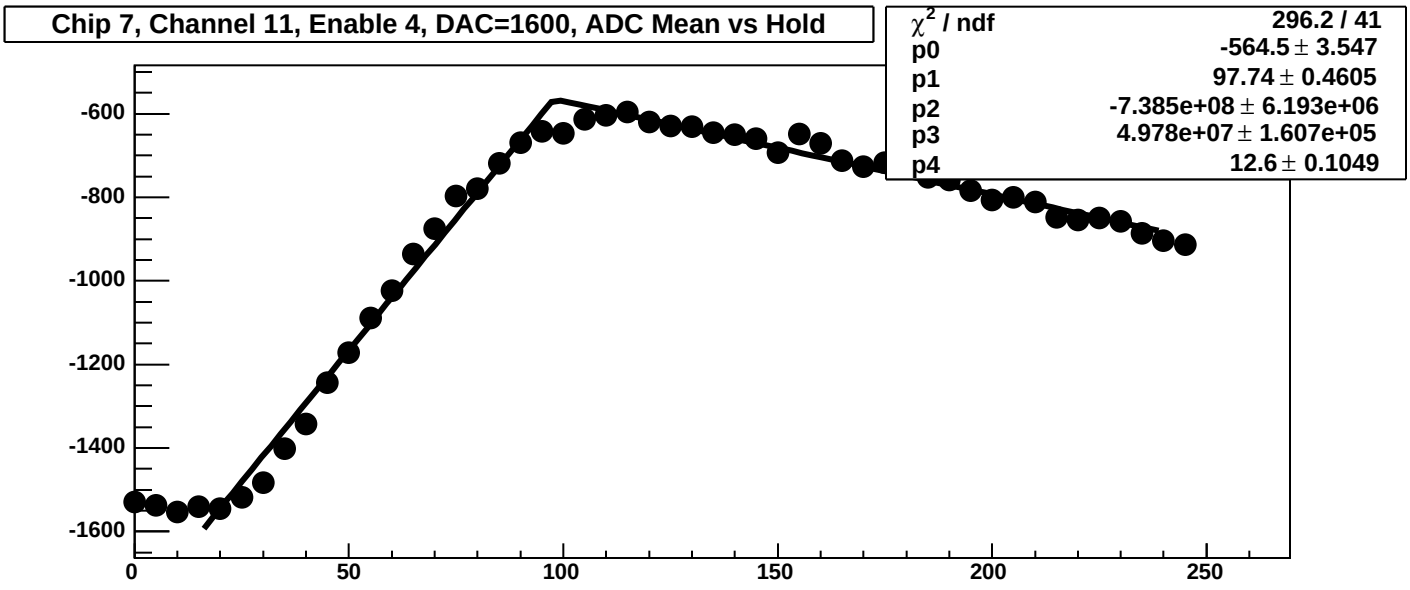


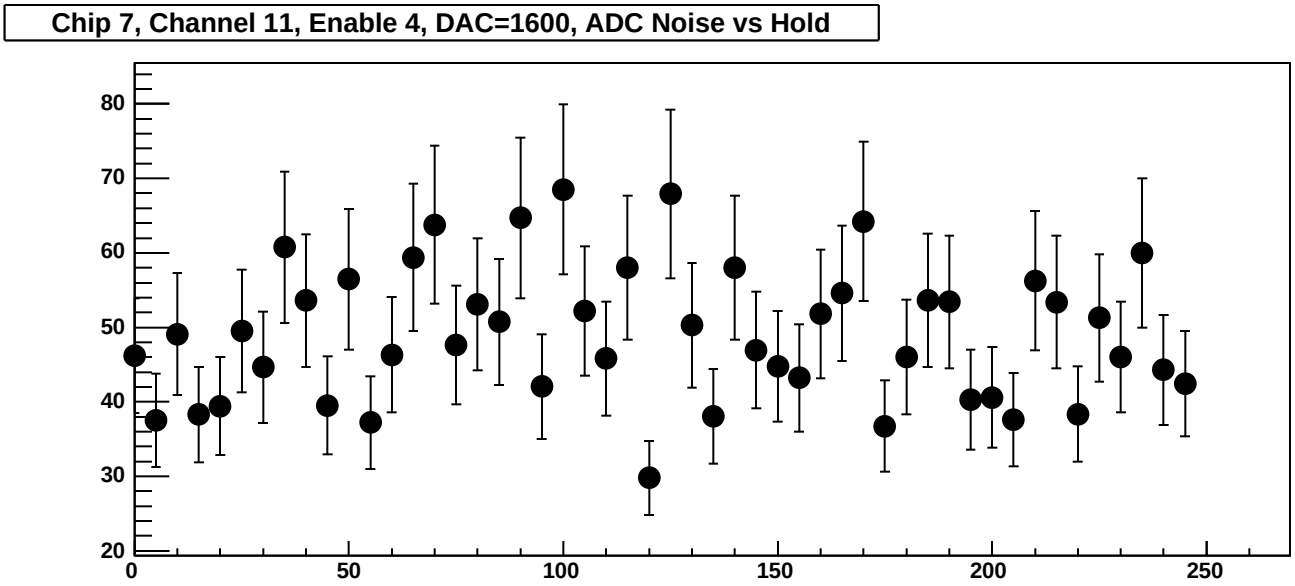
Chip 7, Channel 11, Enable 3!, DAC=1600, ADC Noise vs Hold

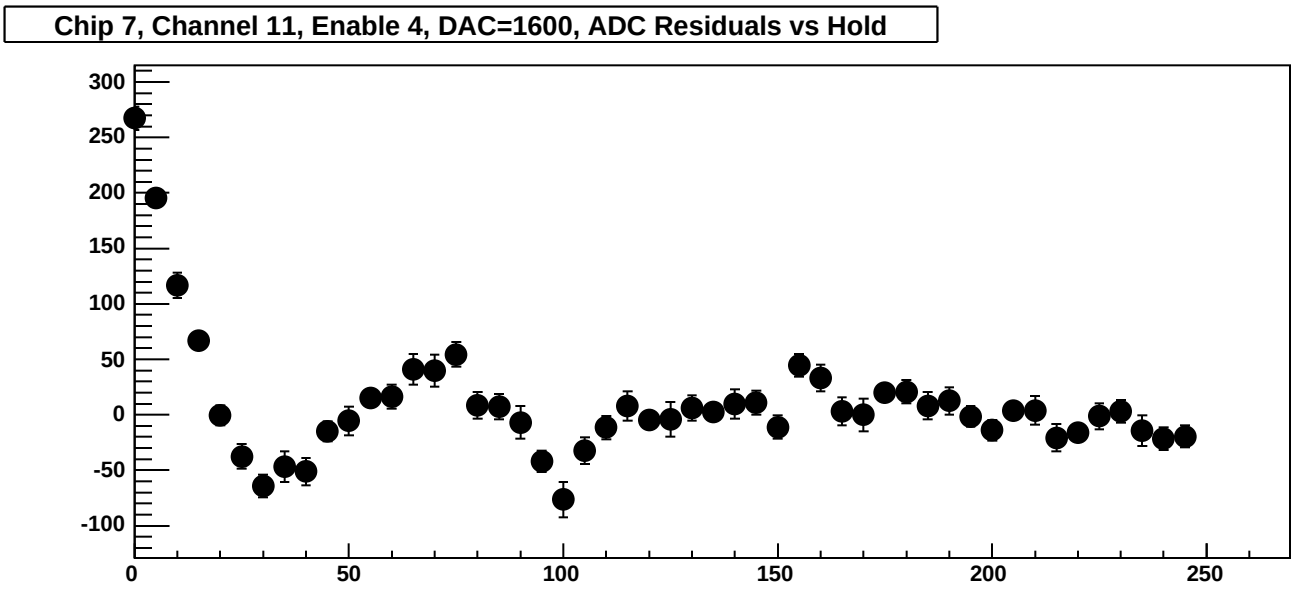


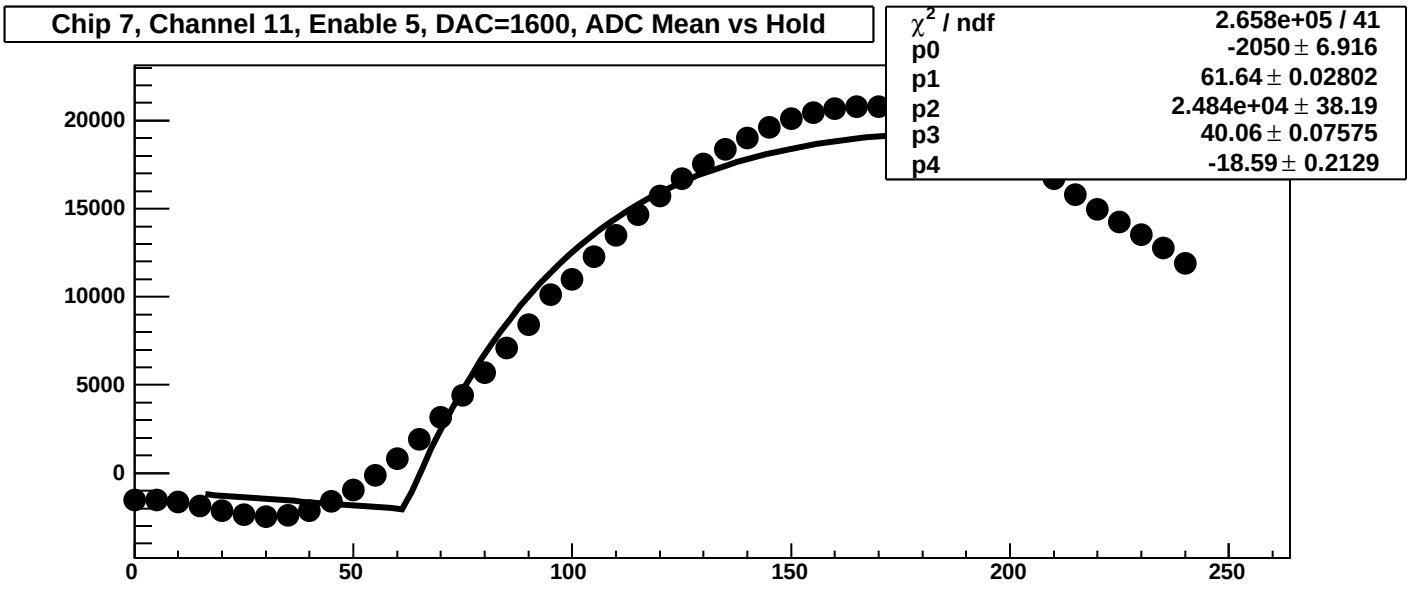
Chip 7, Channel 11, Enable 3!, DAC=1600, ADC Residuals vs Hold

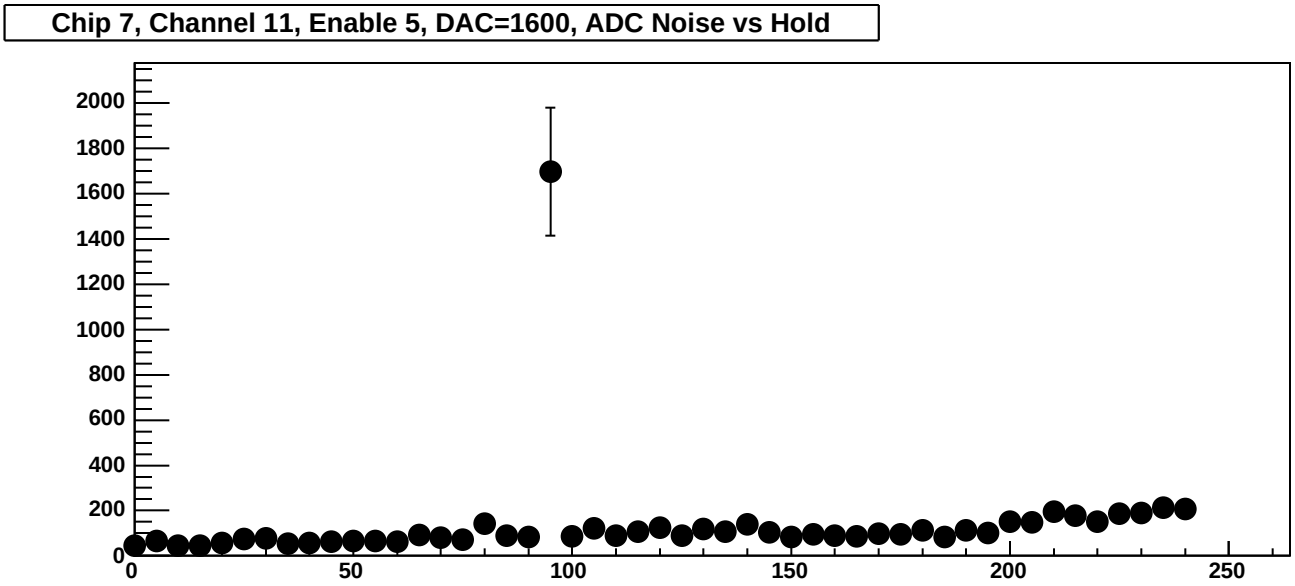


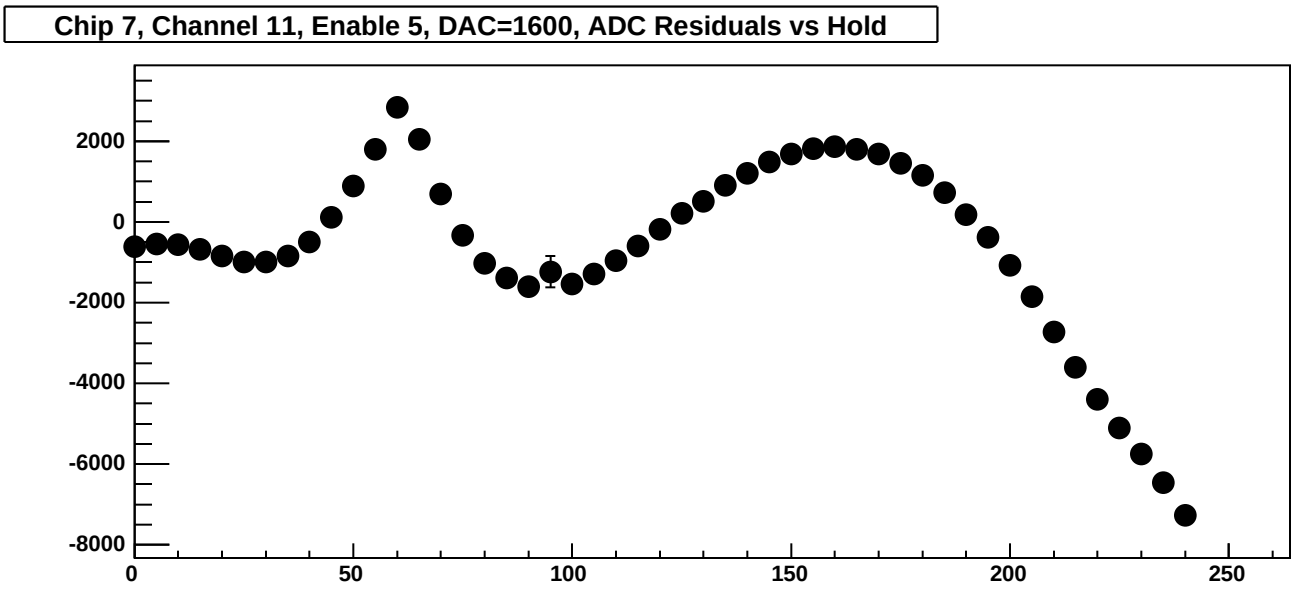

Chip 7, Channel 11, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 296.2 / 41
p0 -564.5 ± 3.547
p1 97.74 ± 0.4605
p2 -7.385e+08 ± 6.193e+06
p3 4.978e+07 ± 1.607e+05
p4 12.6 ± 0.1049
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250


Chip 7, Channel 11, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
20
0
50
100
150
200
250

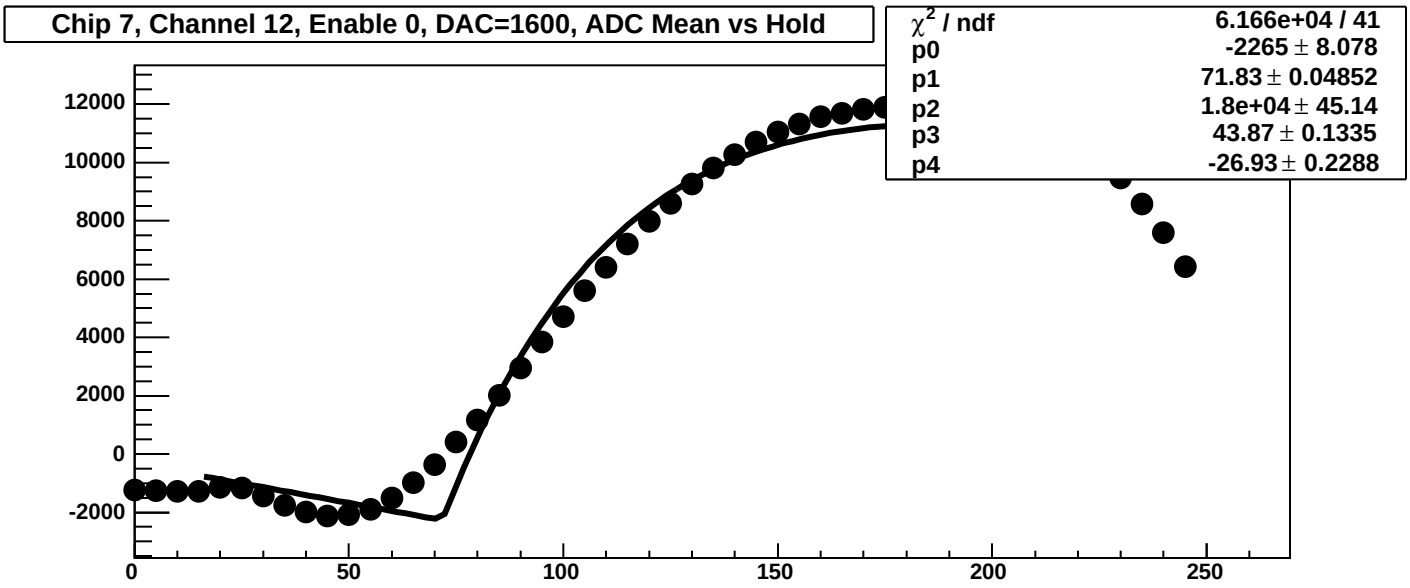

Chip 7, Channel 11, Enable 4, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250


Chip 7, Channel 11, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.658e+05 / 41
p0 -2050 ± 6.916
p1 61.64 ± 0.02802
p2 2.484e+04 ± 38.19
p3 40.06 ± 0.07575
p4 -18.59 ± 0.2129
20000
15000
10000
5000
0
0
50
100
150
200
250

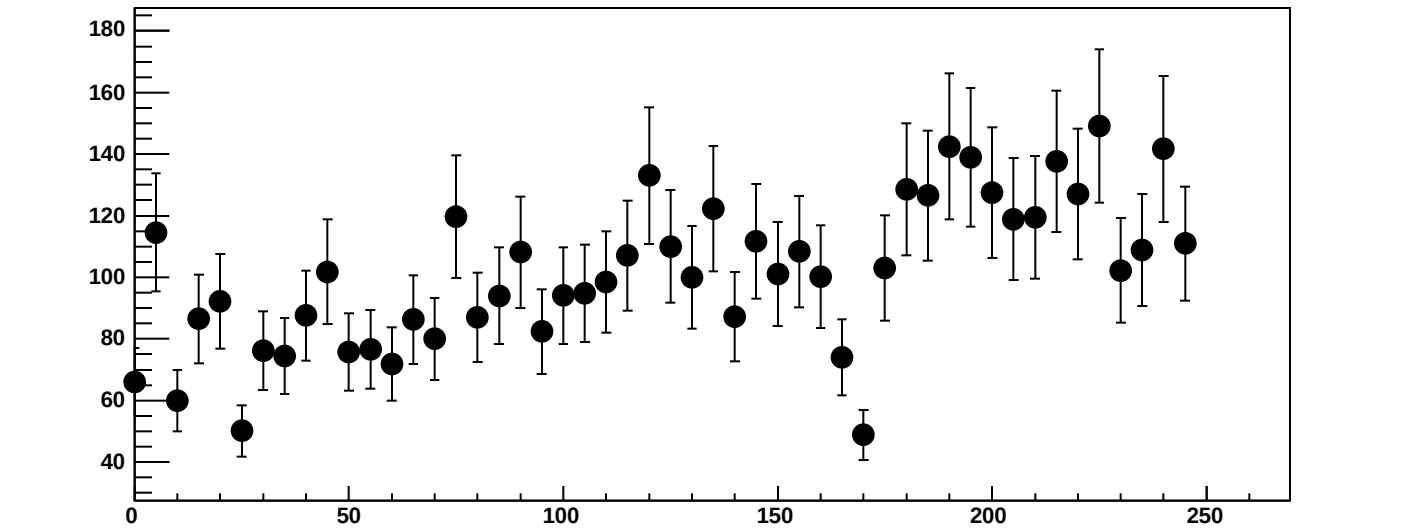

Chip 7, Channel 11, Enable 5, DAC=1600, ADC Noise vs Hold
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 7, Channel 11, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250

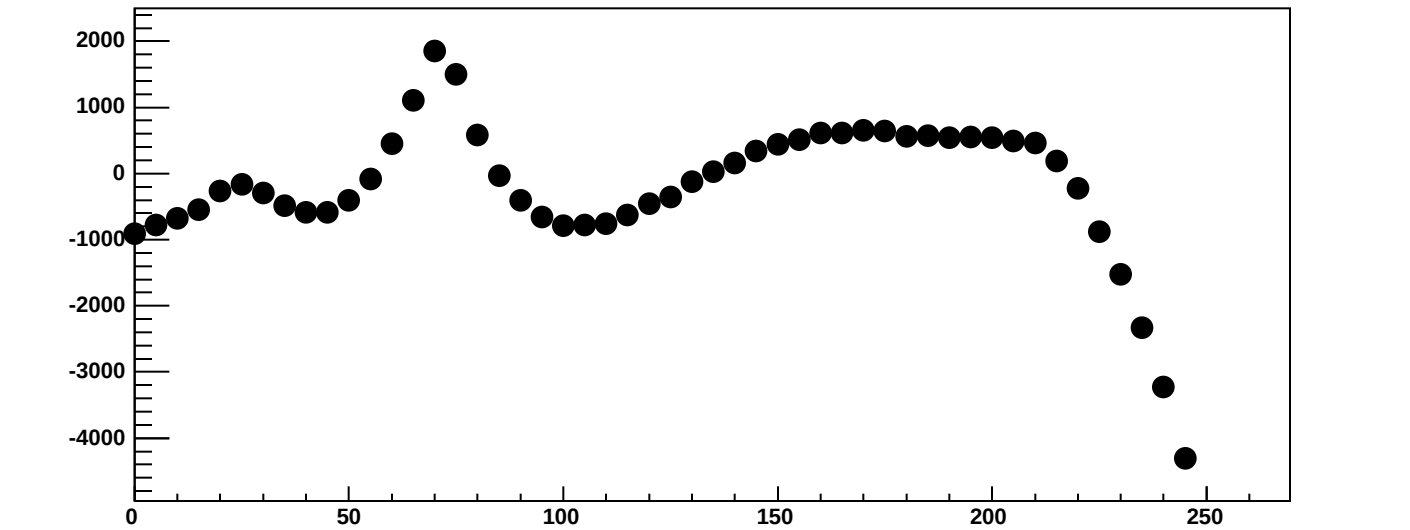
Chip 7, Channel 12, Enable 0, DAC=1600, ADC Mean vs Hold

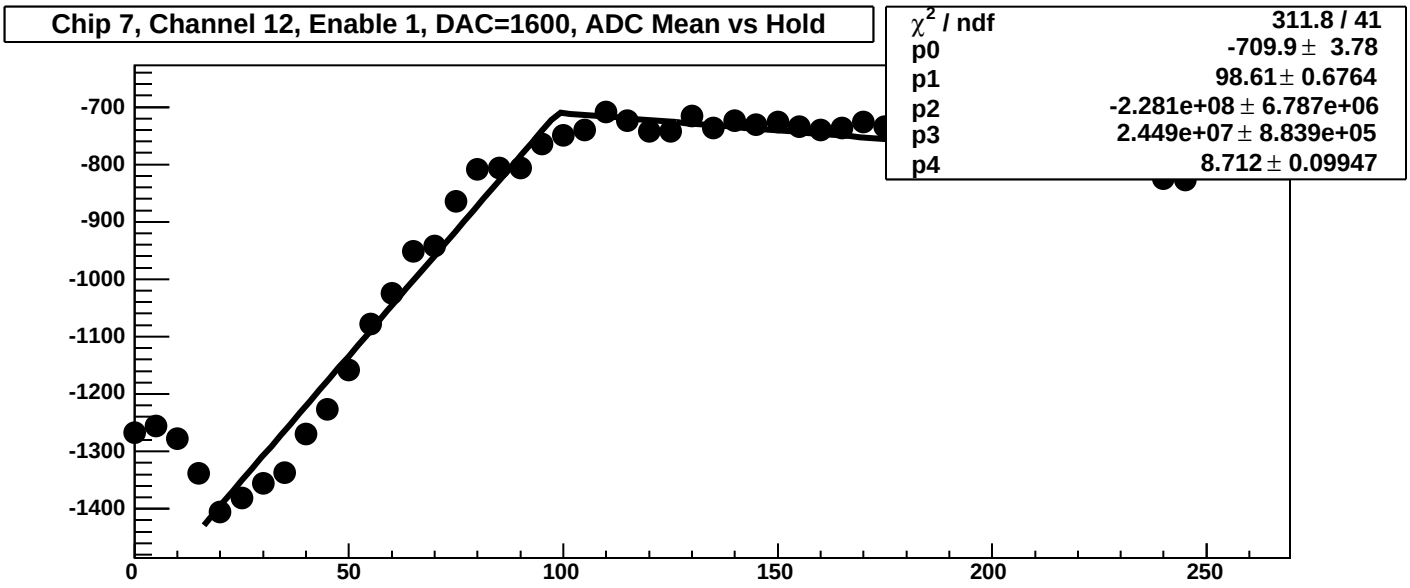


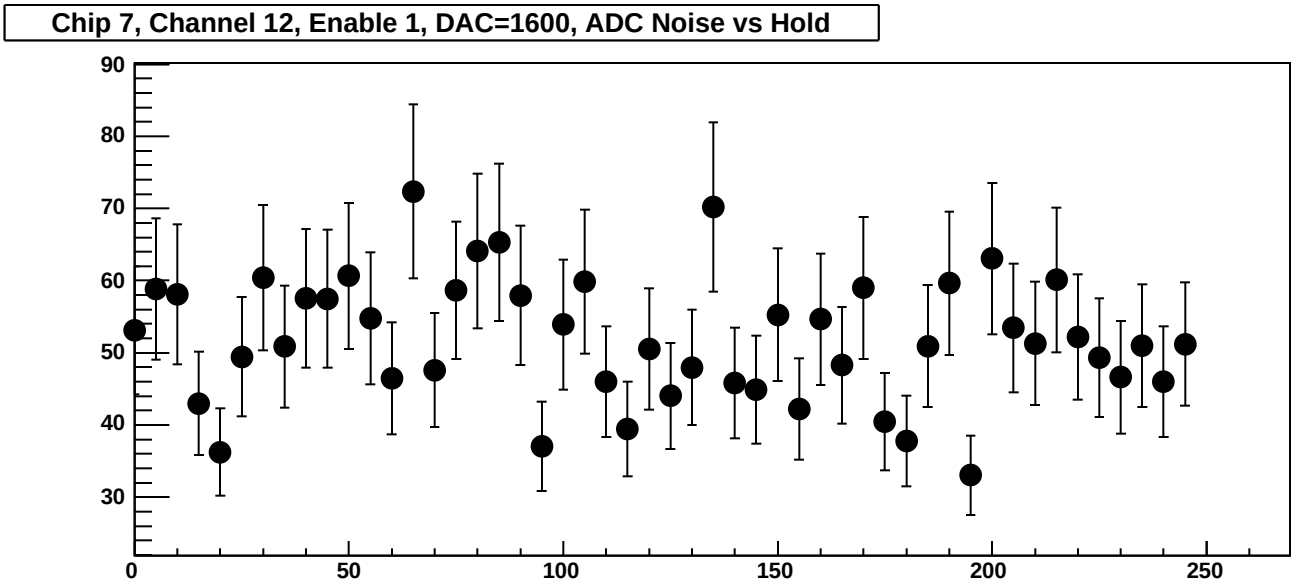
Chip 7, Channel 12, Enable 0, DAC=1600, ADC Noise vs Hold

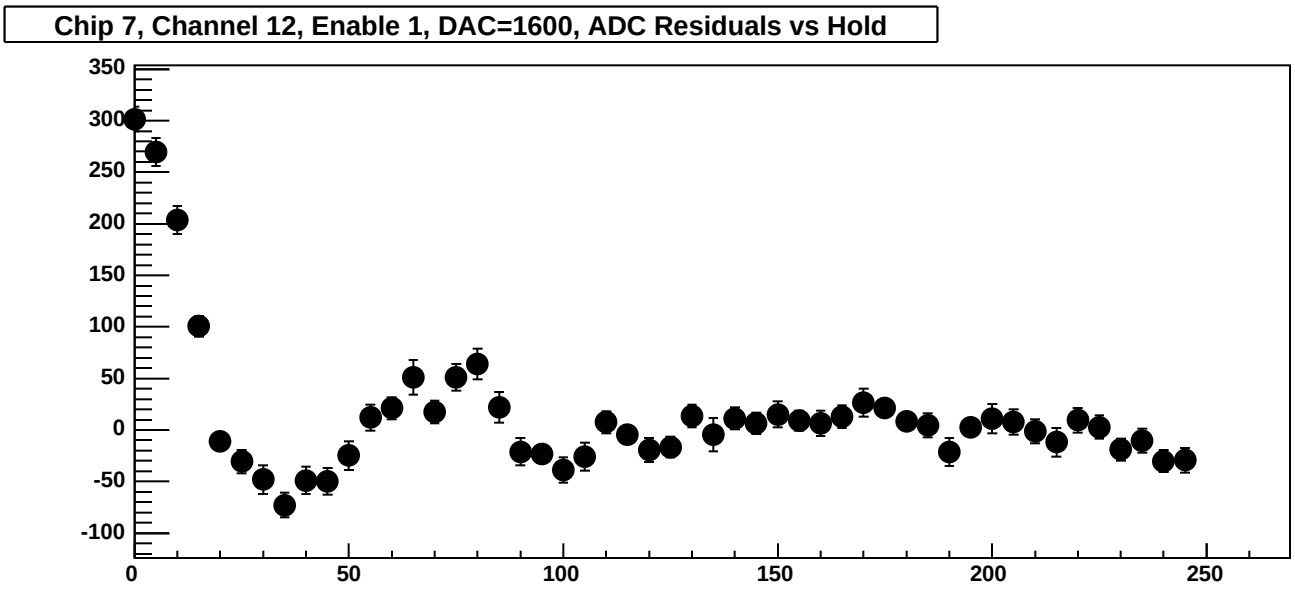


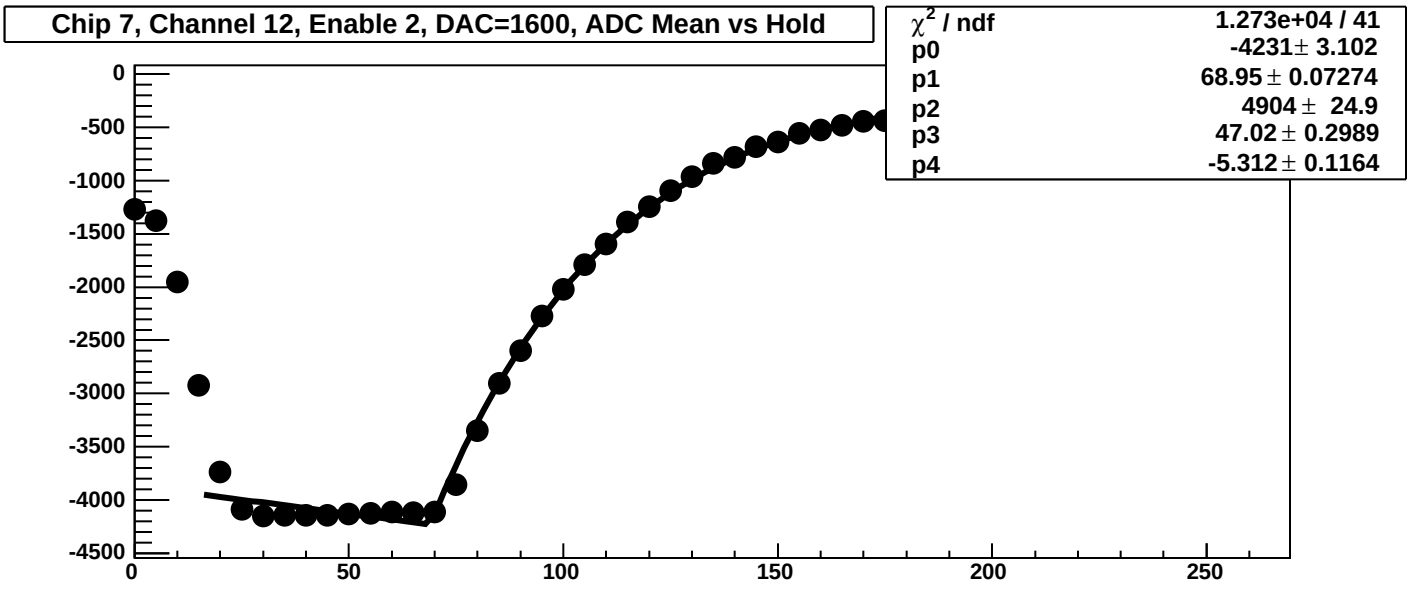
Chip 7, Channel 12, Enable 0, DAC=1600, ADC Residuals vs Hold

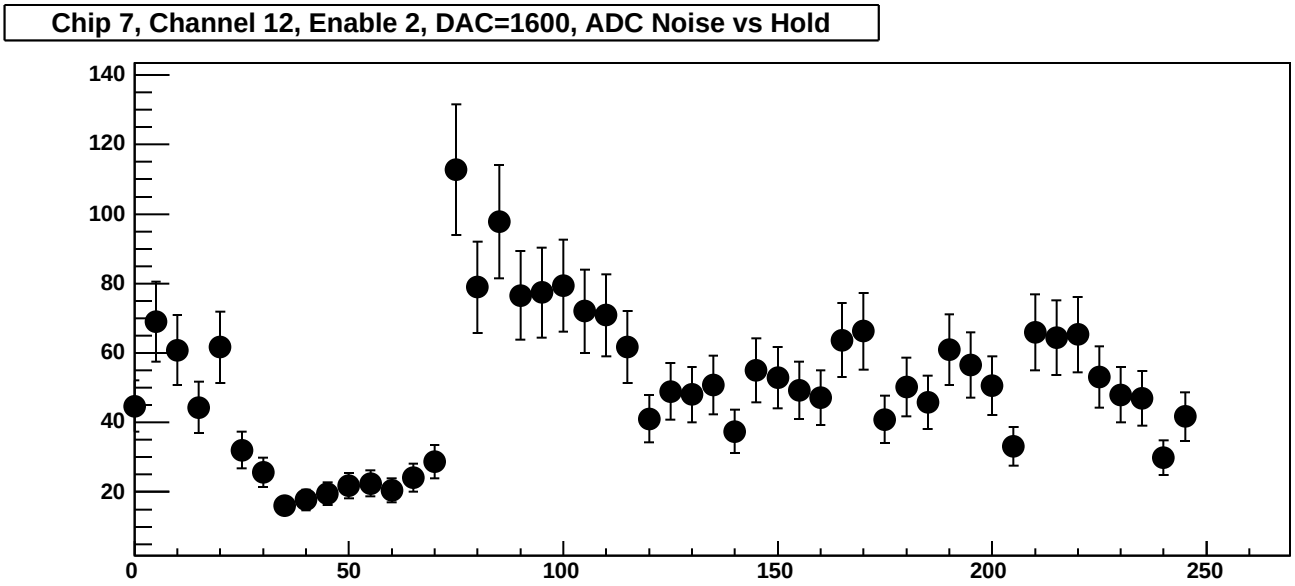


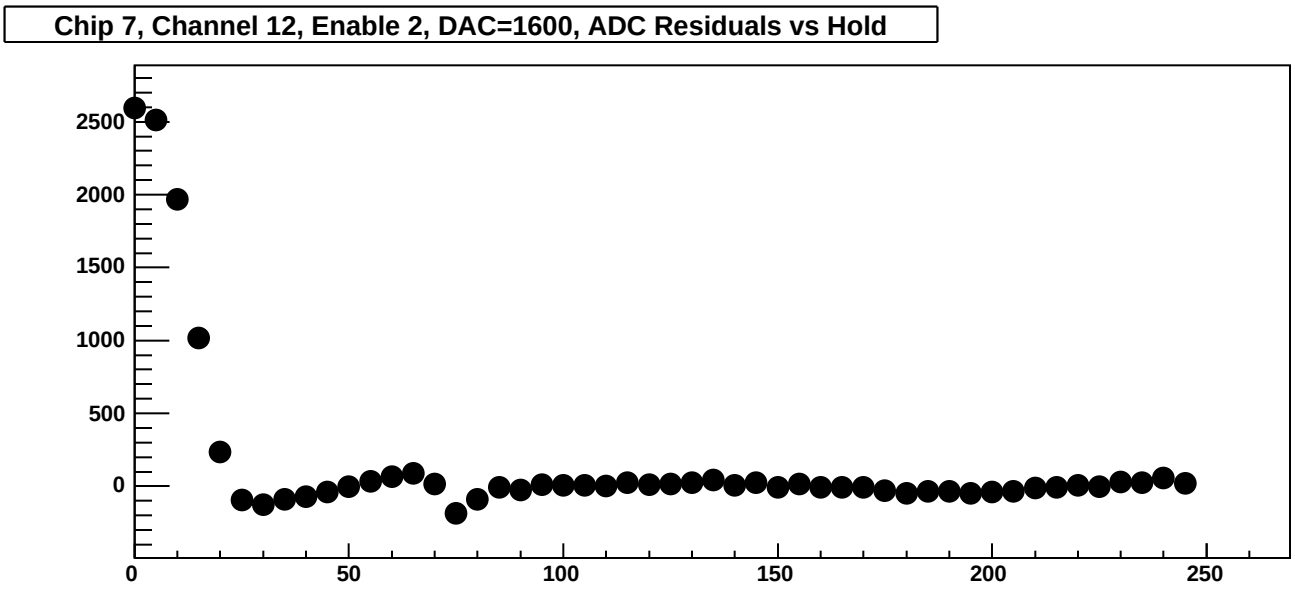

Chip 7, Channel 12, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 311.8 / 41
p0 -709.9 ± 3.78
p1 98.61 ± 0.6764
p2 -2.281e+08 ± 6.787e+06
p3 2.449e+07 ± 8.839e+05
p4 8.712 ± 0.09947
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
50
100
150
200
250


Chip 7, Channel 12, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

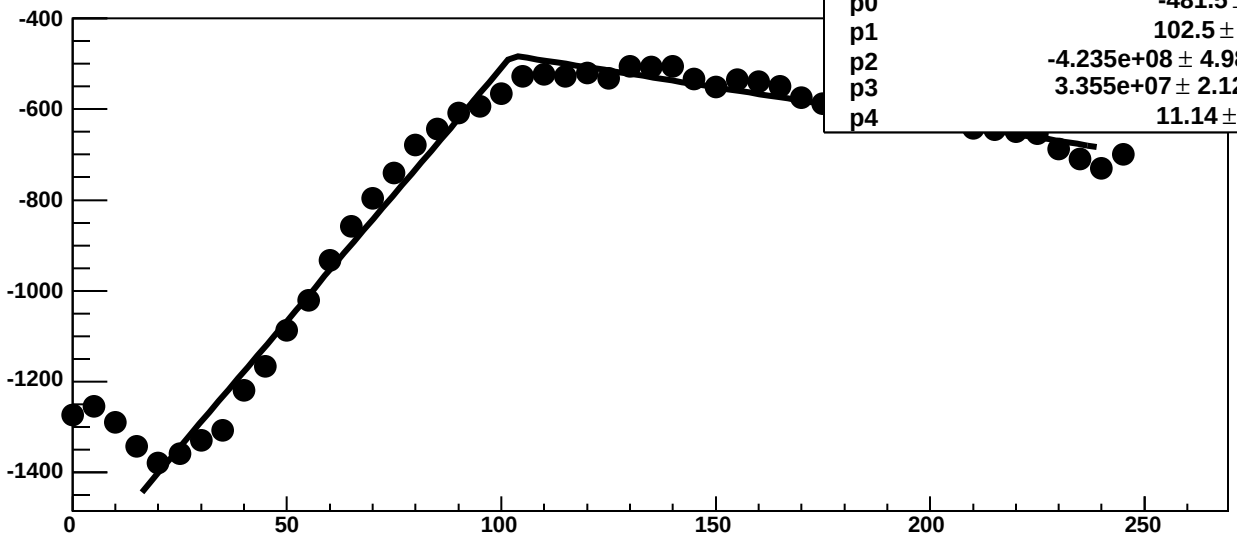

Chip 7, Channel 12, Enable 1, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250


Chip 7, Channel 12, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.273e+04 / 41
p0 -4231 ± 3.102
p1 68.95 ± 0.07274
p2 4904 ± 24.9
p3 47.02 ± 0.2989
p4 -5.312 ± 0.1164
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
-4500
0
50
100
150
200
250

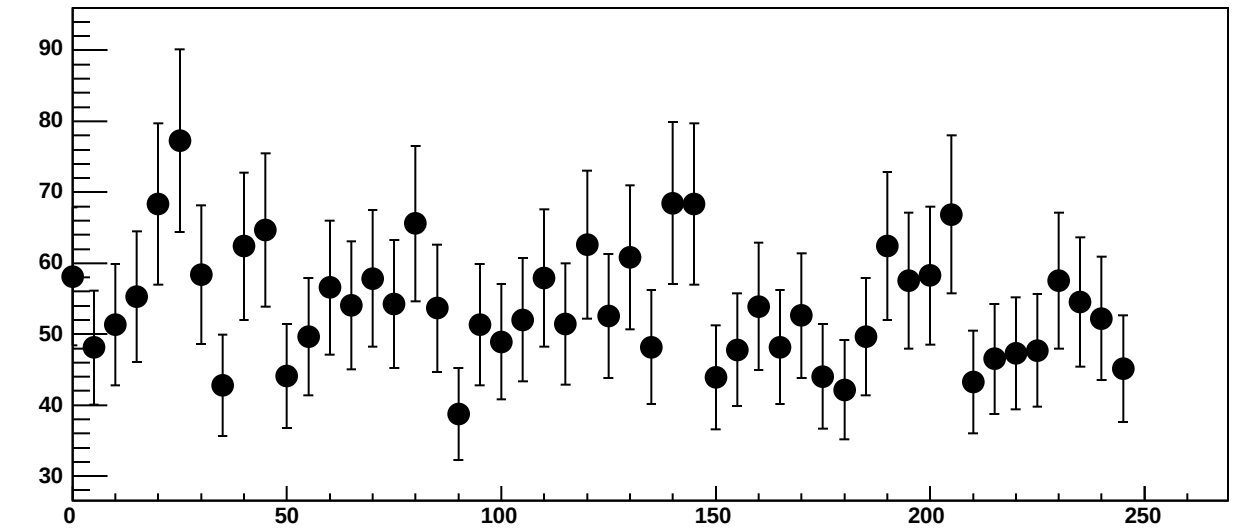

Chip 7, Channel 12, Enable 2, DAC=1600, ADC Noise vs Hold
140
120
100
80
60
40
20
0
50
100
150
200
250


Chip 7, Channel 12, Enable 2, DAC=1600, ADC Residuals vs Hold
2500
2000
1500
1000
500
0
0
50
100
150
200
250

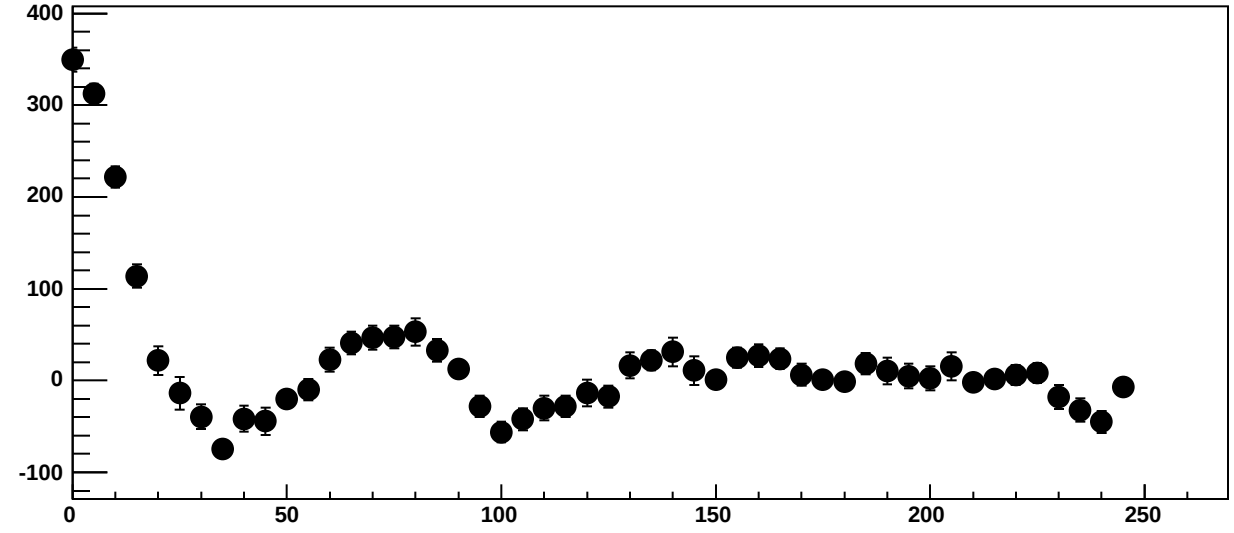
Chip 7, Channel 12, Enable 3, DAC=1600, ADC Mean vs Hold



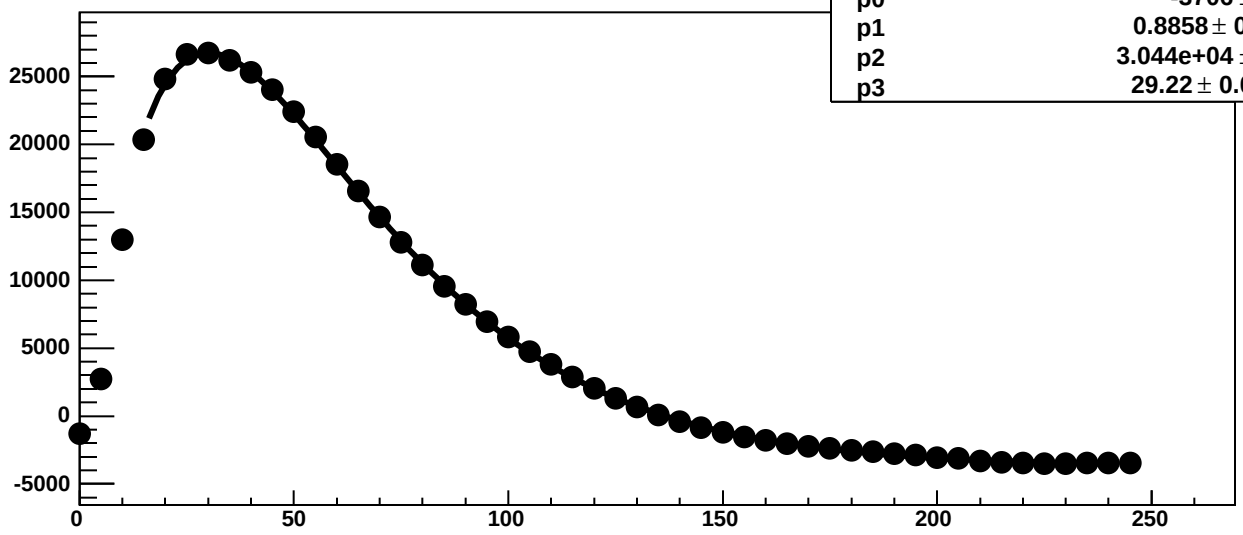
Chip 7, Channel 12, Enable 3, DAC=1600, ADC Noise vs Hold



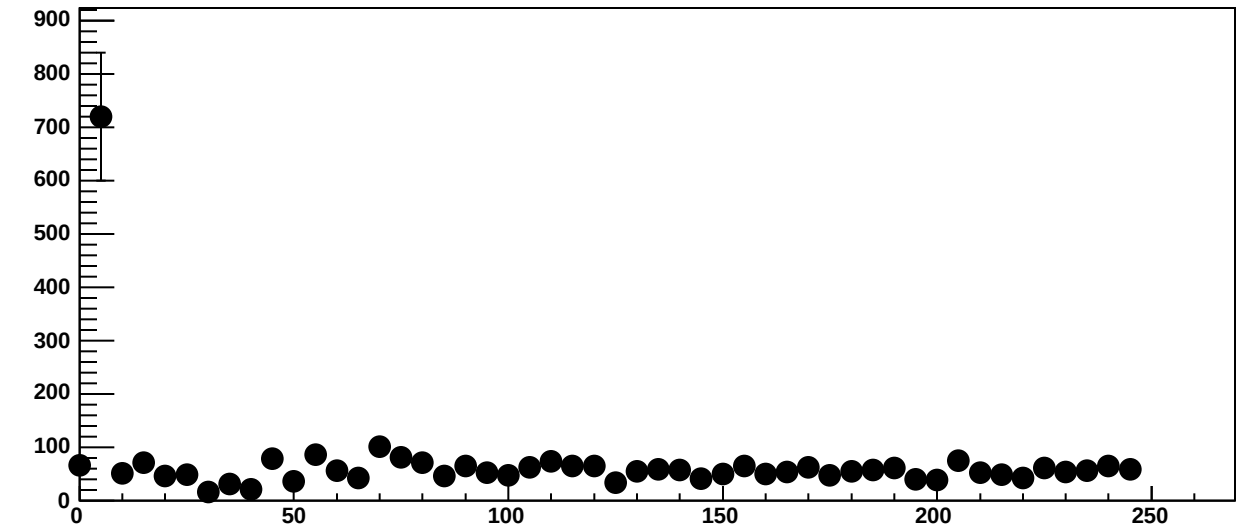
Chip 7, Channel 12, Enable 3, DAC=1600, ADC Residuals vs Hold



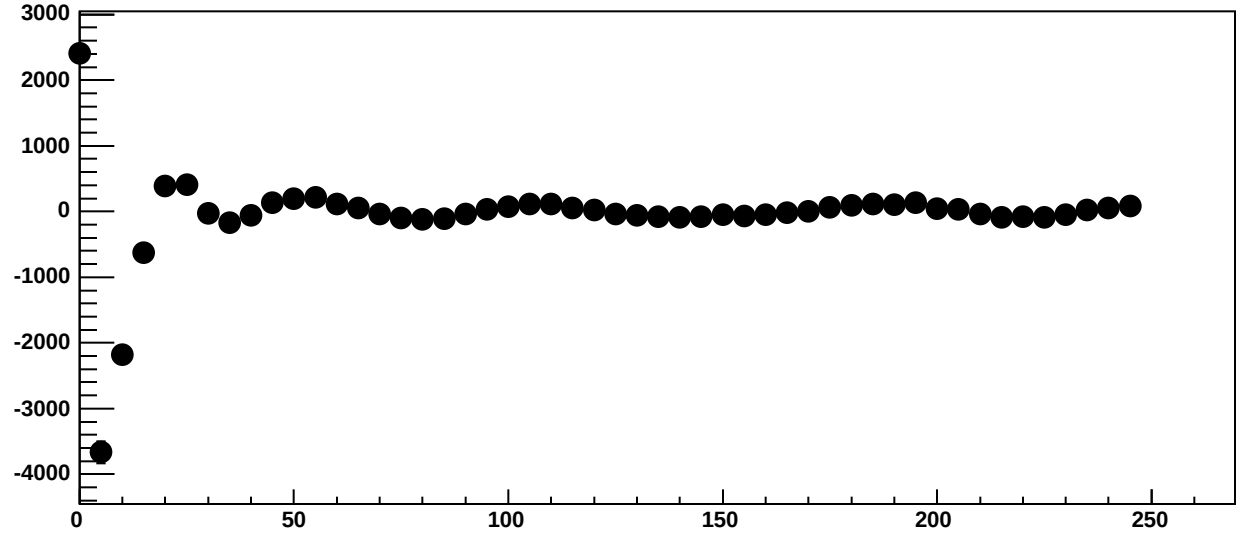
Chip 7, Channel 12, Enable 4!, DAC=1600, ADC Mean vs Hold

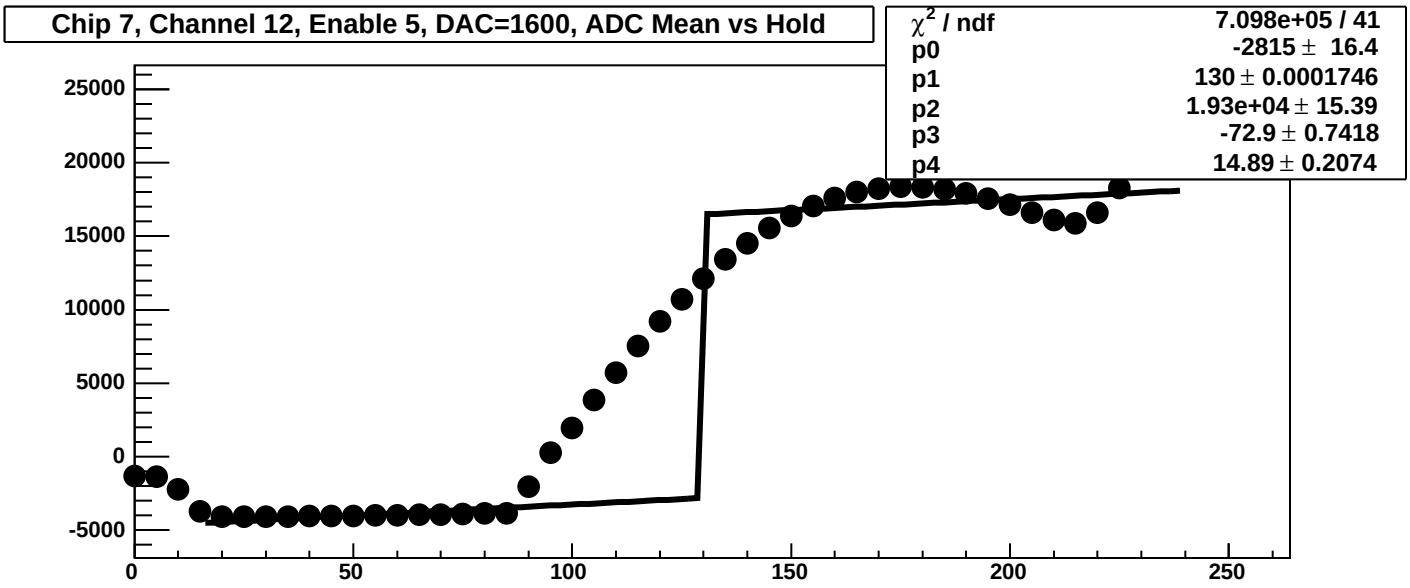


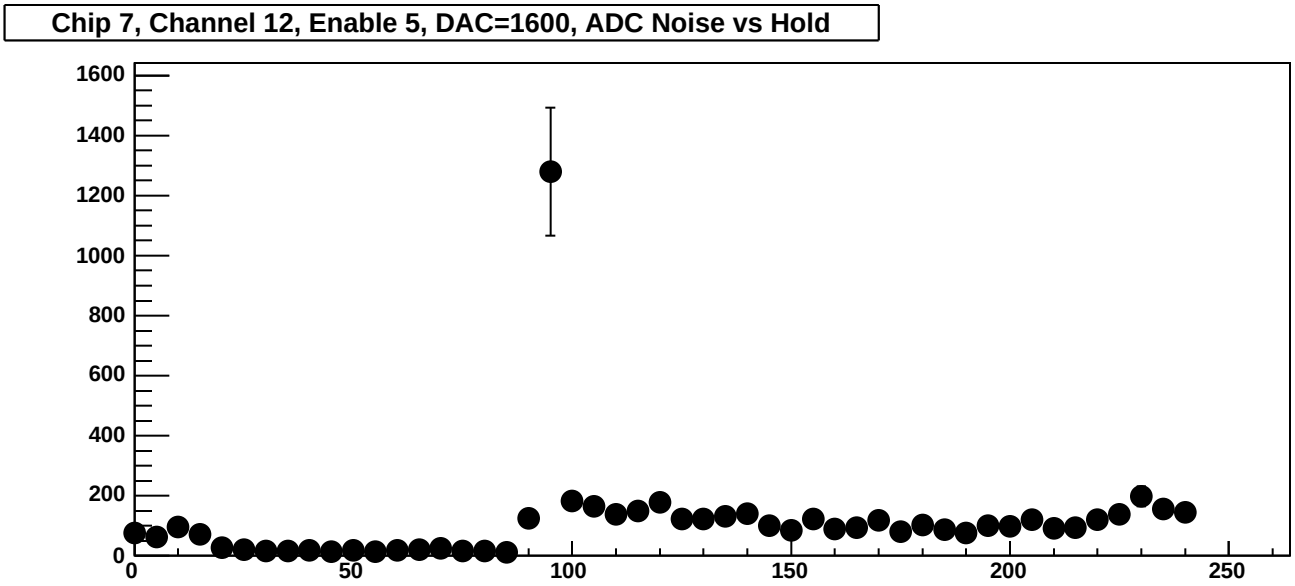
Chip 7, Channel 12, Enable 4!, DAC=1600, ADC Noise vs Hold

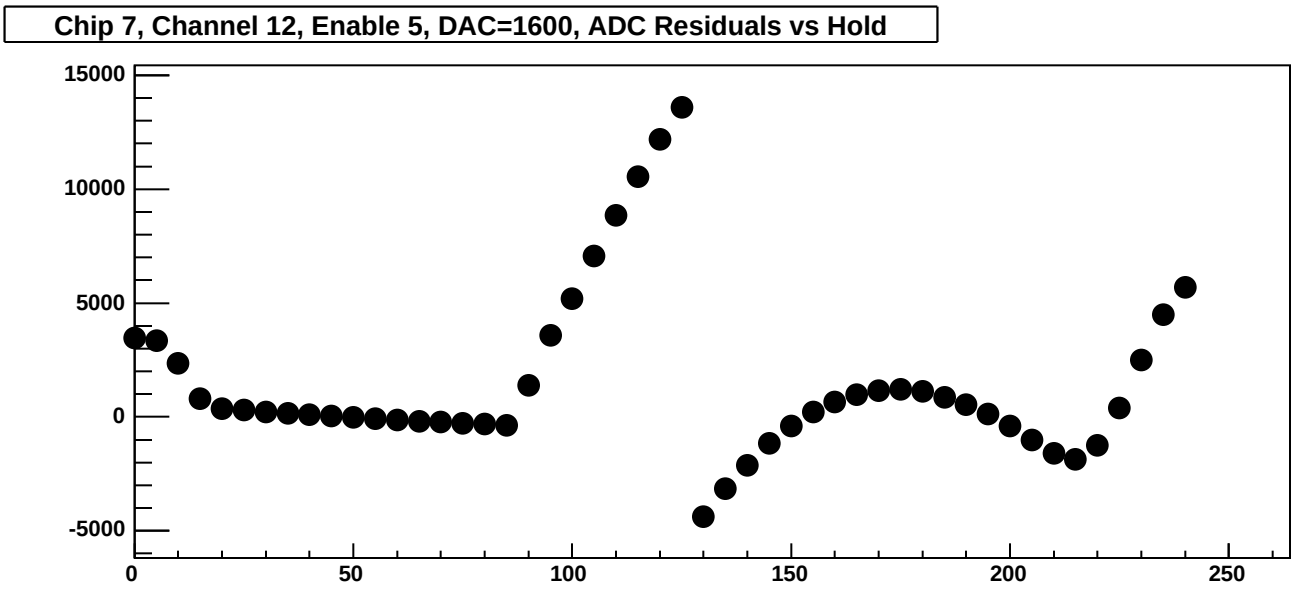


Chip 7, Channel 12, Enable 4!, DAC=1600, ADC Residuals vs Hold

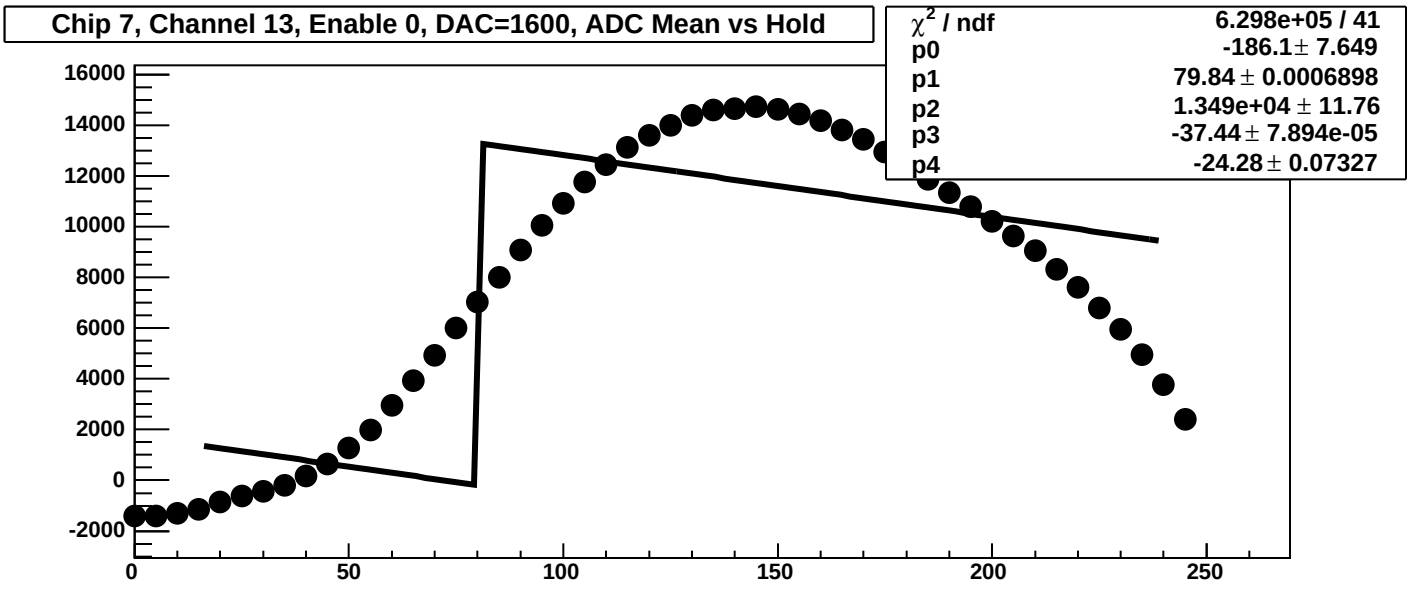



Chip 7, Channel 12, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.098e+05 / 41
p0 -2815 ± 16.4
p1 130 ± 0.0001746
p2 1.93e+04 ± 15.39
p3 -72.9 ± 0.7418
p4 14.89 ± 0.2074
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

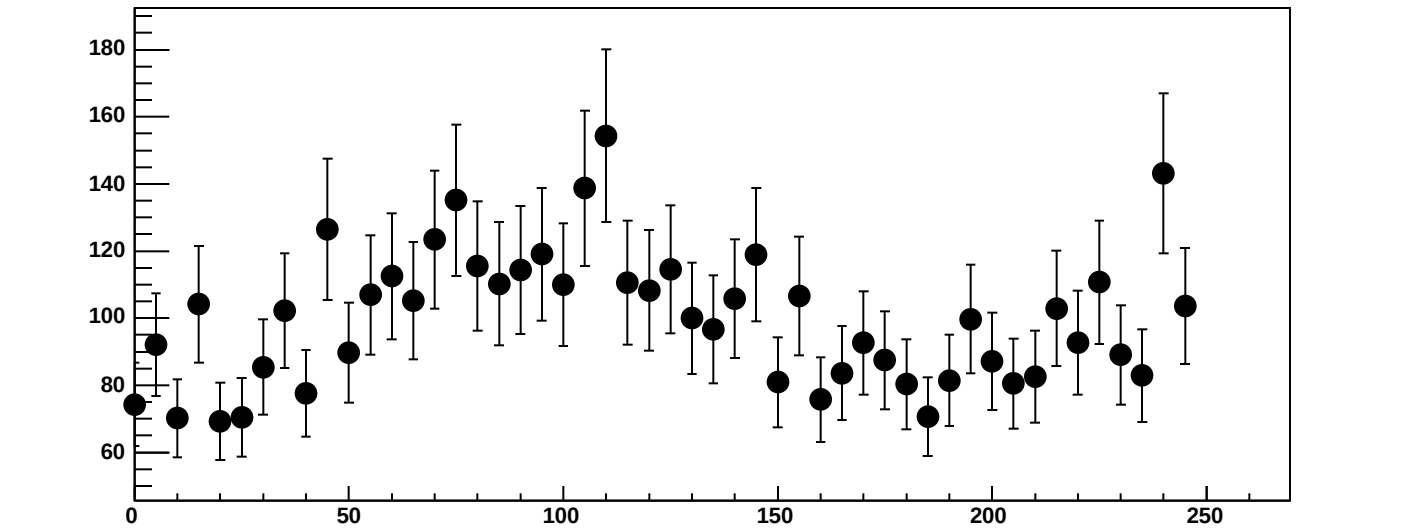

Chip 7, Channel 12, Enable 5, DAC=1600, ADC Noise vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 7, Channel 12, Enable 5, DAC=1600, ADC Residuals vs Hold
15000
10000
5000
0
-5000
0
50
100
150
200
250

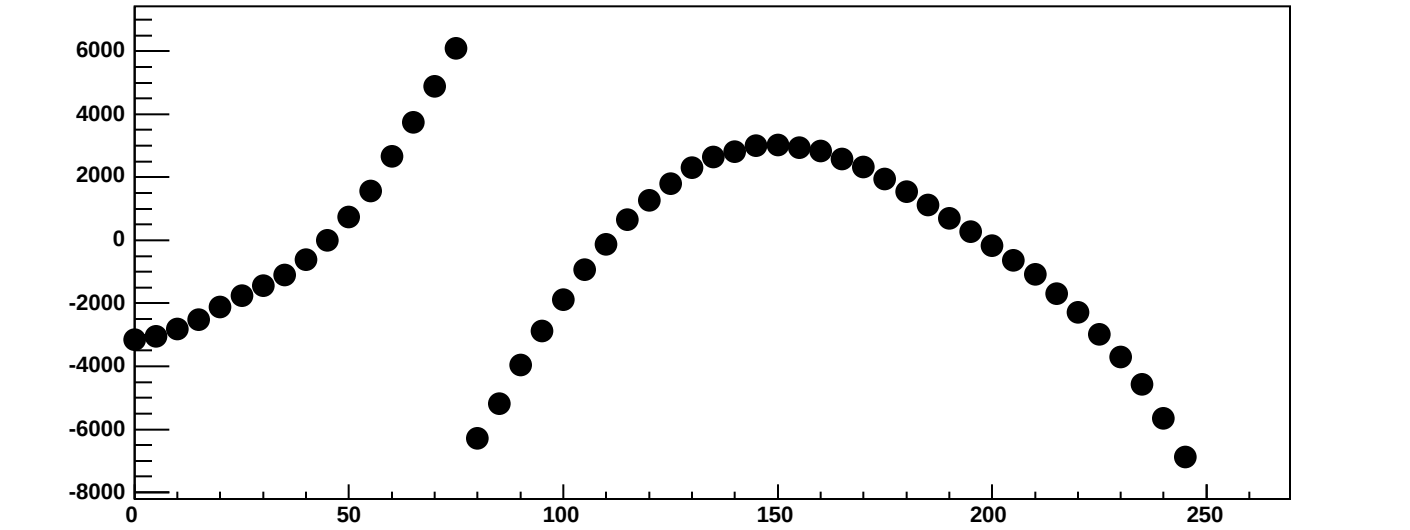
Chip 7, Channel 13, Enable 0, DAC=1600, ADC Mean vs Hold



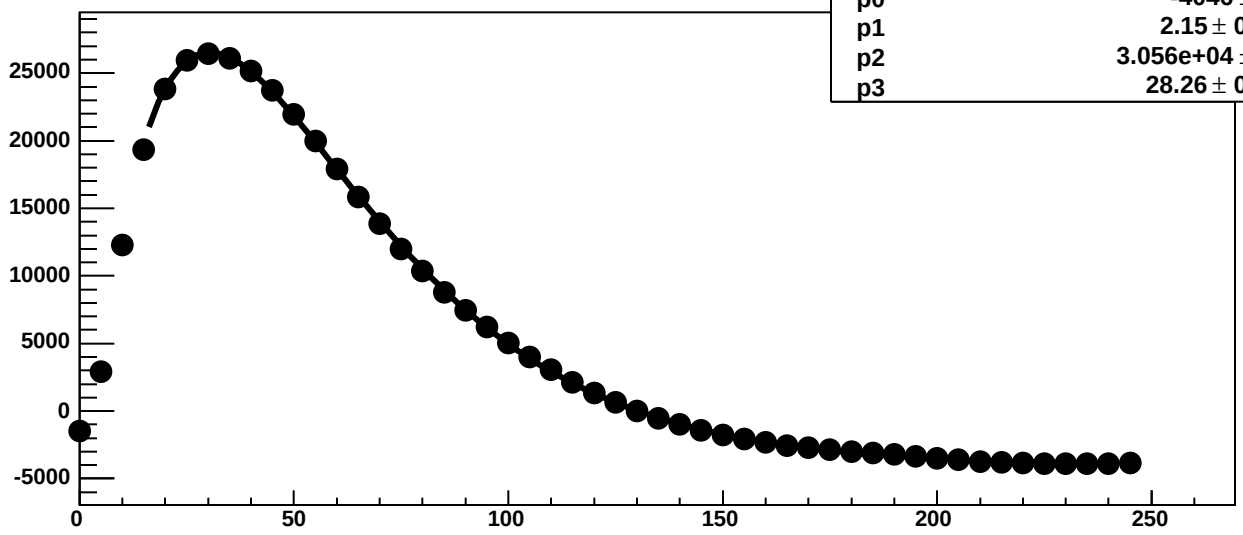
Chip 7, Channel 13, Enable 0, DAC=1600, ADC Noise vs Hold



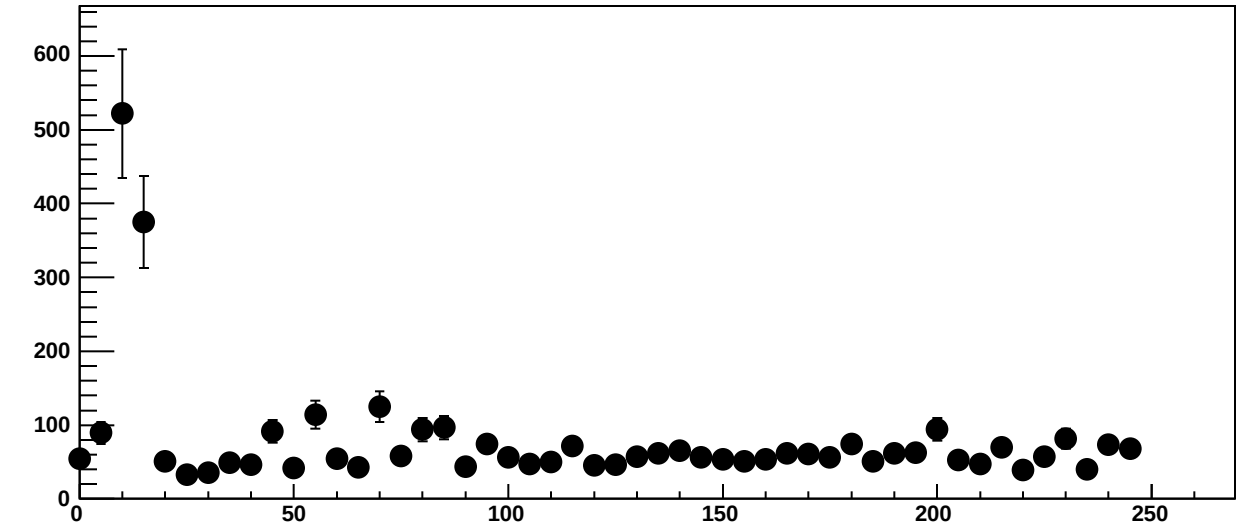
Chip 7, Channel 13, Enable 0, DAC=1600, ADC Residuals vs Hold



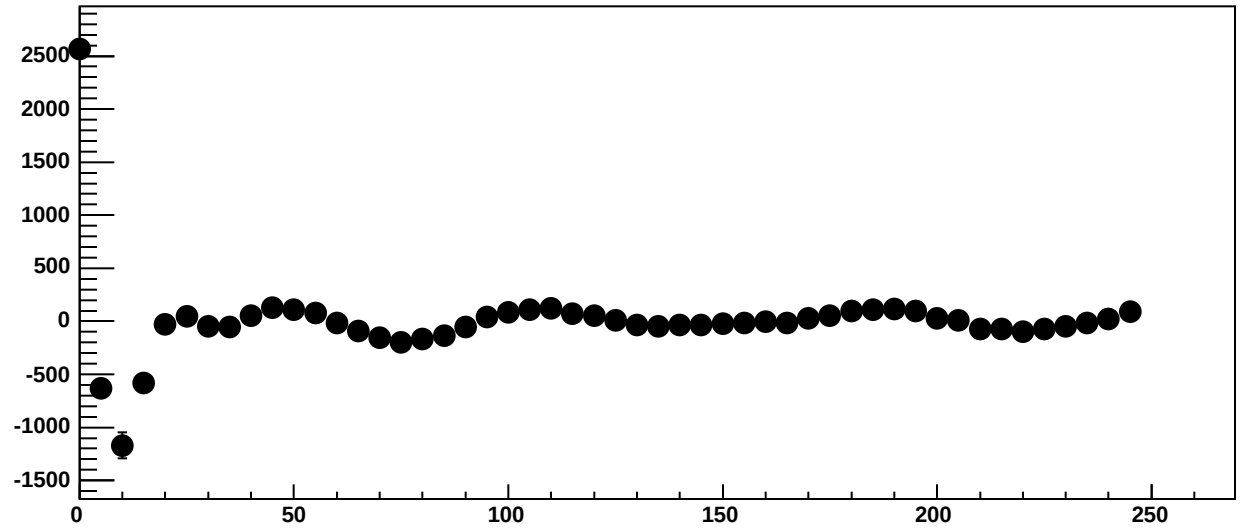
Chip 7, Channel 13, Enable 1!, DAC=1600, ADC Mean vs Hold

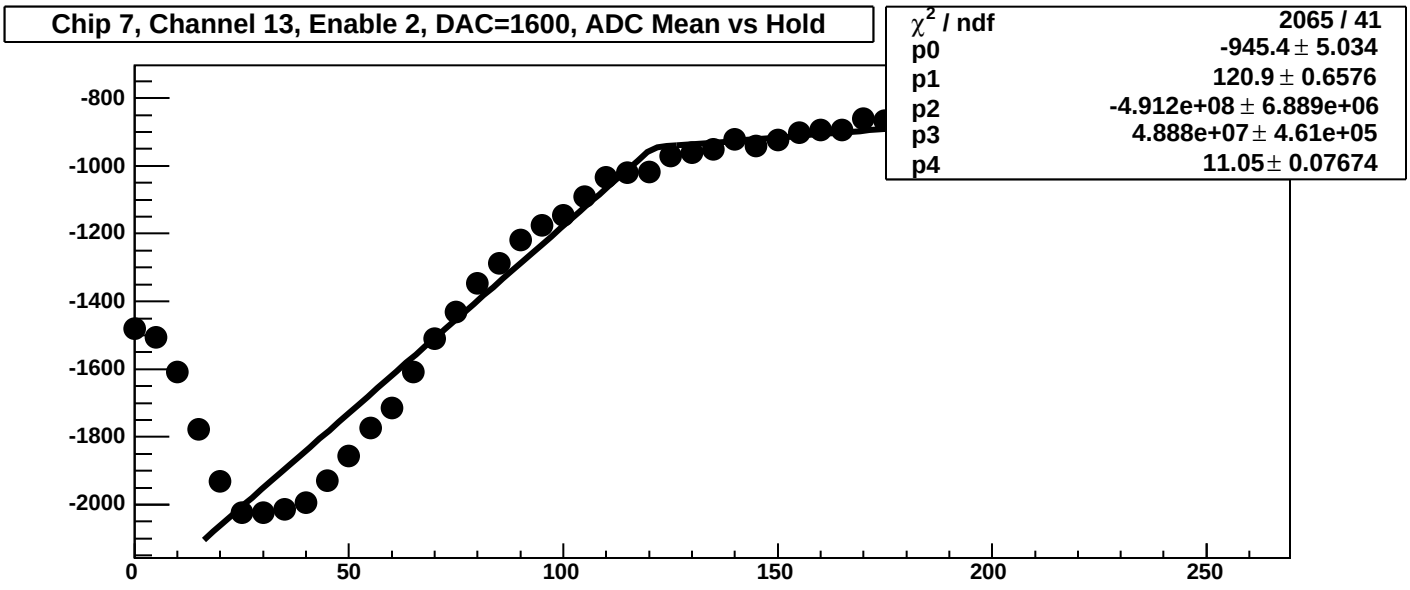


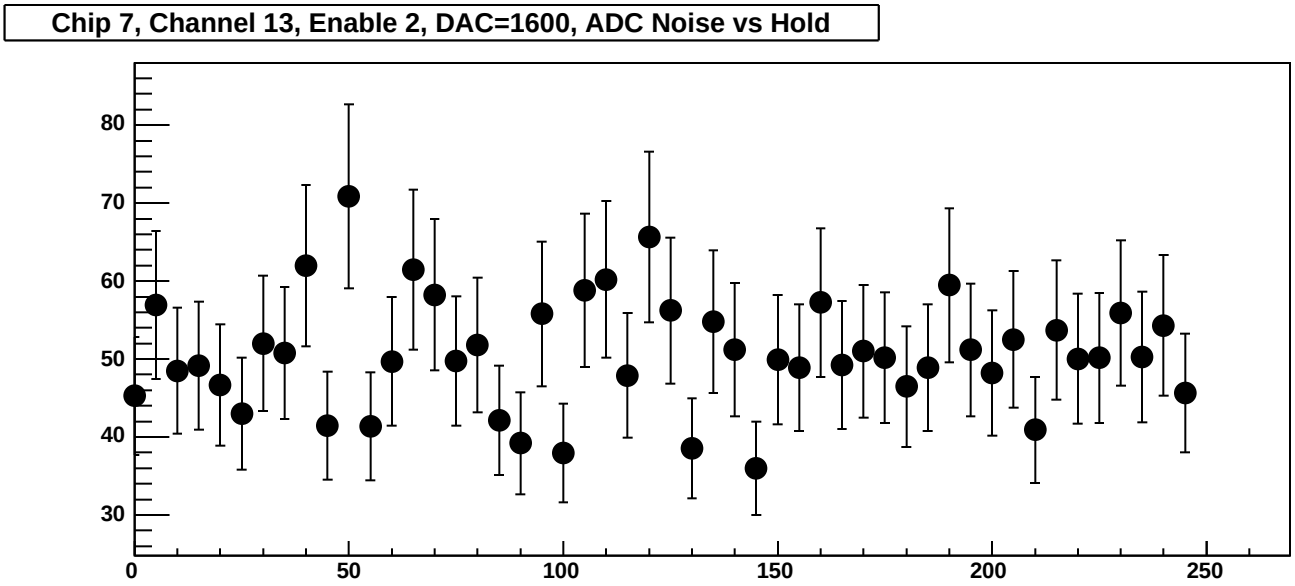
Chip 7, Channel 13, Enable 1!, DAC=1600, ADC Noise vs Hold

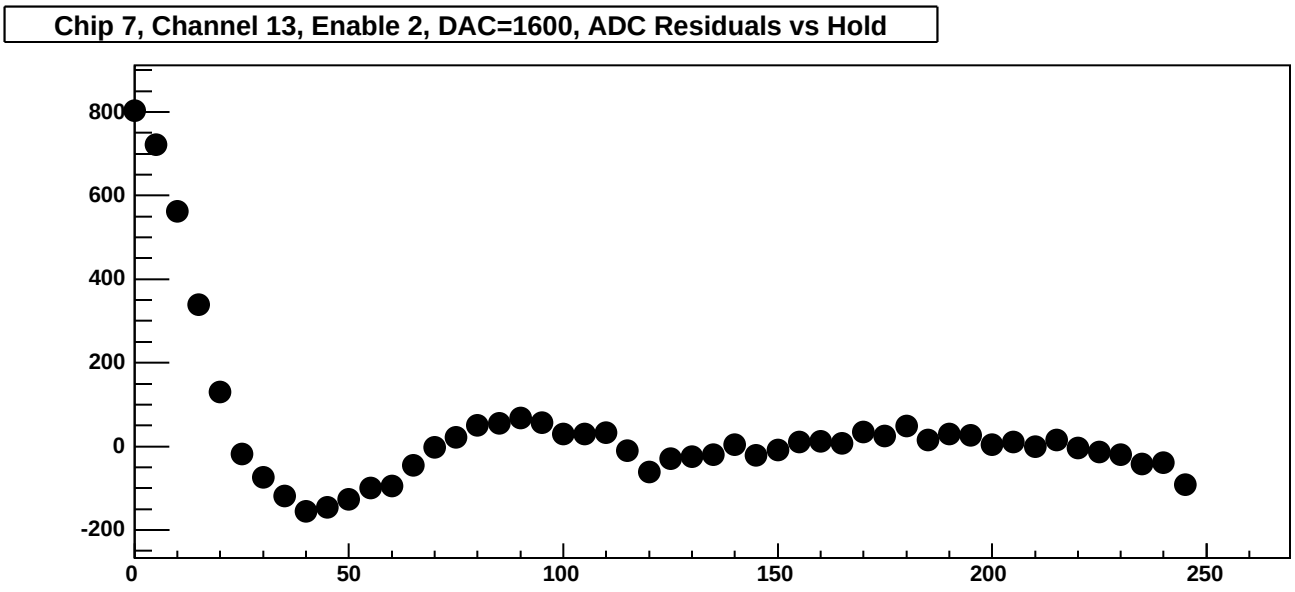


Chip 7, Channel 13, Enable 1!, DAC=1600, ADC Residuals vs Hold

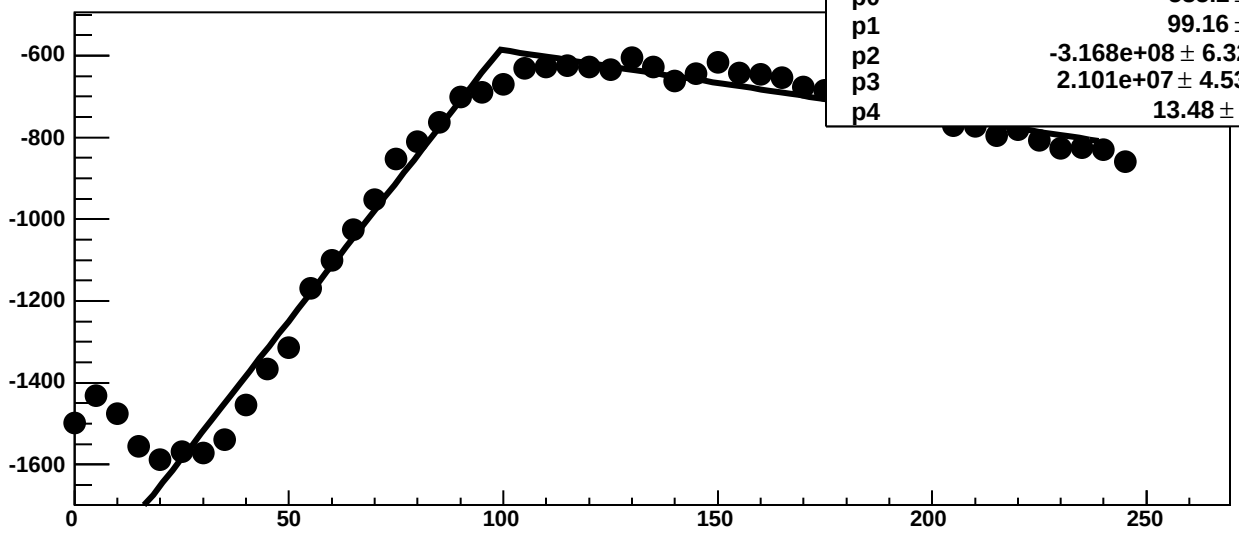



Chip 7, Channel 13, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 2065 / 41
p0 -945.4 ± 5.034
p1 120.9 ± 0.6576
p2 -4.912e+08 ± 6.889e+06
p3 4.888e+07 ± 4.61e+05
p4 11.05 ± 0.07674
-800
-1000
-1200
-1400
-1600
-1800
-2000
0
50
100
150
200
250

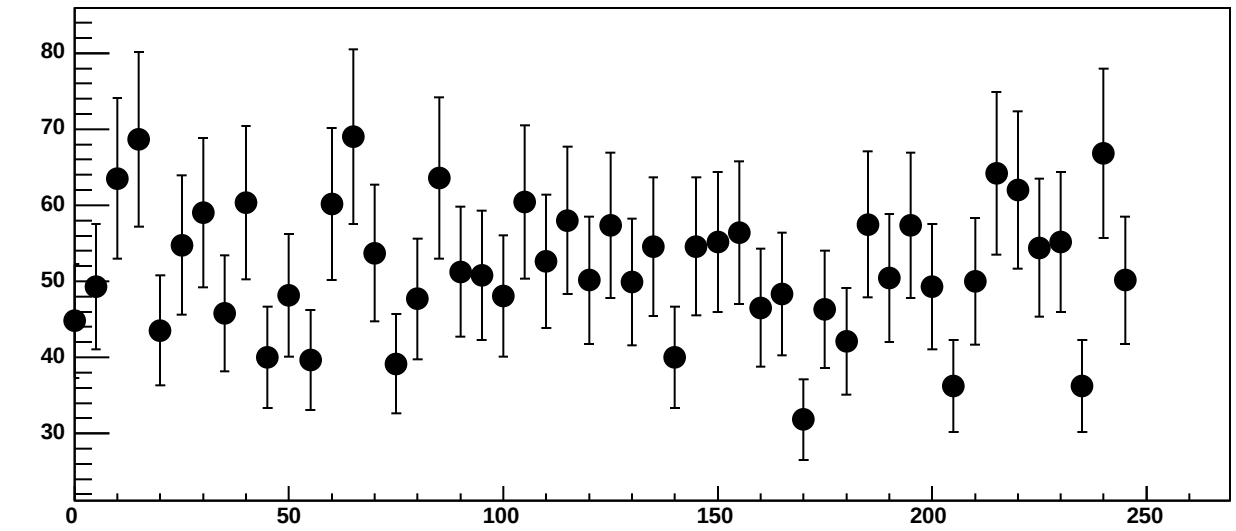

Chip 7, Channel 13, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 7, Channel 13, Enable 2, DAC=1600, ADC Residuals vs Hold
800
600
400
200
0
-200
0
50
100
150
200
250

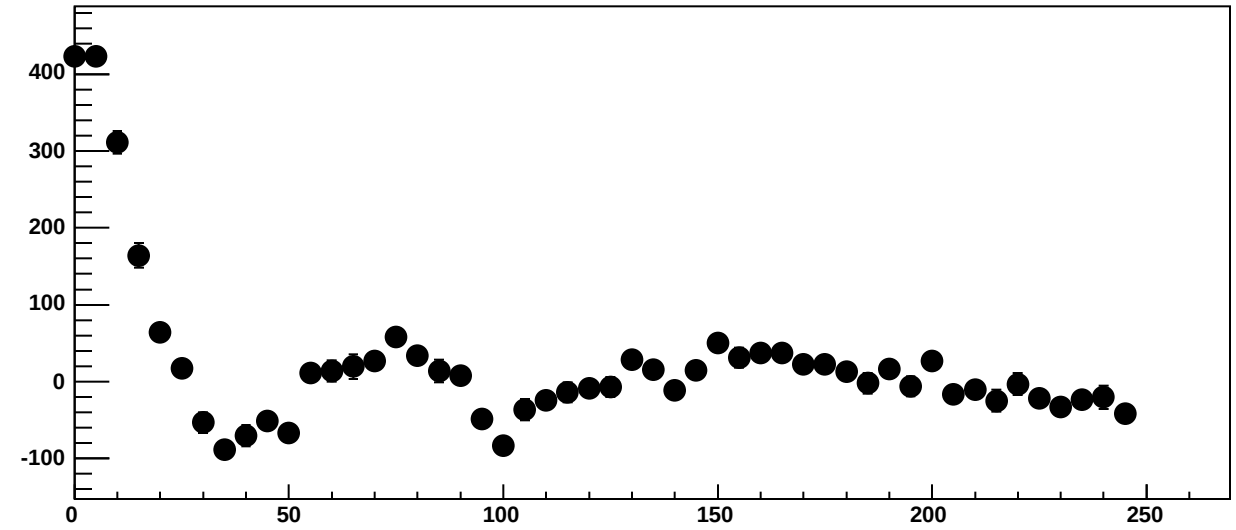
Chip 7, Channel 13, Enable 3, DAC=1600, ADC Mean vs Hold



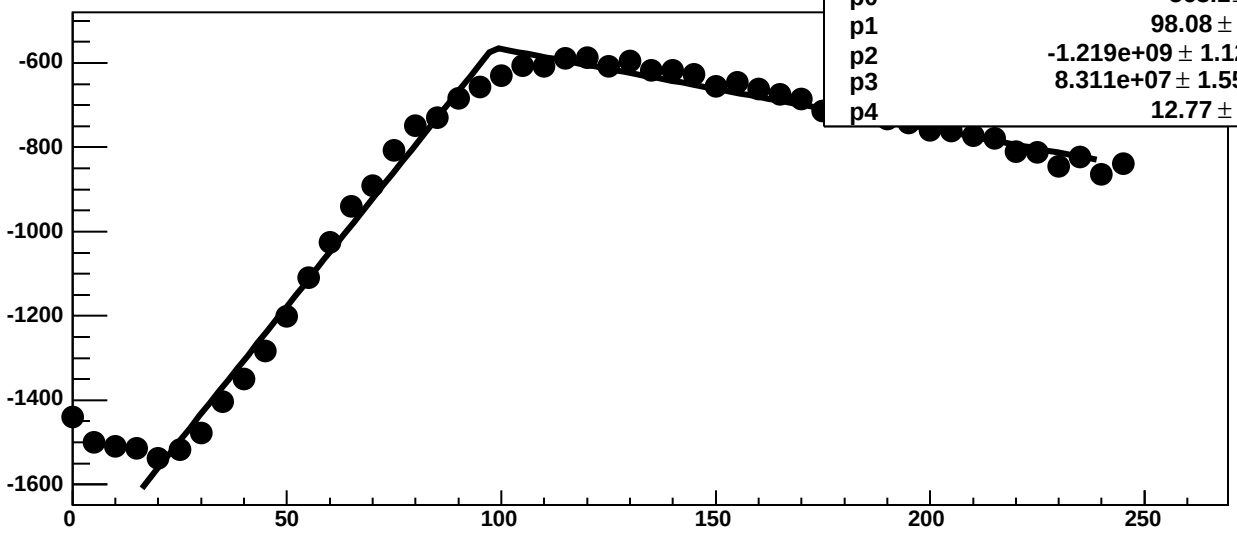
Chip 7, Channel 13, Enable 3, DAC=1600, ADC Noise vs Hold



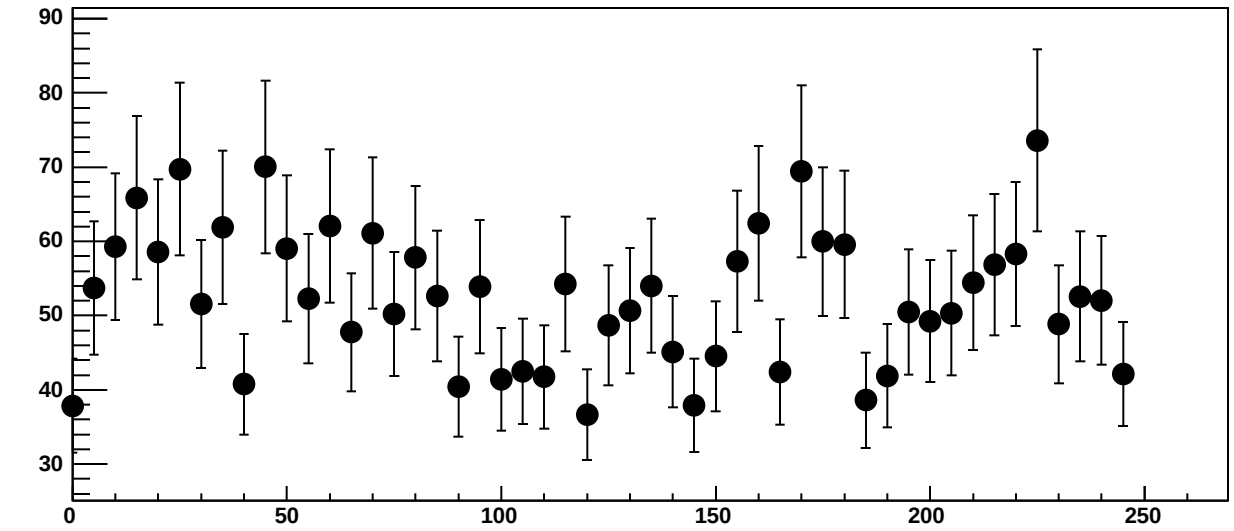
Chip 7, Channel 13, Enable 3, DAC=1600, ADC Residuals vs Hold



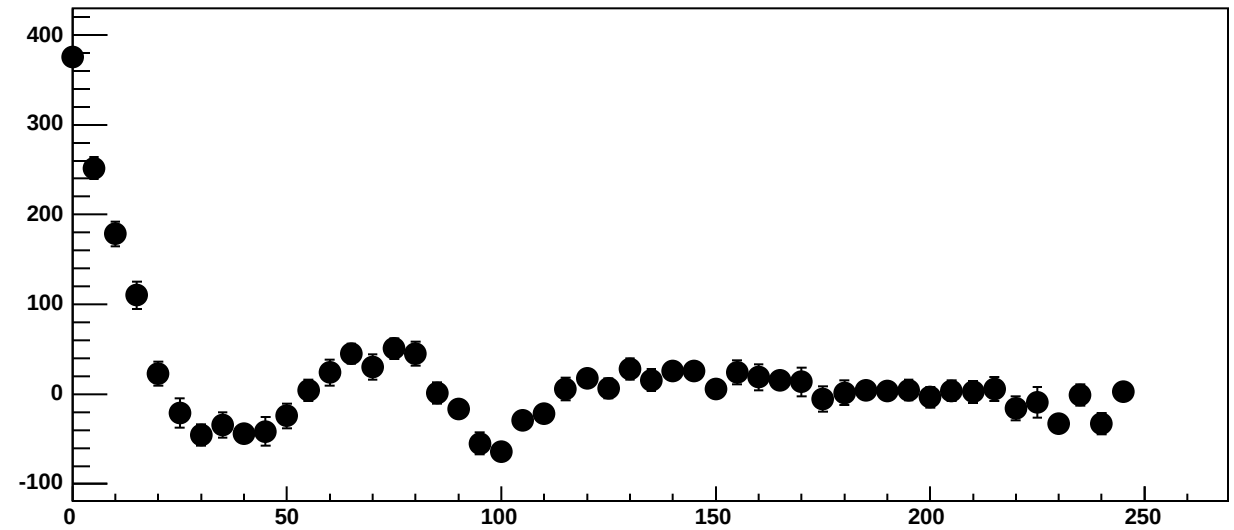
Chip 7, Channel 13, Enable 4, DAC=1600, ADC Mean vs Hold

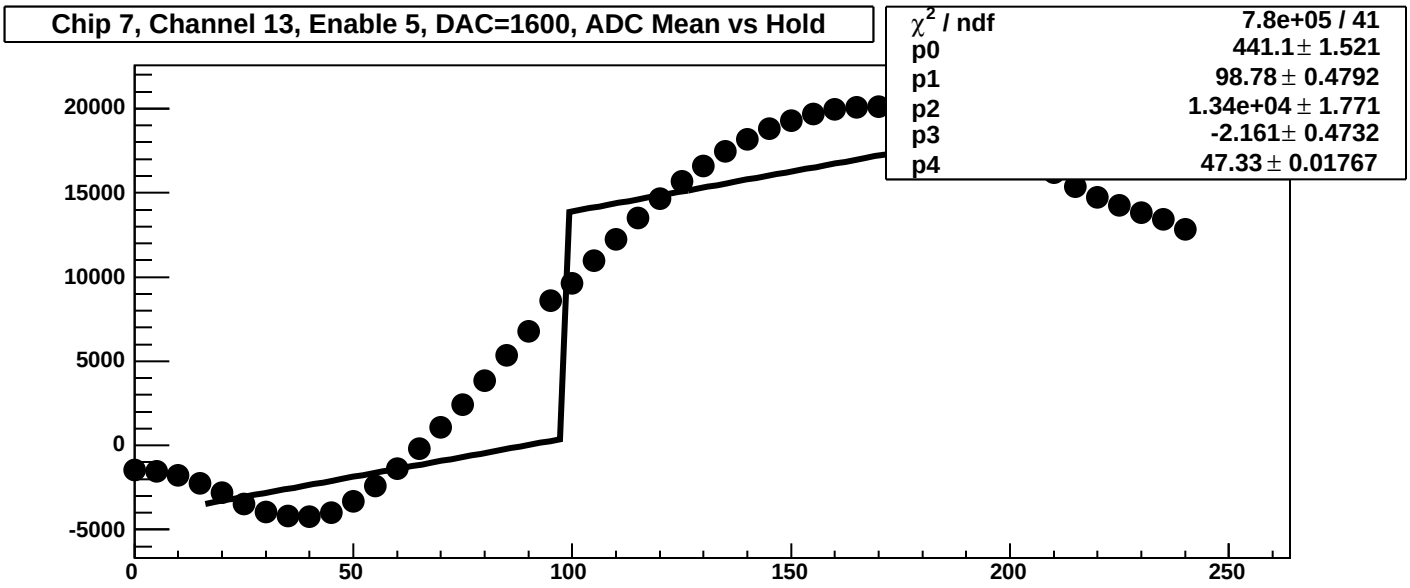


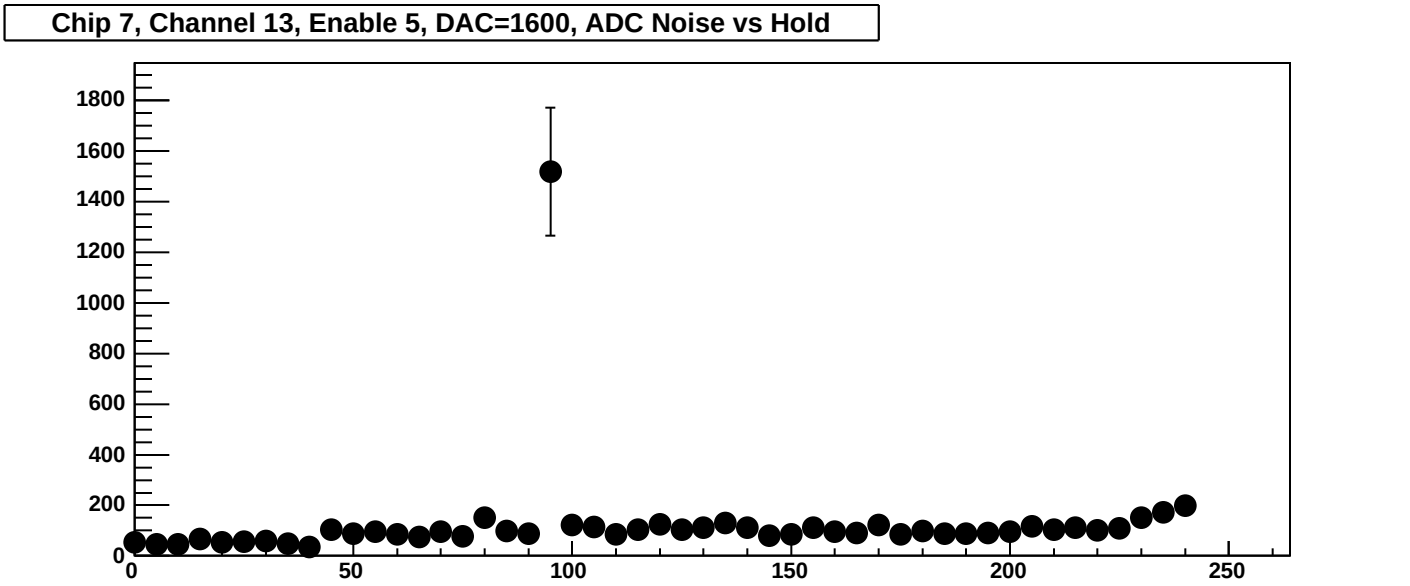
Chip 7, Channel 13, Enable 4, DAC=1600, ADC Noise vs Hold

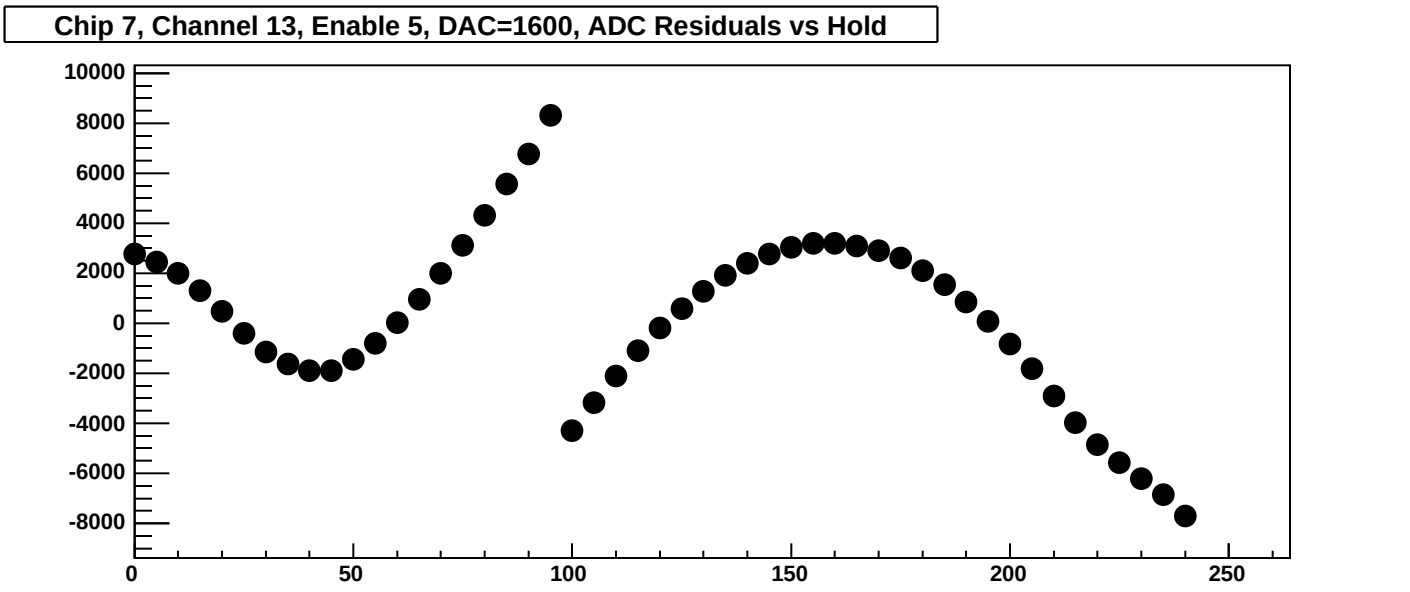


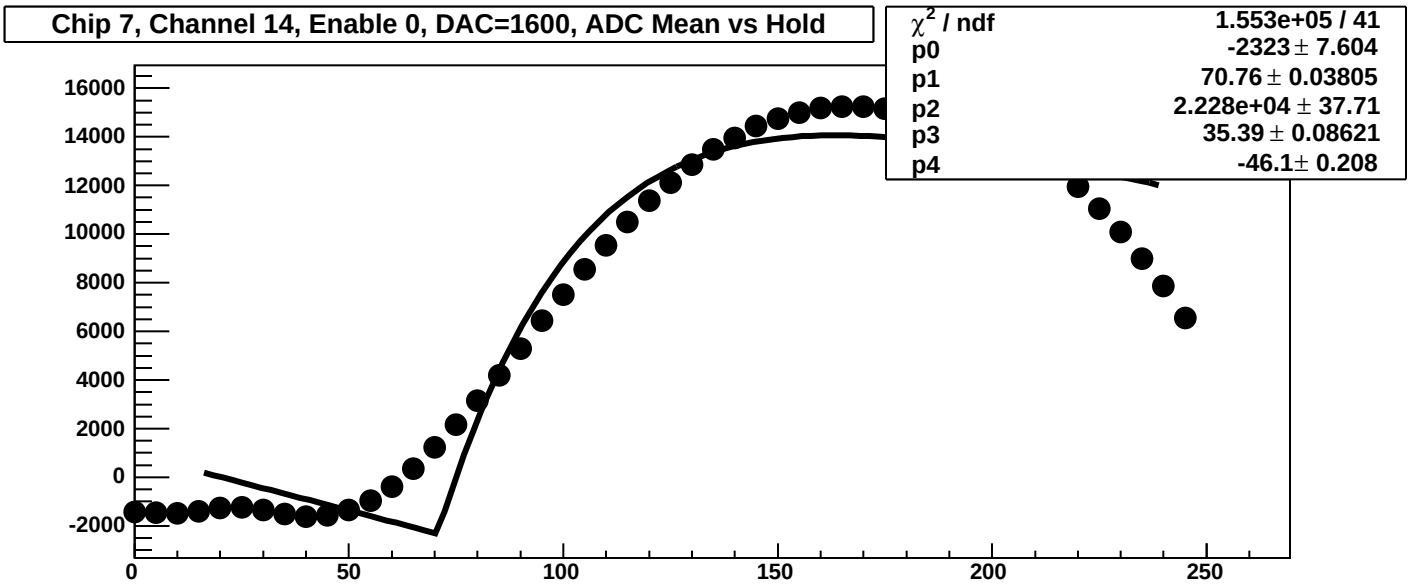
Chip 7, Channel 13, Enable 4, DAC=1600, ADC Residuals vs Hold

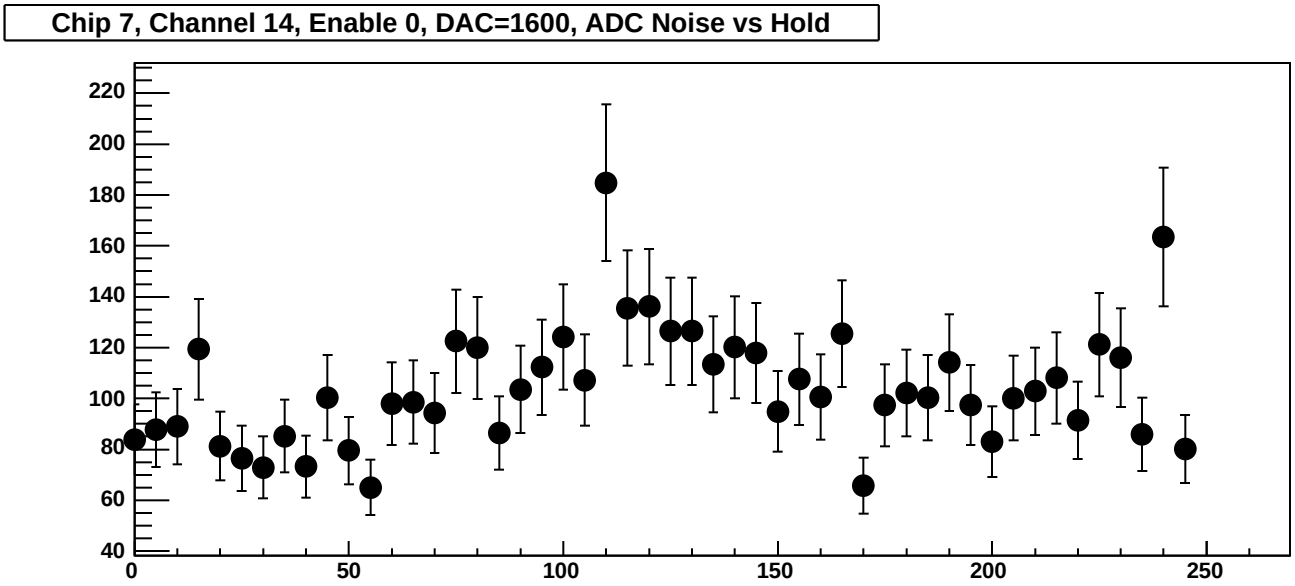


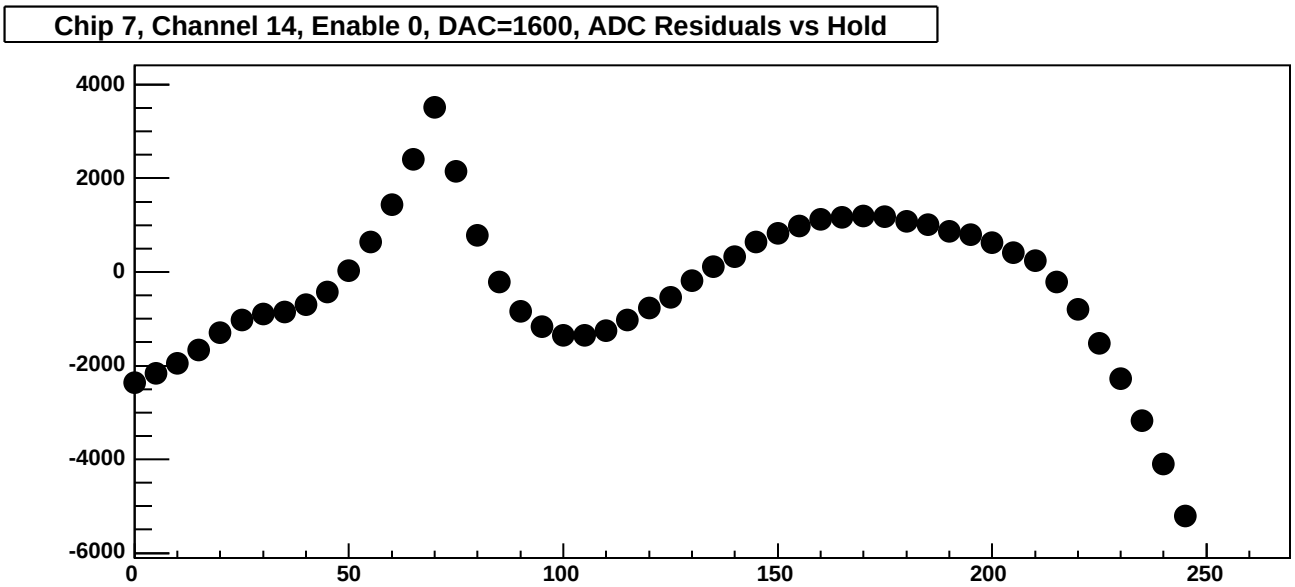

Chip 7, Channel 13, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.8e+05 / 41
p0 441.1 ± 1.521
p1 98.78 ± 0.4792
p2 1.34e+04 ± 1.771
p3 -2.161 ± 0.4732
p4 47.33 ± 0.01767
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

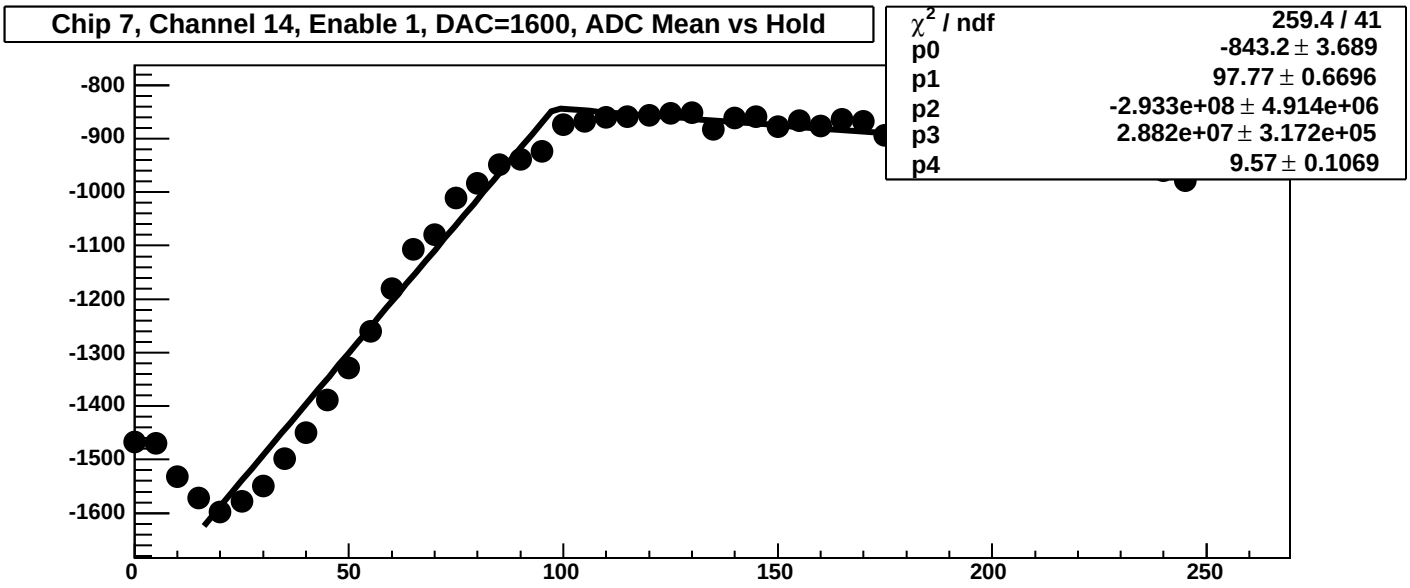

Chip 7, Channel 13, Enable 5, DAC=1600, ADC Noise vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

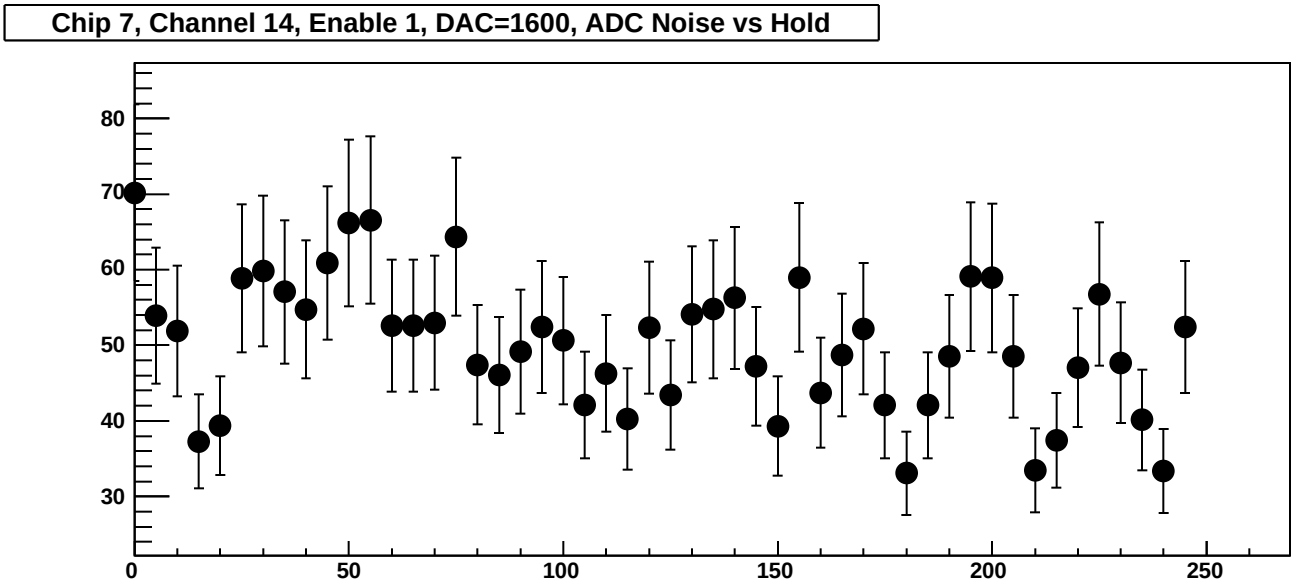

Chip 7, Channel 13, Enable 5, DAC=1600, ADC Residuals vs Hold
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250

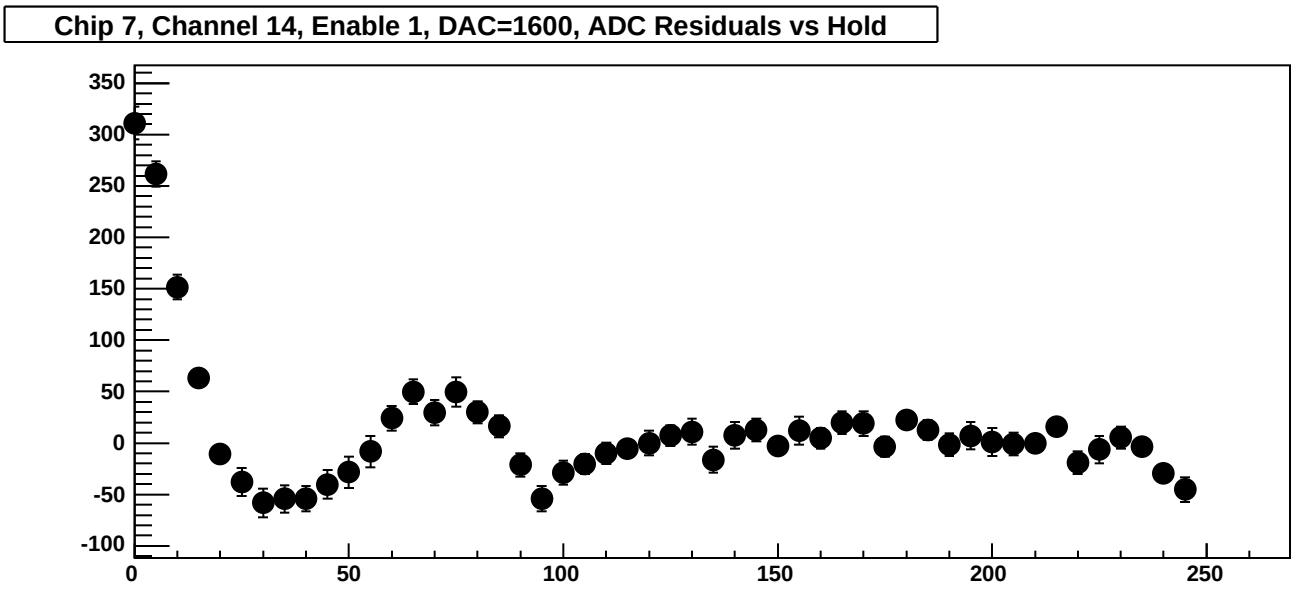

Chip 7, Channel 14, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.553e+05 / 41
p0 -2323 ± 7.604
p1 70.76 ± 0.03805
p2 2.228e+04 ± 37.71
p3 35.39 ± 0.08621
p4 -46.1 ± 0.208
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250


Chip 7, Channel 14, Enable 0, DAC=1600, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250

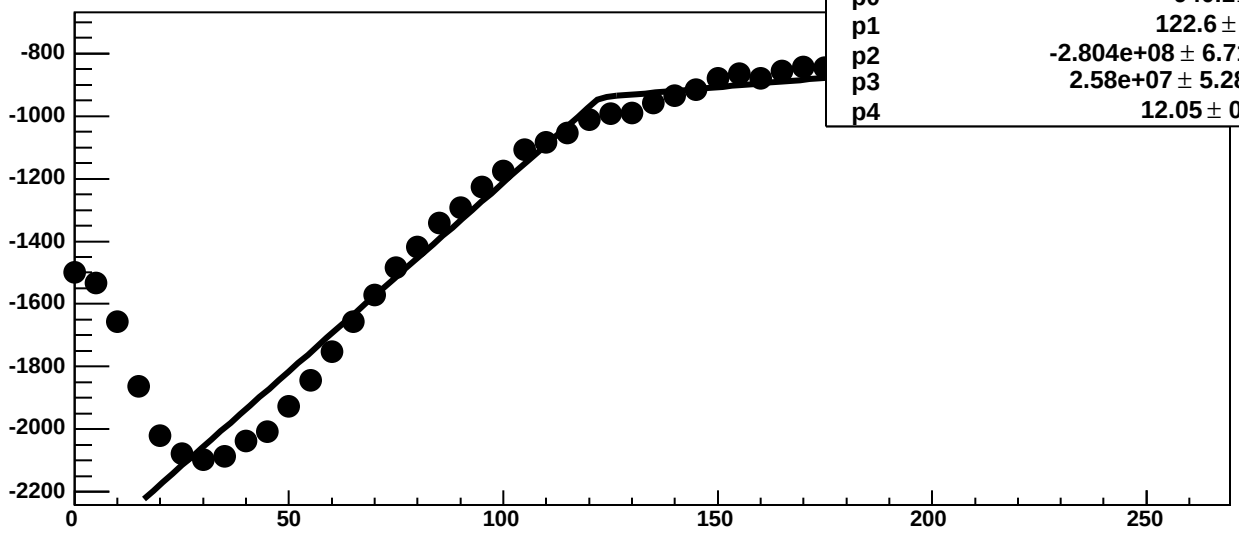

Chip 7, Channel 14, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
0
50
100
150
200
250


Chip 7, Channel 14, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 259.4 / 41
p0 -843.2 ± 3.689
p1 97.77 ± 0.6696
p2 -2.933e+08 ± 4.914e+06
p3 2.882e+07 ± 3.172e+05
p4 9.57 ± 0.1069
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
50
100
150
200
250

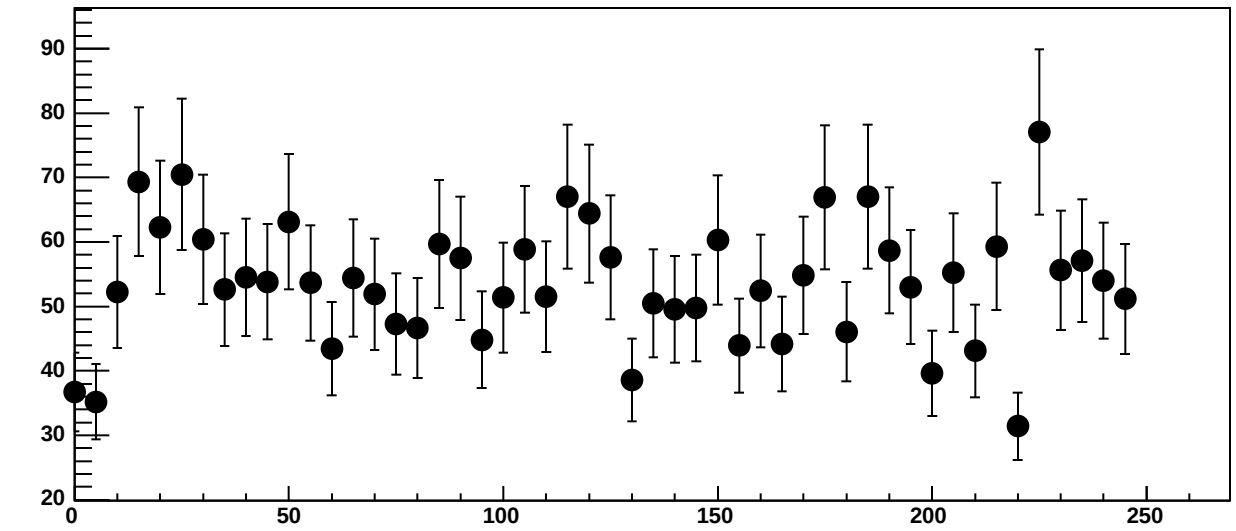

Chip 7, Channel 14, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 7, Channel 14, Enable 1, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

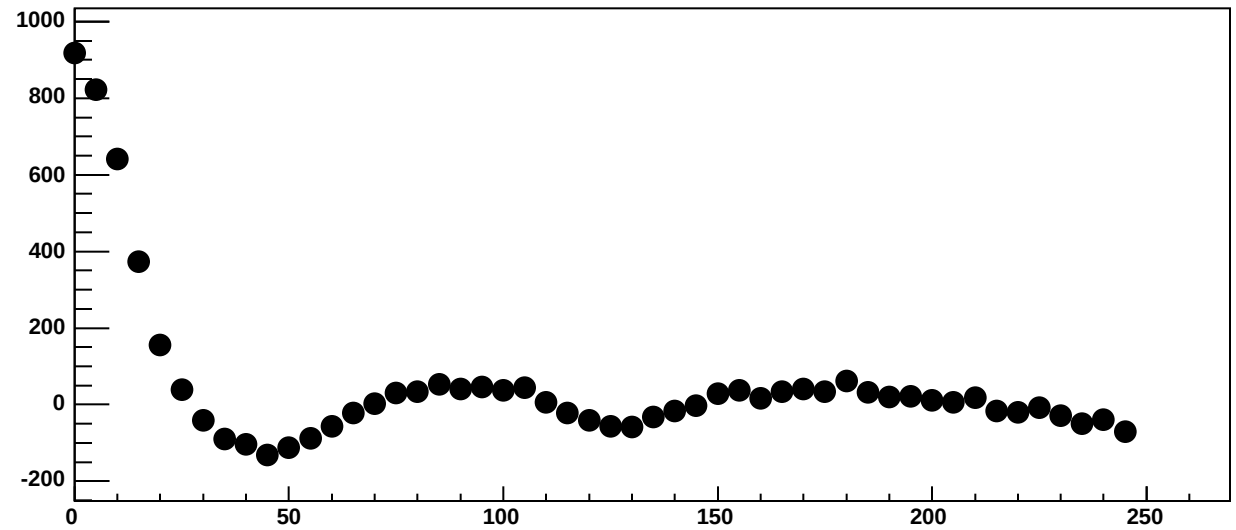
Chip 7, Channel 14, Enable 2, DAC=1600, ADC Mean vs Hold



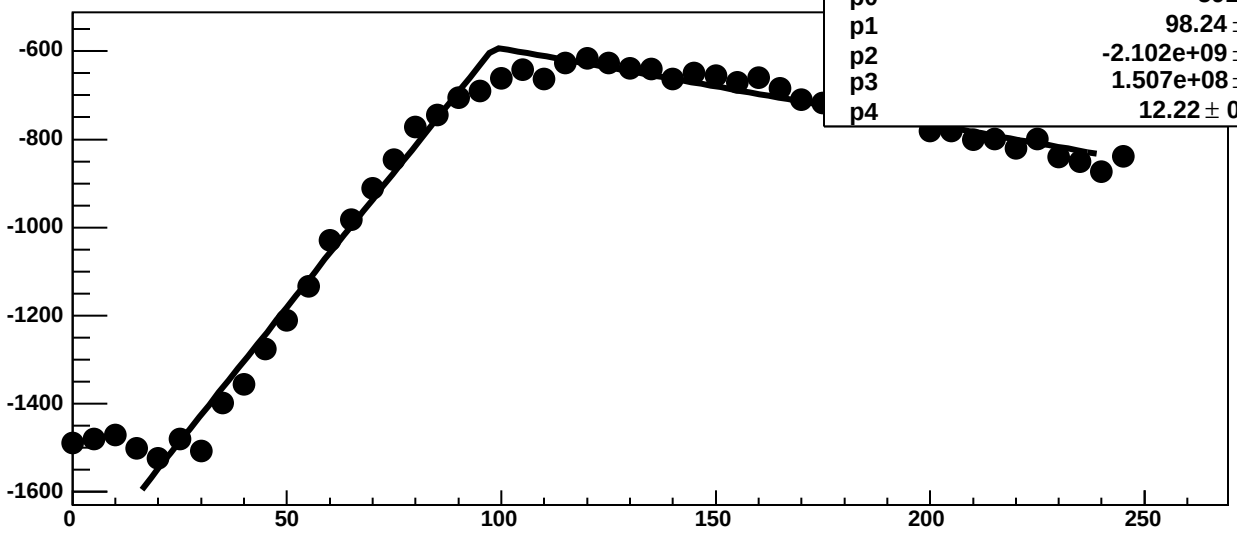
Chip 7, Channel 14, Enable 2, DAC=1600, ADC Noise vs Hold



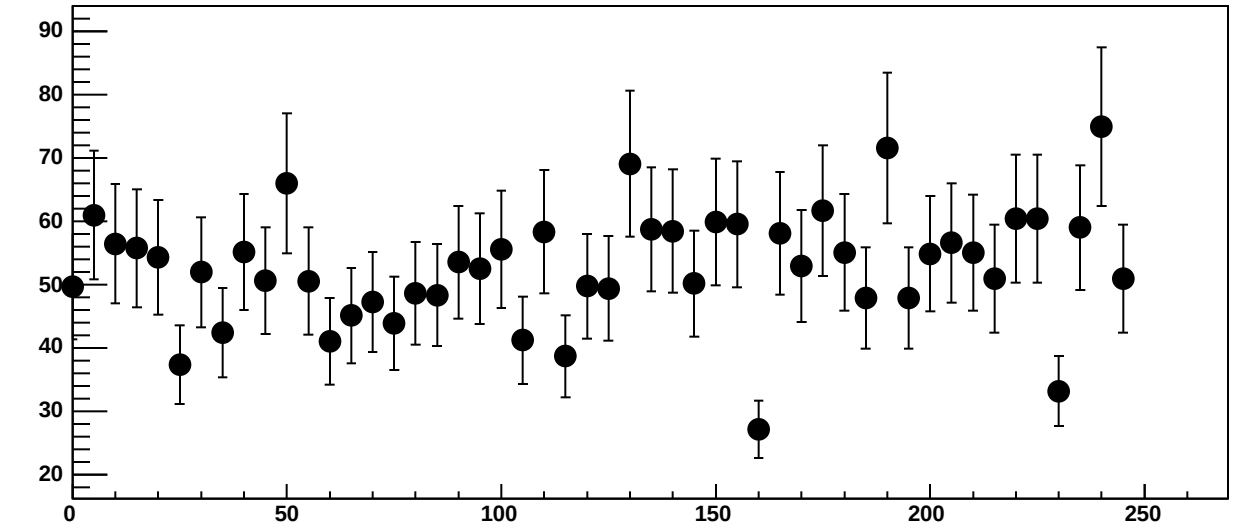
Chip 7, Channel 14, Enable 2, DAC=1600, ADC Residuals vs Hold



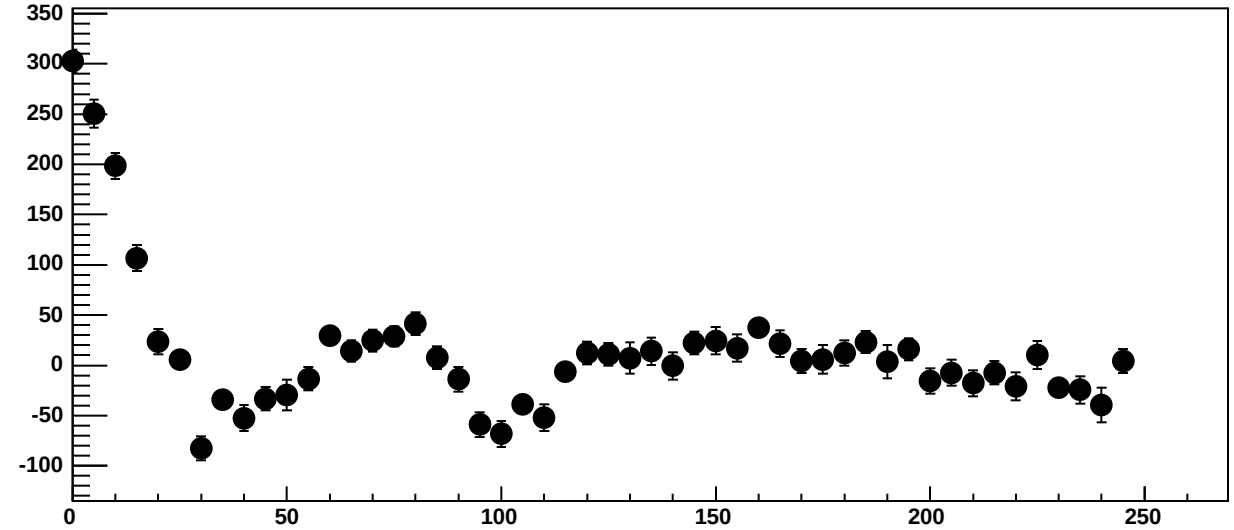
Chip 7, Channel 14, Enable 3, DAC=1600, ADC Mean vs Hold



Chip 7, Channel 14, Enable 3, DAC=1600, ADC Noise vs Hold

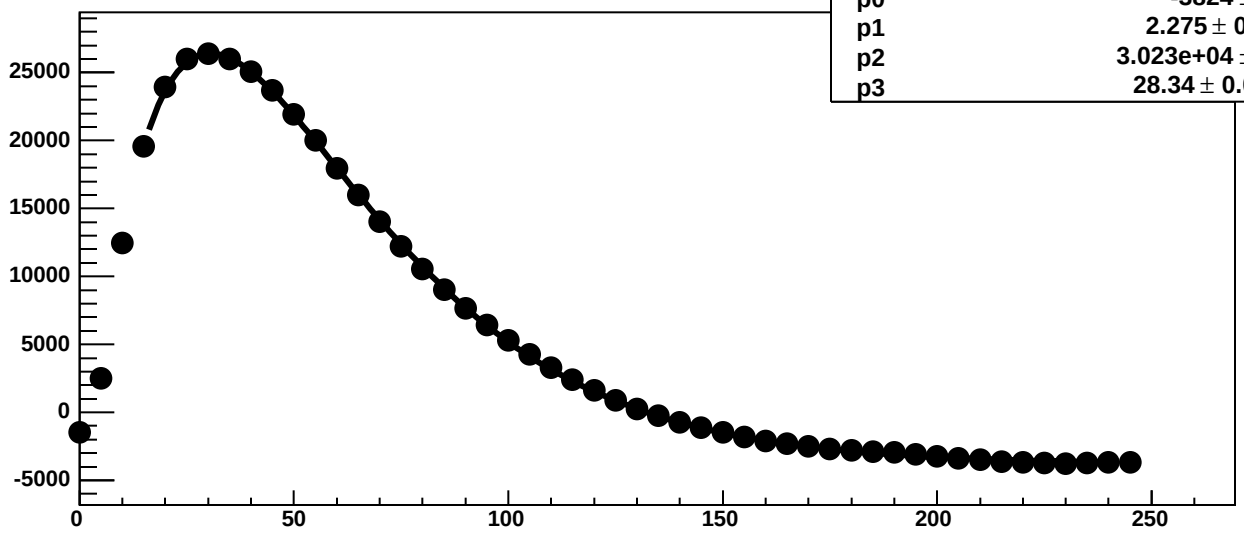


Chip 7, Channel 14, Enable 3, DAC=1600, ADC Residuals vs Hold

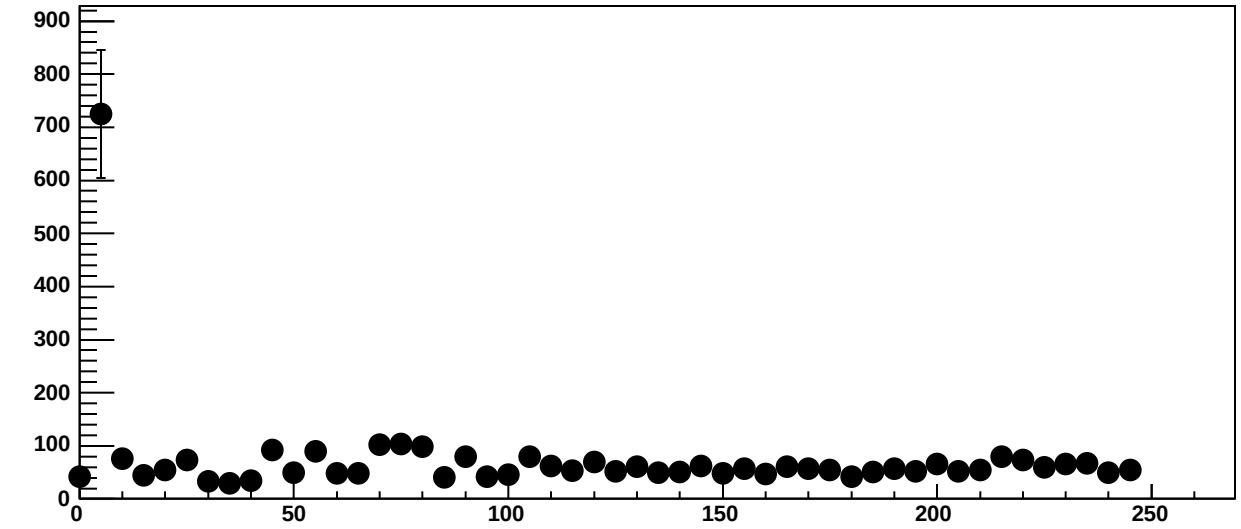


Chip 7, Channel 14, Enable 4!, DAC=1600, ADC Mean vs Hold

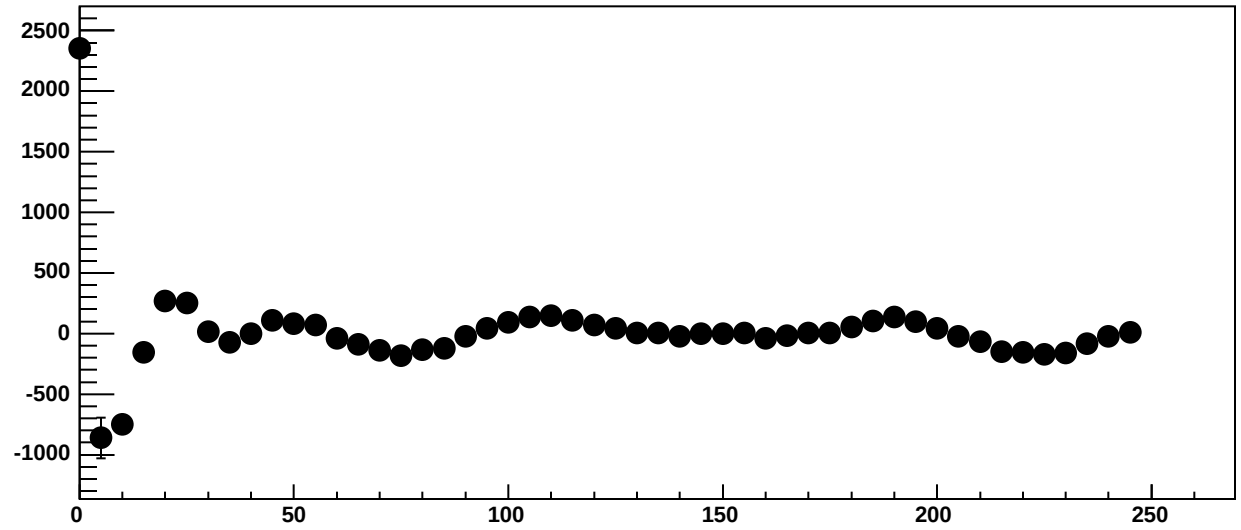
χ^2 / ndf	2644 / 42
p0	-3824 ± 3.576
p1	2.275 ± 0.01225
p2	3.023e+04 ± 4.683
p3	28.34 ± 0.009262

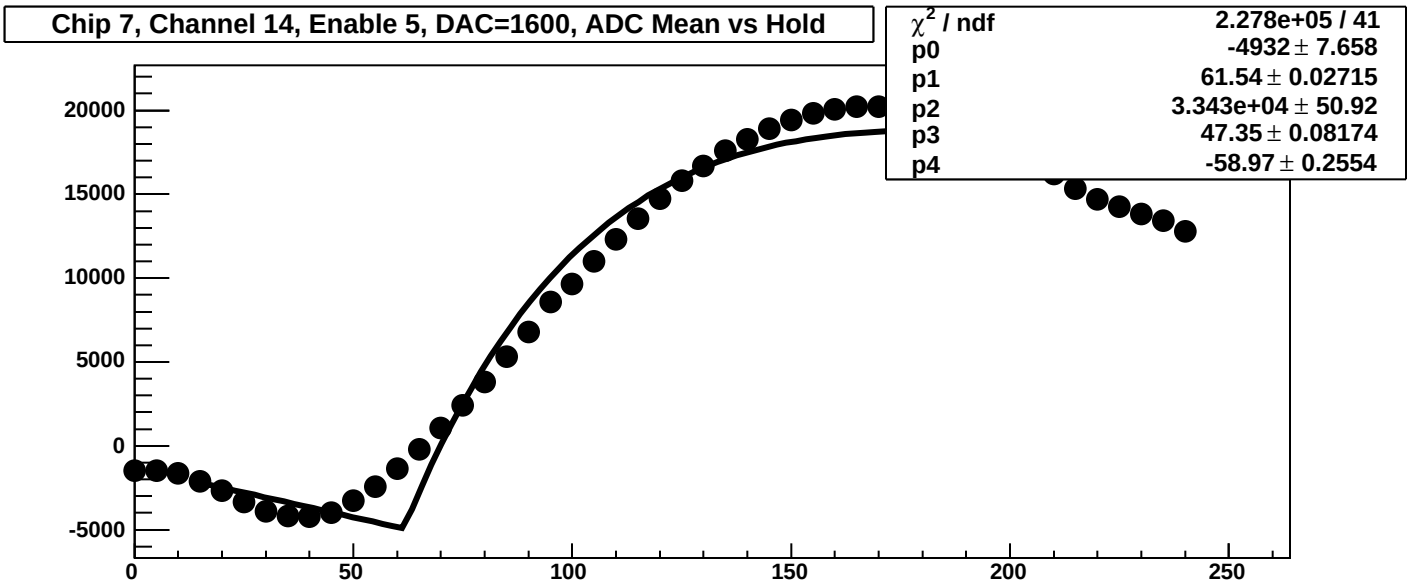


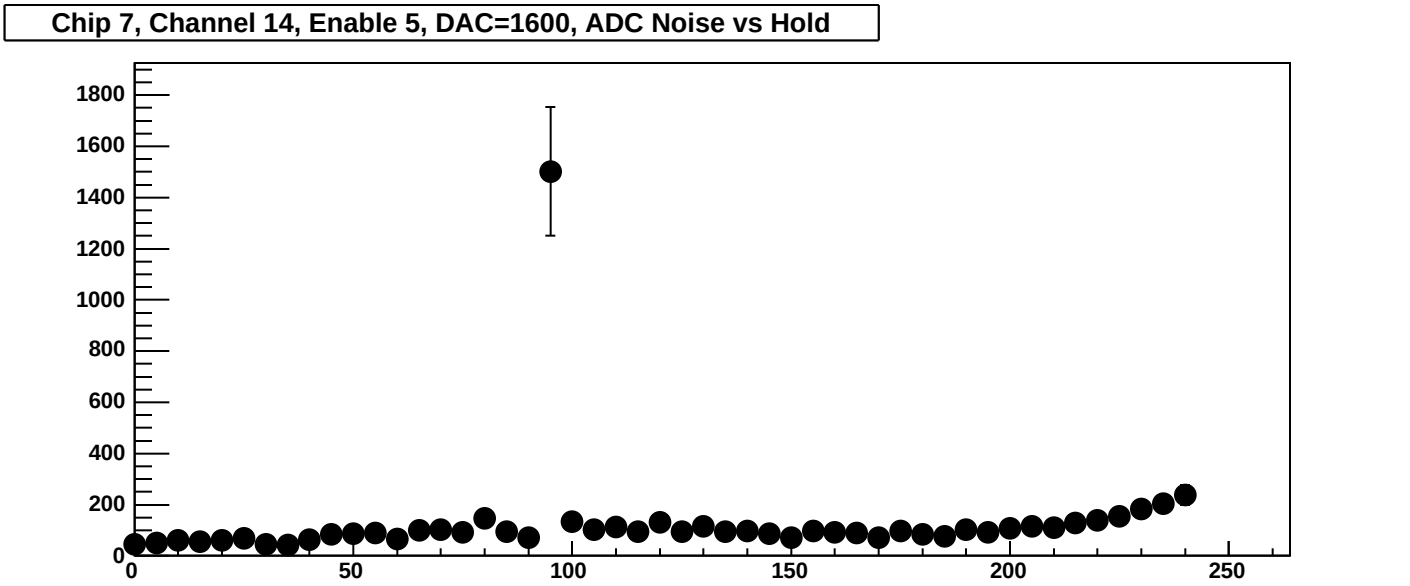
Chip 7, Channel 14, Enable 4!, DAC=1600, ADC Noise vs Hold

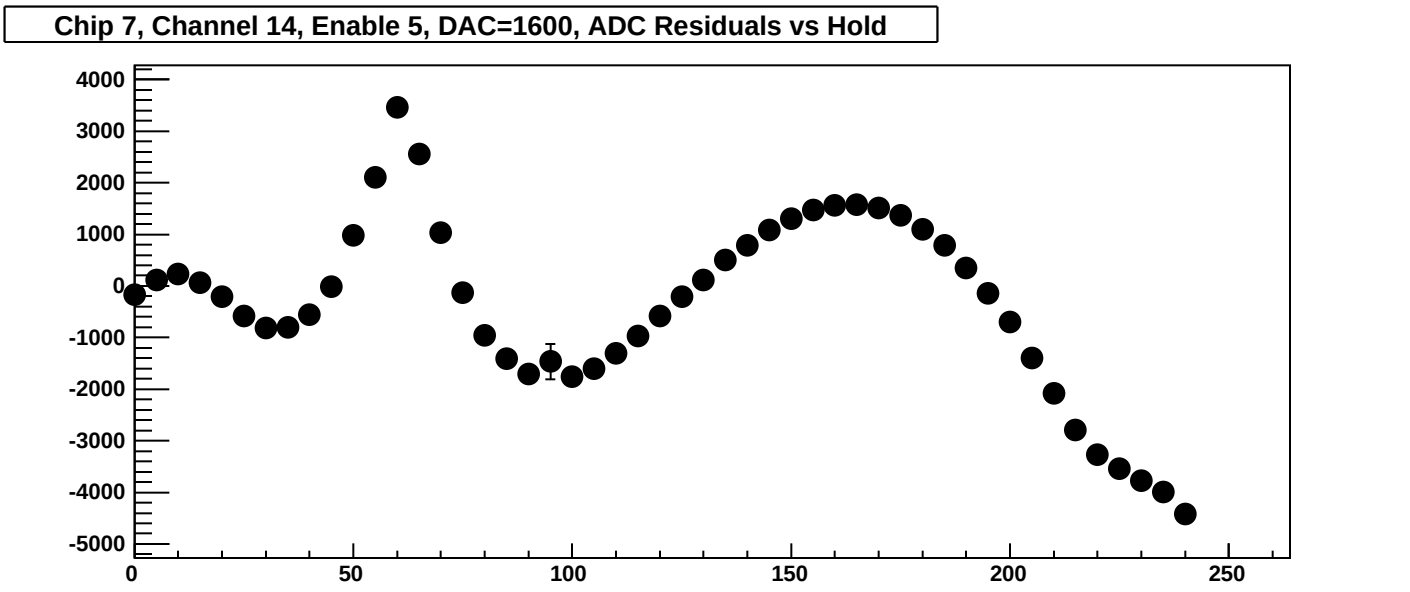


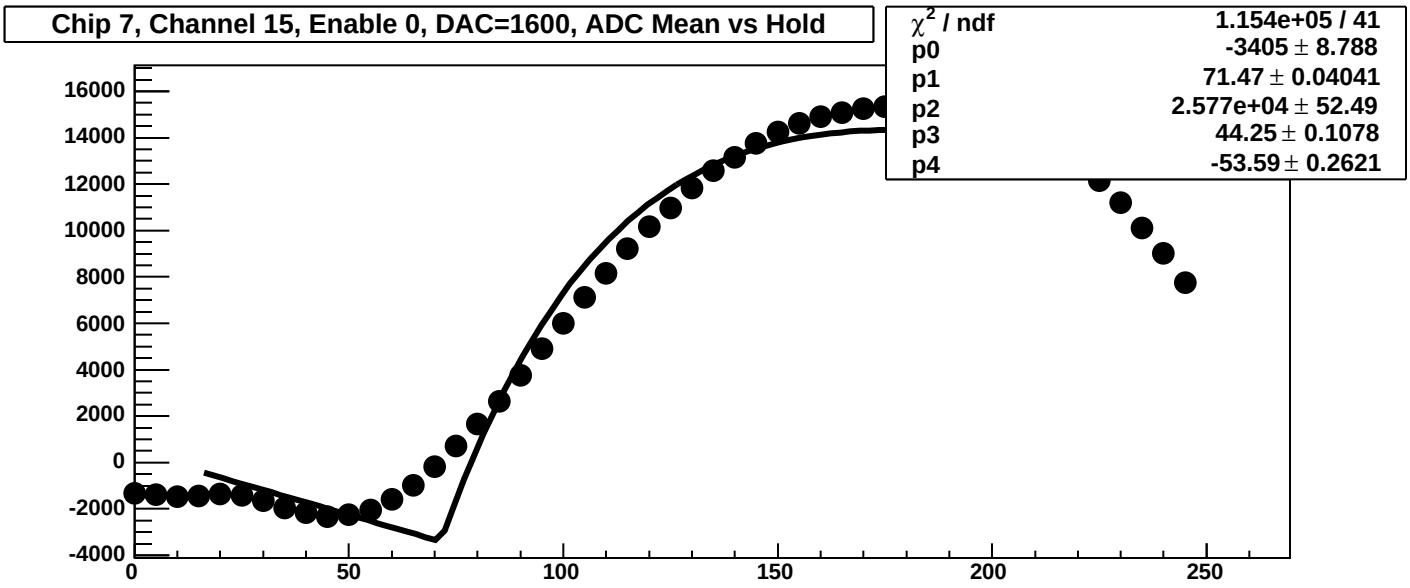
Chip 7, Channel 14, Enable 4!, DAC=1600, ADC Residuals vs Hold

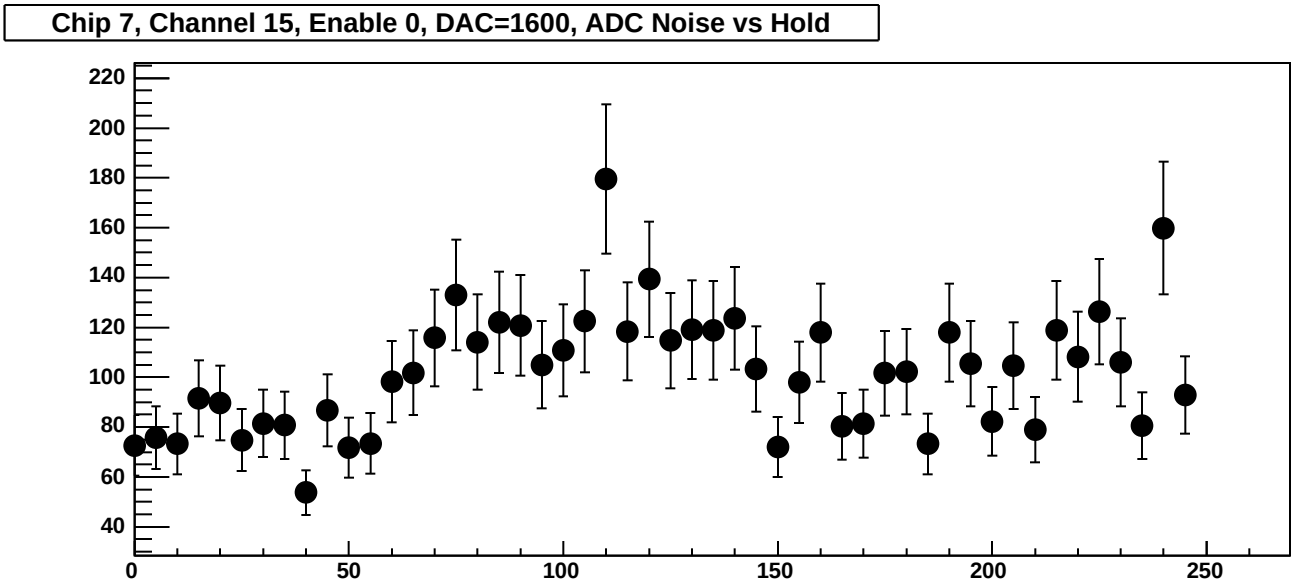


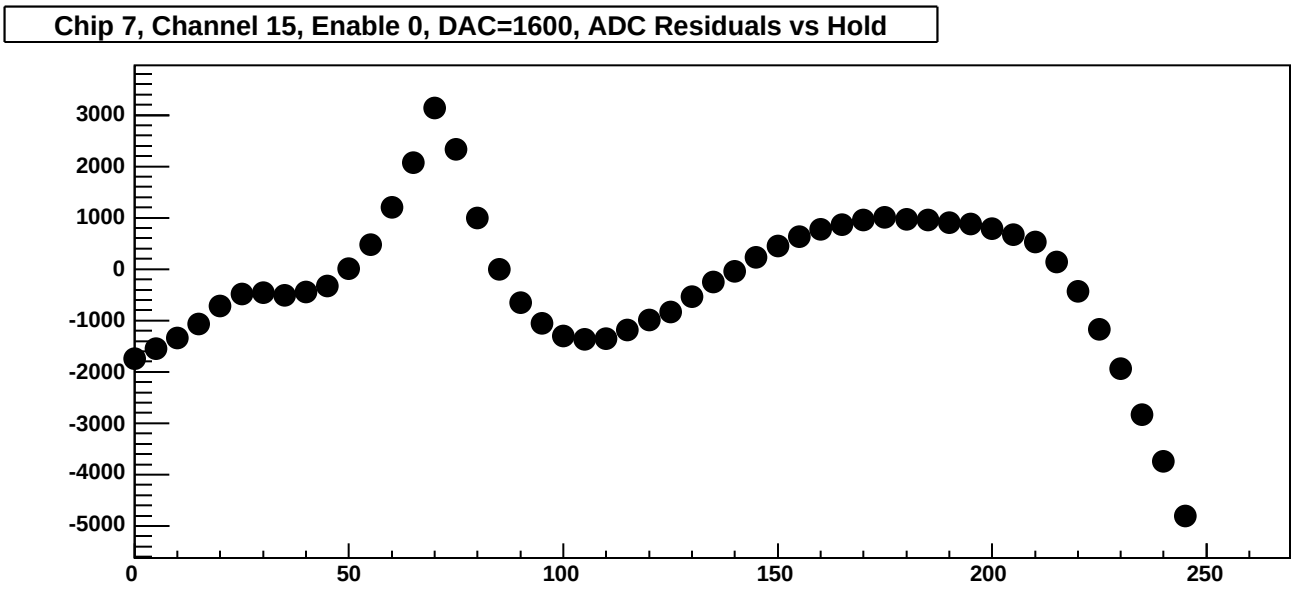

Chip 7, Channel 14, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.278e+05 / 41
p0 -4932 ± 7.658
p1 61.54 ± 0.02715
p2 3.343e+04 ± 50.92
p3 47.35 ± 0.08174
p4 -58.97 ± 0.2554
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 7, Channel 14, Enable 5, DAC=1600, ADC Noise vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

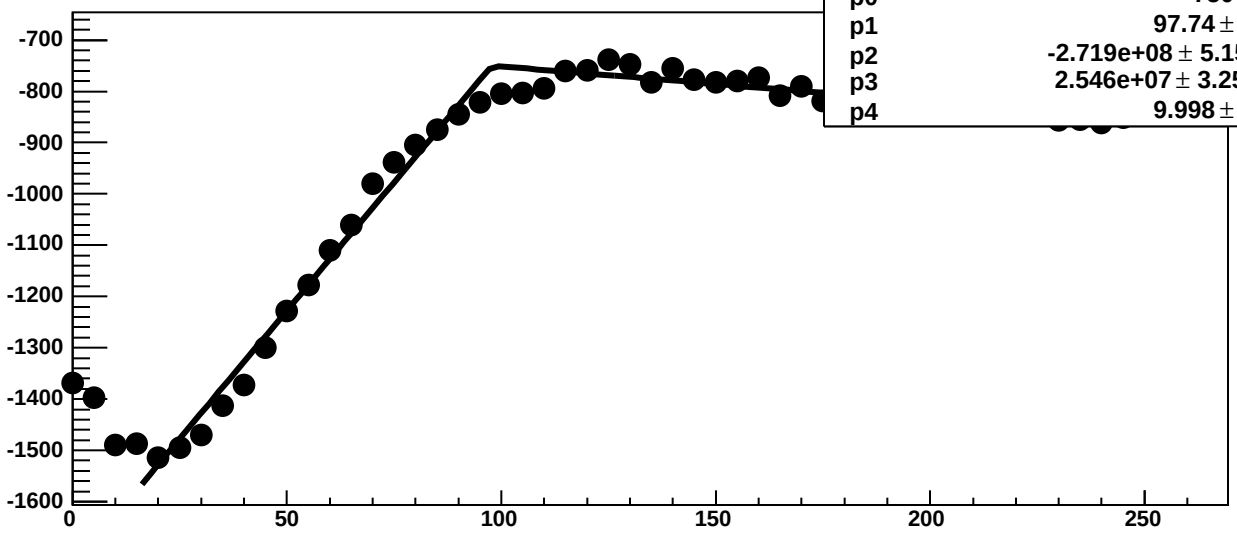

Chip 7, Channel 14, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250


Chip 7, Channel 15, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.154e+05 / 41
p0 -3405 ± 8.788
p1 71.47 ± 0.04041
p2 2.577e+04 ± 52.49
p3 44.25 ± 0.1078
p4 -53.59 ± 0.2621
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

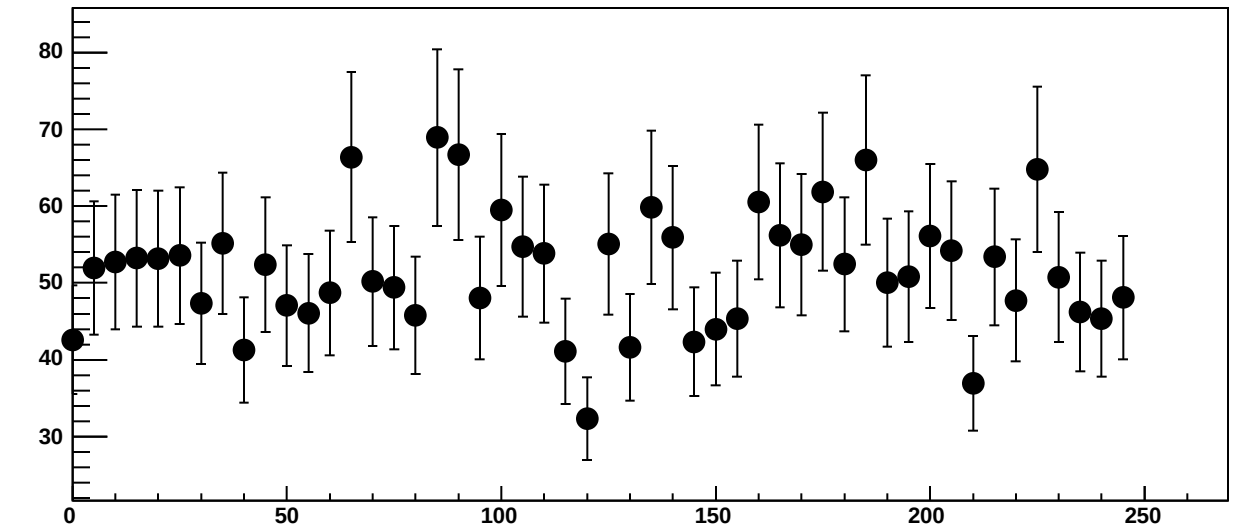

Chip 7, Channel 15, Enable 0, DAC=1600, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250


Chip 7, Channel 15, Enable 0, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

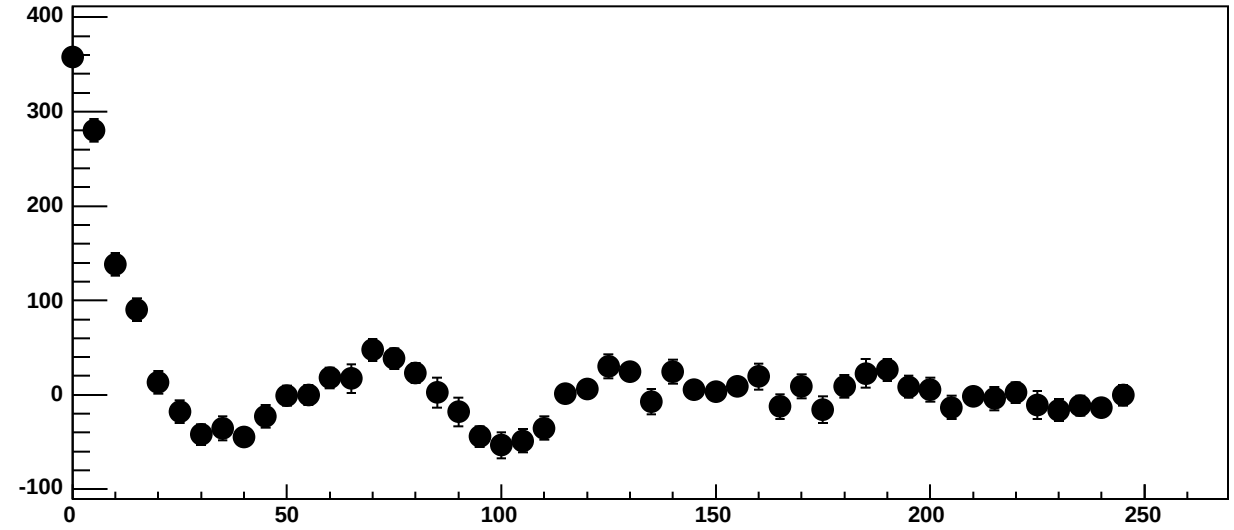
Chip 7, Channel 15, Enable 1, DAC=1600, ADC Mean vs Hold

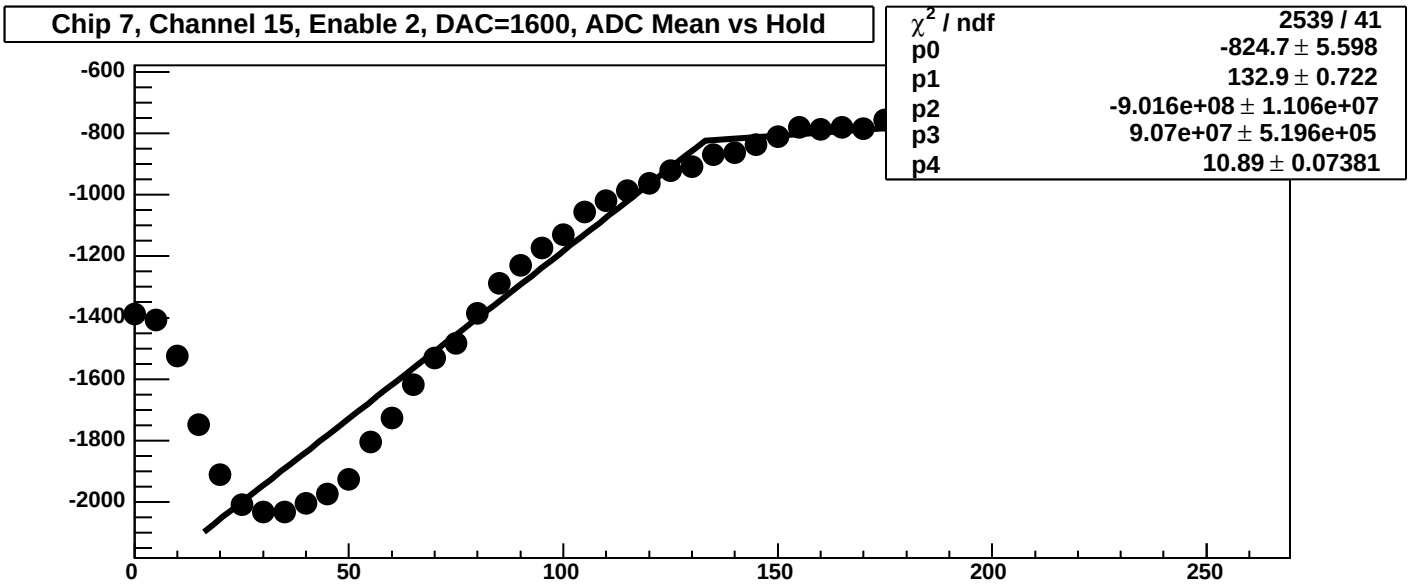


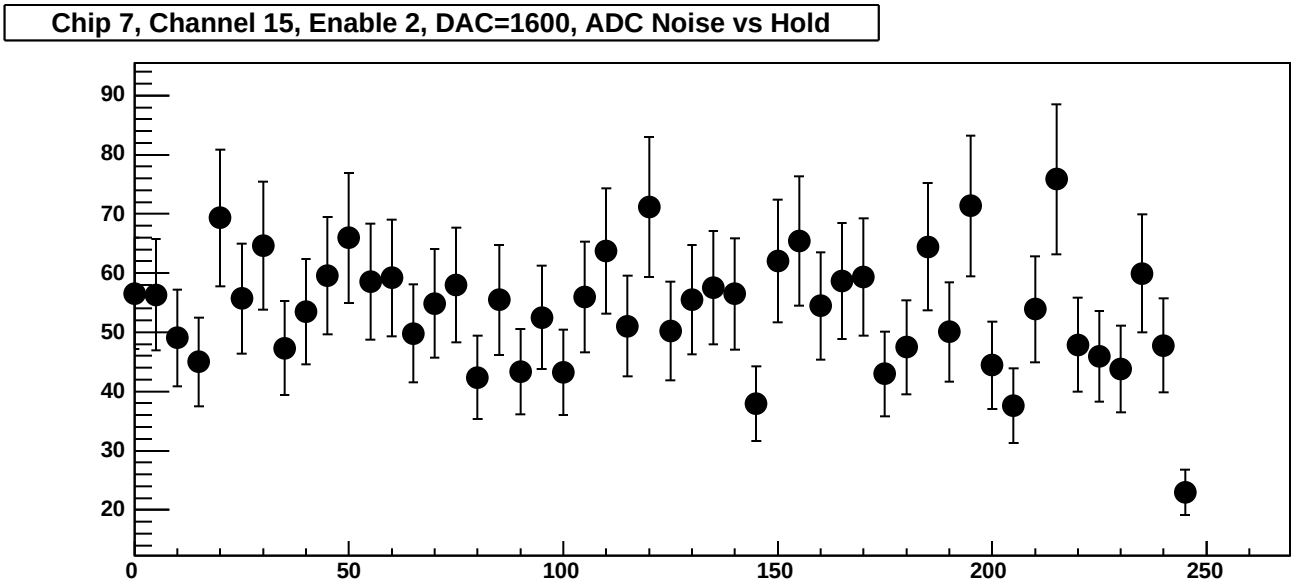
Chip 7, Channel 15, Enable 1, DAC=1600, ADC Noise vs Hold

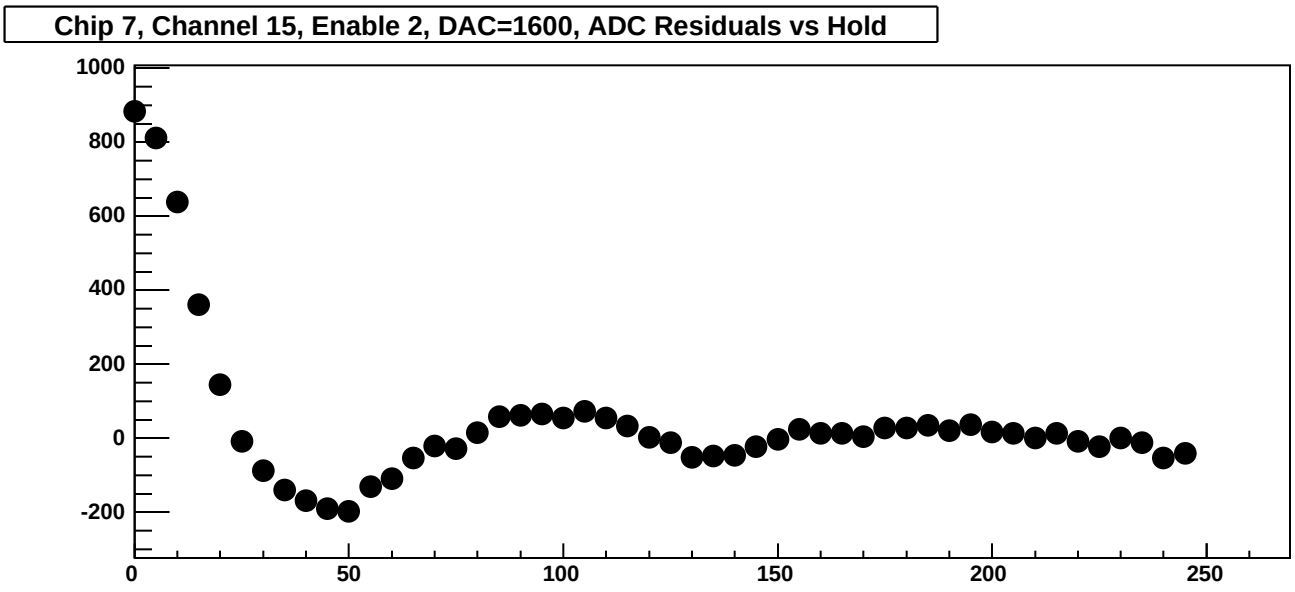


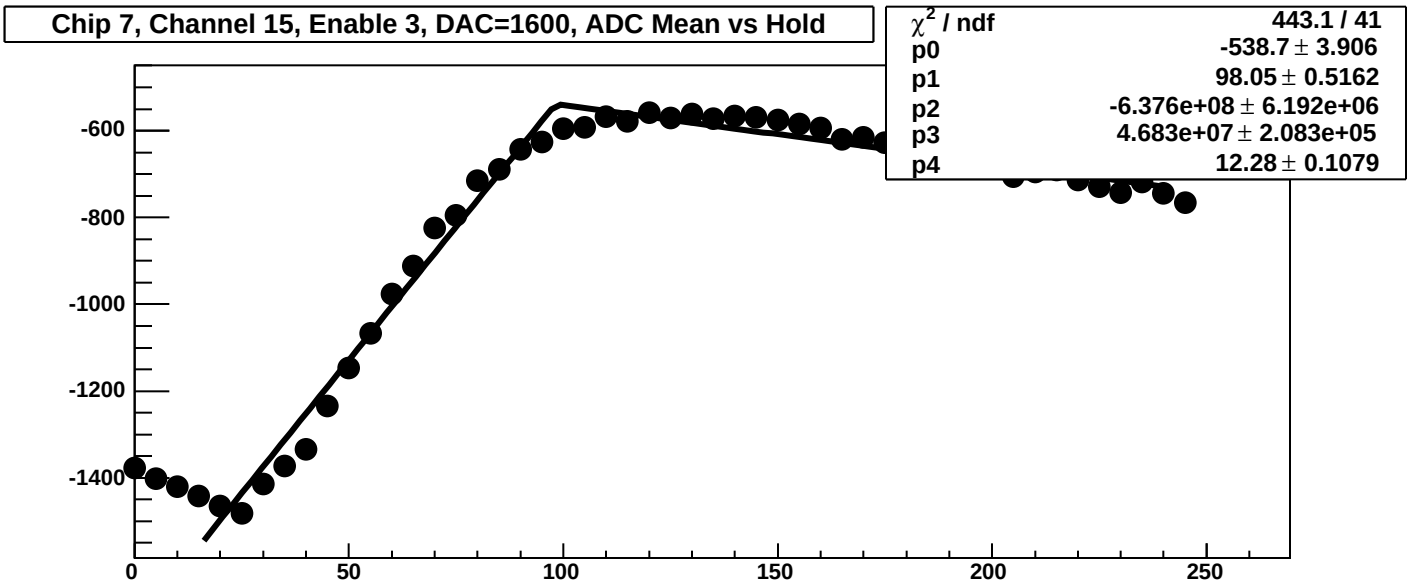
Chip 7, Channel 15, Enable 1, DAC=1600, ADC Residuals vs Hold

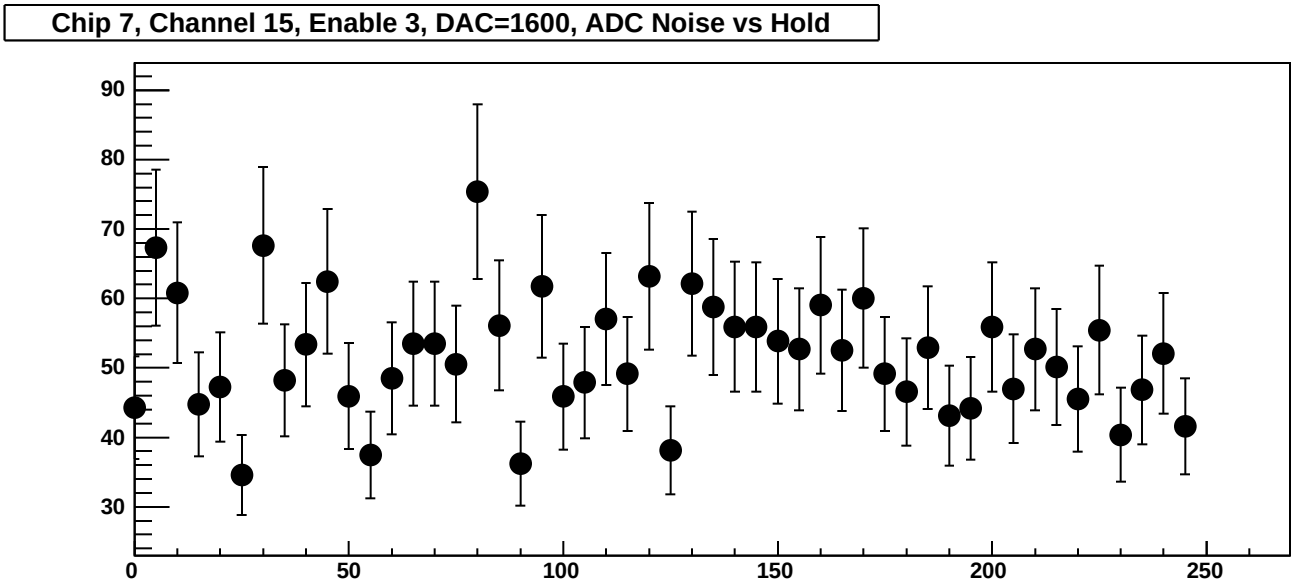


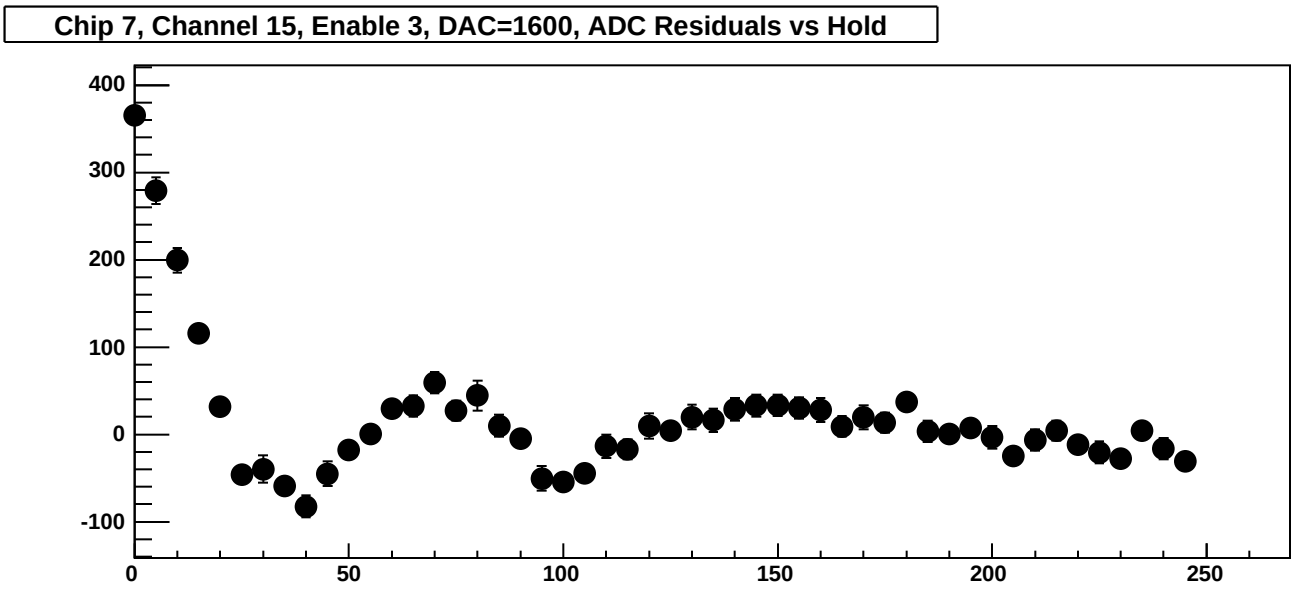

Chip 7, Channel 15, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 2539 / 41
p0 -824.7 ± 5.598
p1 132.9 ± 0.722
p2 -9.016e+08 ± 1.106e+07
p3 9.07e+07 ± 5.196e+05
p4 10.89 ± 0.07381
-600
-800
-1000
-1200
-1400
-1600
-1800
-2000
0
50
100
150
200
250


Chip 7, Channel 15, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
20
0
50
100
150
200
250

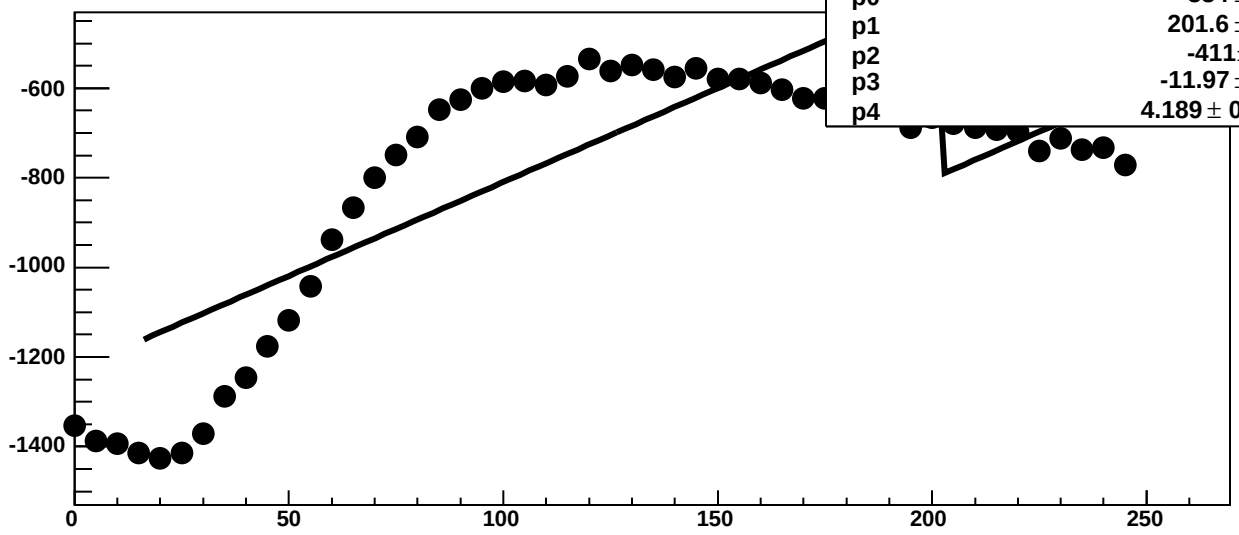

Chip 7, Channel 15, Enable 2, DAC=1600, ADC Residuals vs Hold
1000
800
600
400
200
0
-200
0
50
100
150
200
250


Chip 7, Channel 15, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 443.1 / 41
p0 -538.7 ± 3.906
p1 98.05 ± 0.5162
p2 -6.376e+08 ± 6.192e+06
p3 4.683e+07 ± 2.083e+05
p4 12.28 ± 0.1079
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

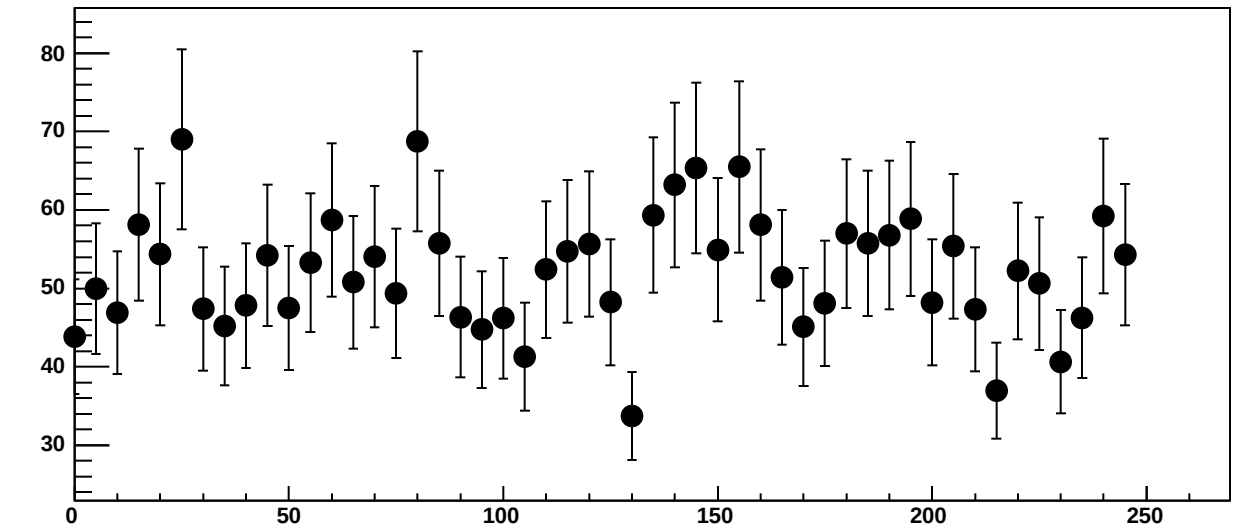

Chip 7, Channel 15, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 7, Channel 15, Enable 3, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

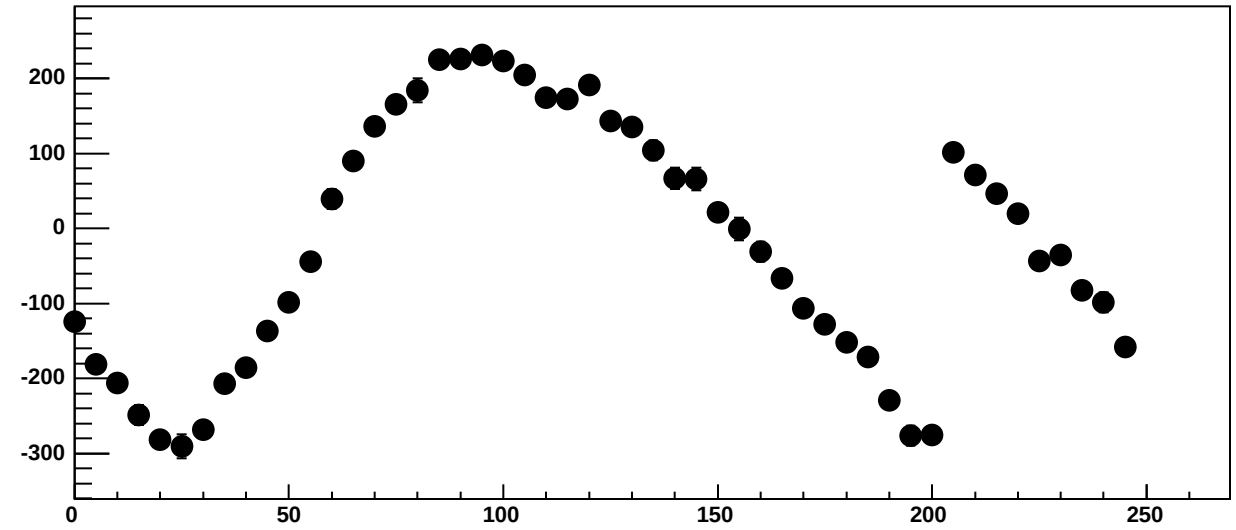
Chip 7, Channel 15, Enable 4, DAC=1600, ADC Mean vs Hold

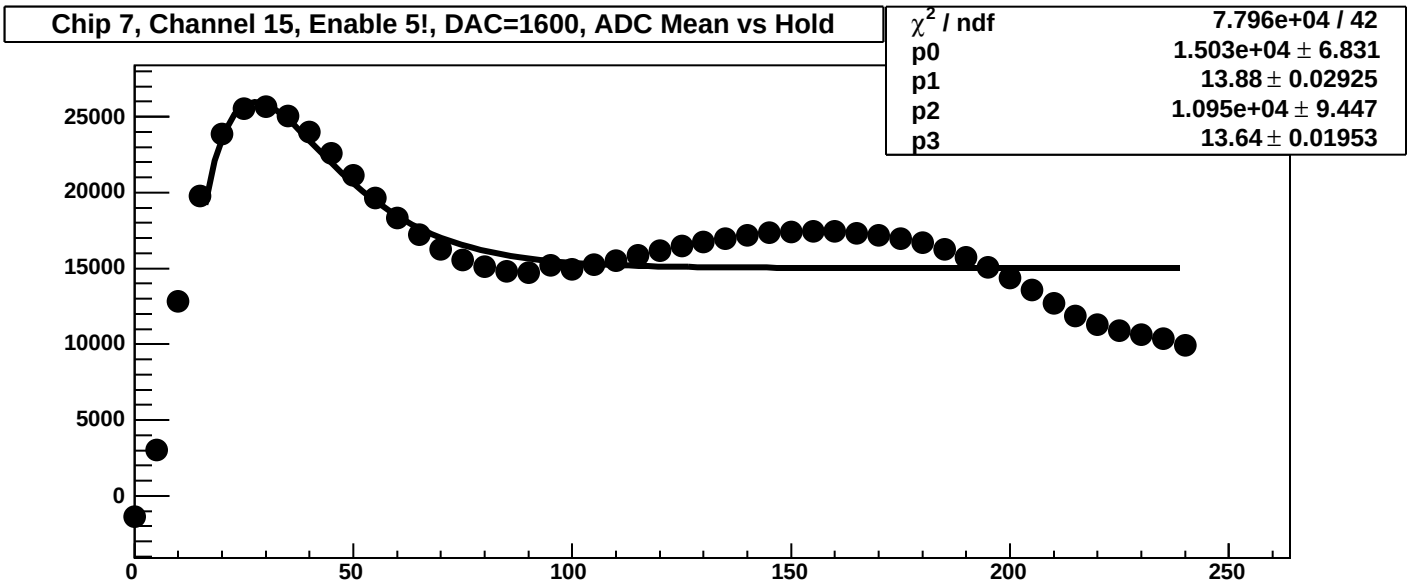


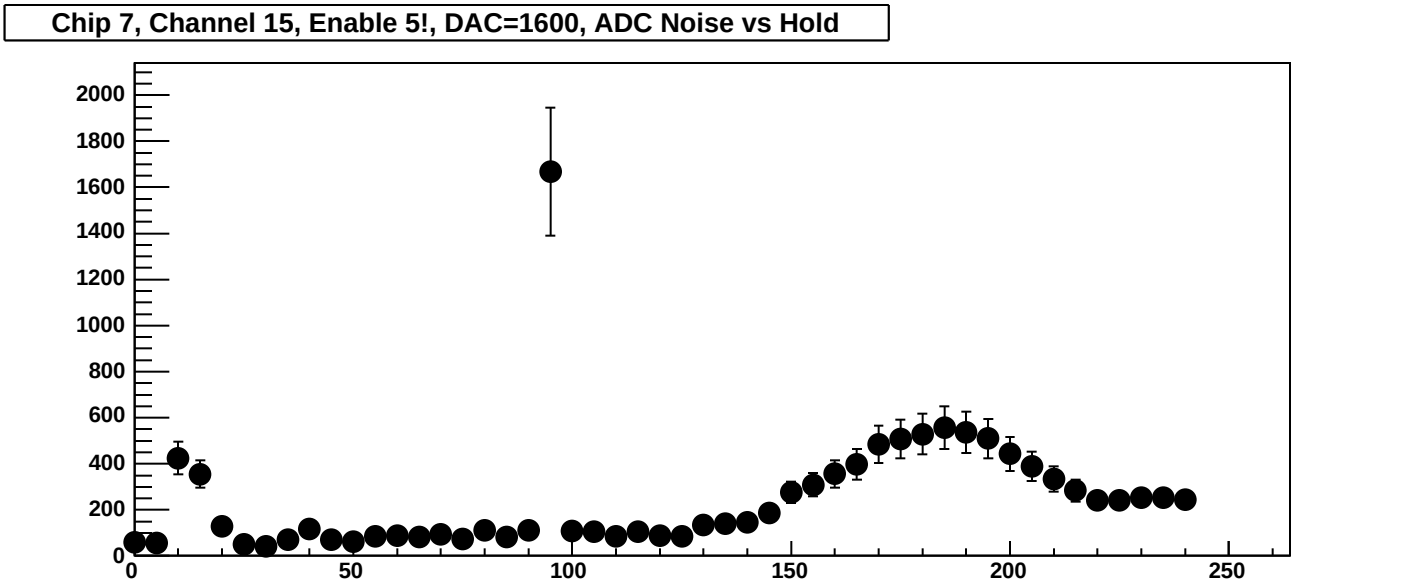
Chip 7, Channel 15, Enable 4, DAC=1600, ADC Noise vs Hold

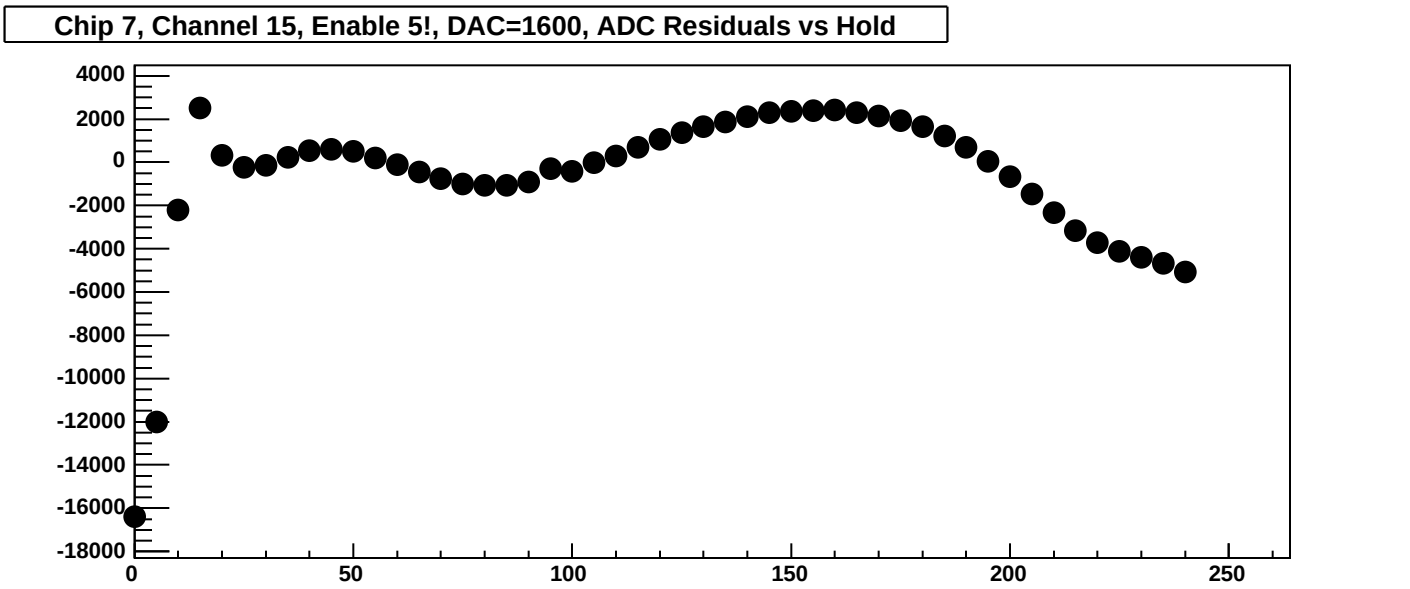


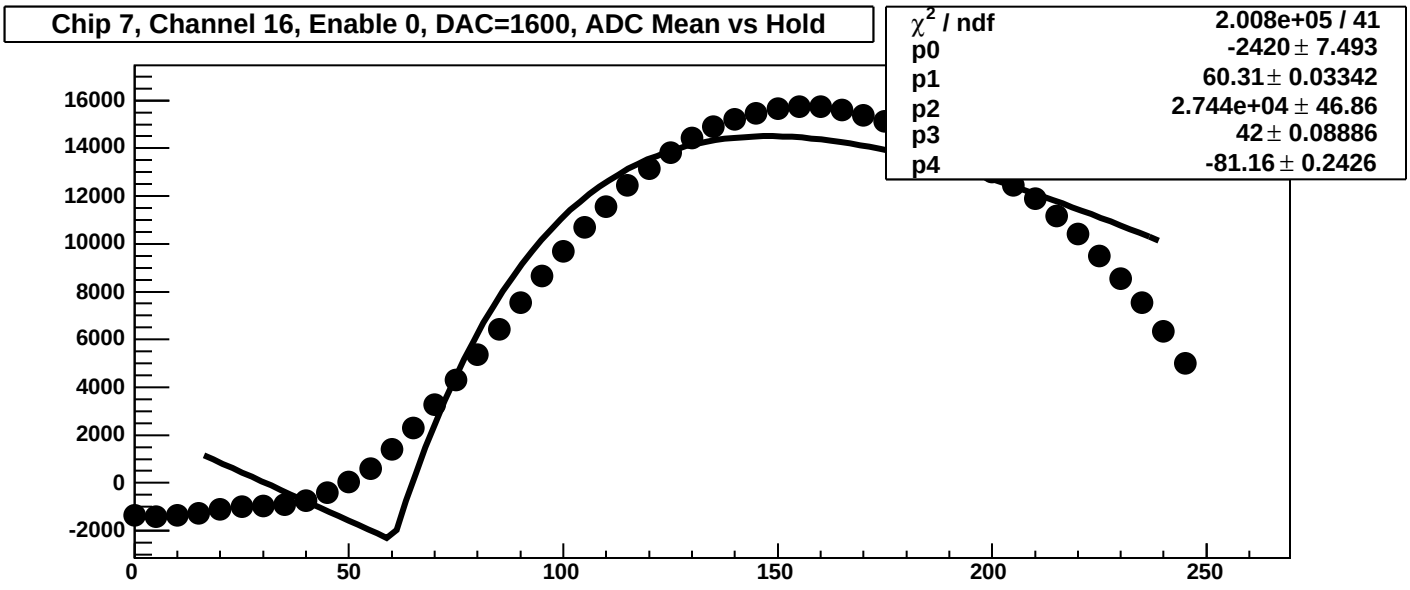
Chip 7, Channel 15, Enable 4, DAC=1600, ADC Residuals vs Hold

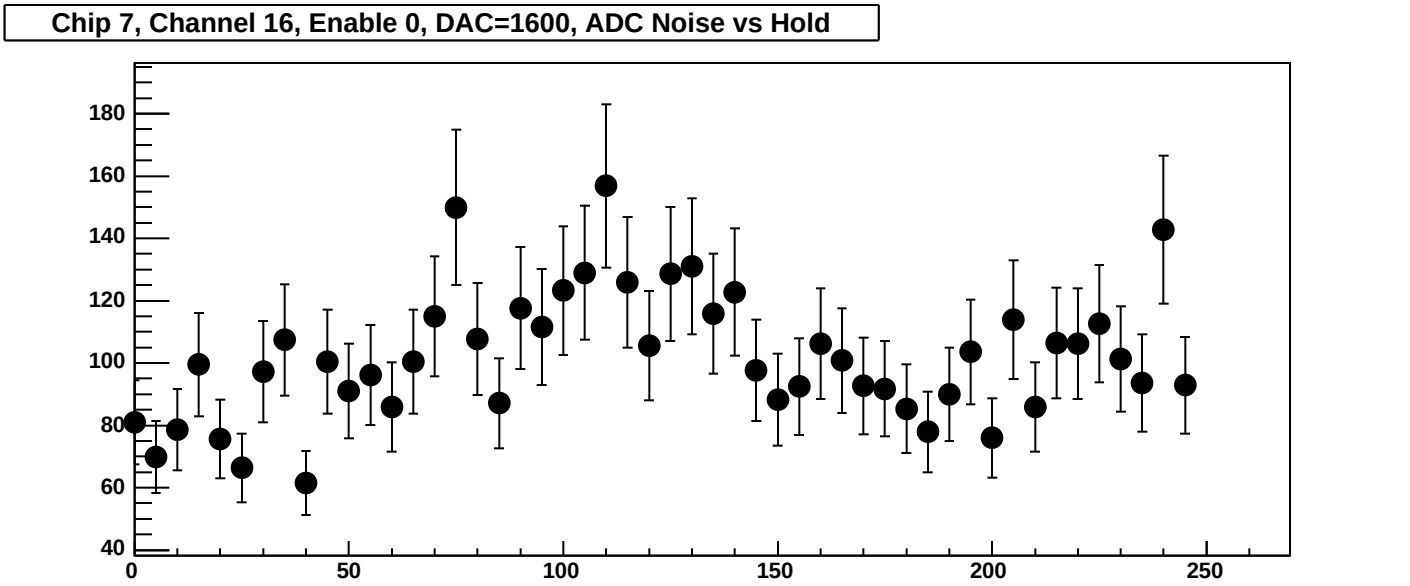


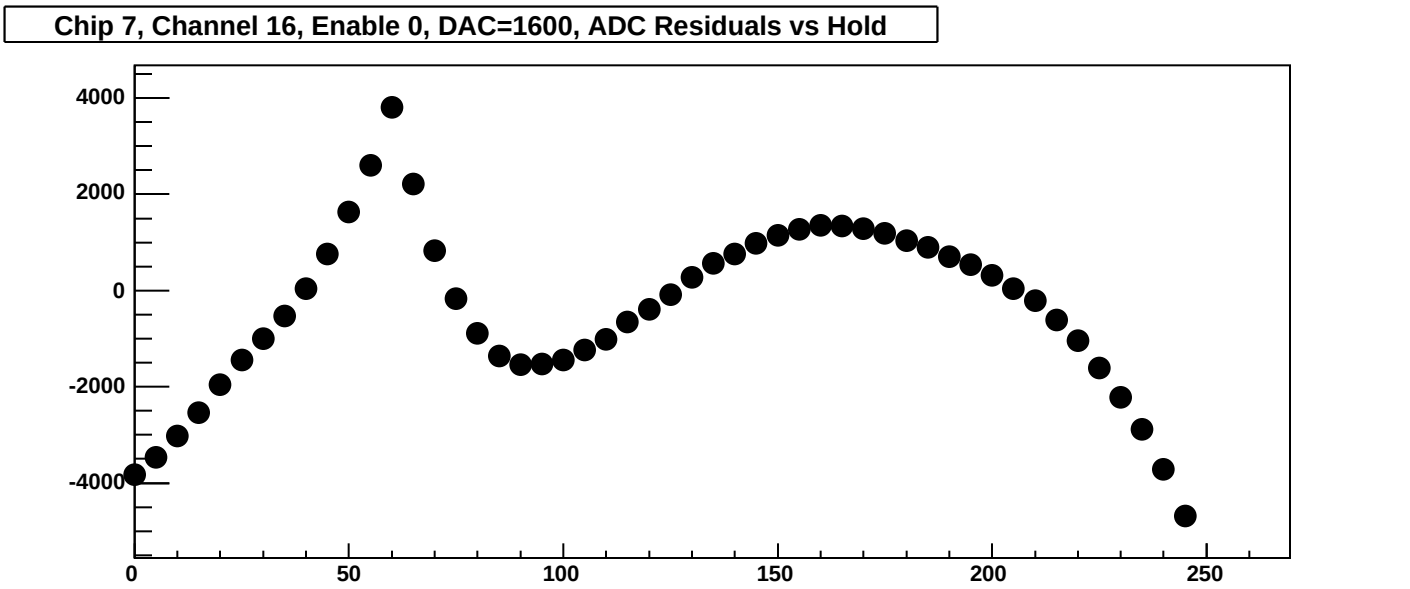

Chip 7, Channel 15, Enable 5!, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.796e+04 / 42
p0 1.503e+04 ± 6.831
p1 13.88 ± 0.02925
p2 1.095e+04 ± 9.447
p3 13.64 ± 0.01953
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

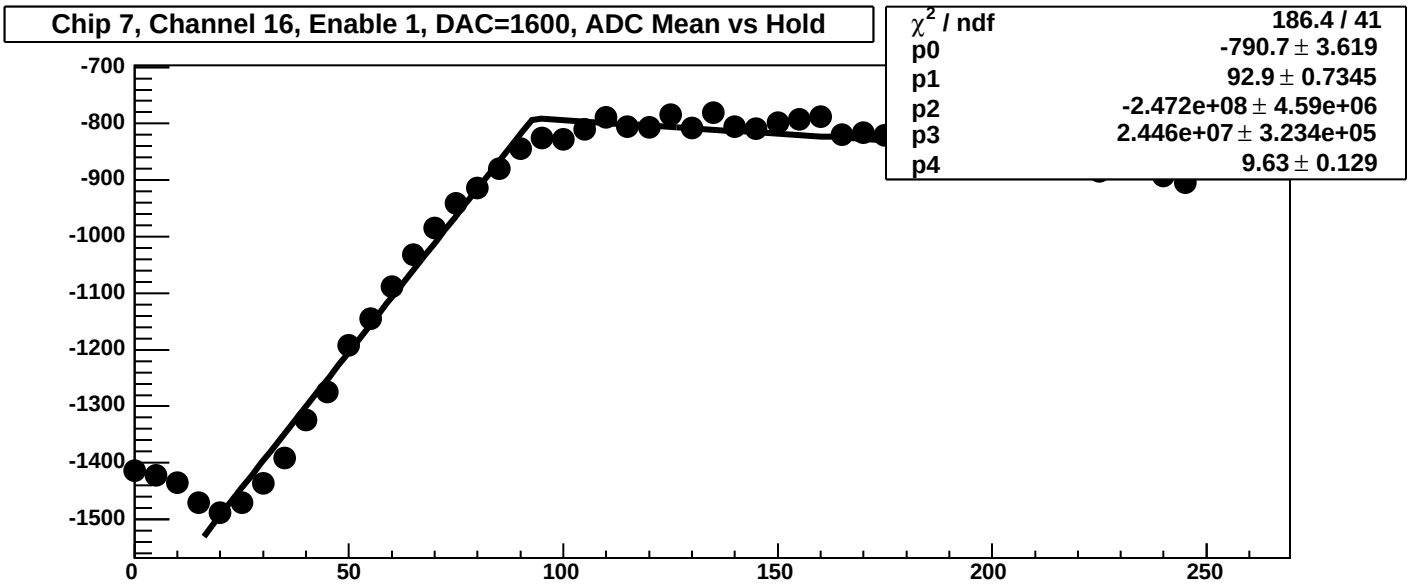

Chip 7, Channel 15, Enable 5!, DAC=1600, ADC Noise vs Hold
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

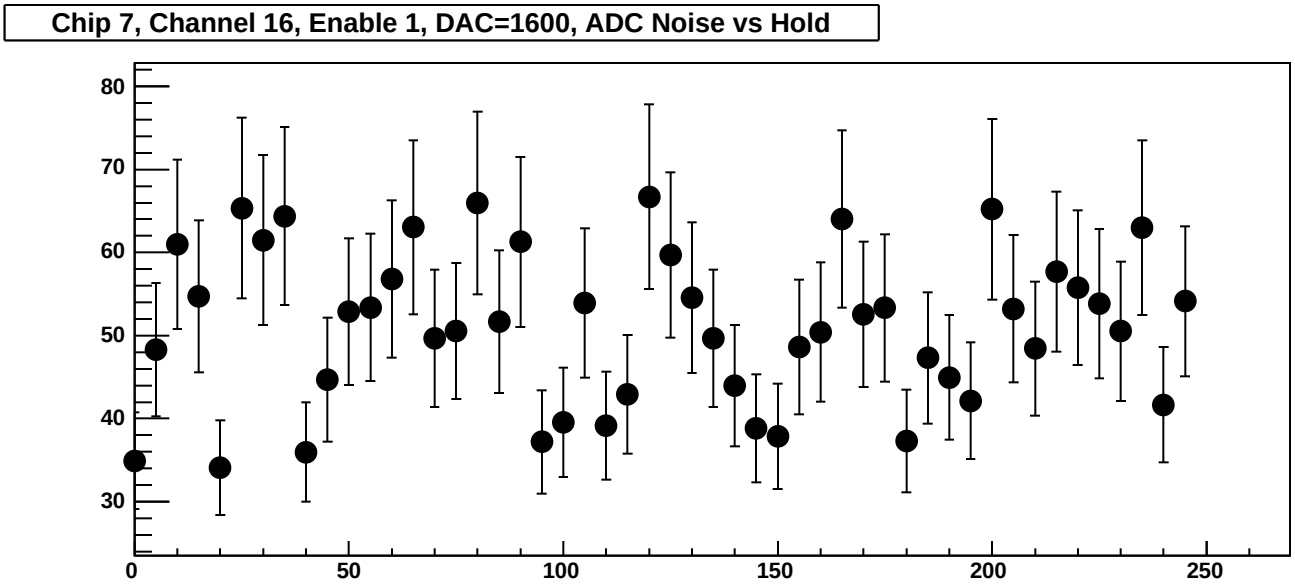

Chip 7, Channel 15, Enable 5!, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
-18000
0
50
100
150
200
250

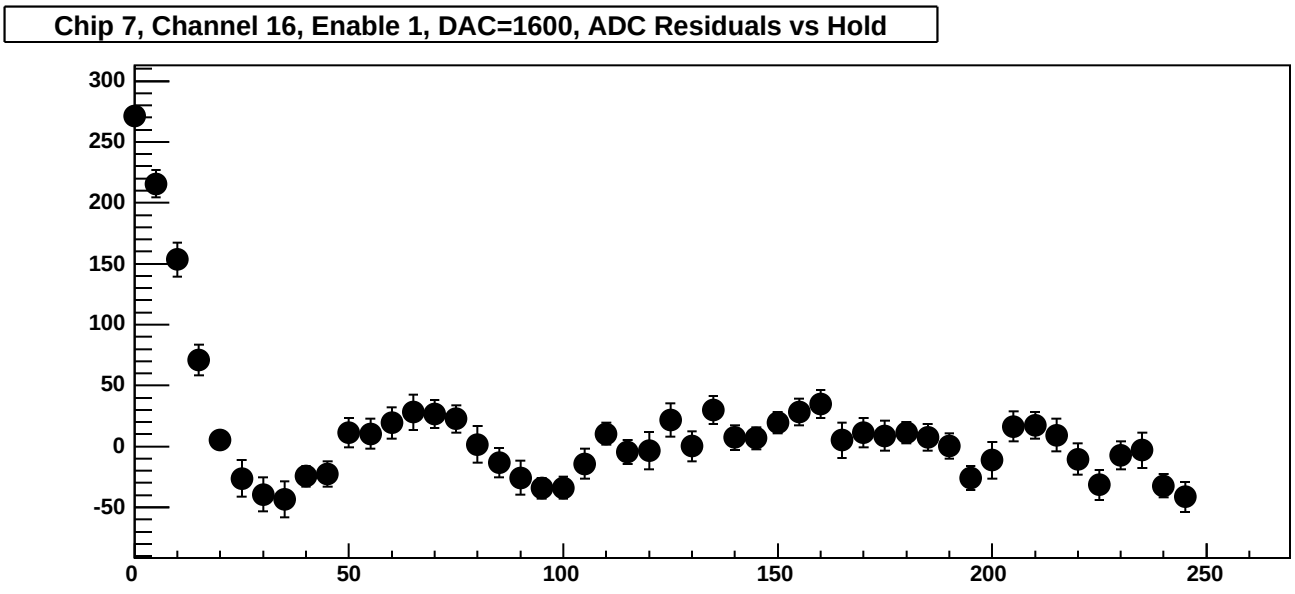

Chip 7, Channel 16, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.008e+05 / 41
p0 -2420 ± 7.493
p1 60.31 ± 0.03342
p2 2.744e+04 ± 46.86
p3 42 ± 0.08886
p4 -81.16 ± 0.2426
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

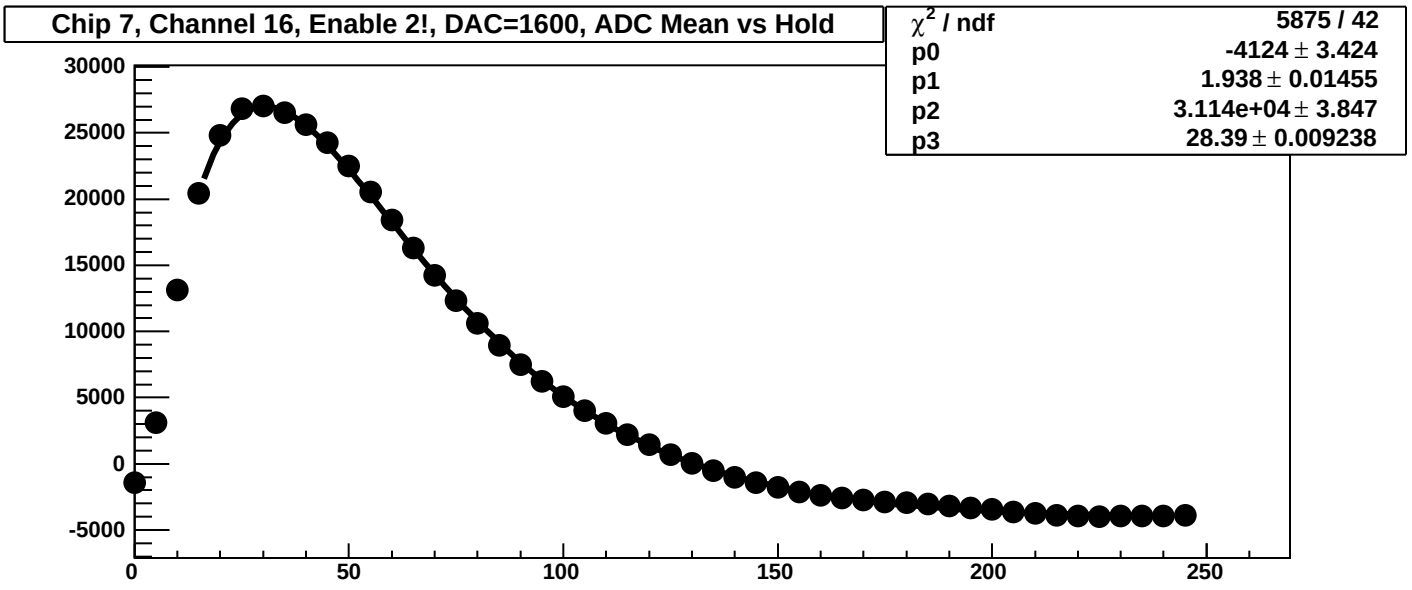

Chip 7, Channel 16, Enable 0, DAC=1600, ADC Noise vs Hold
180
160
140
120
100
80
60
40
0
50
100
150
200
250

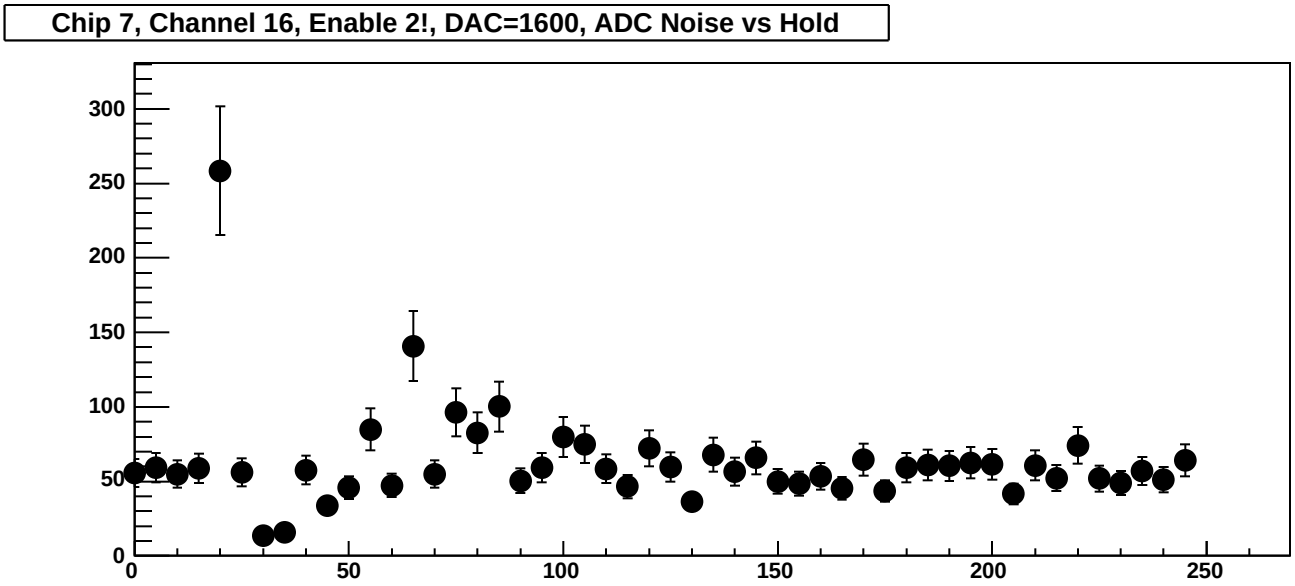

Chip 7, Channel 16, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
0
50
100
150
200
250

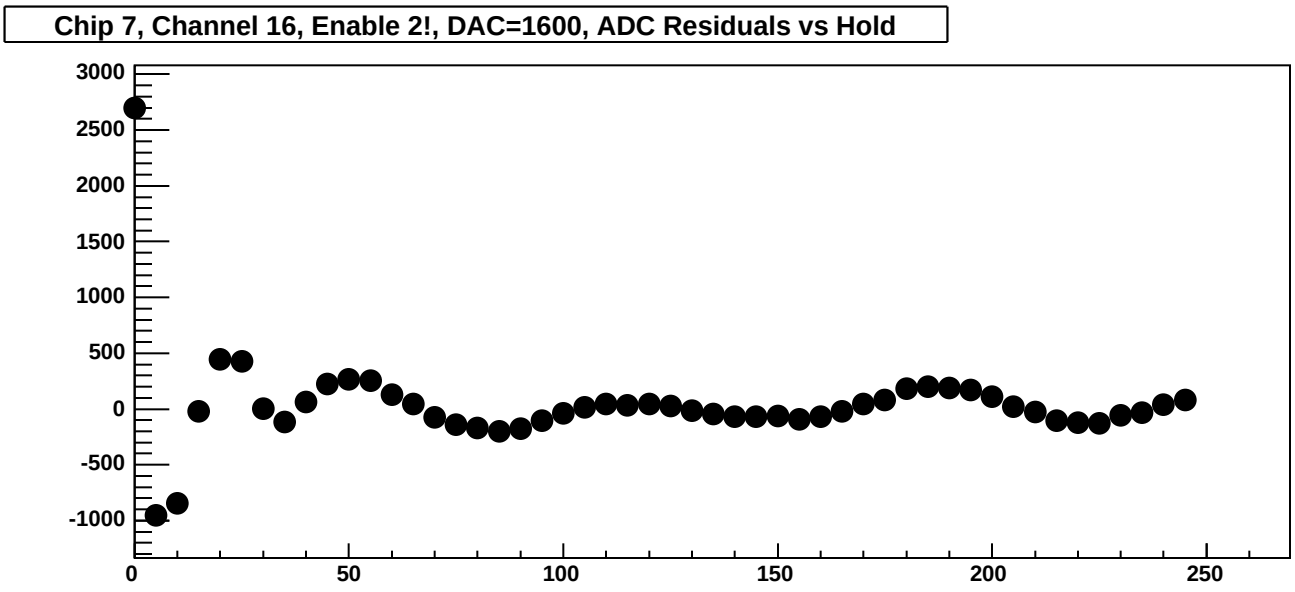

Chip 7, Channel 16, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 186.4 / 41
p0 -790.7 ± 3.619
p1 92.9 ± 0.7345
p2 -2.472e+08 ± 4.59e+06
p3 2.446e+07 ± 3.234e+05
p4 9.63 ± 0.129
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250


Chip 7, Channel 16, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

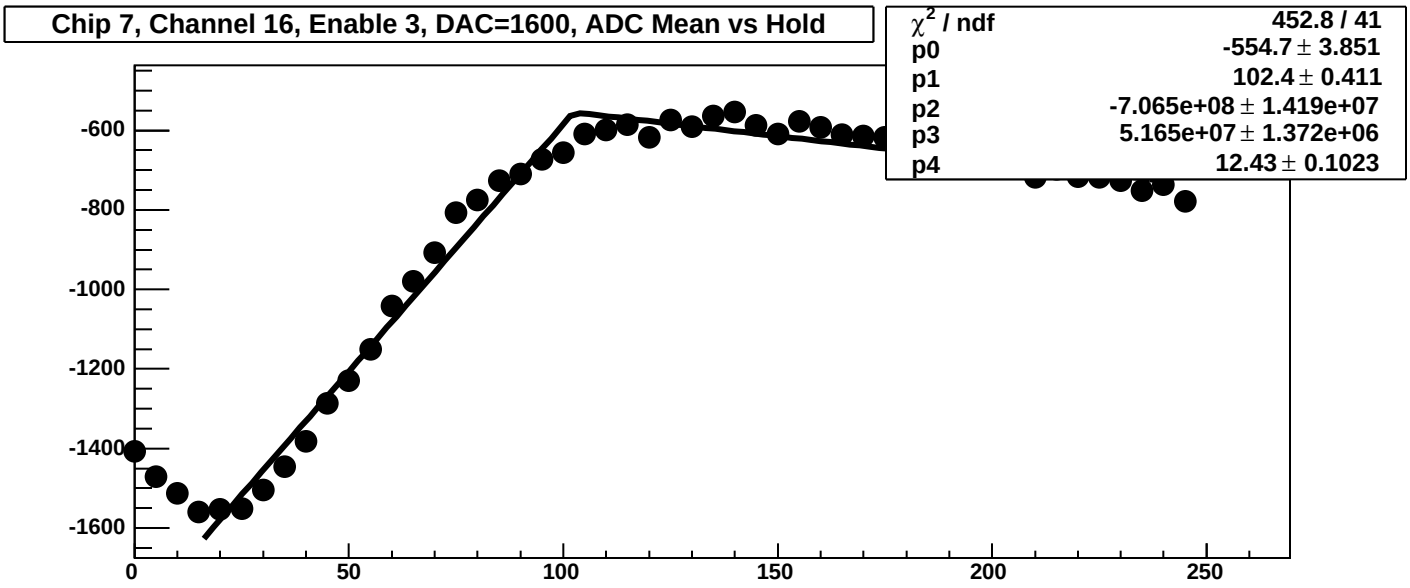

Chip 7, Channel 16, Enable 1, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
0
50
100
150
200
250


Chip 7, Channel 16, Enable 2!, DAC=1600, ADC Mean vs Hold
χ² / ndf 5875 / 42
p0 -4124 ± 3.424
p1 1.938 ± 0.01455
p2 3.114e+04 ± 3.847
p3 28.39 ± 0.009238
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

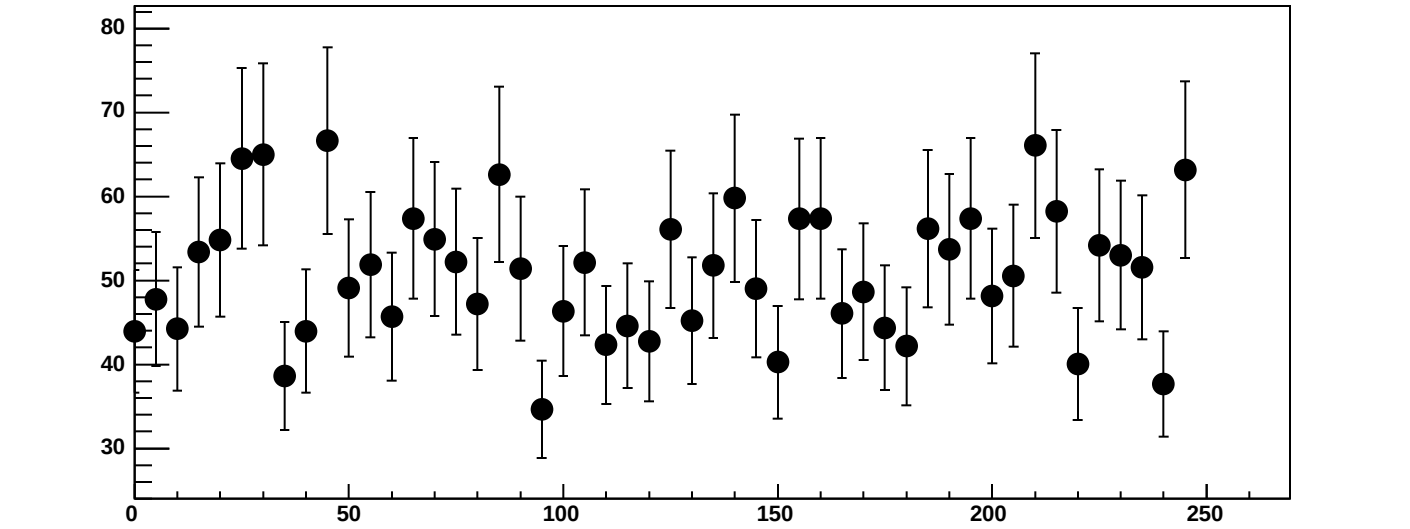

Chip 7, Channel 16, Enable 2!, DAC=1600, ADC Noise vs Hold
300
250
200
150
100
50
0
0
50
100
150
200
250


Chip 7, Channel 16, Enable 2!, DAC=1600, ADC Residuals vs Hold
3000
2500
2000
1500
1000
500
0
-500
-1000
0
50
100
150
200
250

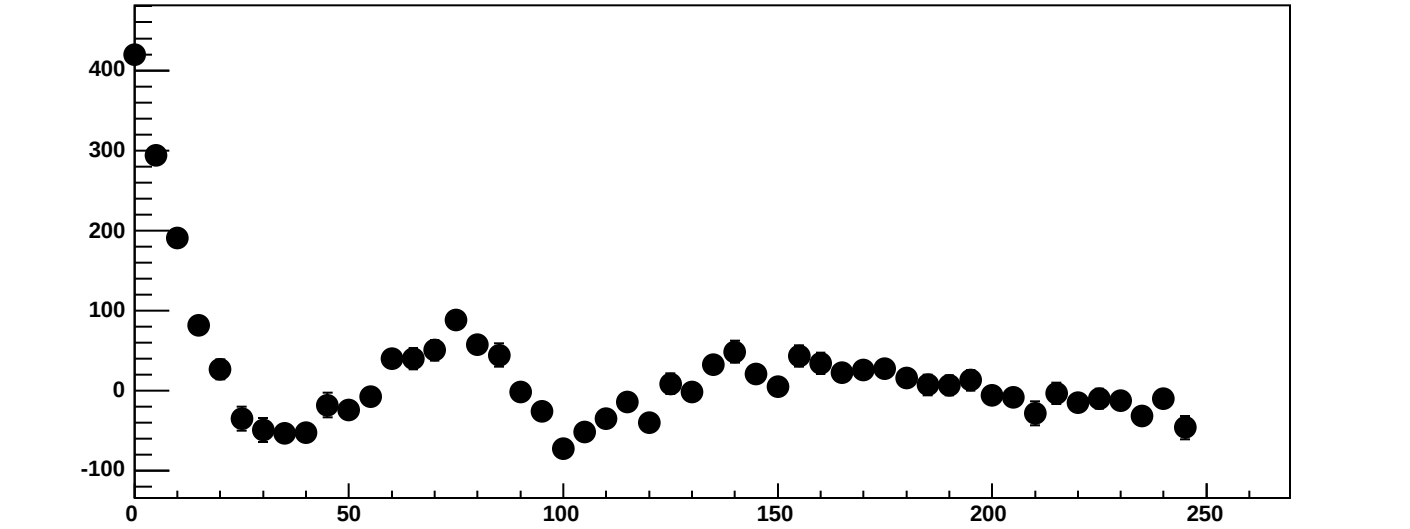
Chip 7, Channel 16, Enable 3, DAC=1600, ADC Mean vs Hold



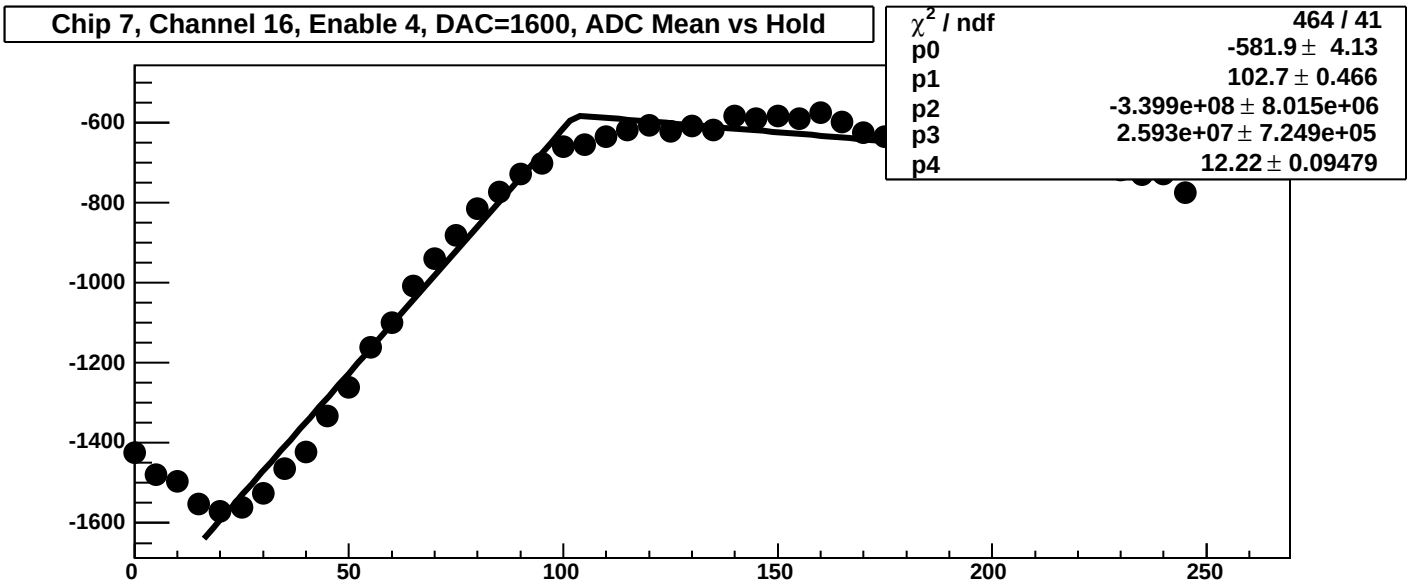
Chip 7, Channel 16, Enable 3, DAC=1600, ADC Noise vs Hold



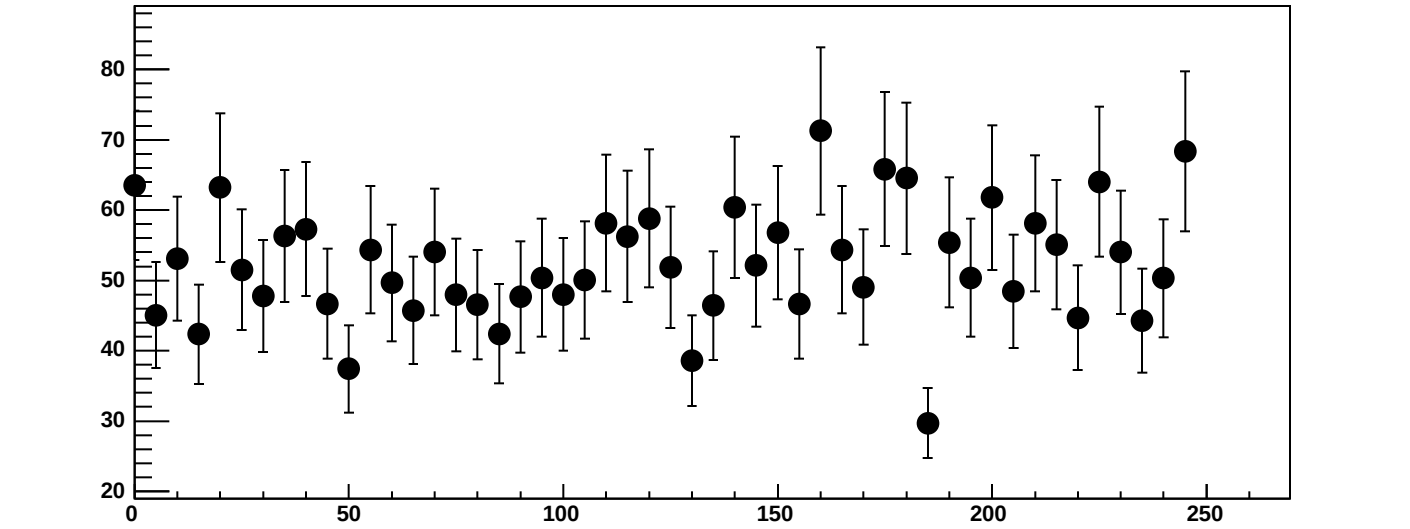
Chip 7, Channel 16, Enable 3, DAC=1600, ADC Residuals vs Hold



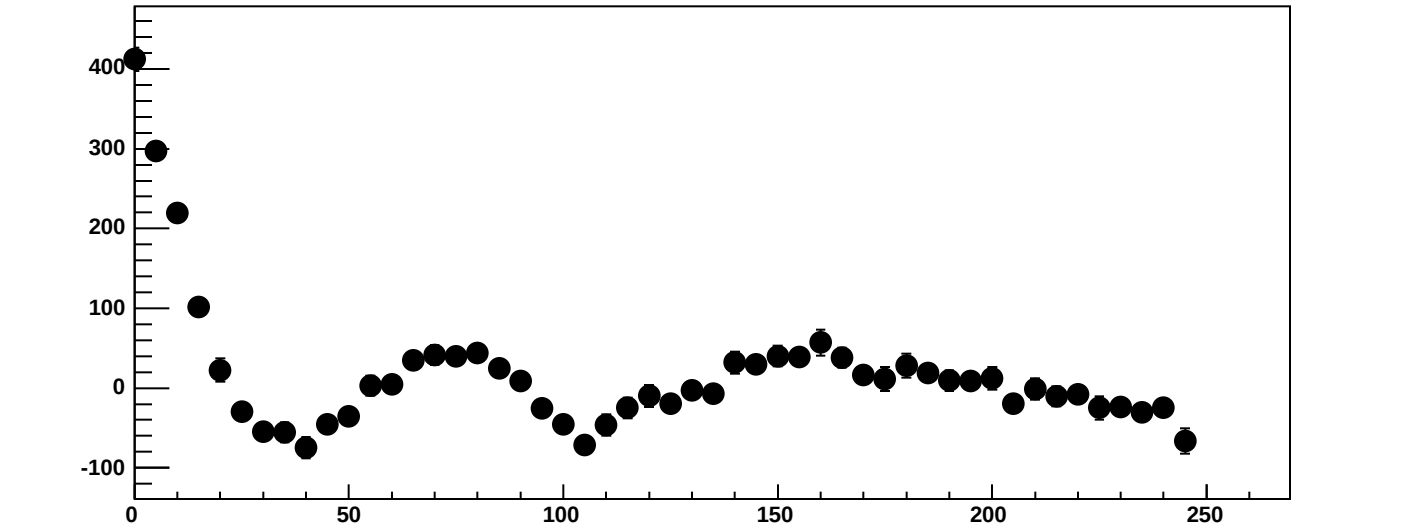
Chip 7, Channel 16, Enable 4, DAC=1600, ADC Mean vs Hold

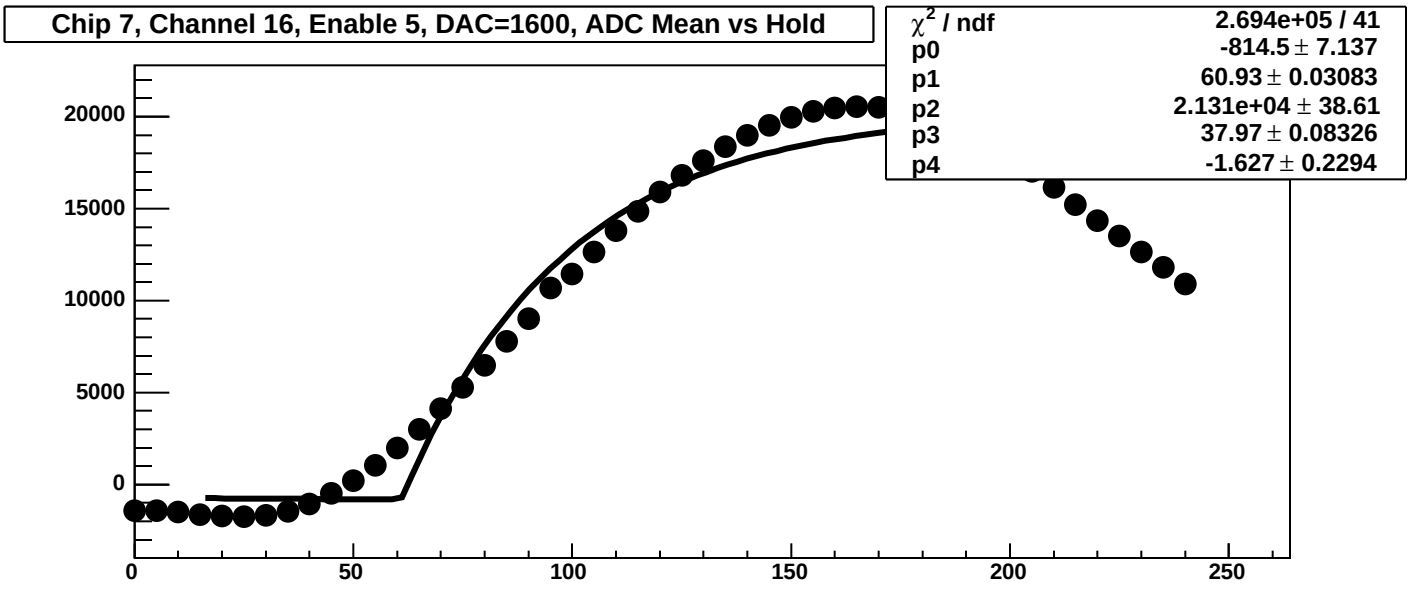


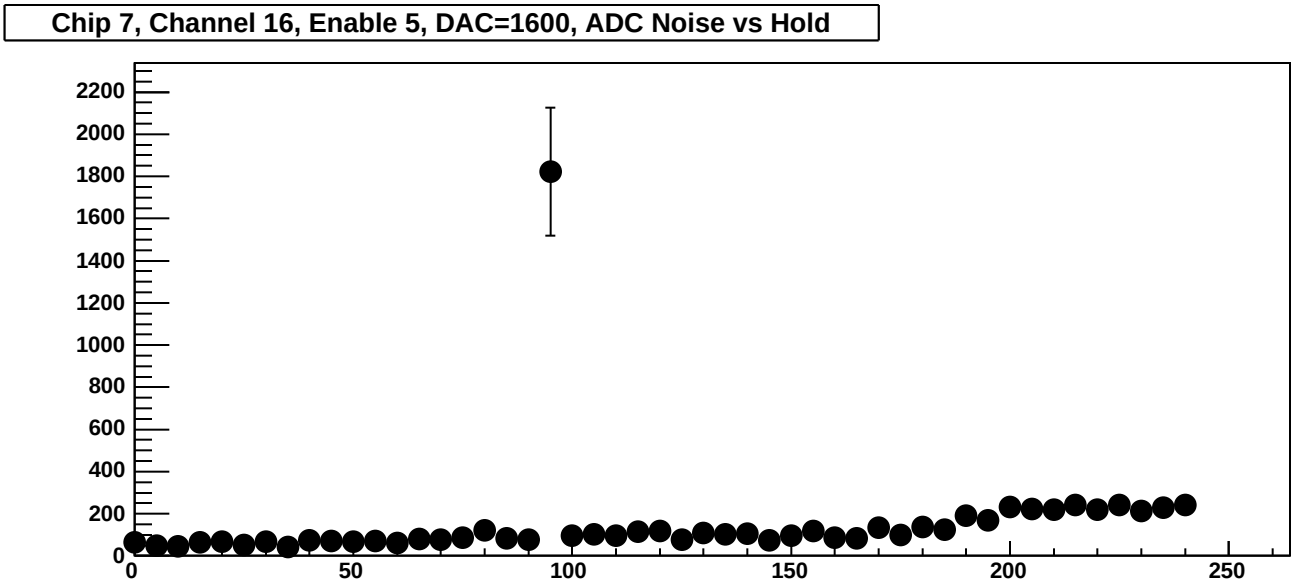
Chip 7, Channel 16, Enable 4, DAC=1600, ADC Noise vs Hold

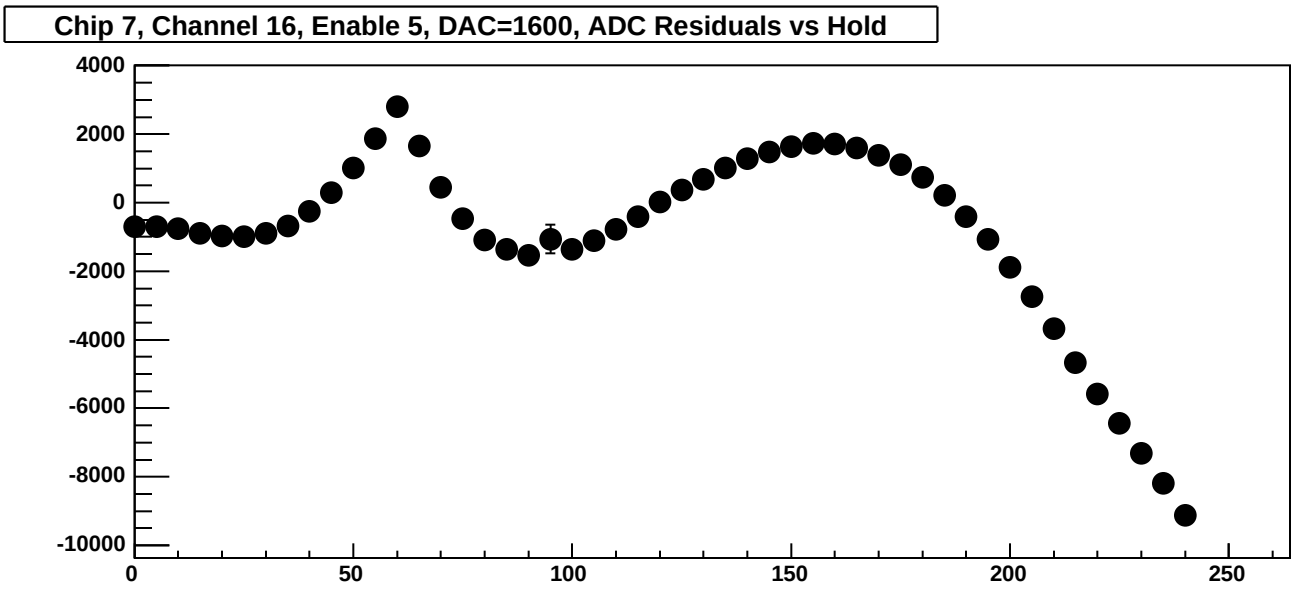


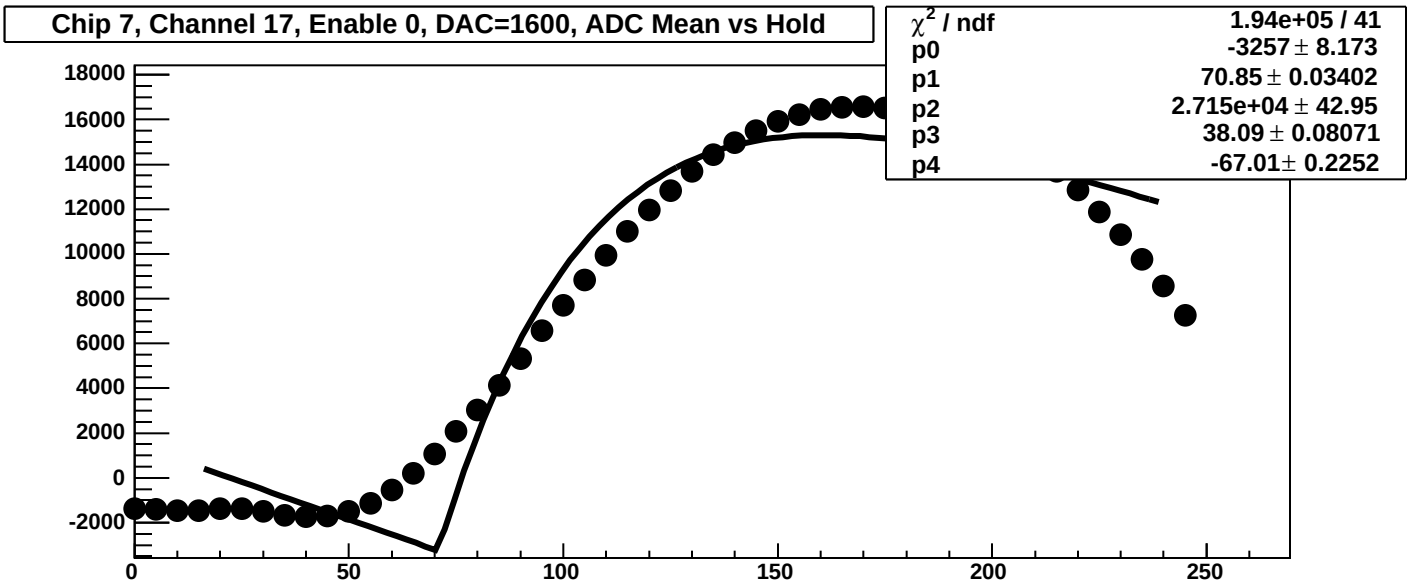
Chip 7, Channel 16, Enable 4, DAC=1600, ADC Residuals vs Hold

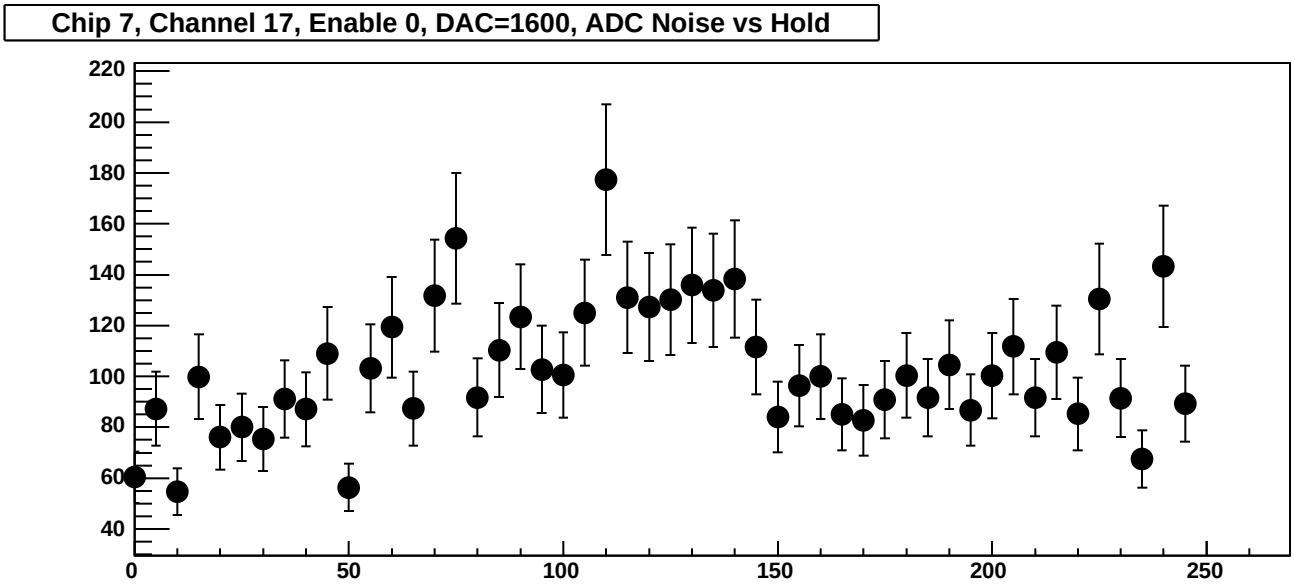


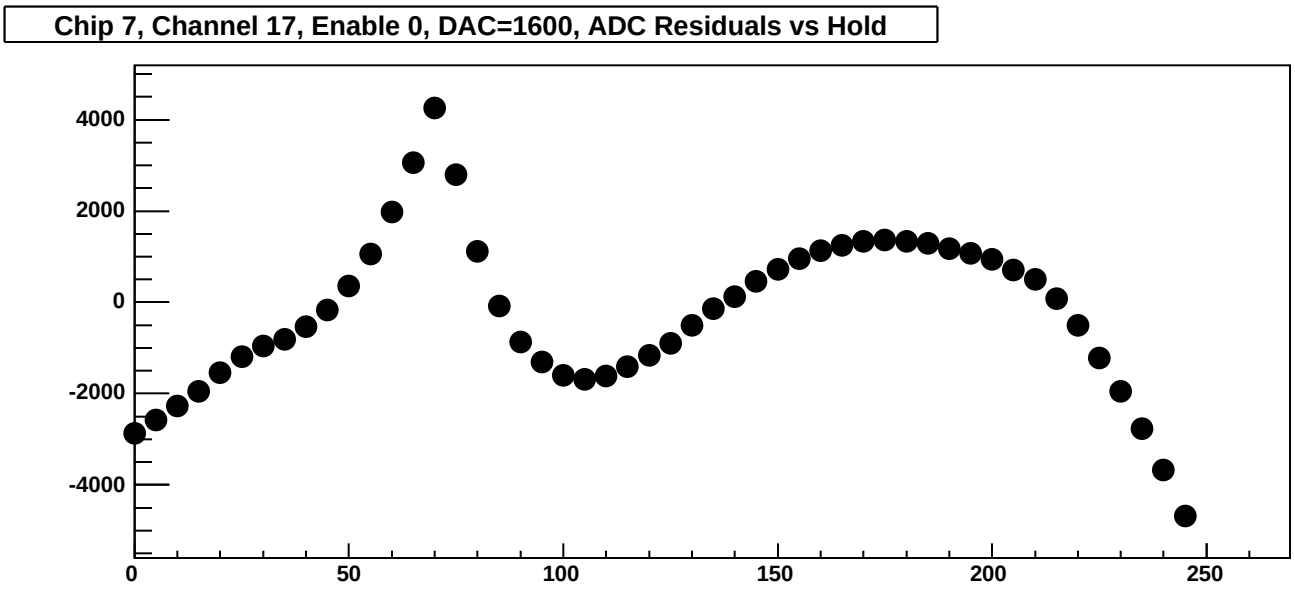

Chip 7, Channel 16, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.694e+05 / 41
p0 -814.5 ± 7.137
p1 60.93 ± 0.03083
p2 2.131e+04 ± 38.61
p3 37.97 ± 0.08326
p4 -1.627 ± 0.2294
20000
15000
10000
5000
0
0
50
100
150
200
250

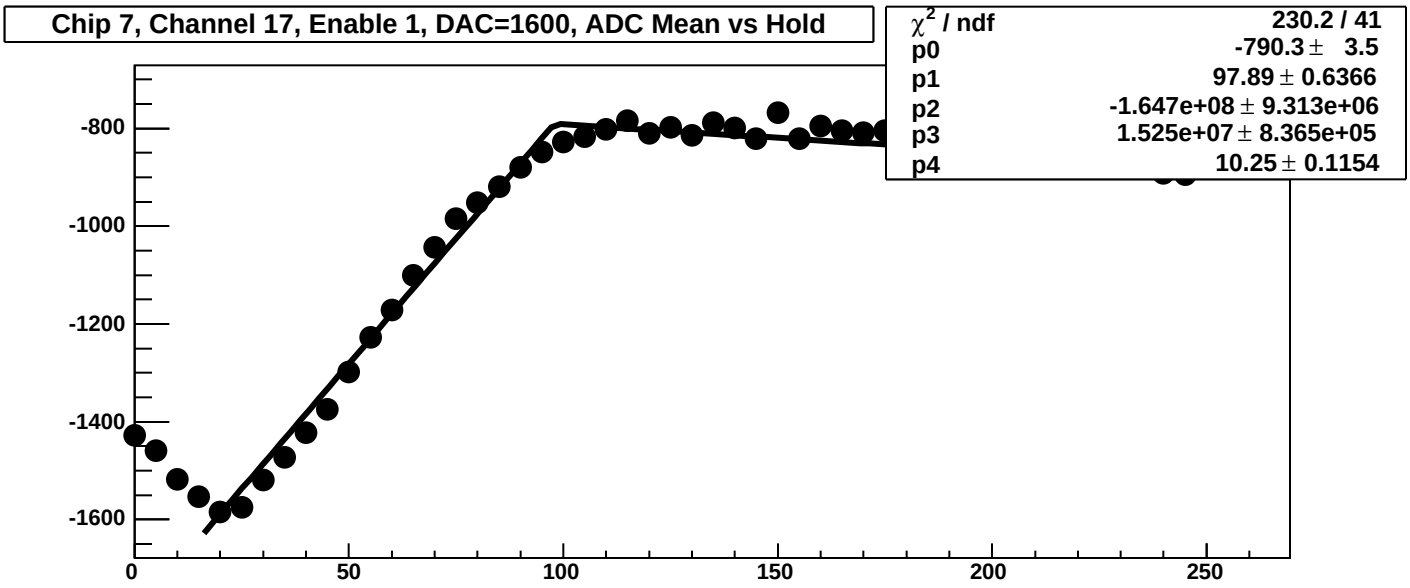

Chip 7, Channel 16, Enable 5, DAC=1600, ADC Noise vs Hold
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

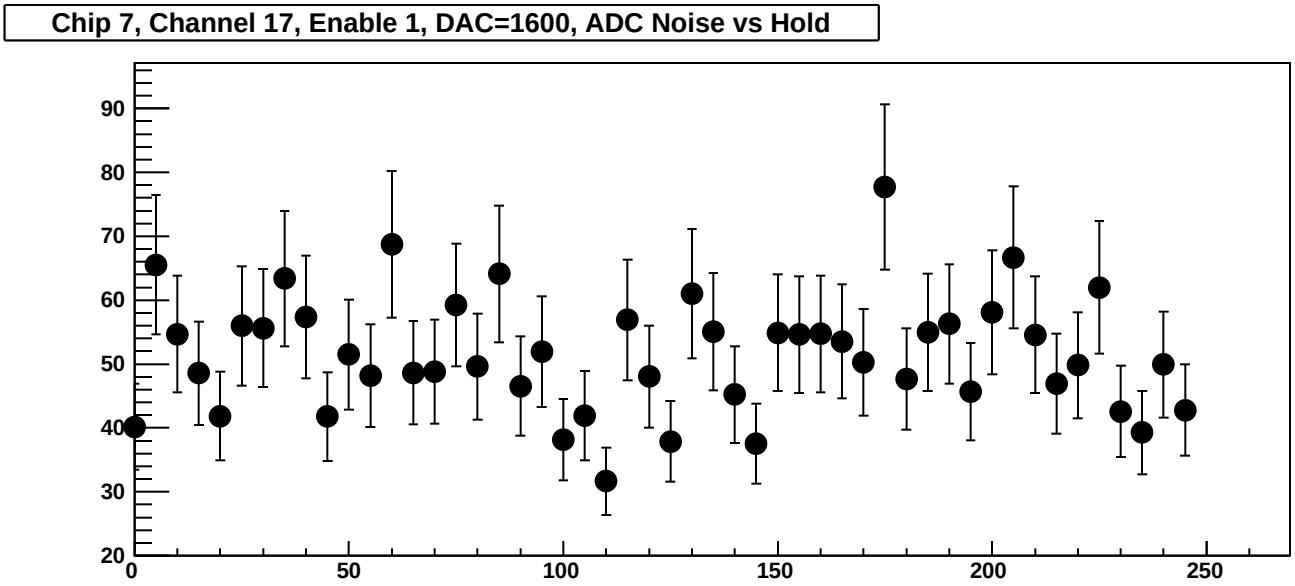

Chip 7, Channel 16, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

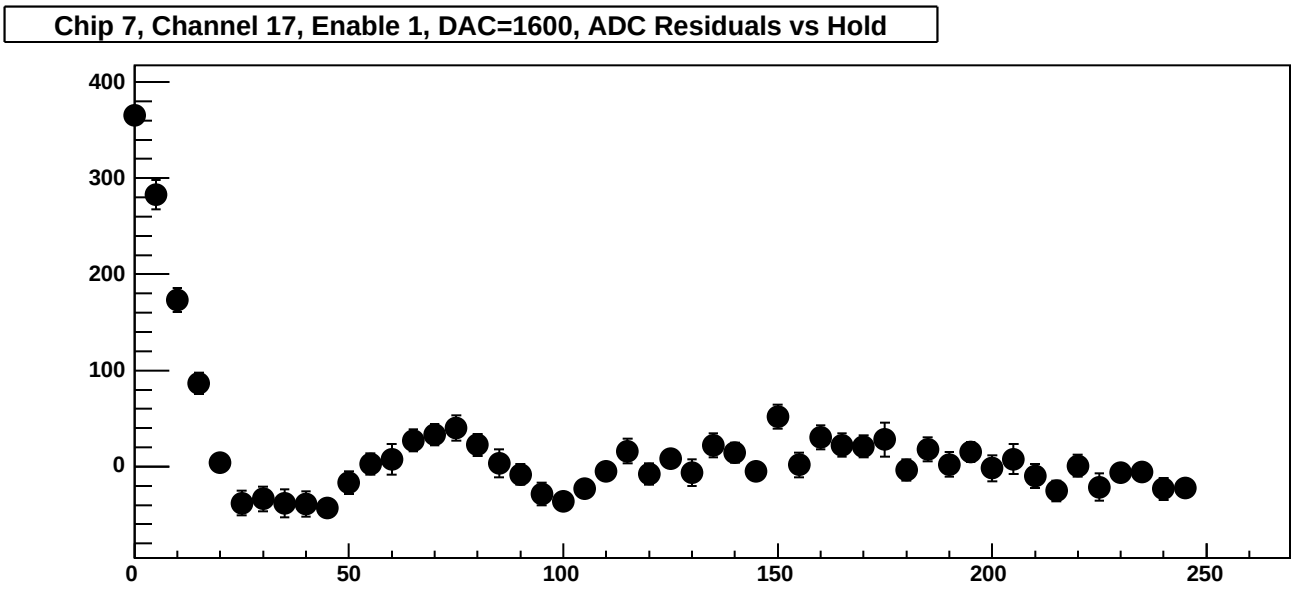

Chip 7, Channel 17, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.94e+05 / 41
p0 -3257 ± 8.173
p1 70.85 ± 0.03402
p2 2.715e+04 ± 42.95
p3 38.09 ± 0.08071
p4 -67.01 ± 0.2252
18000
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250


Chip 7, Channel 17, Enable 0, DAC=1600, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250

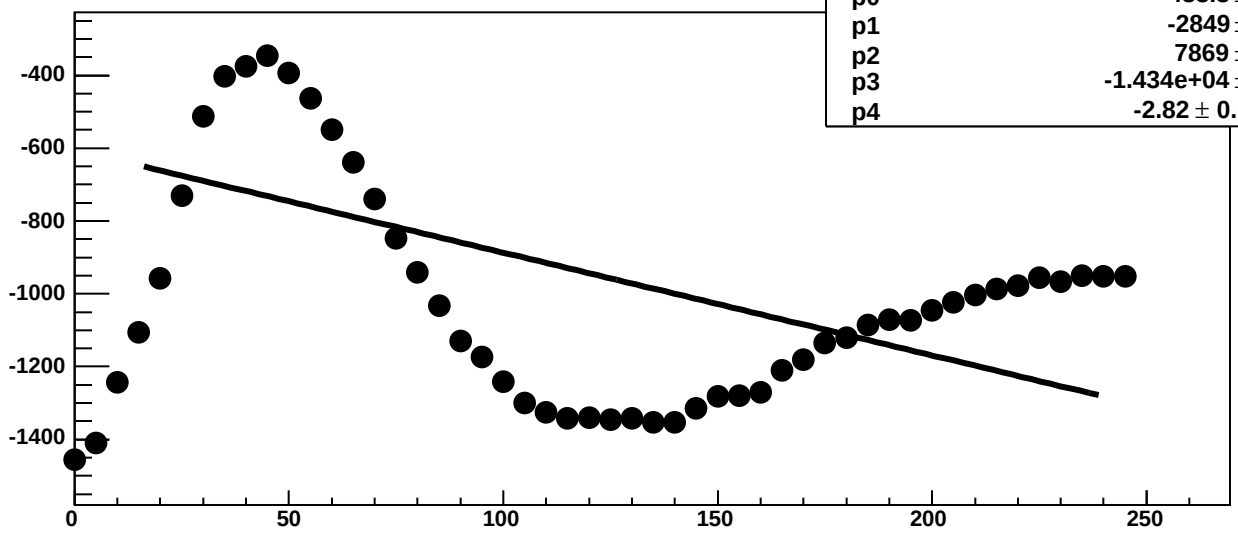

Chip 7, Channel 17, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
0
50
100
150
200
250


Chip 7, Channel 17, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 230.2 / 41
p0 -790.3 ± 3.5
p1 97.89 ± 0.6366
p2 -1.647e+08 ± 9.313e+06
p3 1.525e+07 ± 8.365e+05
p4 10.25 ± 0.1154
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250

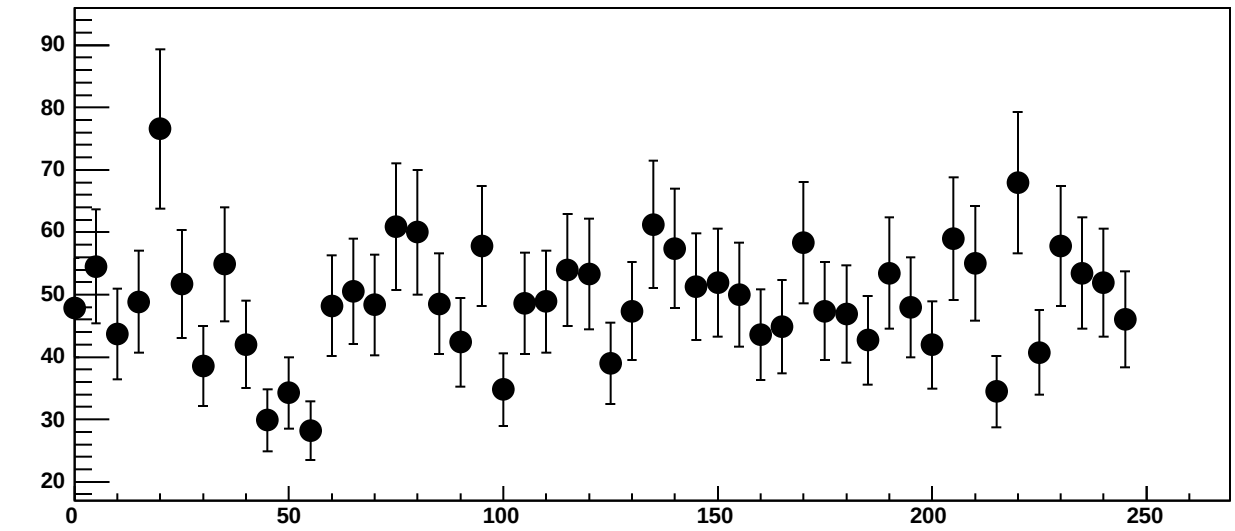

Chip 7, Channel 17, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 7, Channel 17, Enable 1, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
0
50
100
150
200
250

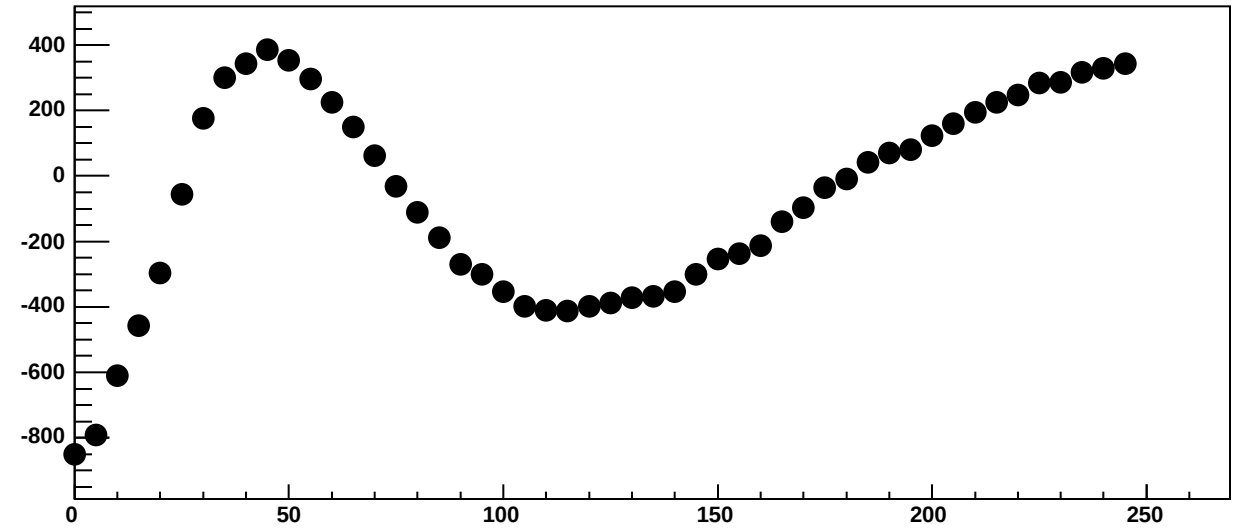
Chip 7, Channel 17, Enable 2, DAC=1600, ADC Mean vs Hold



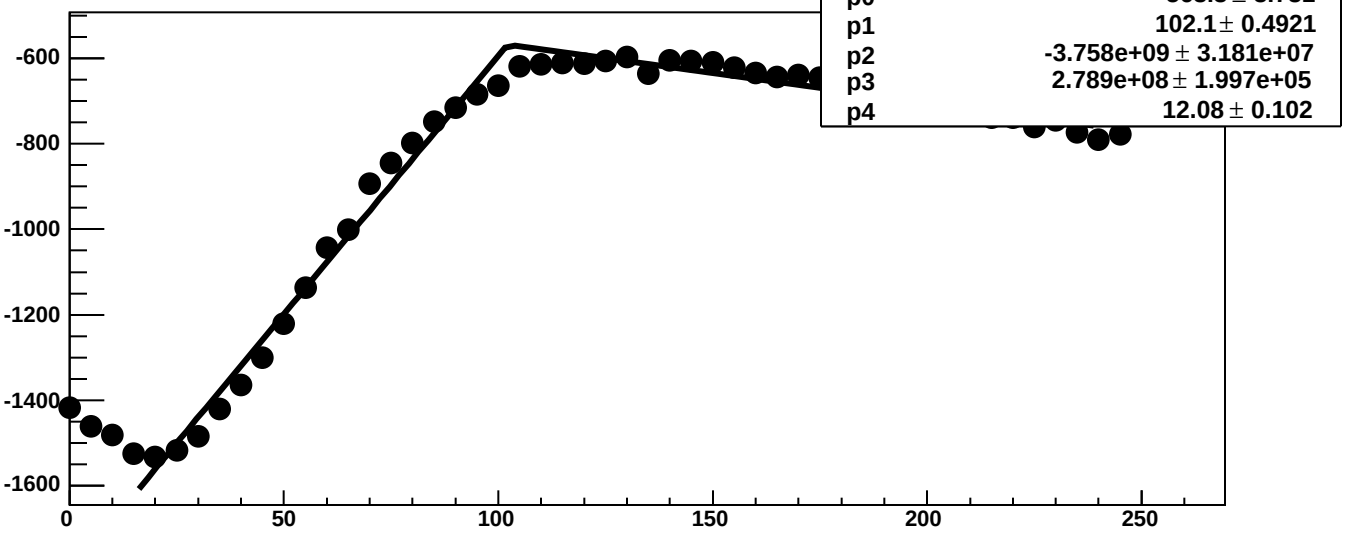
Chip 7, Channel 17, Enable 2, DAC=1600, ADC Noise vs Hold



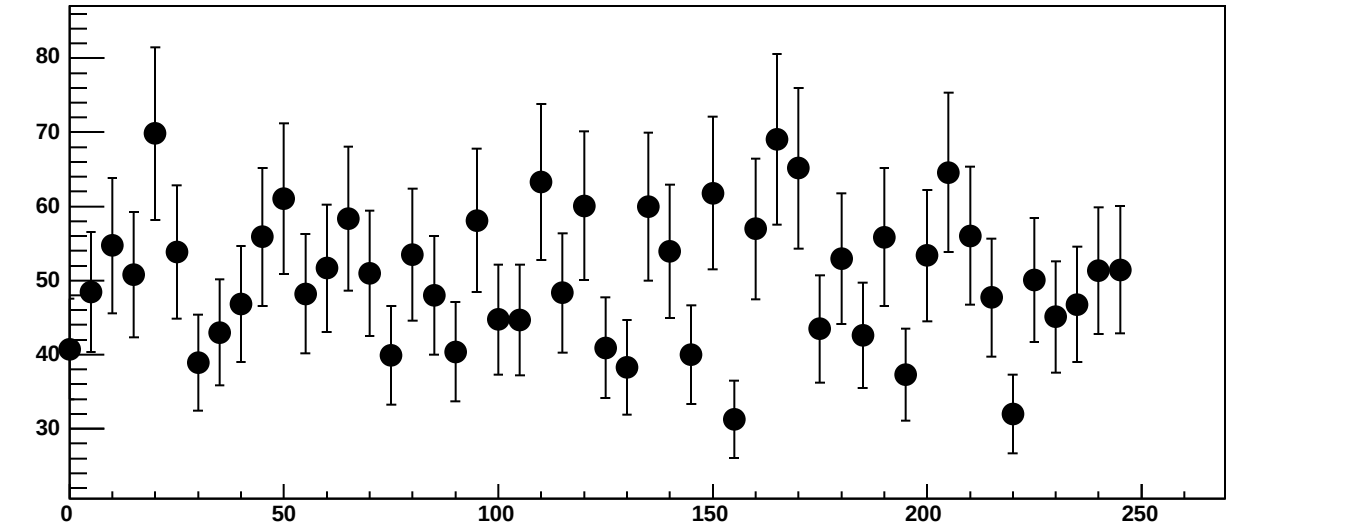
Chip 7, Channel 17, Enable 2, DAC=1600, ADC Residuals vs Hold



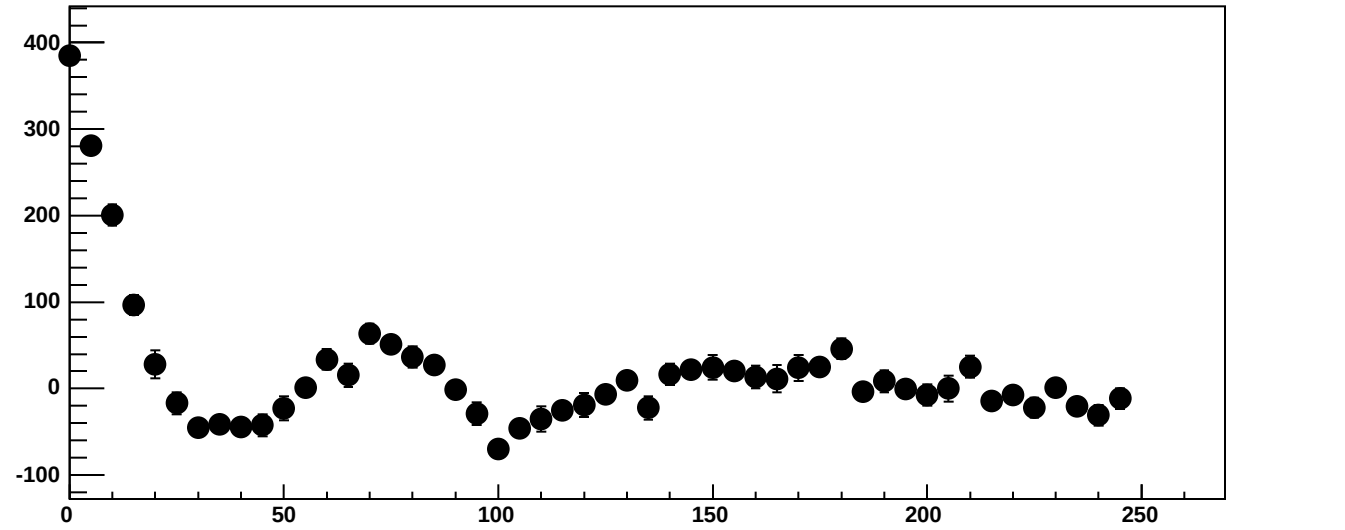
Chip 7, Channel 17, Enable 3, DAC=1600, ADC Mean vs Hold



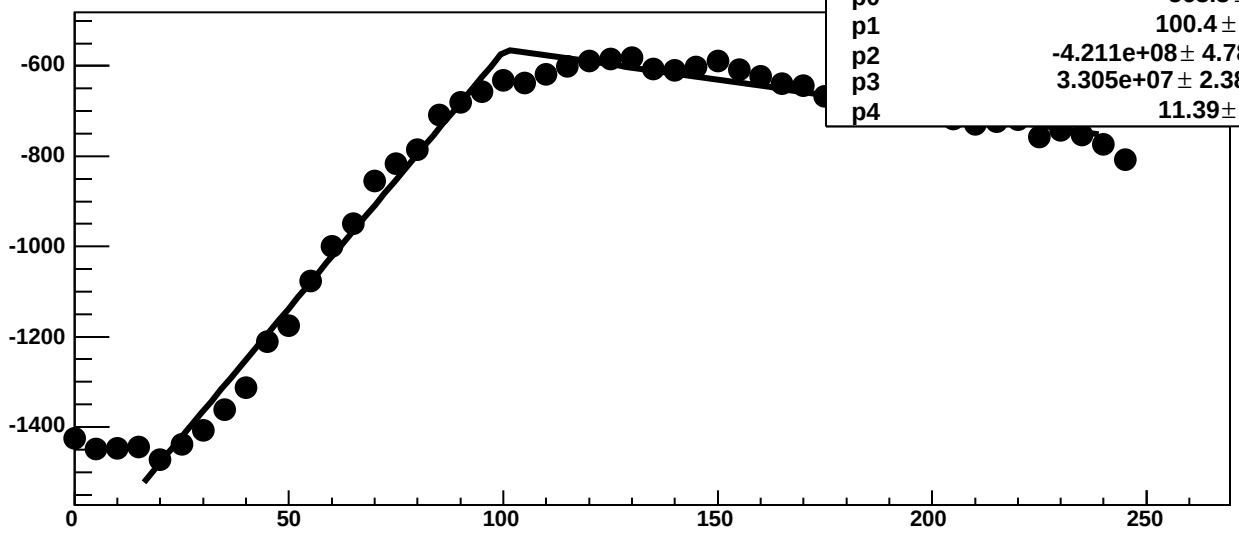
Chip 7, Channel 17, Enable 3, DAC=1600, ADC Noise vs Hold



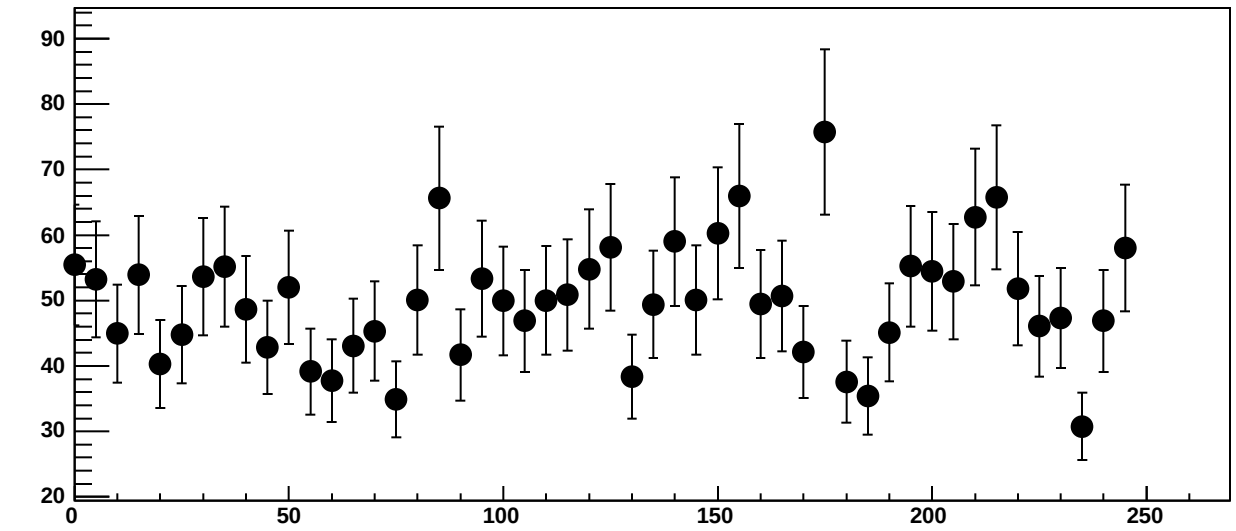
Chip 7, Channel 17, Enable 3, DAC=1600, ADC Residuals vs Hold



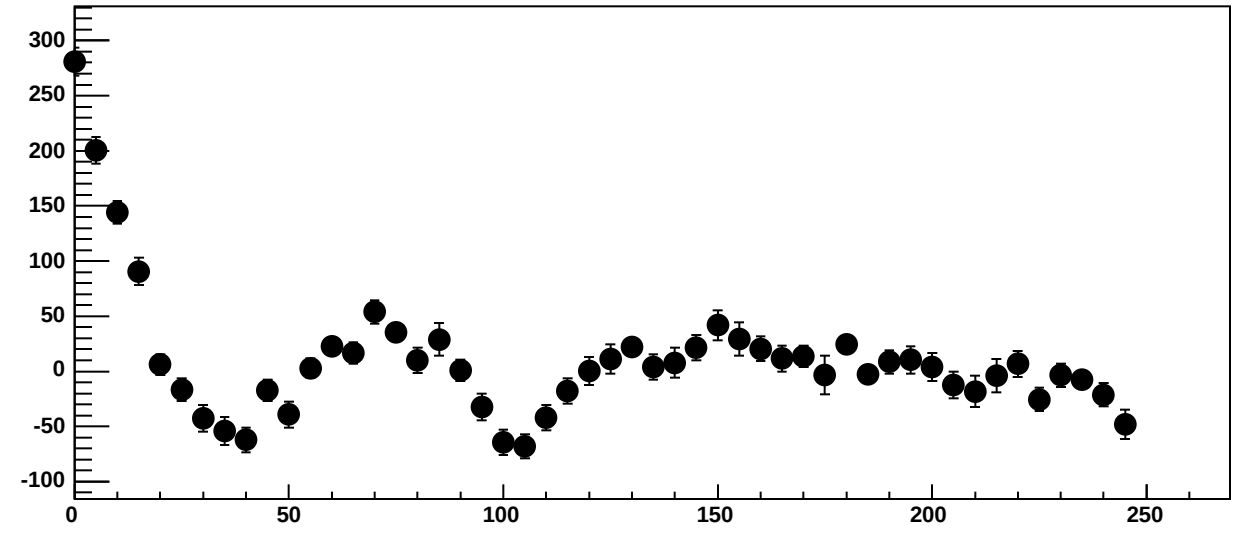
Chip 7, Channel 17, Enable 4, DAC=1600, ADC Mean vs Hold

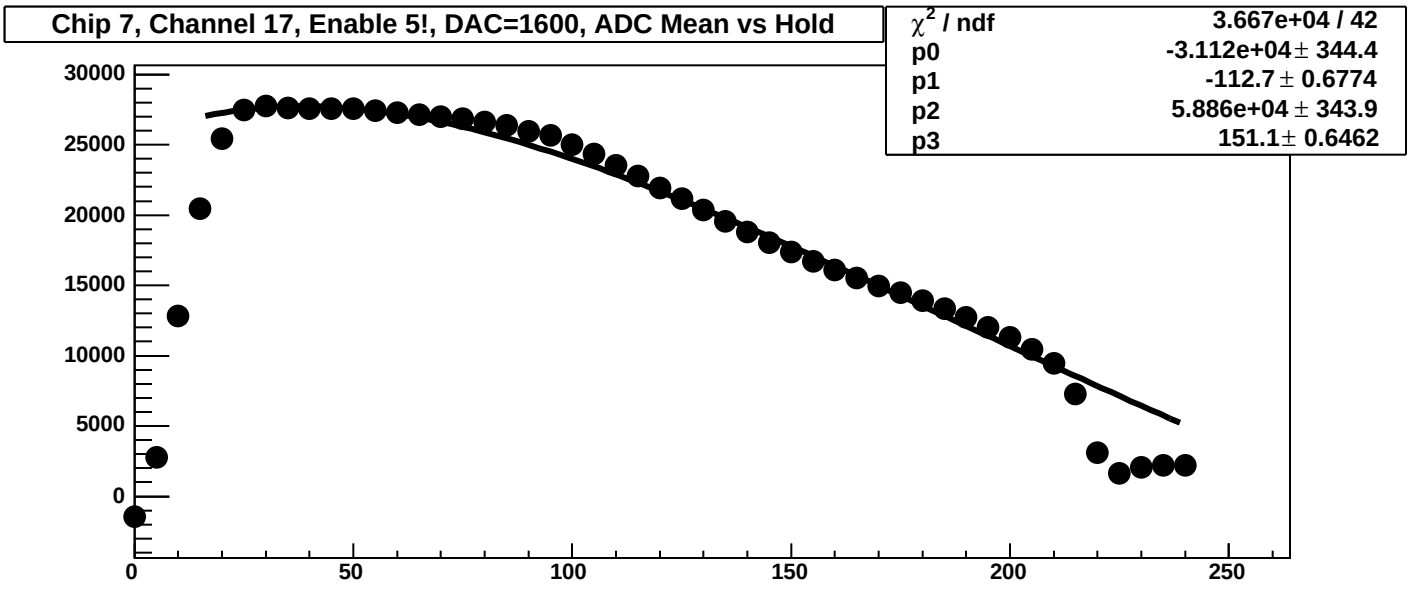


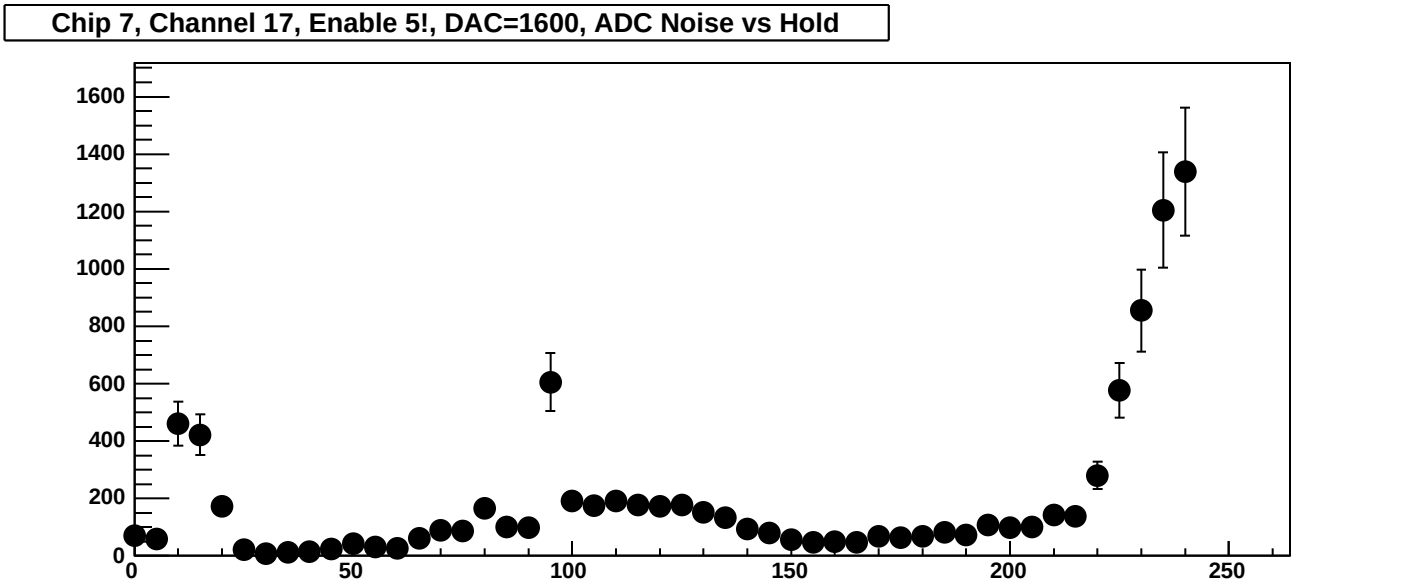
Chip 7, Channel 17, Enable 4, DAC=1600, ADC Noise vs Hold

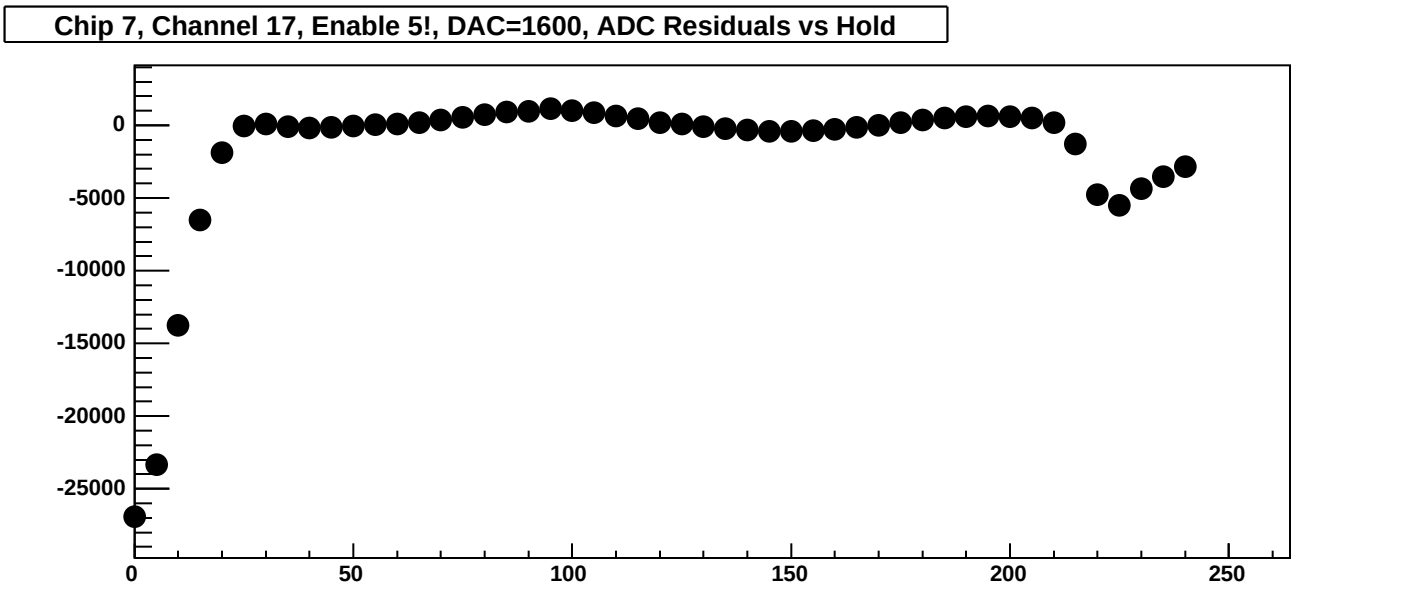


Chip 7, Channel 17, Enable 4, DAC=1600, ADC Residuals vs Hold

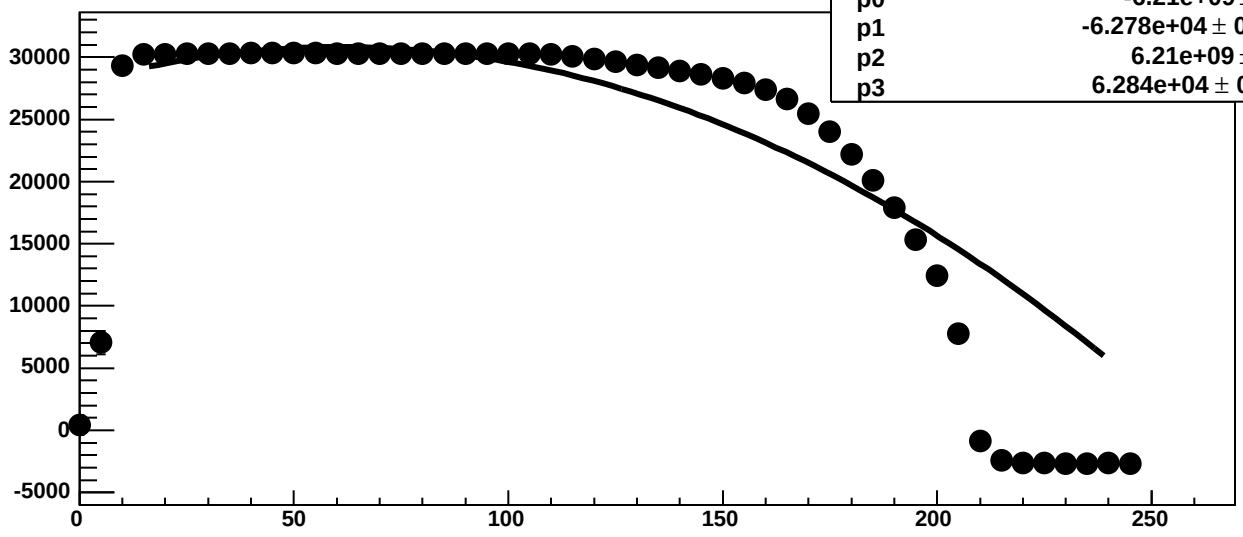



Chip 7, Channel 17, Enable 5!, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.667e+04 / 42
p0 -3.112e+04 ± 344.4
p1 -112.7 ± 0.6774
p2 5.886e+04 ± 343.9
p3 151.1 ± 0.6462
30000
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

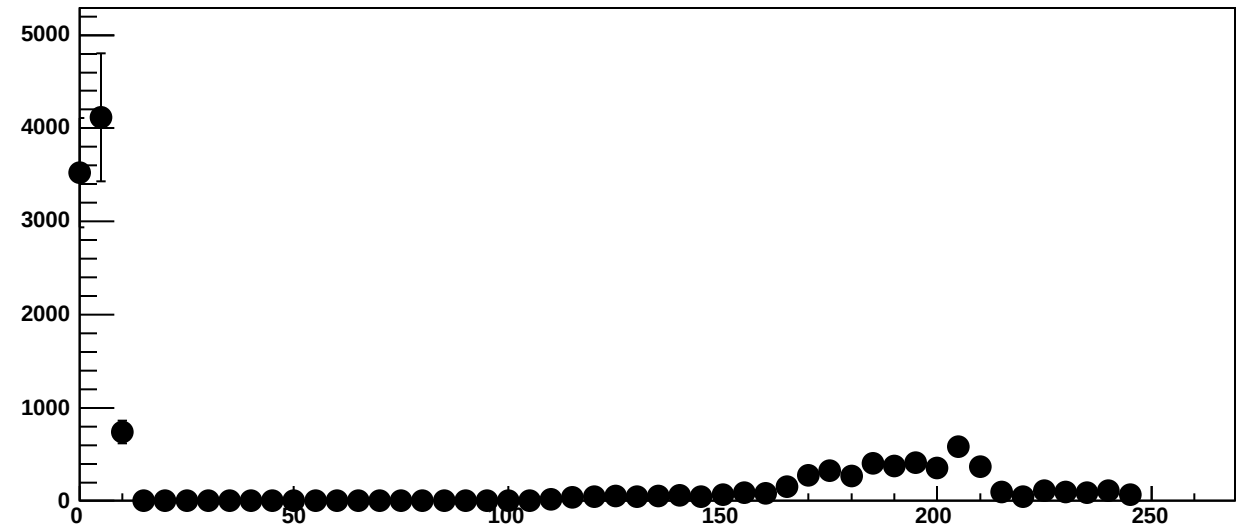

Chip 7, Channel 17, Enable 5!, DAC=1600, ADC Noise vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 7, Channel 17, Enable 5!, DAC=1600, ADC Residuals vs Hold
0
-5000
-10000
-15000
-20000
-25000
0
50
100
150
200
250

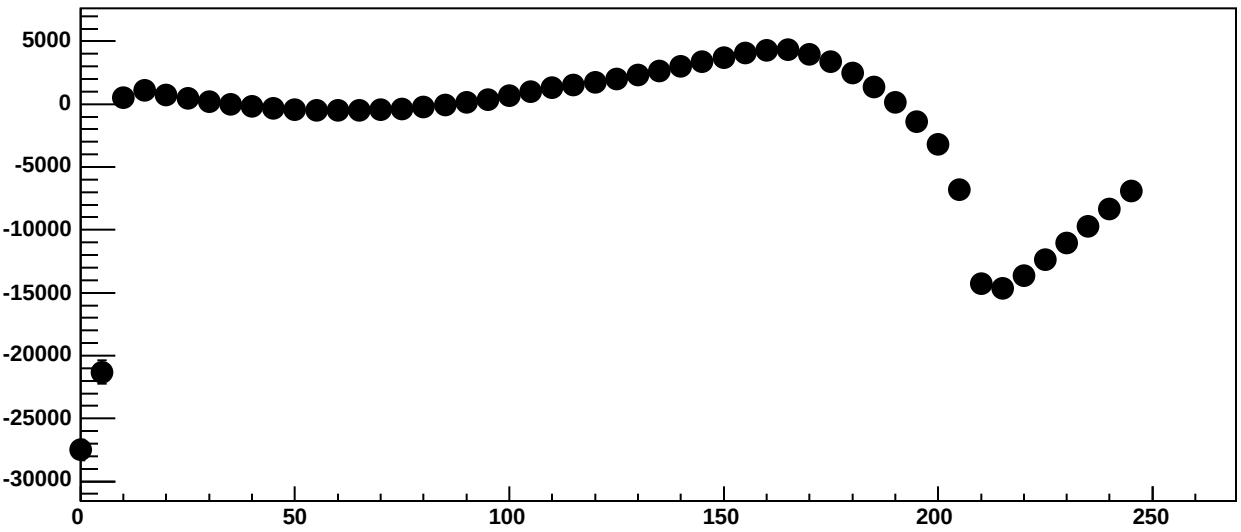
Chip 8, Channel 0, Enable 0!, DAC=1600, ADC Mean vs Hold



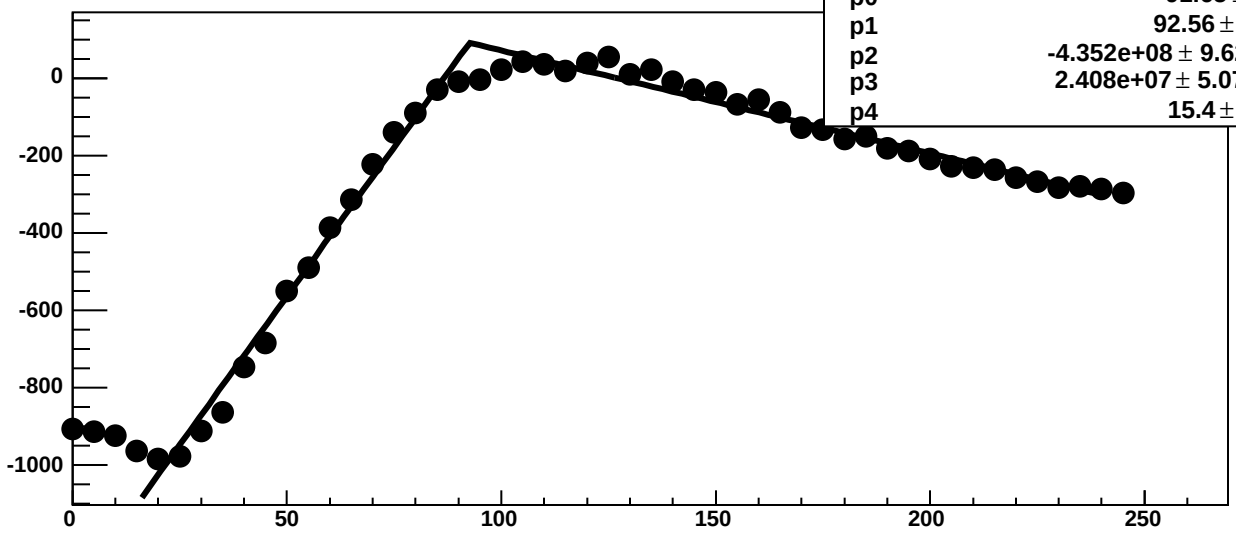
Chip 8, Channel 0, Enable 0!, DAC=1600, ADC Noise vs Hold



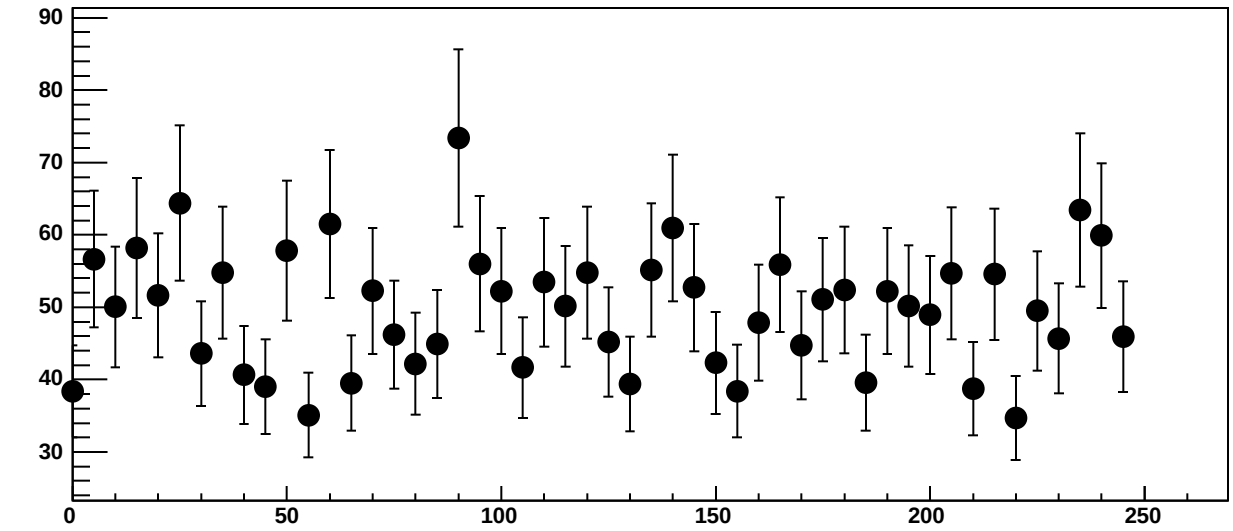
Chip 8, Channel 0, Enable 0!, DAC=1600, ADC Residuals vs Hold



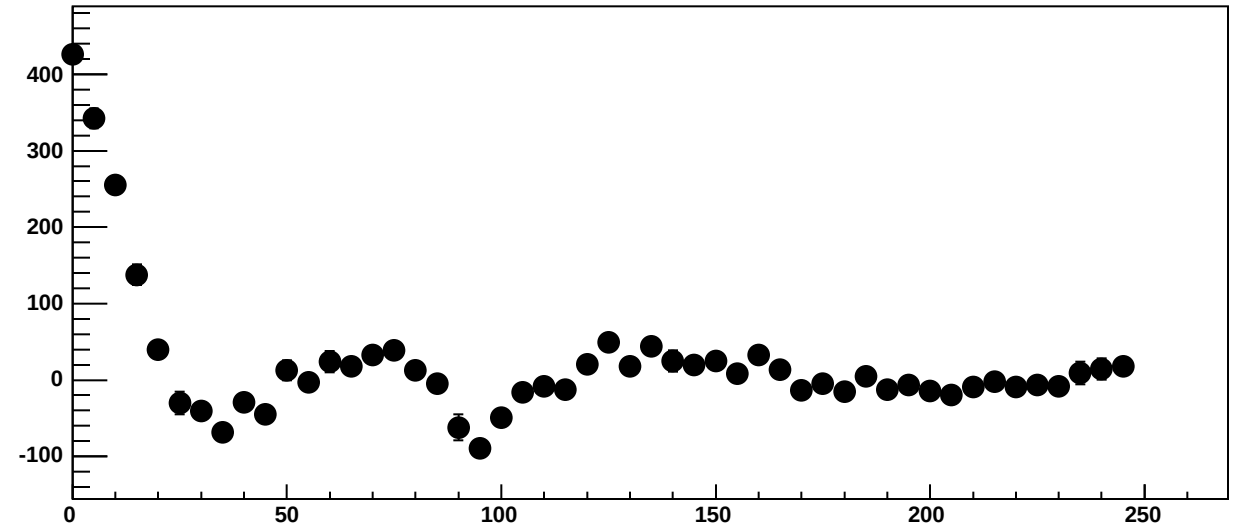
Chip 8, Channel 0, Enable 1, DAC=1600, ADC Mean vs Hold

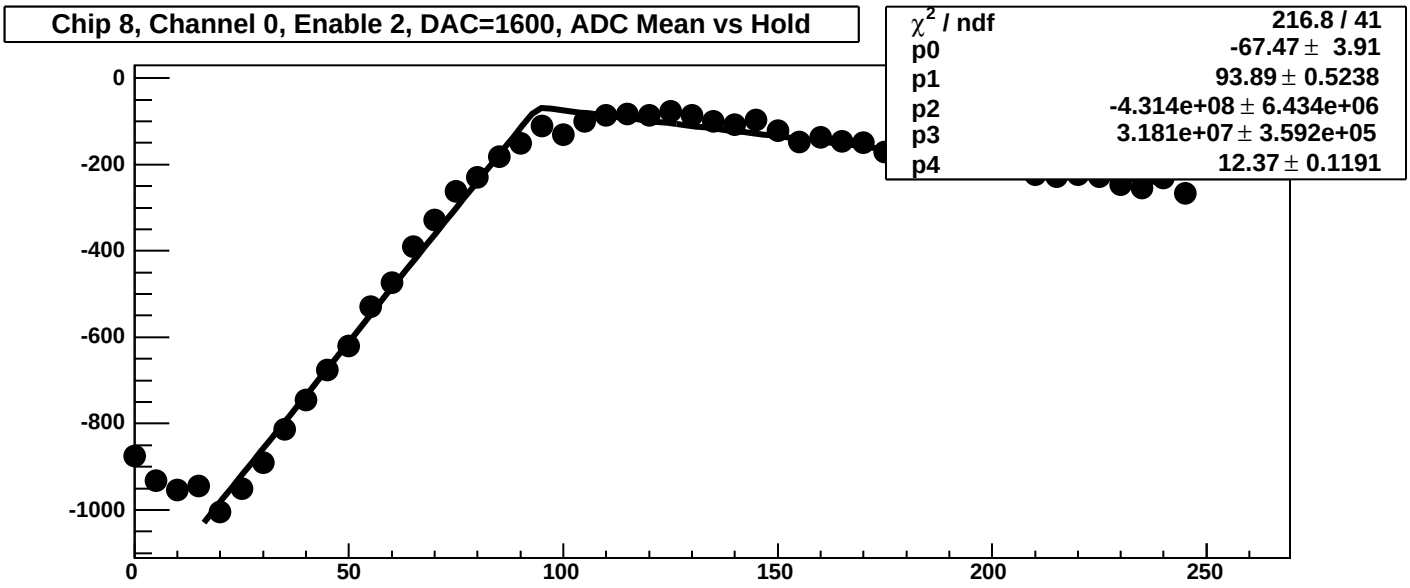


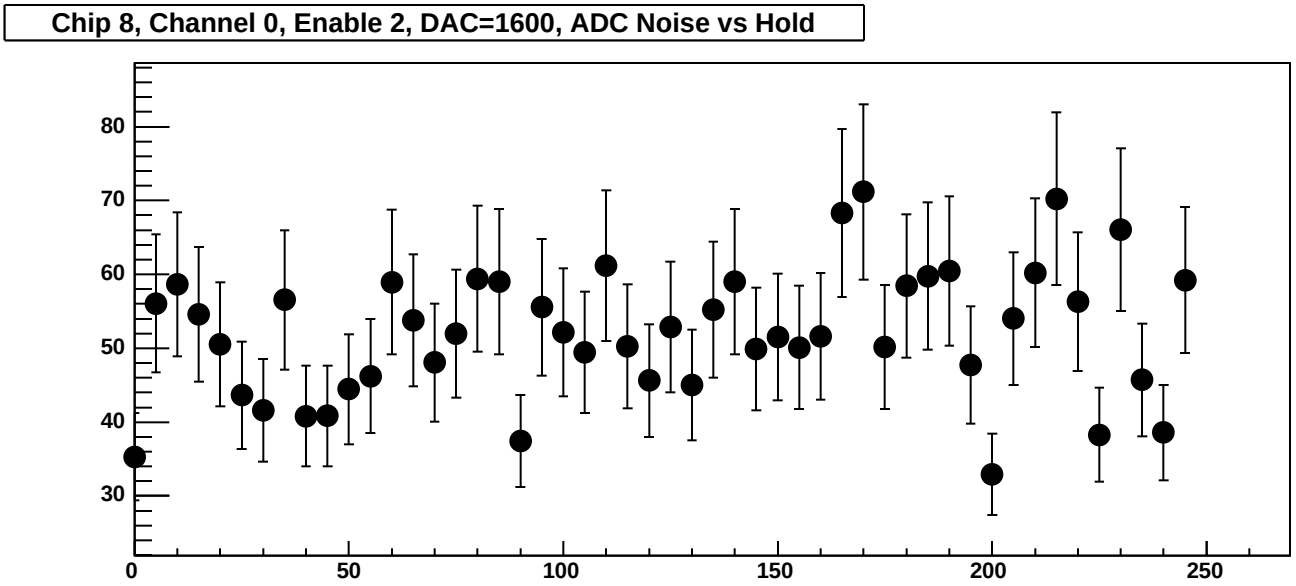
Chip 8, Channel 0, Enable 1, DAC=1600, ADC Noise vs Hold

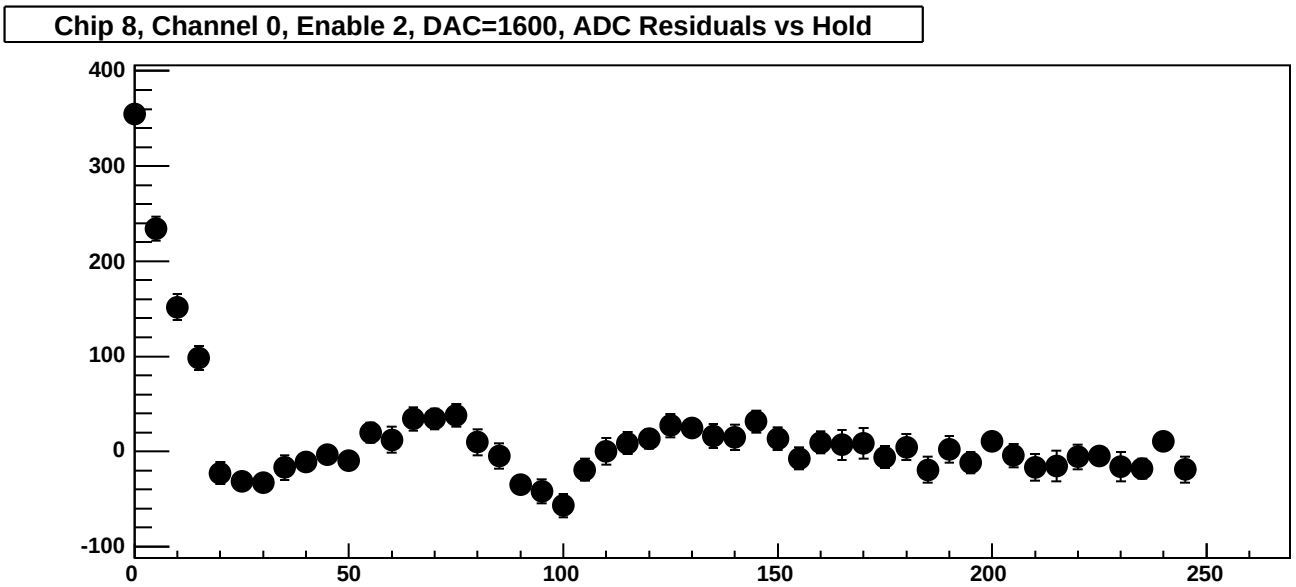


Chip 8, Channel 0, Enable 1, DAC=1600, ADC Residuals vs Hold

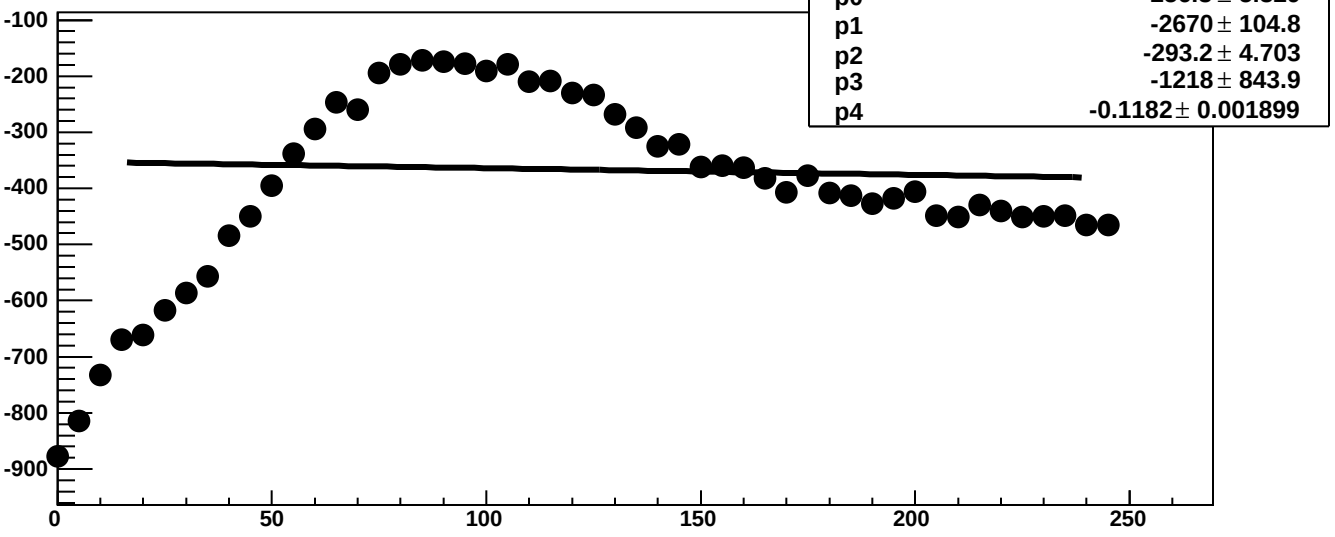



Chip 8, Channel 0, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 216.8 / 41
p0 -67.47 ± 3.91
p1 93.89 ± 0.5238
p2 -4.314e+08 ± 6.434e+06
p3 3.181e+07 ± 3.592e+05
p4 12.37 ± 0.1191
0
-200
-400
-600
-800
-1000
0
50
100
150
200
250

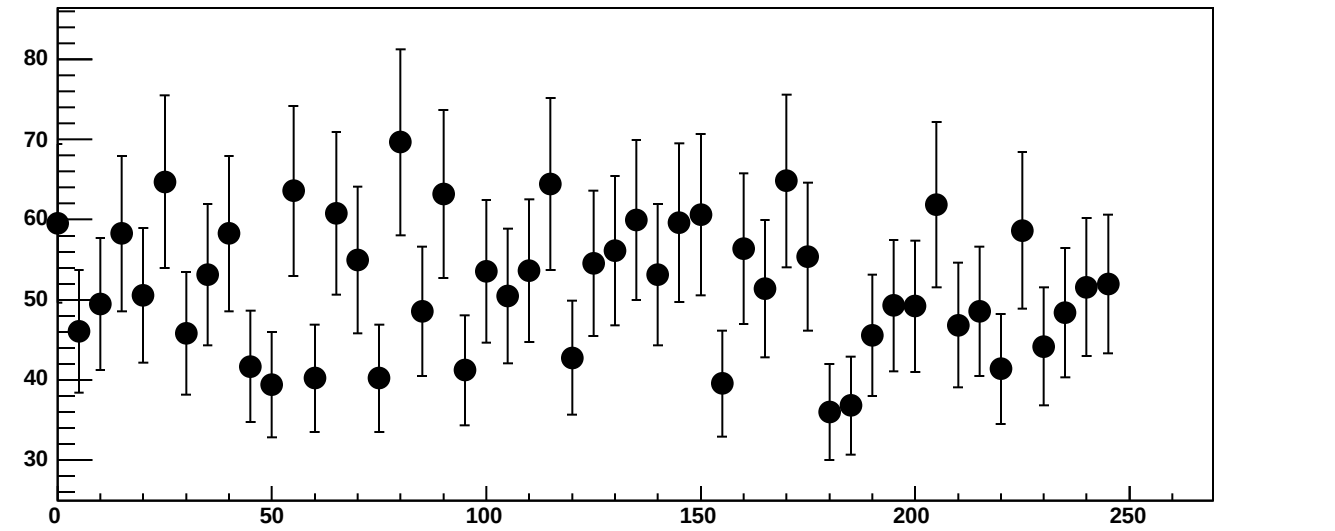

Chip 8, Channel 0, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 8, Channel 0, Enable 2, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

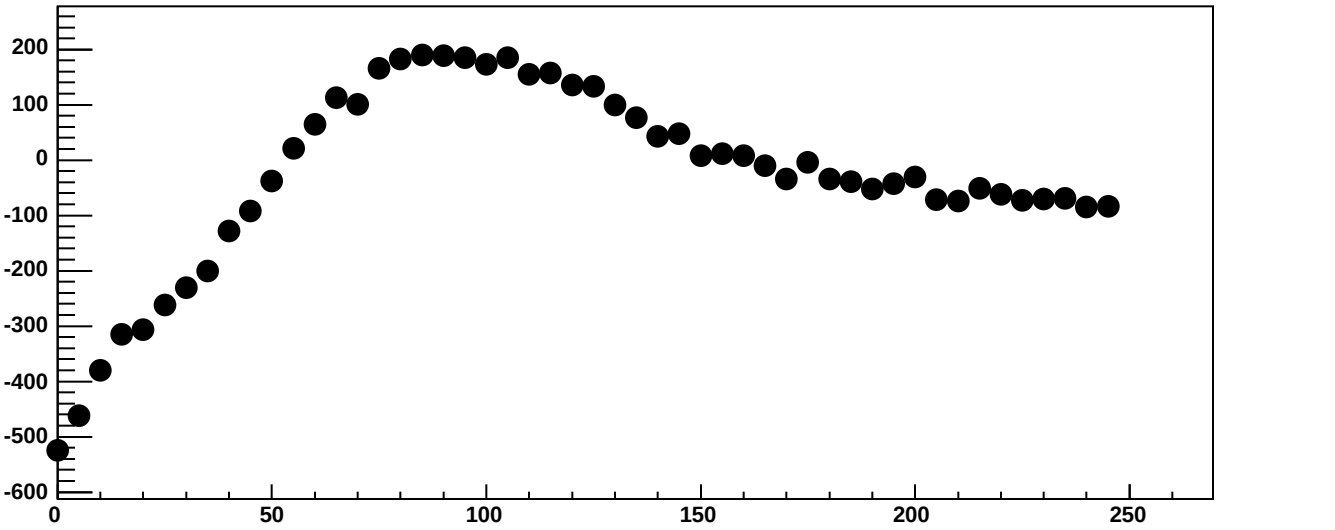
Chip 8, Channel 0, Enable 3, DAC=1600, ADC Mean vs Hold



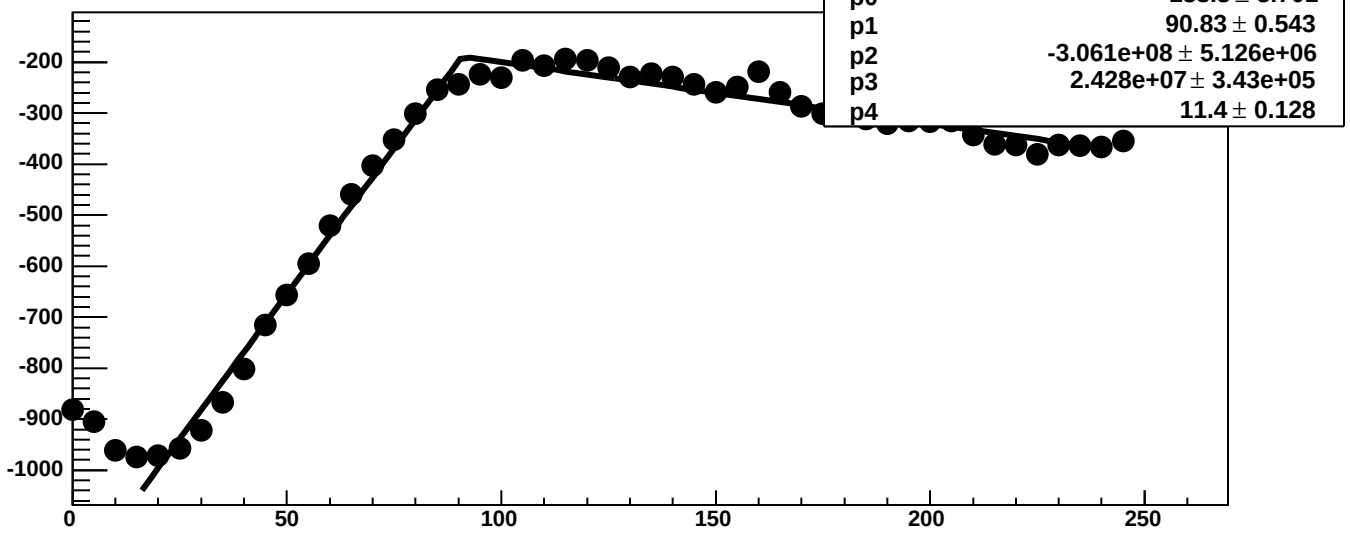
Chip 8, Channel 0, Enable 3, DAC=1600, ADC Noise vs Hold



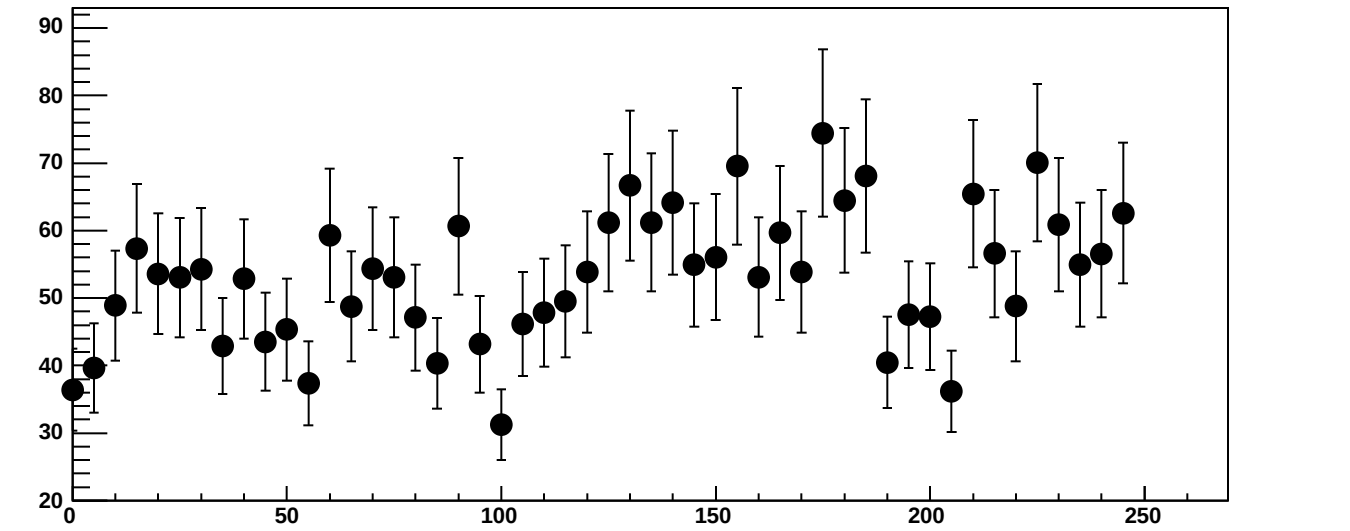
Chip 8, Channel 0, Enable 3, DAC=1600, ADC Residuals vs Hold



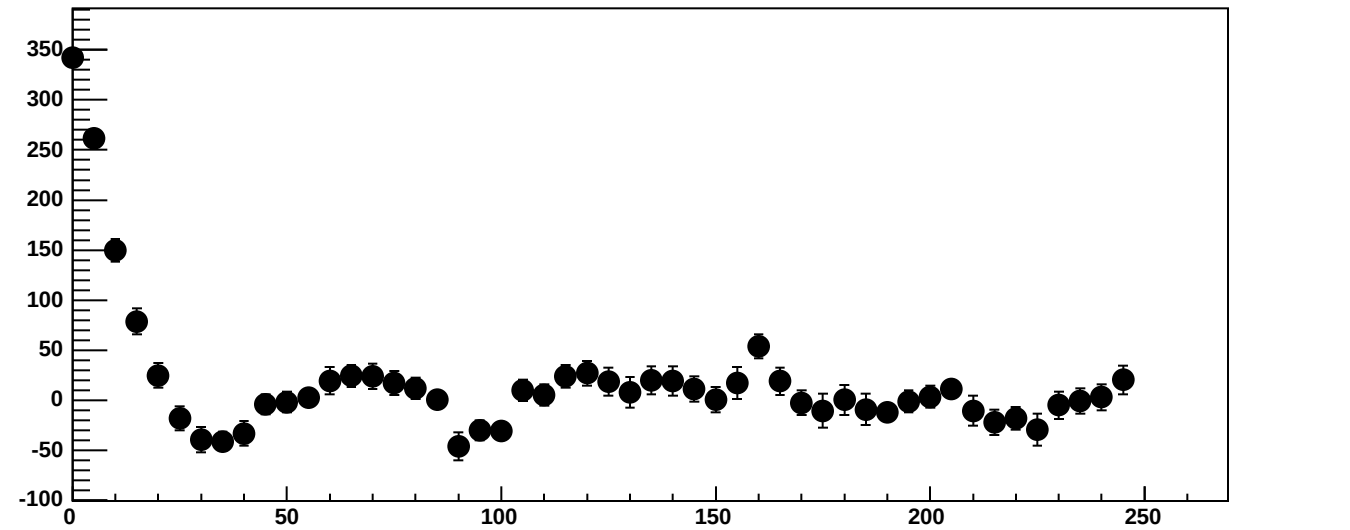
Chip 8, Channel 0, Enable 4, DAC=1600, ADC Mean vs Hold



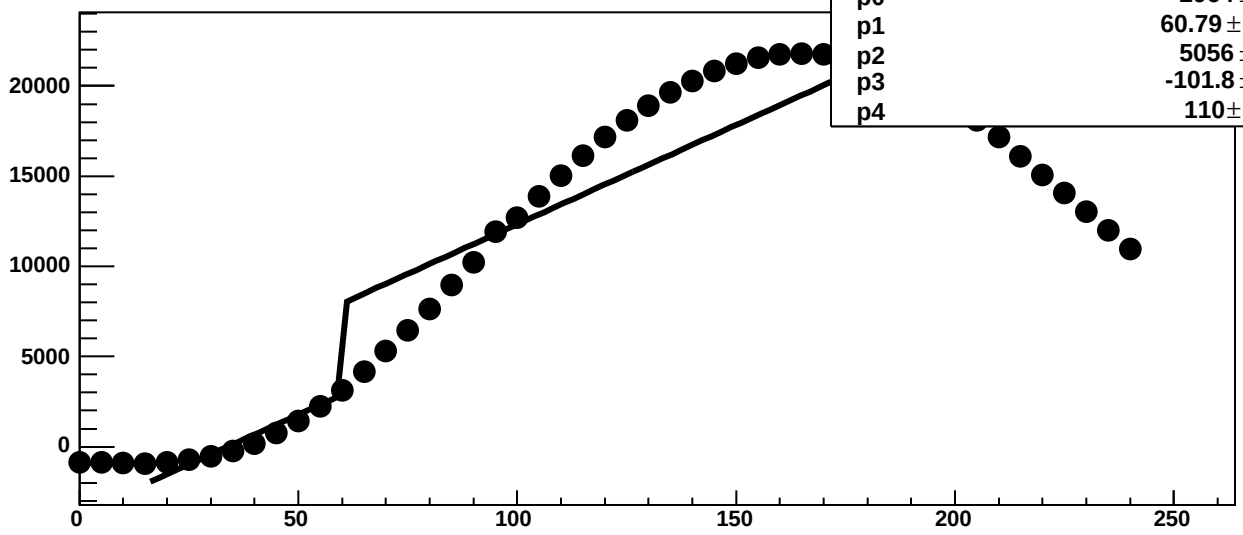
Chip 8, Channel 0, Enable 4, DAC=1600, ADC Noise vs Hold



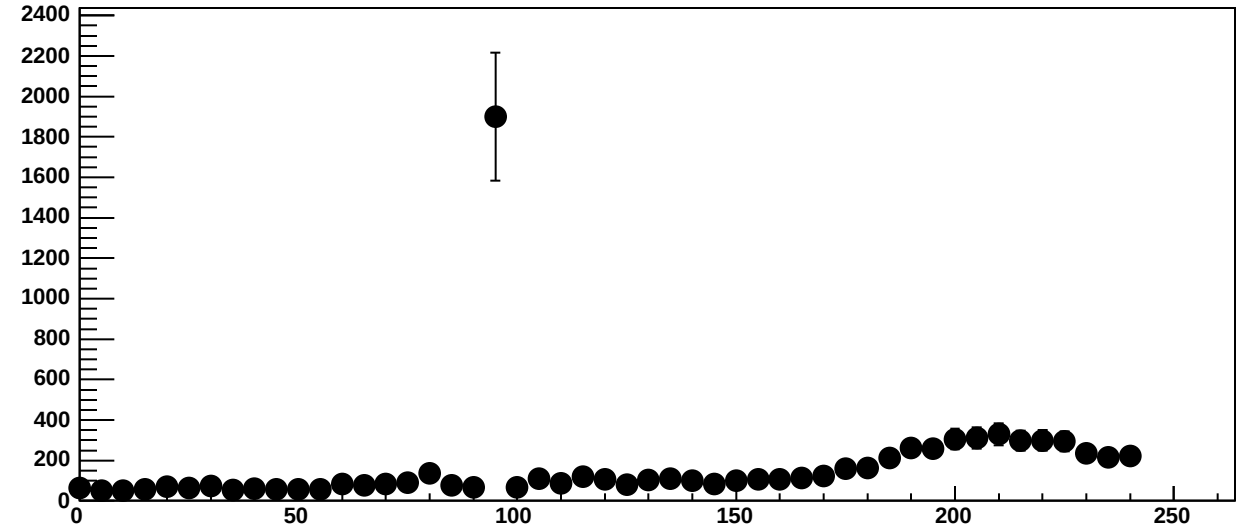
Chip 8, Channel 0, Enable 4, DAC=1600, ADC Residuals vs Hold



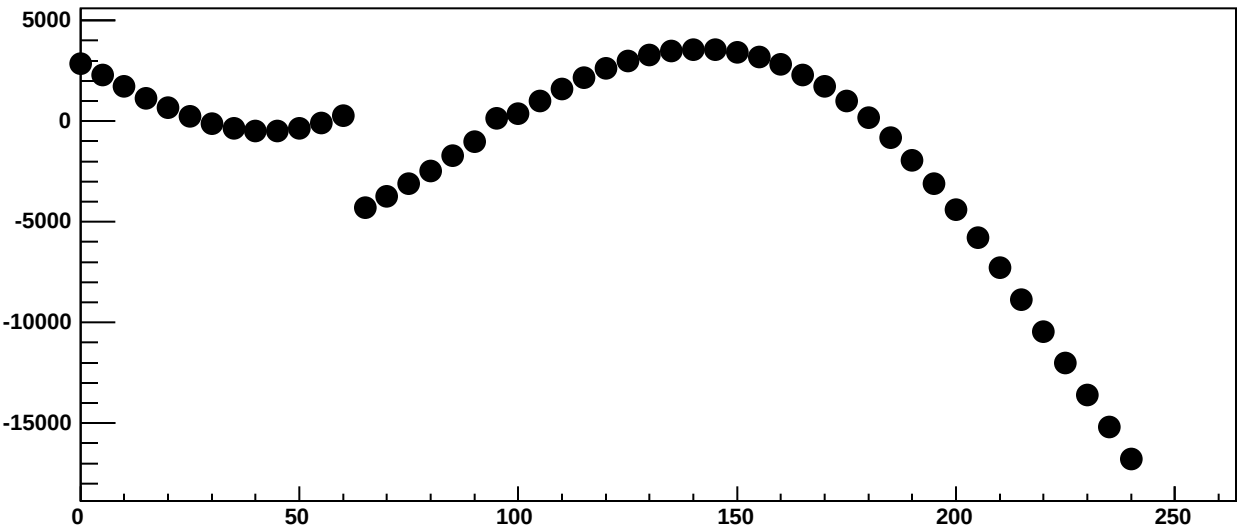
Chip 8, Channel 0, Enable 5, DAC=1600, ADC Mean vs Hold



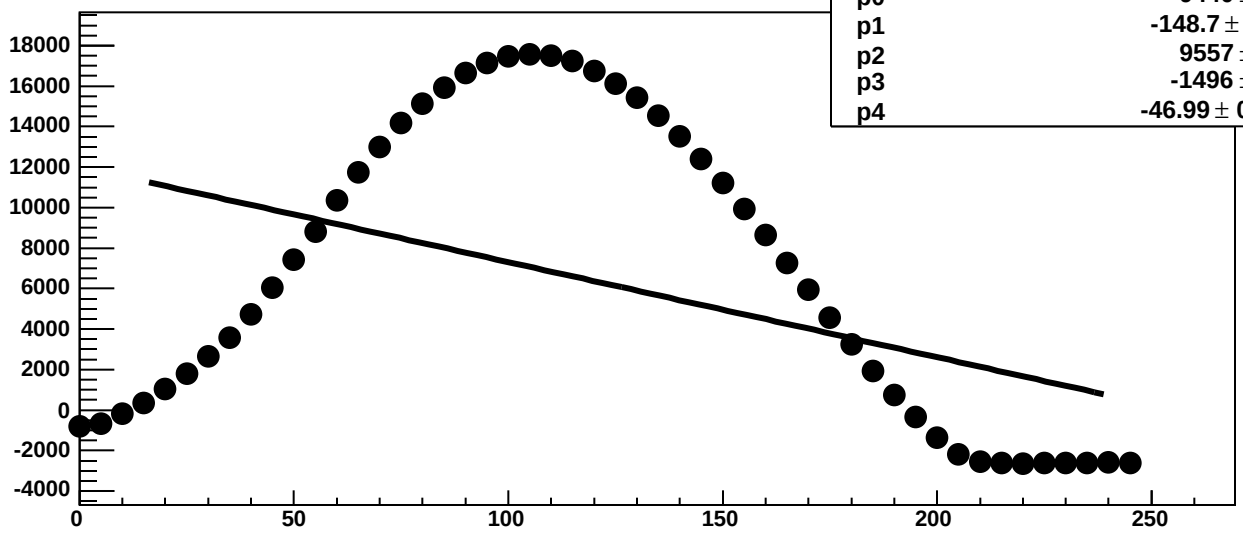
Chip 8, Channel 0, Enable 5, DAC=1600, ADC Noise vs Hold



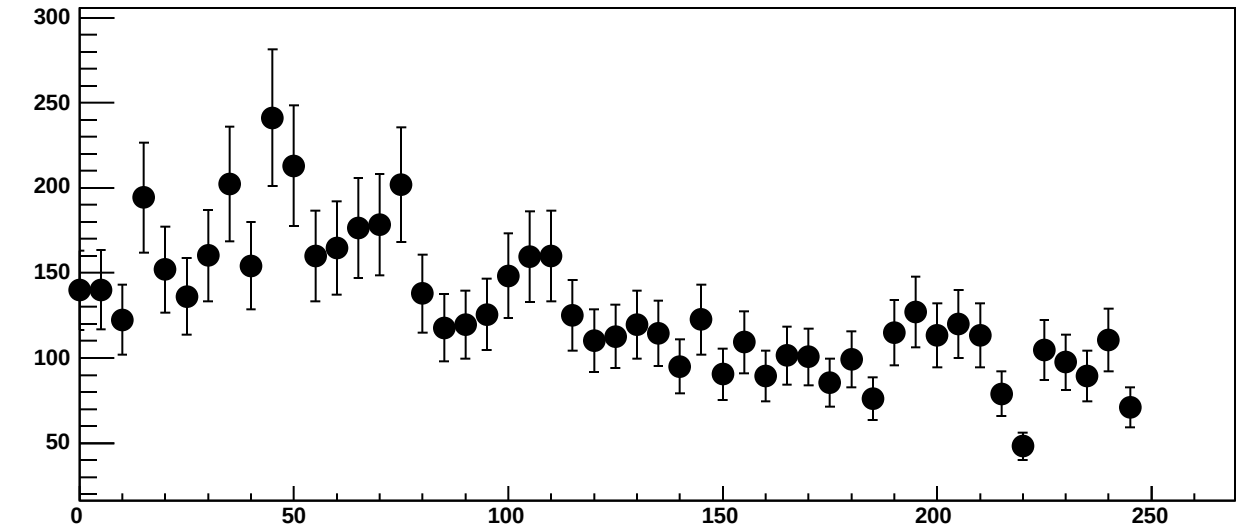
Chip 8, Channel 0, Enable 5, DAC=1600, ADC Residuals vs Hold



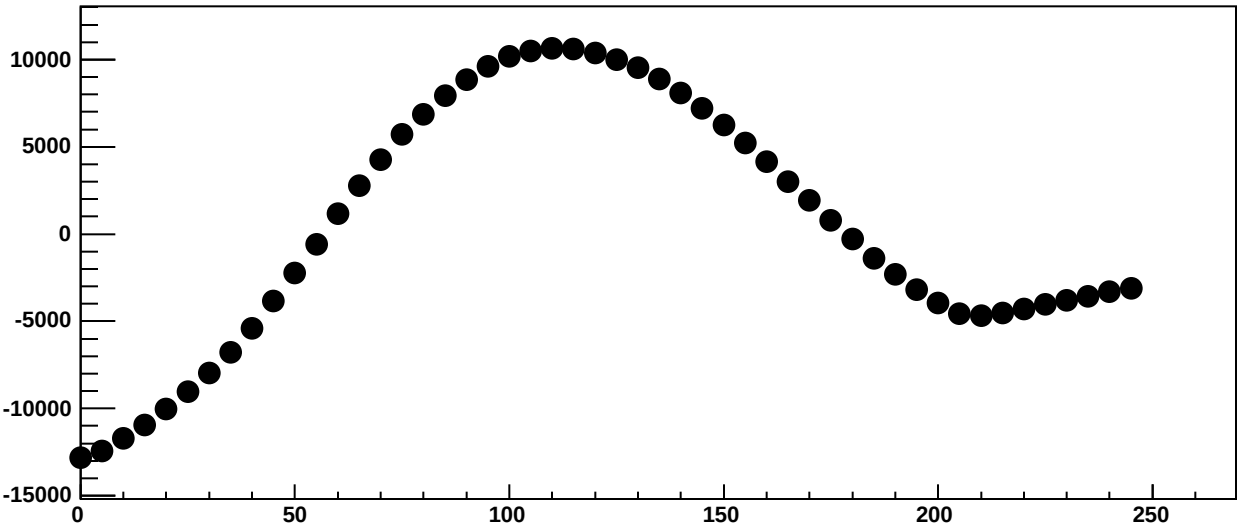
Chip 8, Channel 1, Enable 0, DAC=1600, ADC Mean vs Hold



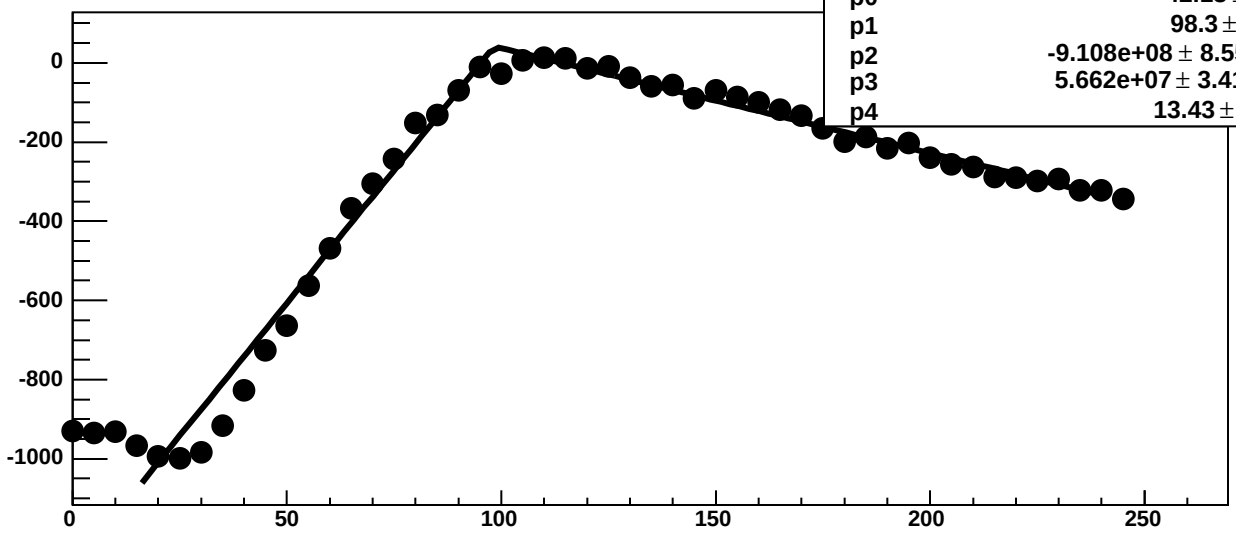
Chip 8, Channel 1, Enable 0, DAC=1600, ADC Noise vs Hold



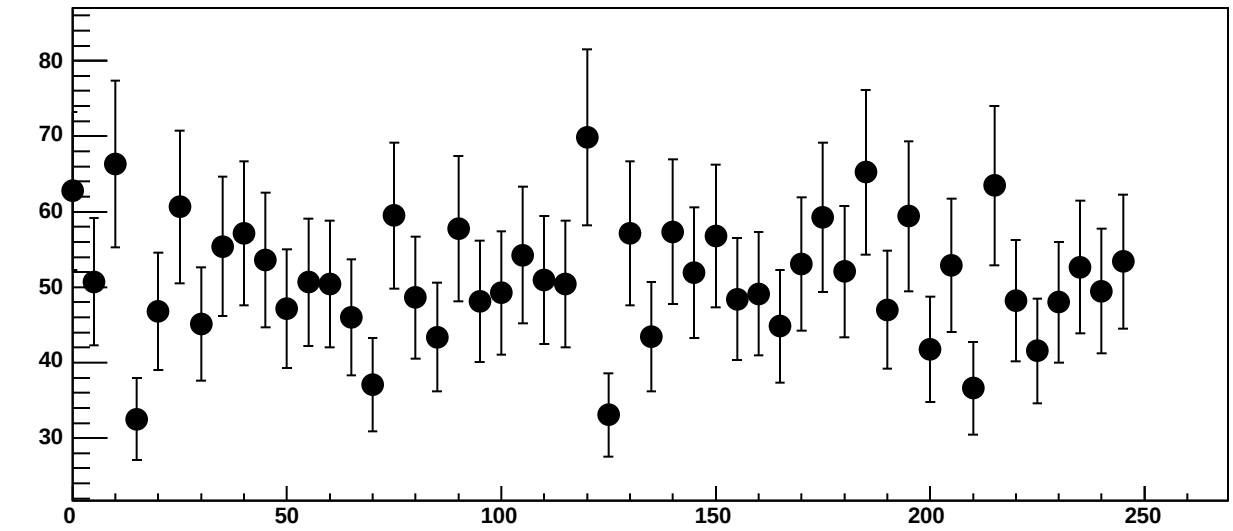
Chip 8, Channel 1, Enable 0, DAC=1600, ADC Residuals vs Hold



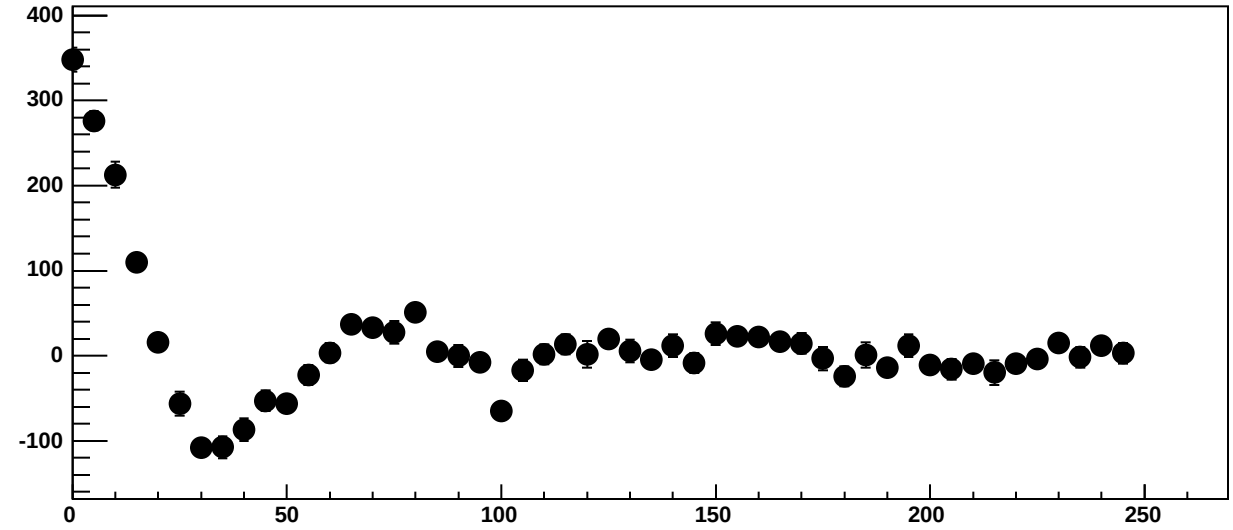
Chip 8, Channel 1, Enable 1, DAC=1600, ADC Mean vs Hold



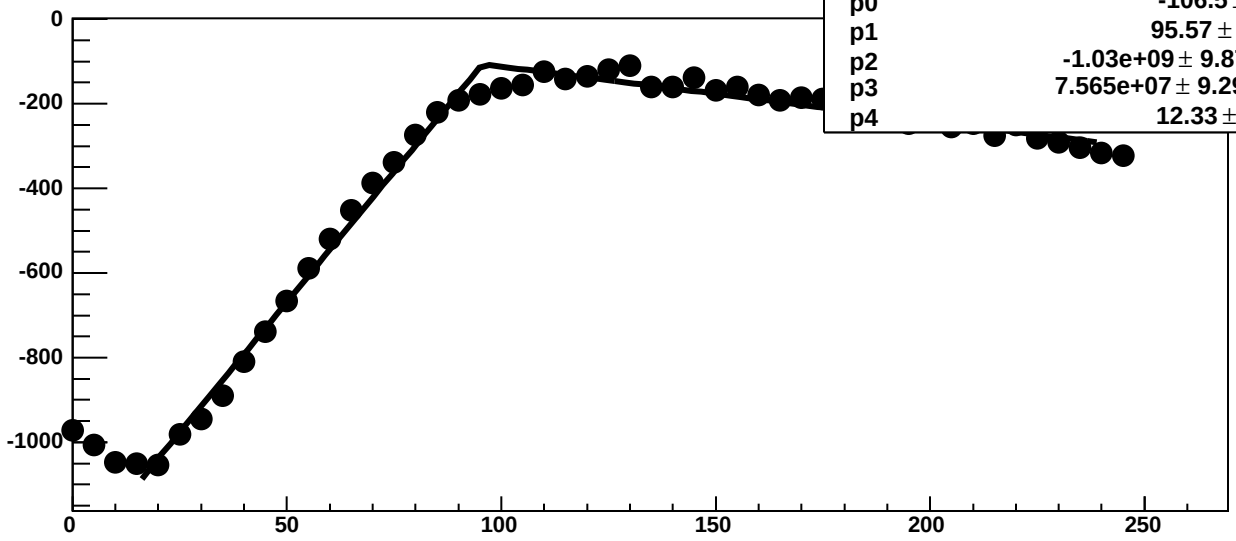
Chip 8, Channel 1, Enable 1, DAC=1600, ADC Noise vs Hold



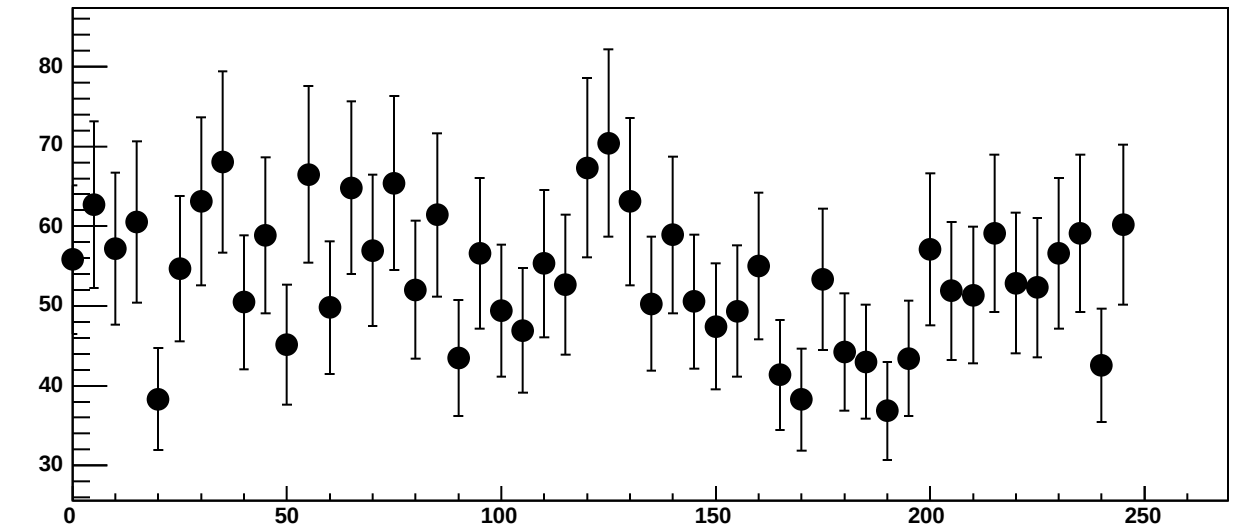
Chip 8, Channel 1, Enable 1, DAC=1600, ADC Residuals vs Hold



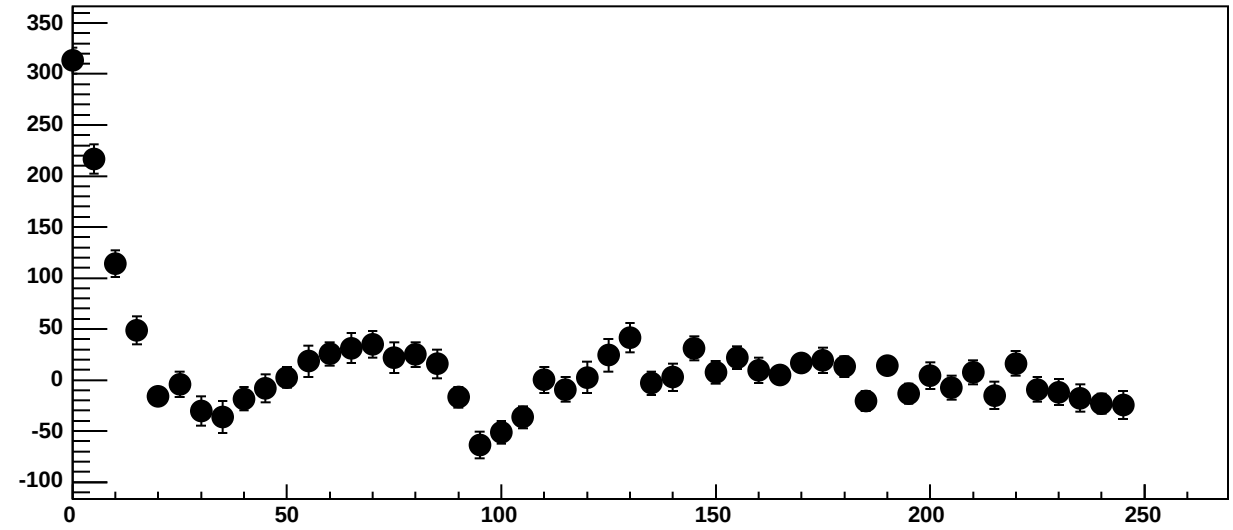
Chip 8, Channel 1, Enable 2, DAC=1600, ADC Mean vs Hold



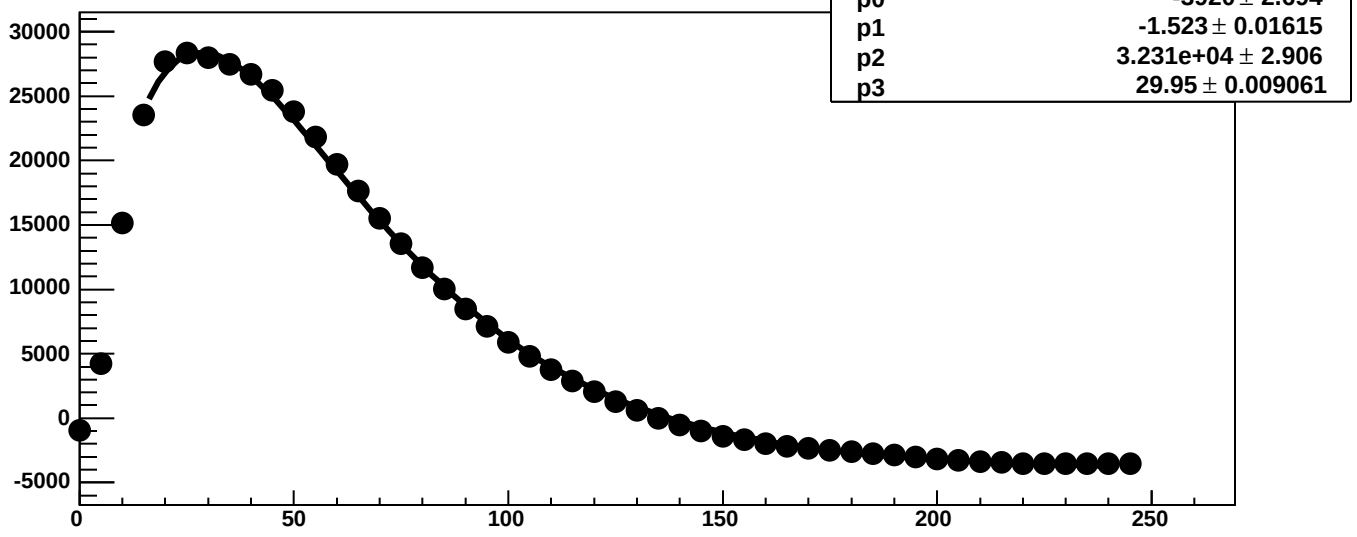
Chip 8, Channel 1, Enable 2, DAC=1600, ADC Noise vs Hold



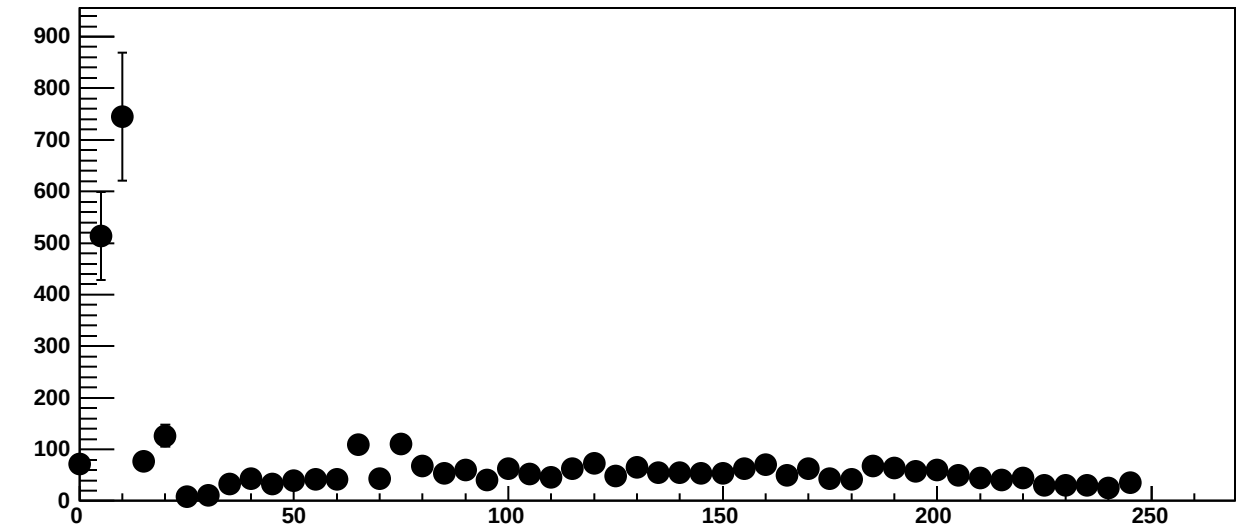
Chip 8, Channel 1, Enable 2, DAC=1600, ADC Residuals vs Hold



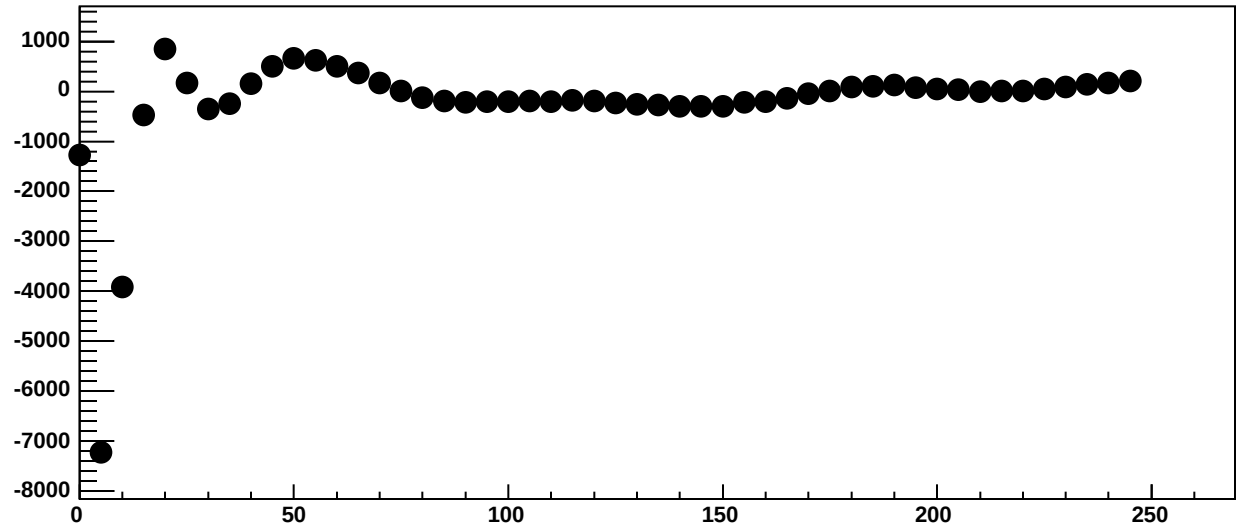
Chip 8, Channel 1, Enable 3!, DAC=1600, ADC Mean vs Hold



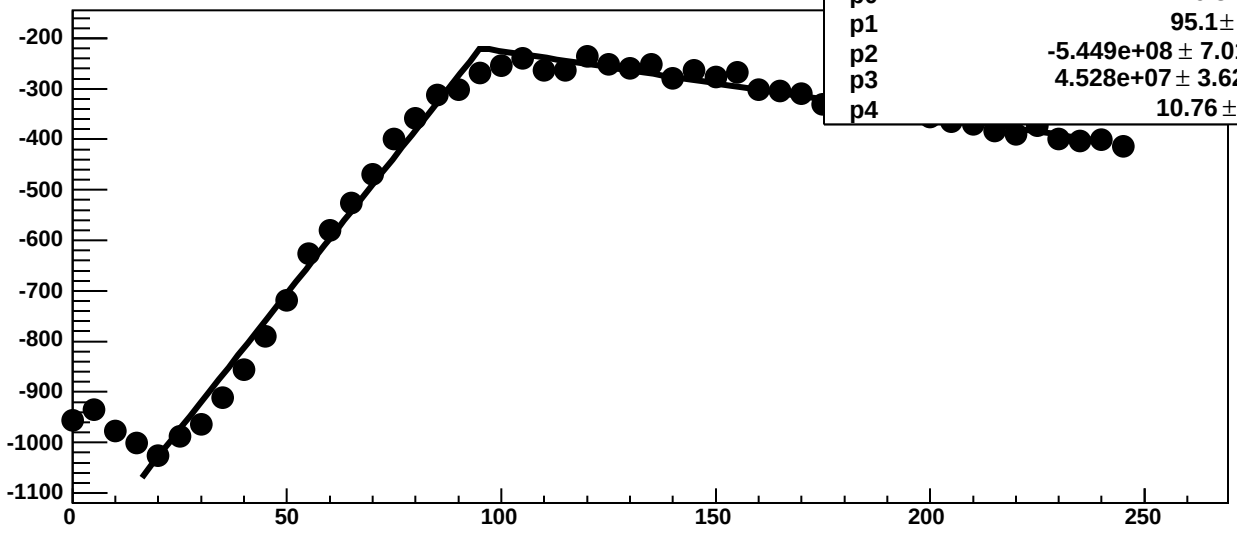
Chip 8, Channel 1, Enable 3!, DAC=1600, ADC Noise vs Hold



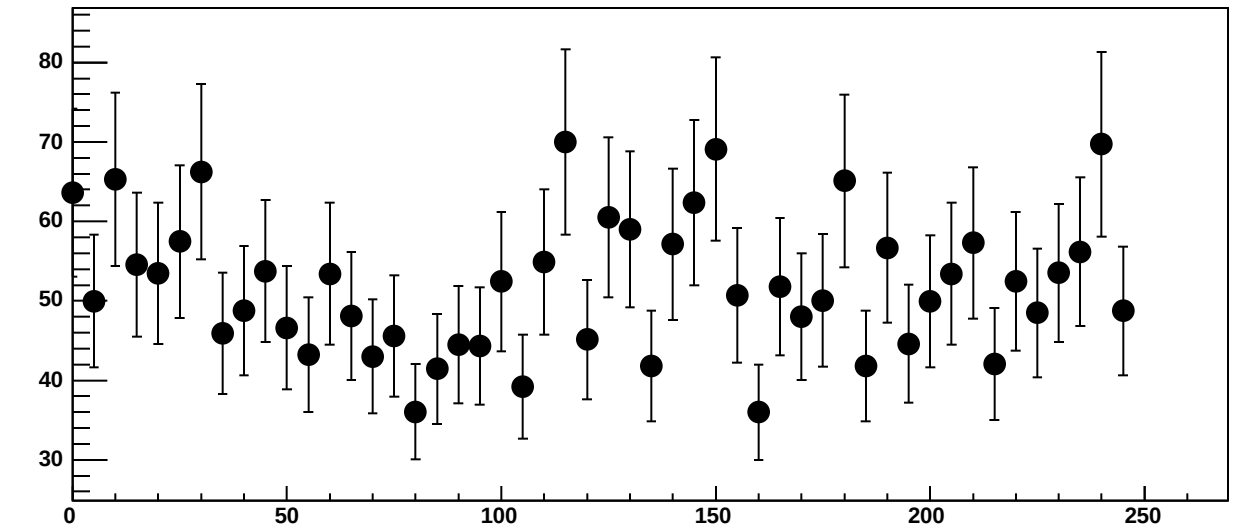
Chip 8, Channel 1, Enable 3!, DAC=1600, ADC Residuals vs Hold



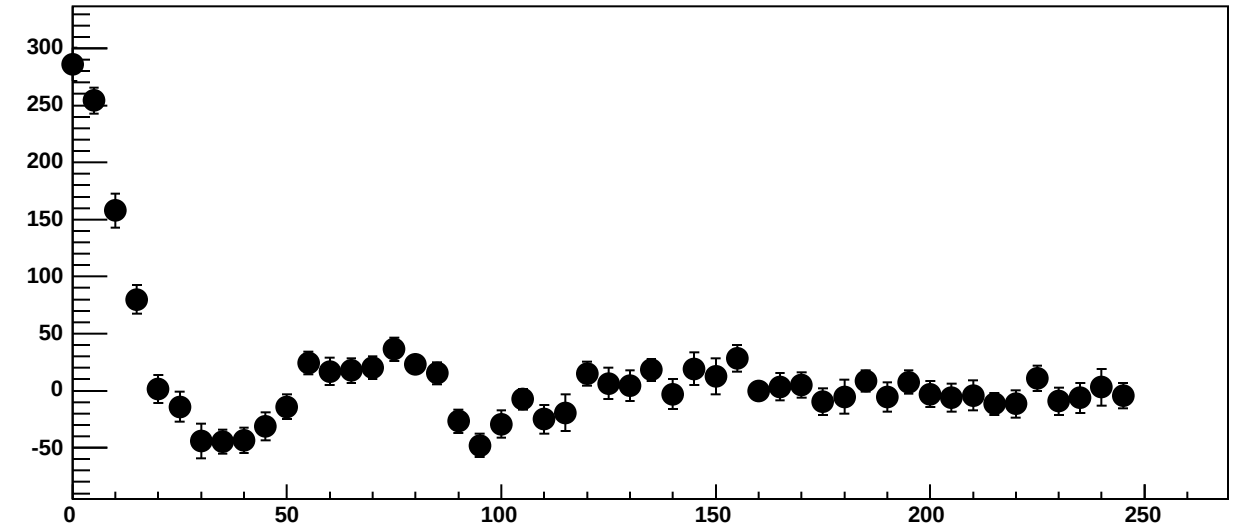
Chip 8, Channel 1, Enable 4, DAC=1600, ADC Mean vs Hold

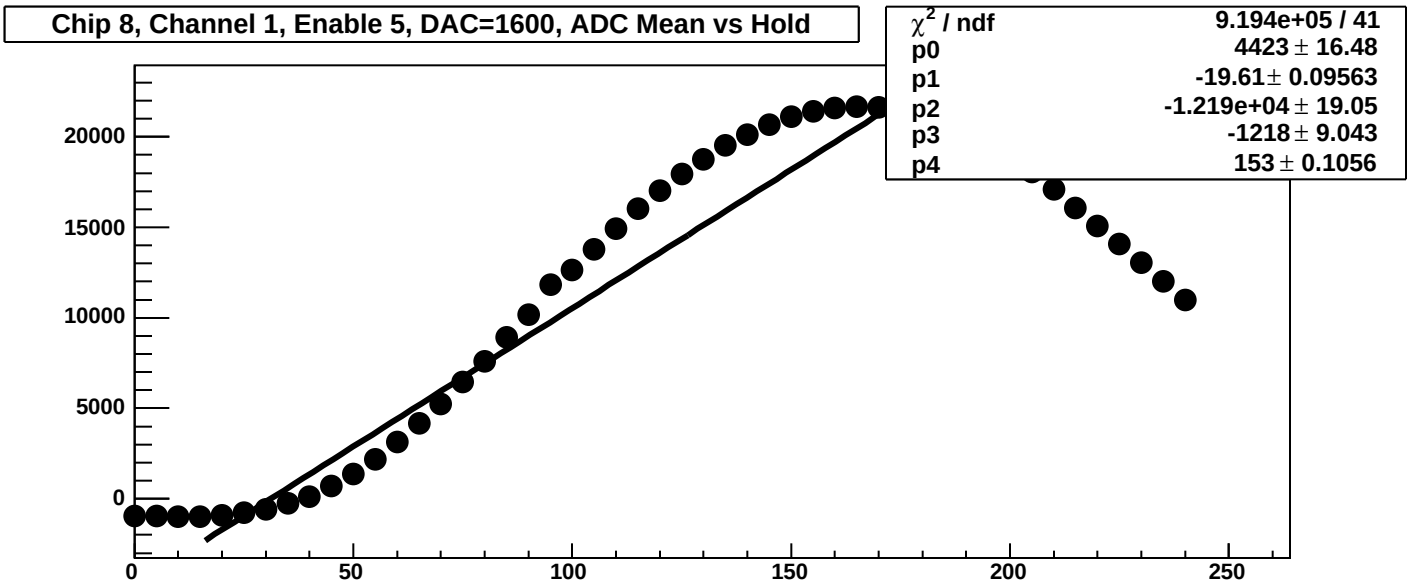


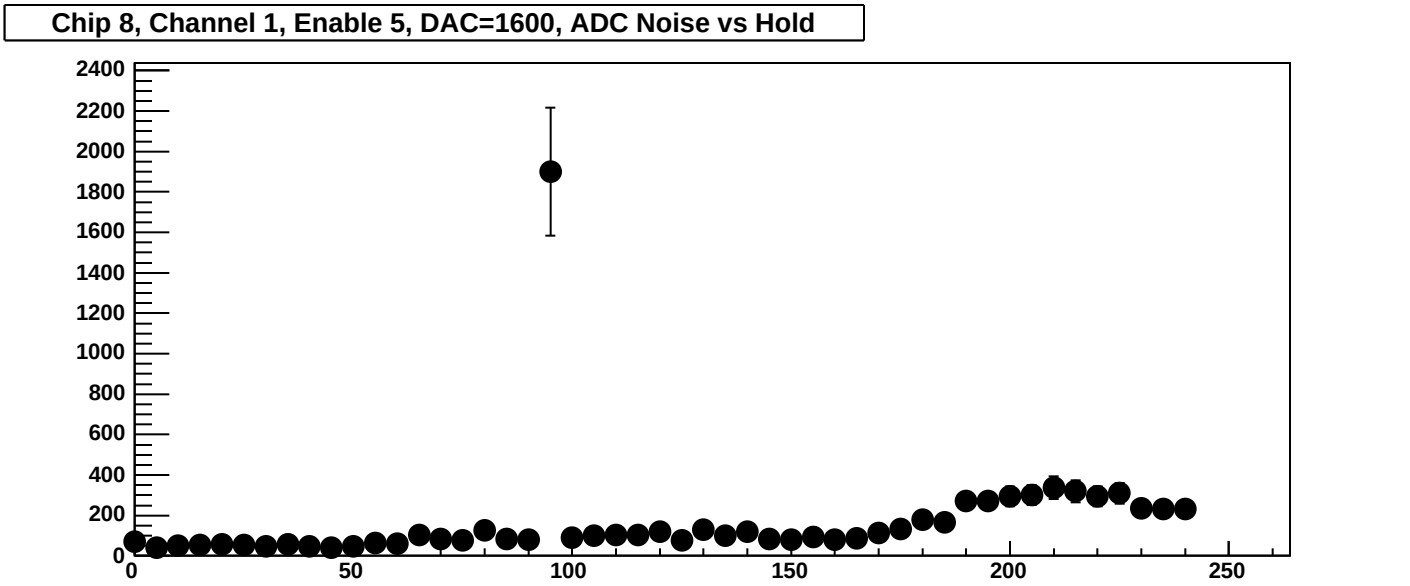
Chip 8, Channel 1, Enable 4, DAC=1600, ADC Noise vs Hold

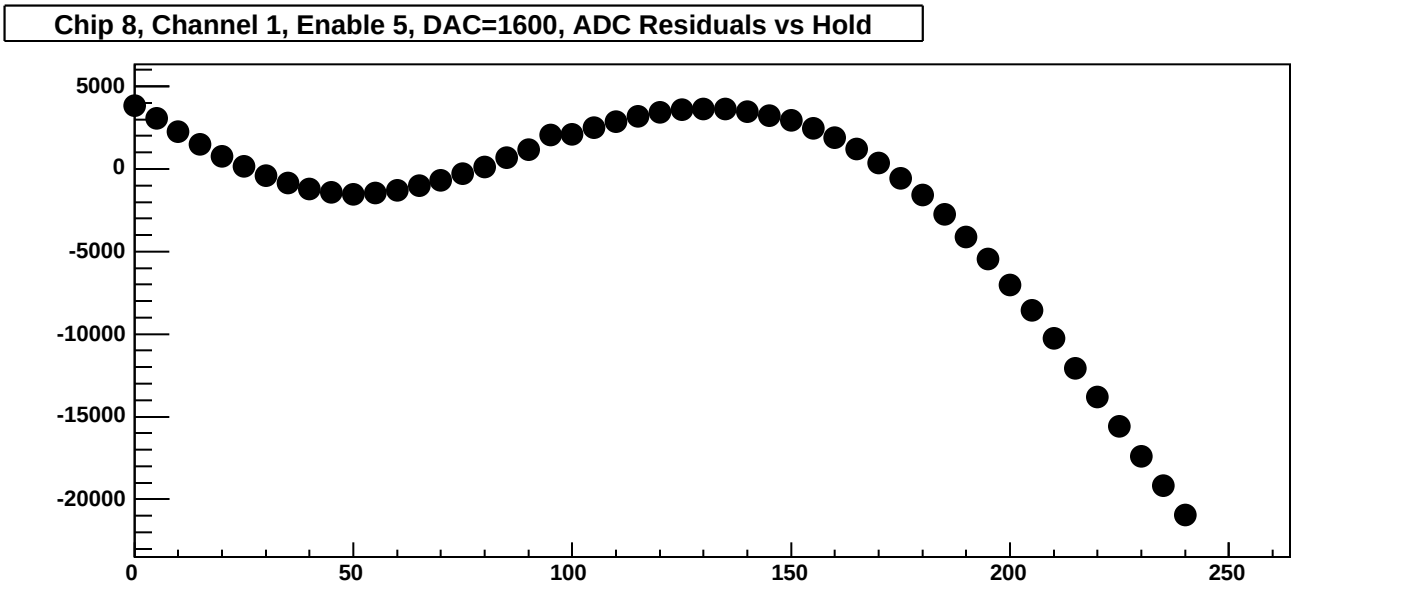


Chip 8, Channel 1, Enable 4, DAC=1600, ADC Residuals vs Hold

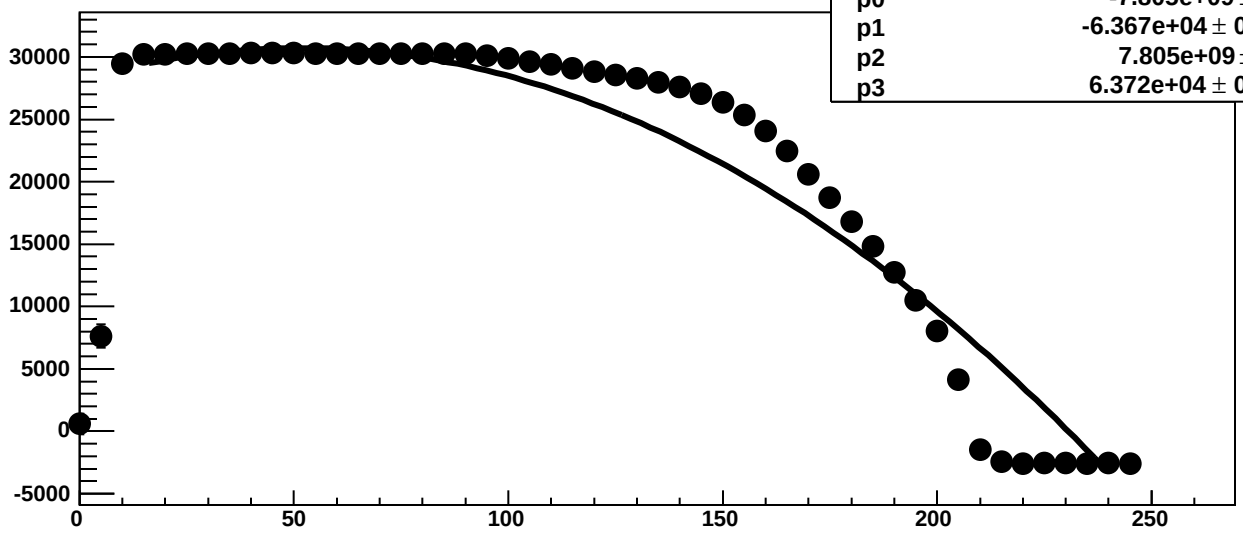



Chip 8, Channel 1, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 9.194e+05 / 41
p0 4423 ± 16.48
p1 -19.61 ± 0.09563
p2 -1.219e+04 ± 19.05
p3 -1218 ± 9.043
p4 153 ± 0.1056
20000
15000
10000
5000
0
0
50
100
150
200
250

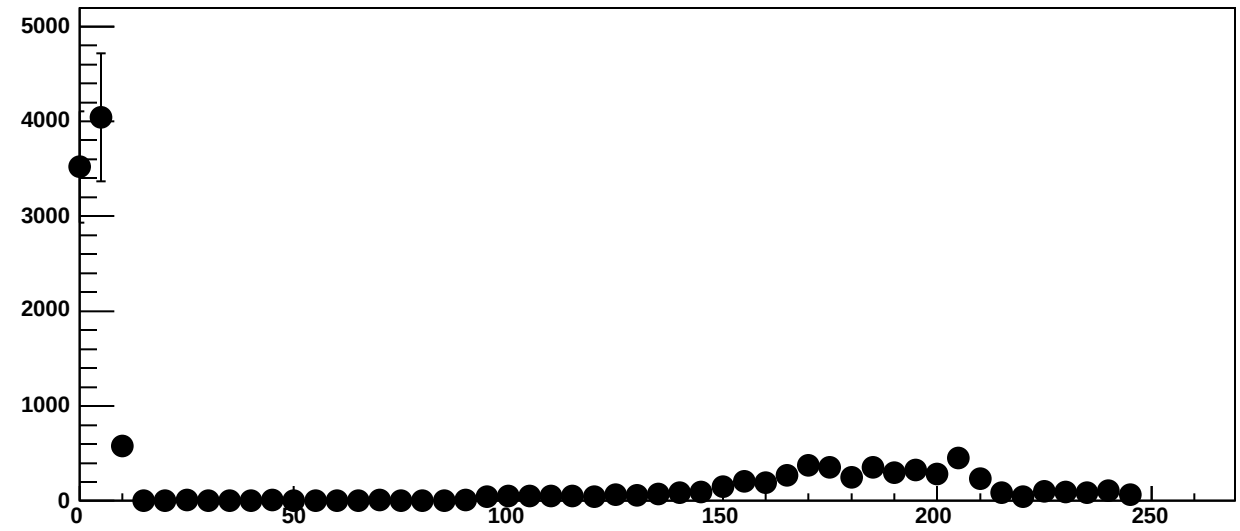

Chip 8, Channel 1, Enable 5, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 8, Channel 1, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

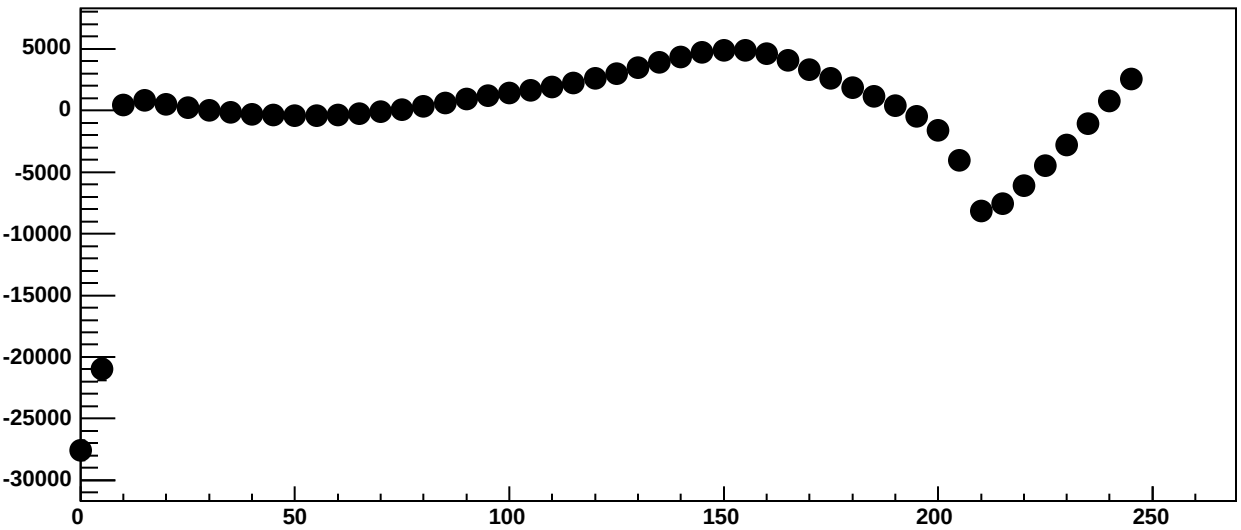
Chip 8, Channel 2, Enable 0!, DAC=1600, ADC Mean vs Hold



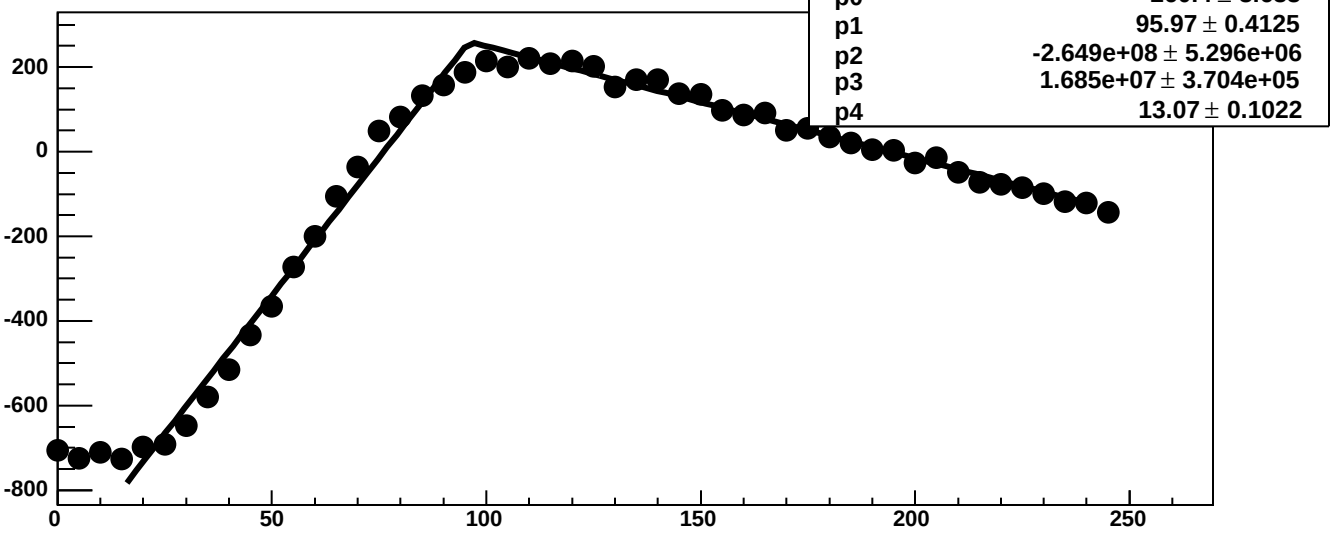
Chip 8, Channel 2, Enable 0!, DAC=1600, ADC Noise vs Hold



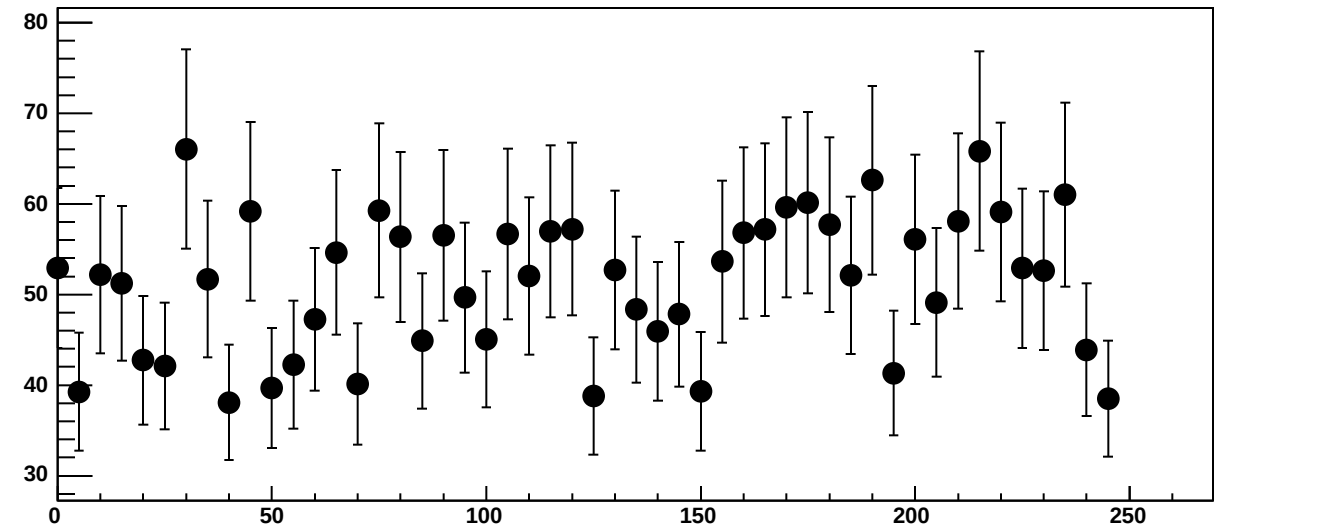
Chip 8, Channel 2, Enable 0!, DAC=1600, ADC Residuals vs Hold



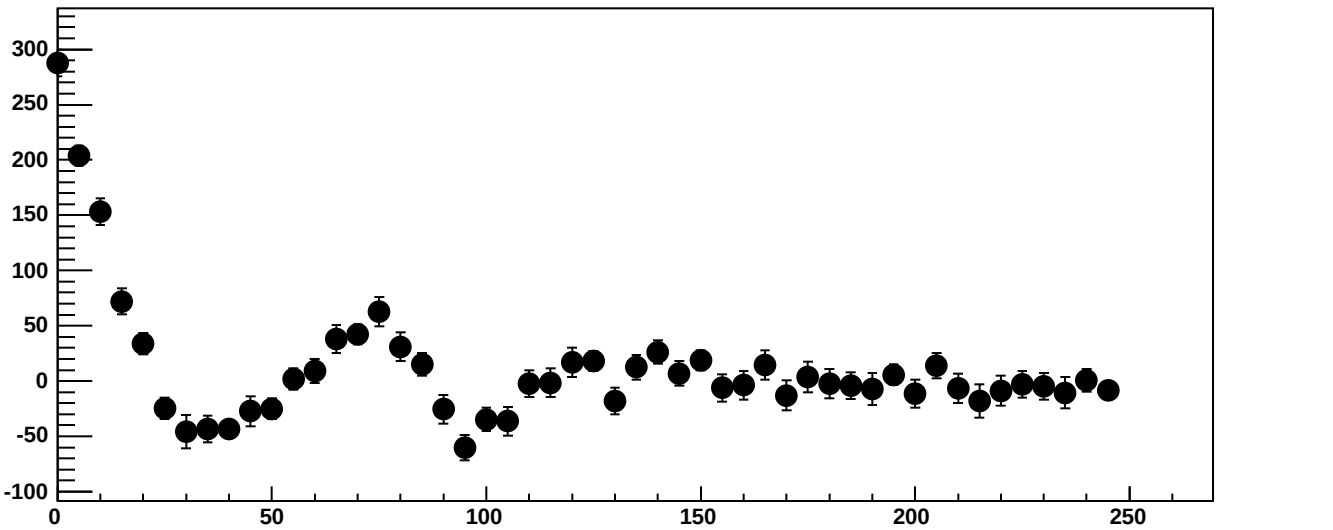
Chip 8, Channel 2, Enable 1, DAC=1600, ADC Mean vs Hold

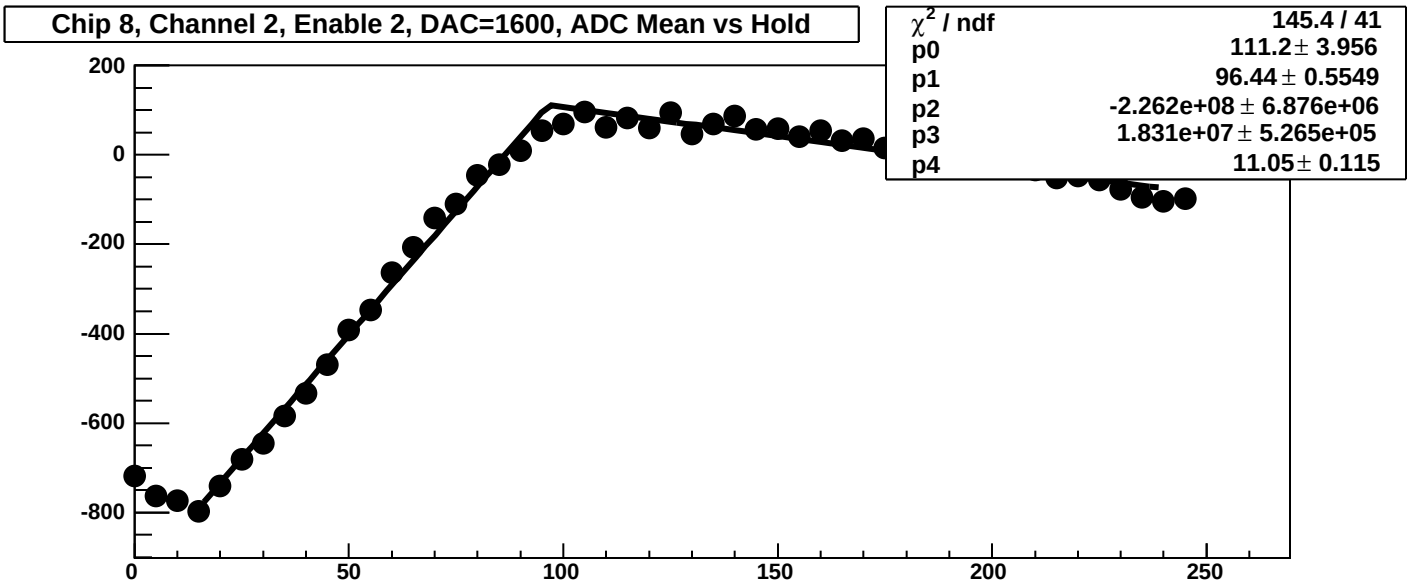


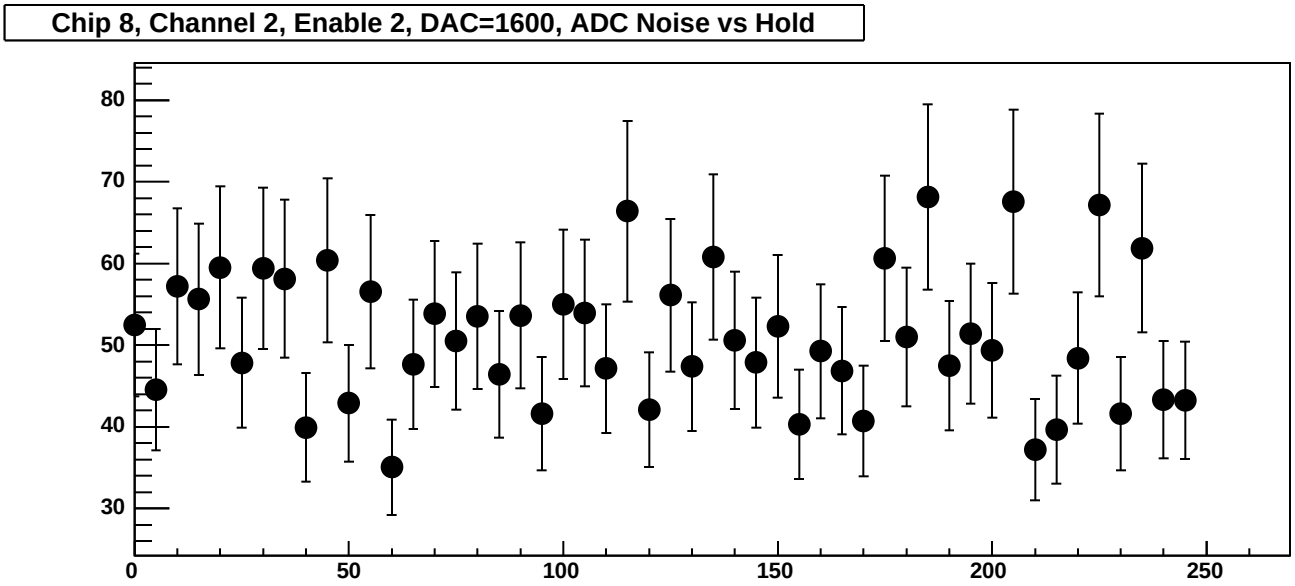
Chip 8, Channel 2, Enable 1, DAC=1600, ADC Noise vs Hold

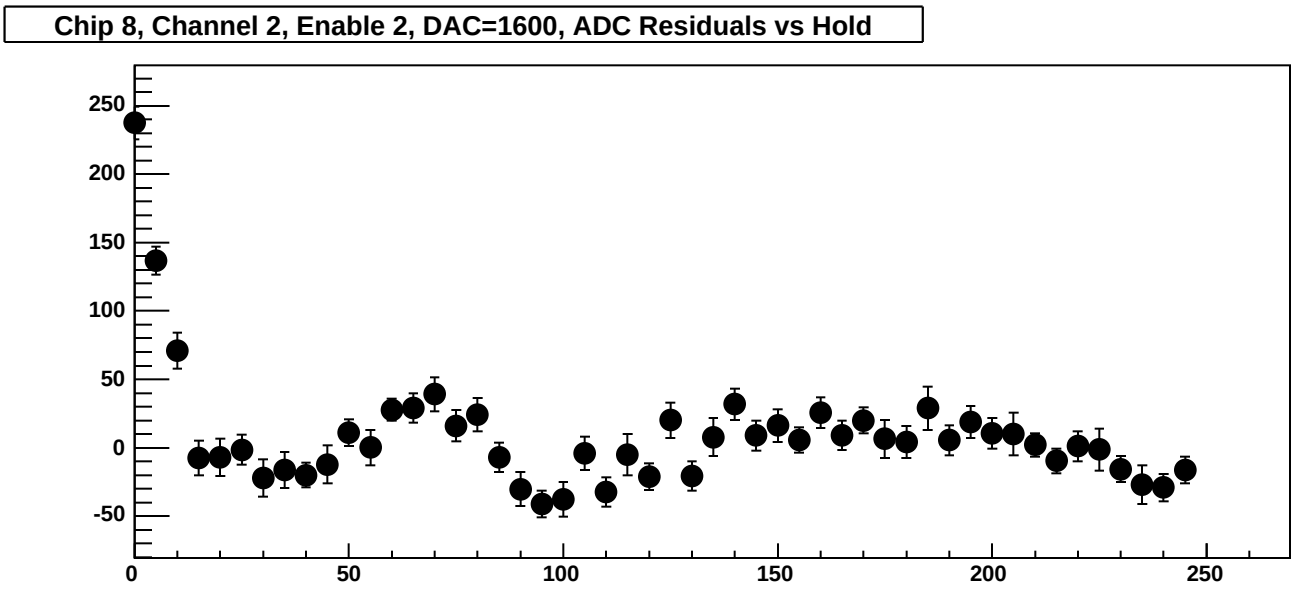


Chip 8, Channel 2, Enable 1, DAC=1600, ADC Residuals vs Hold



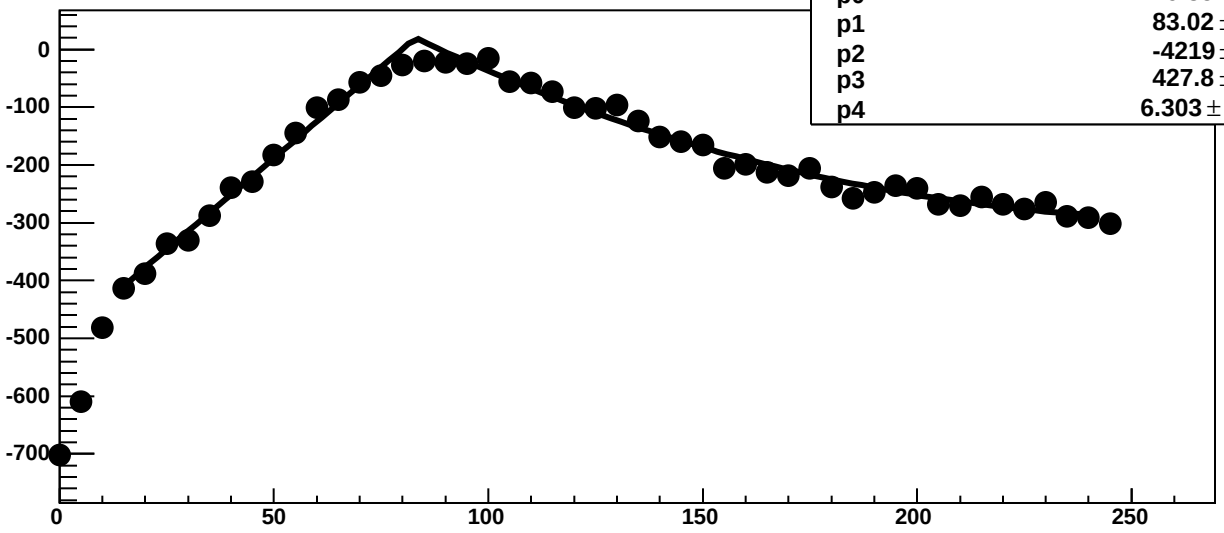

Chip 8, Channel 2, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 145.4 / 41
p0 111.2 ± 3.956
p1 96.44 ± 0.5549
p2 -2.262e+08 ± 6.876e+06
p3 1.831e+07 ± 5.265e+05
p4 11.05 ± 0.115
200
0
-200
-400
-600
-800
0
50
100
150
200
250


Chip 8, Channel 2, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

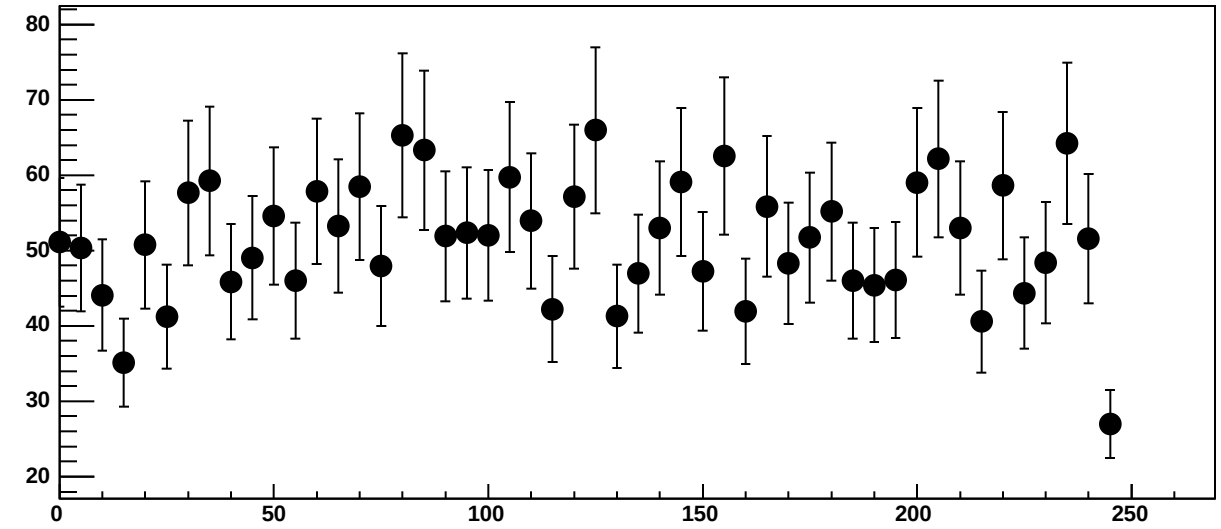

Chip 8, Channel 2, Enable 2, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
0
50
100
150
200
250

Chip 8, Channel 2, Enable 3, DAC=1600, ADC Mean vs Hold

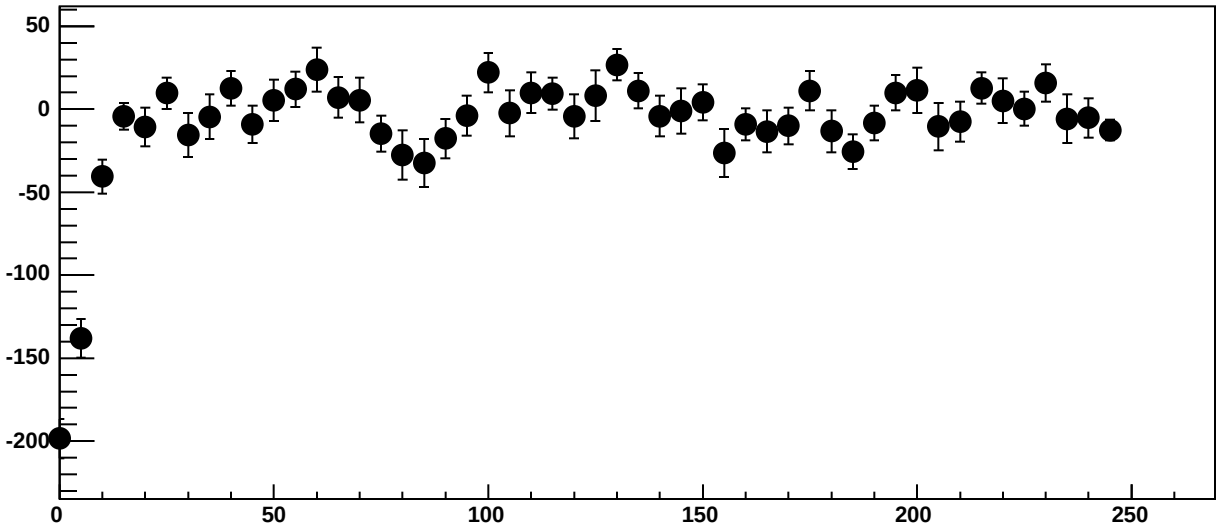
χ^2 / ndf	59.94 / 41
p0	19.85 ± 4.947
p1	83.02 ± 0.959
p2	-4219 ± 430.9
p3	427.8 ± 50.29
p4	6.303 ± 0.1459

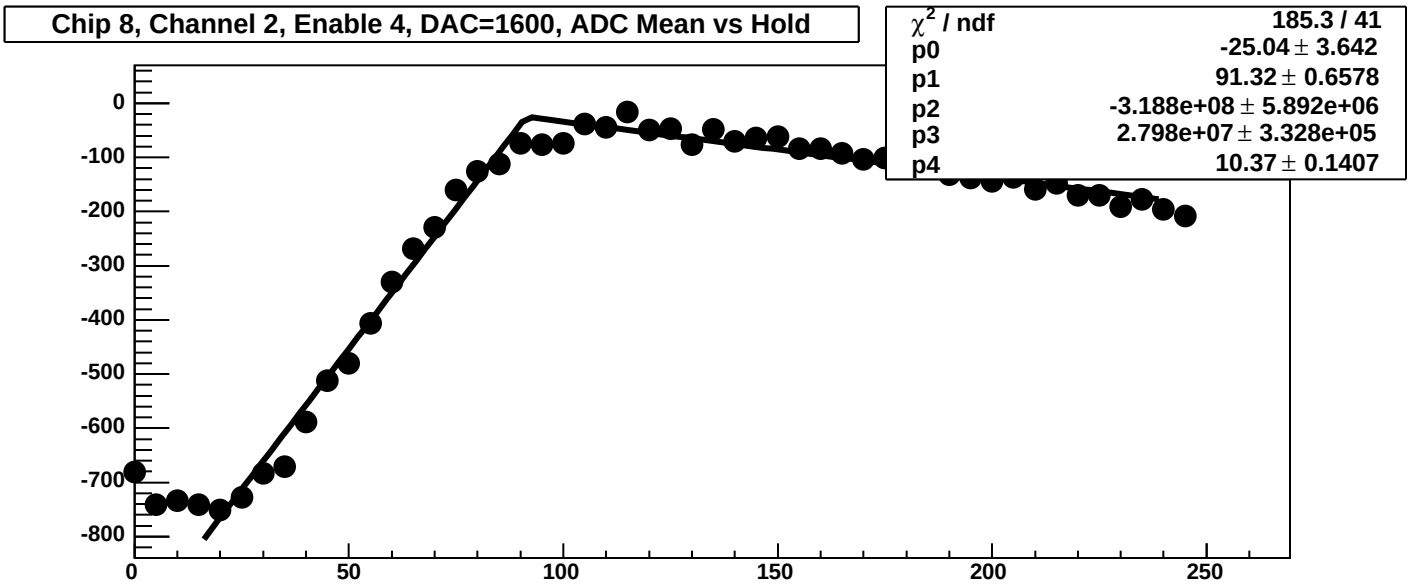


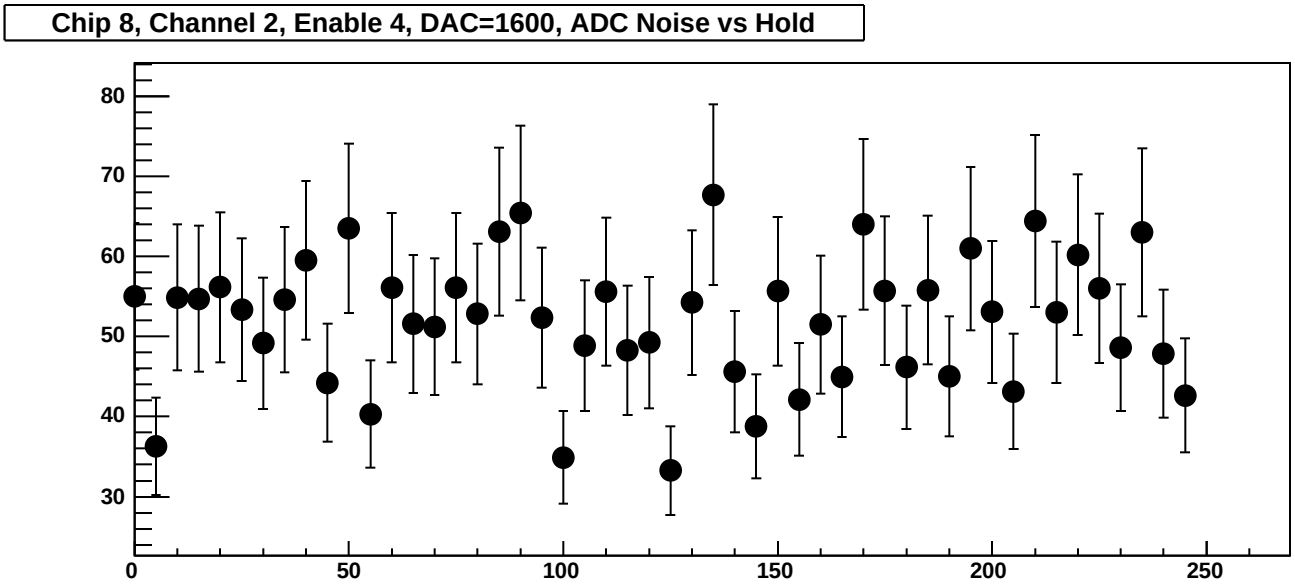
Chip 8, Channel 2, Enable 3, DAC=1600, ADC Noise vs Hold

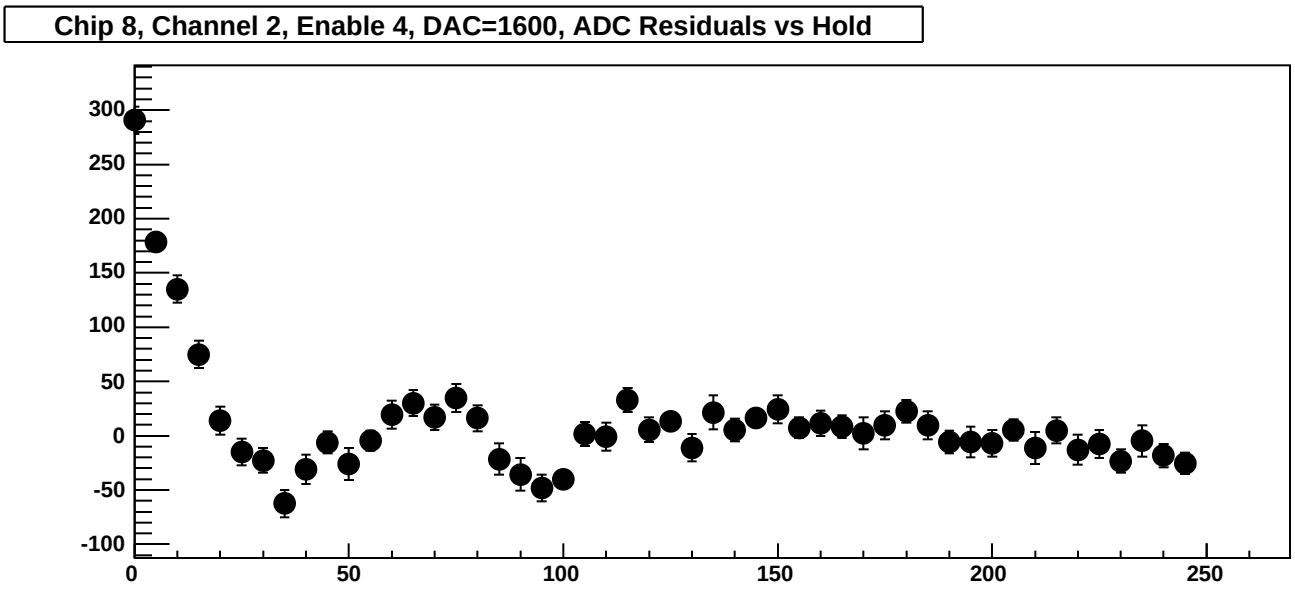


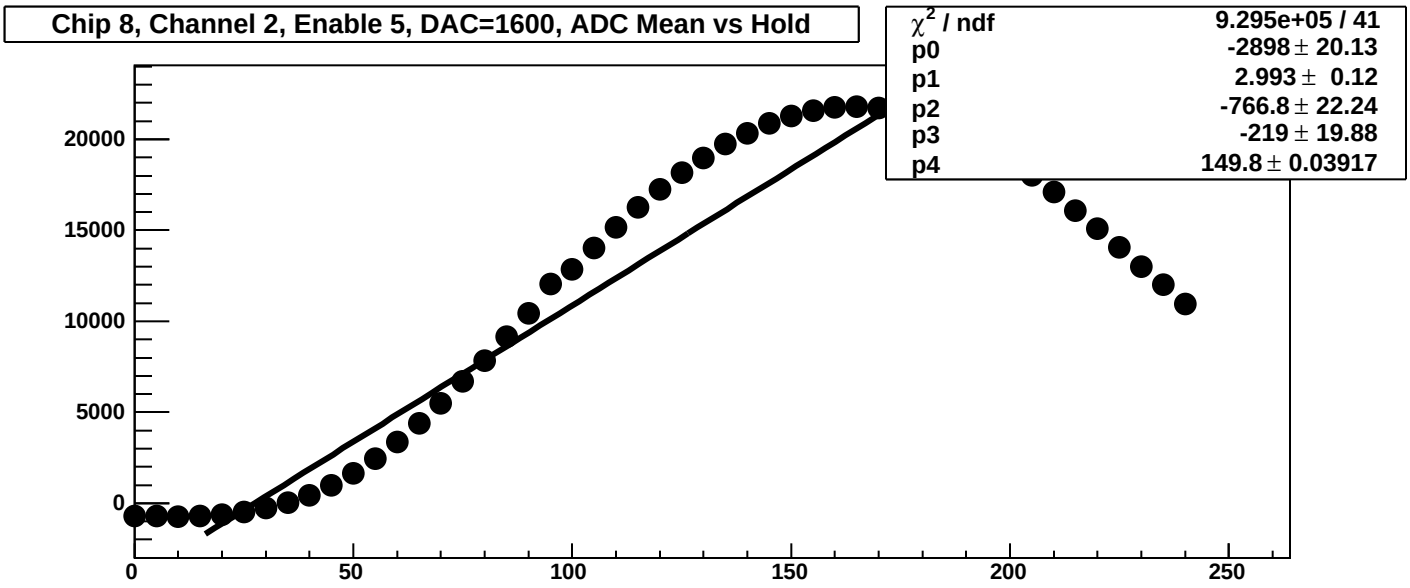
Chip 8, Channel 2, Enable 3, DAC=1600, ADC Residuals vs Hold

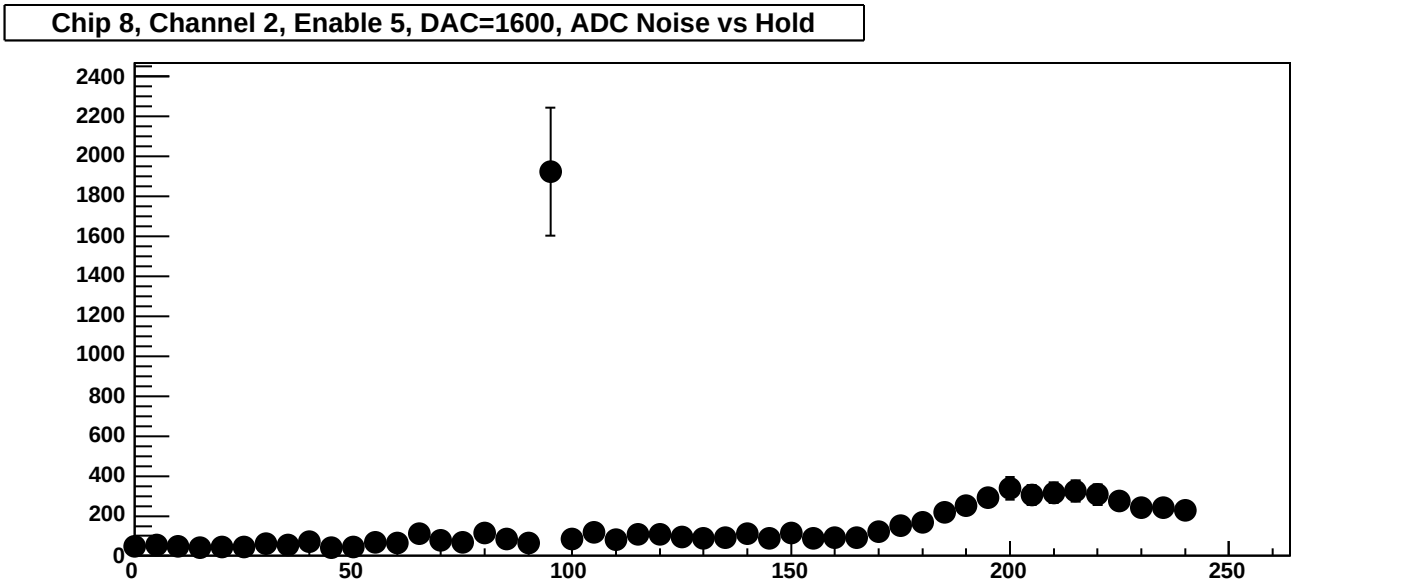


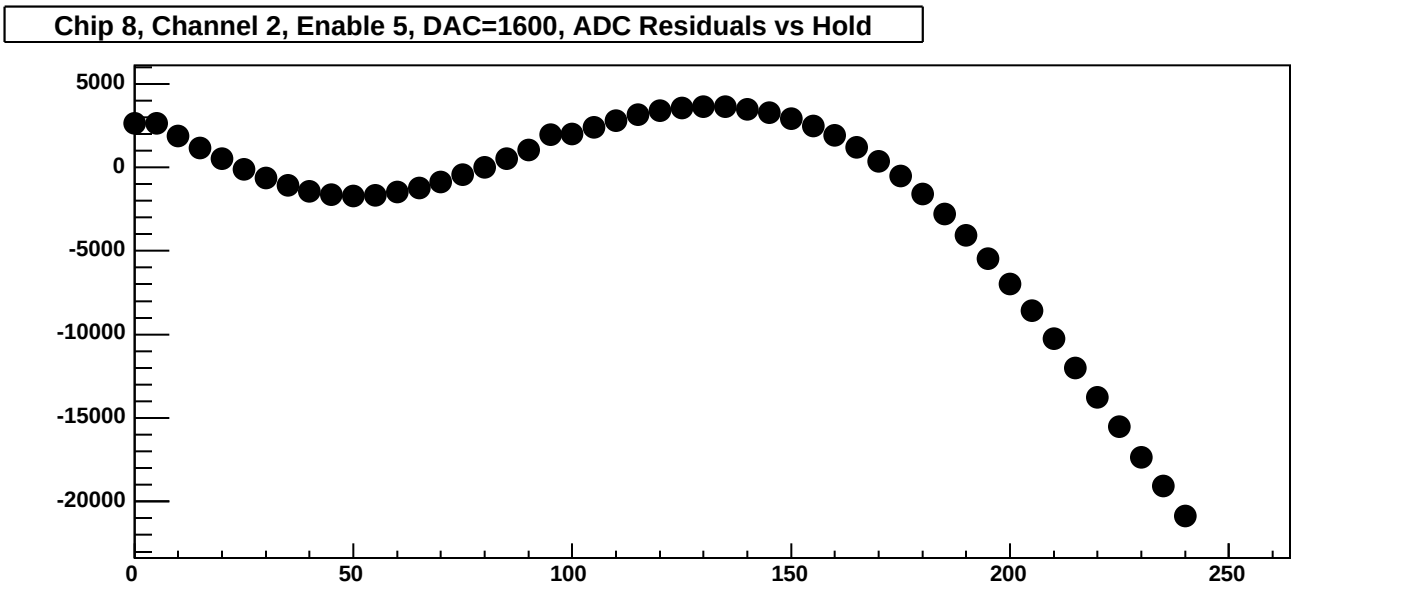

Chip 8, Channel 2, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 185.3 / 41
p0 -25.04 ± 3.642
p1 91.32 ± 0.6578
p2 -3.188e+08 ± 5.892e+06
p3 2.798e+07 ± 3.328e+05
p4 10.37 ± 0.1407
0
-100
-200
-300
-400
-500
-600
-700
-800
0
50
100
150
200
250


Chip 8, Channel 2, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

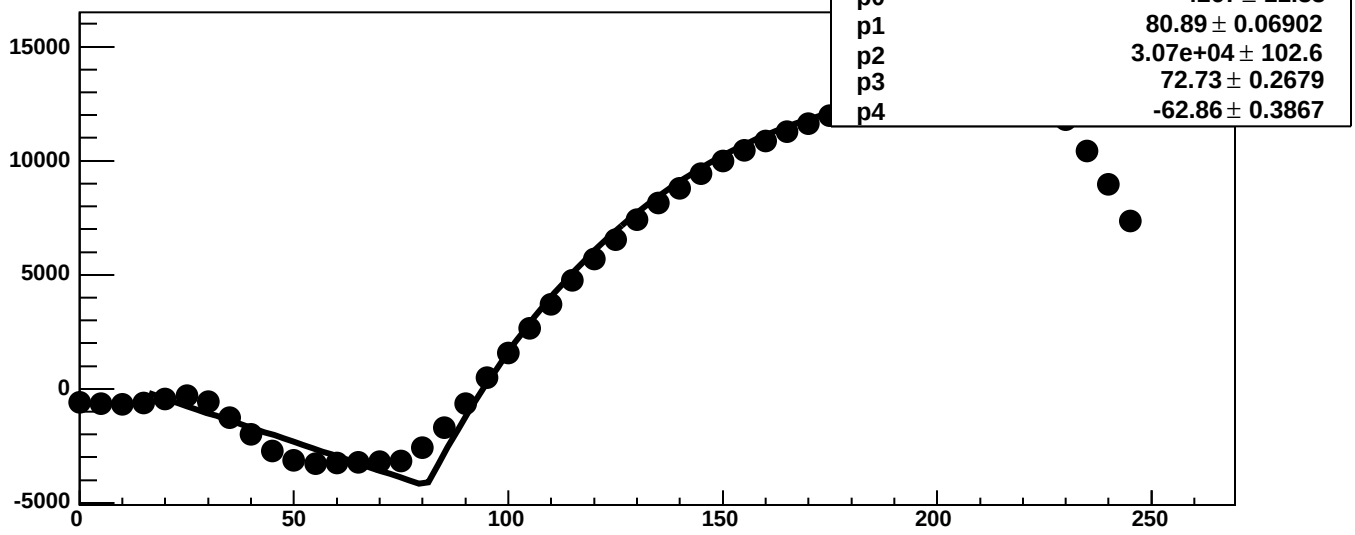

Chip 8, Channel 2, Enable 4, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250


Chip 8, Channel 2, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 9.295e+05 / 41
p0 -2898 ± 20.13
p1 2.993 ± 0.12
p2 -766.8 ± 22.24
p3 -219 ± 19.88
p4 149.8 ± 0.03917
20000
15000
10000
5000
0
0
50
100
150
200
250

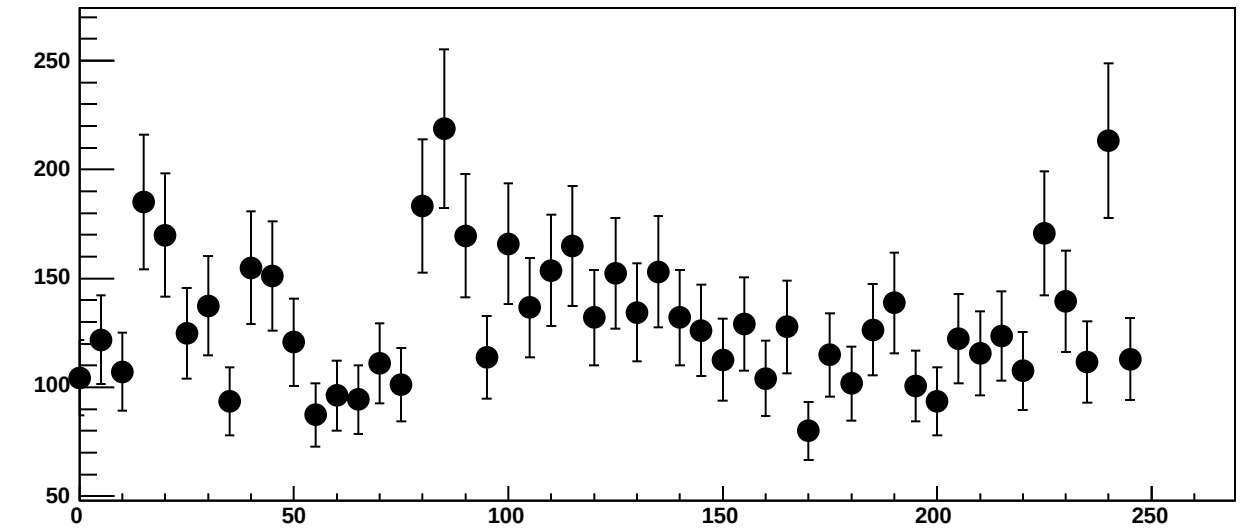

Chip 8, Channel 2, Enable 5, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 8, Channel 2, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

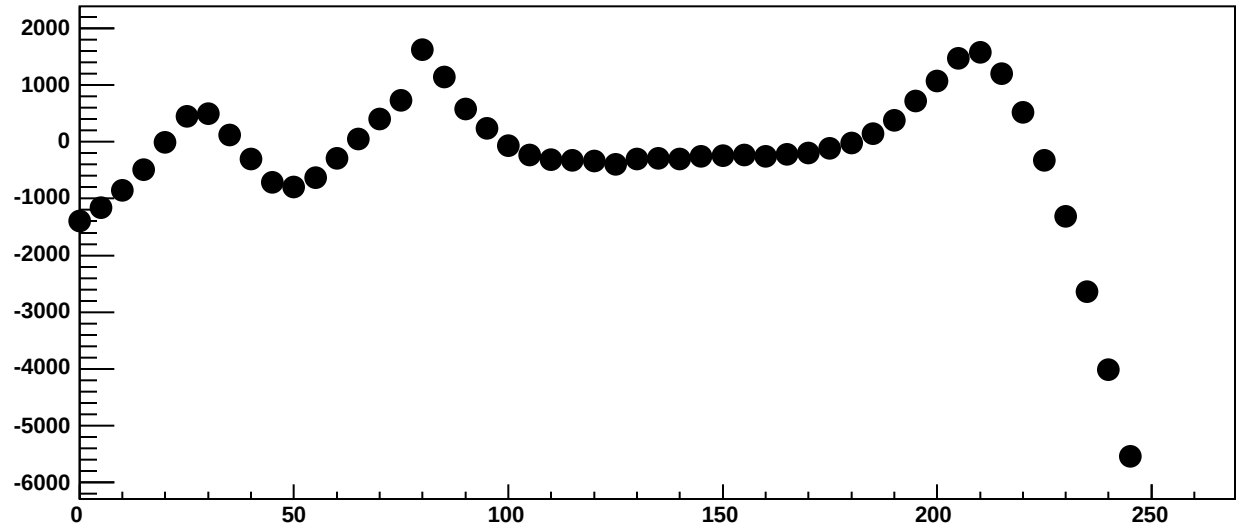
Chip 8, Channel 3, Enable 0, DAC=1600, ADC Mean vs Hold



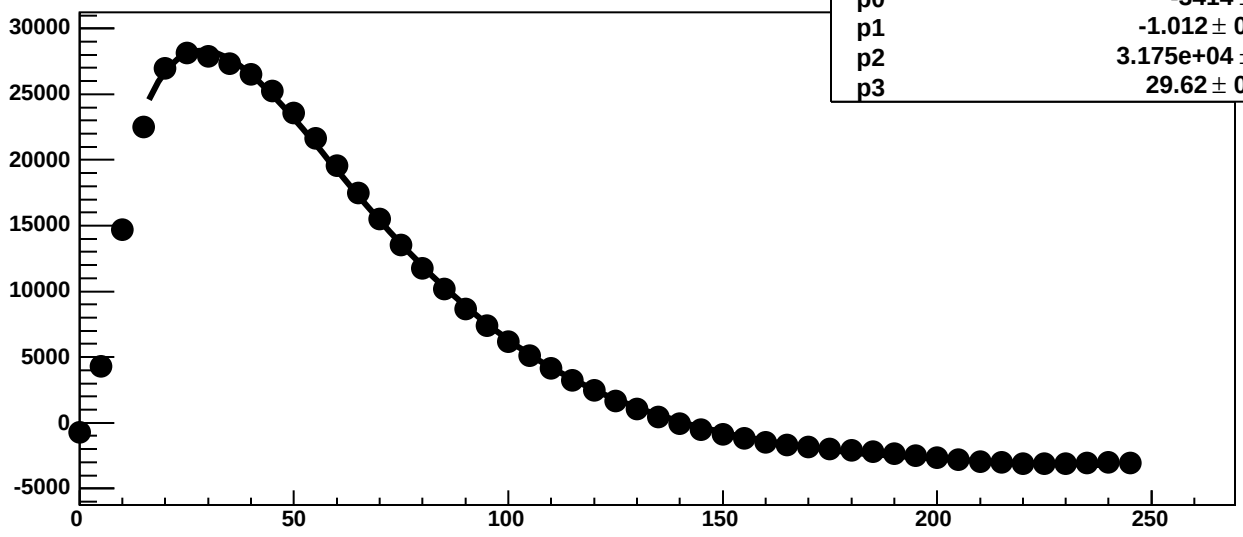
Chip 8, Channel 3, Enable 0, DAC=1600, ADC Noise vs Hold



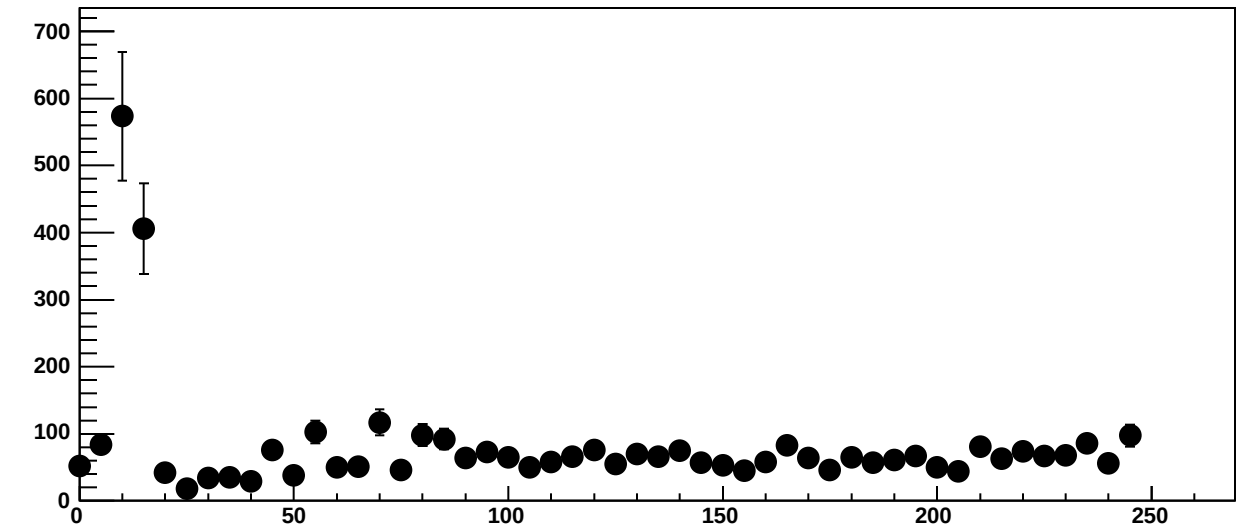
Chip 8, Channel 3, Enable 0, DAC=1600, ADC Residuals vs Hold



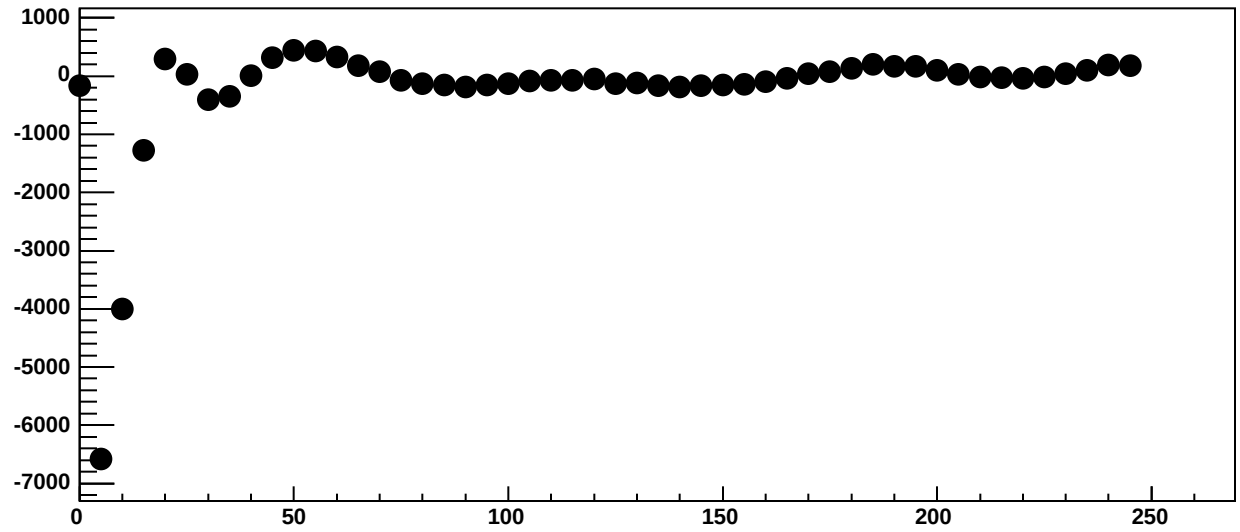
Chip 8, Channel 3, Enable 1!, DAC=1600, ADC Mean vs Hold



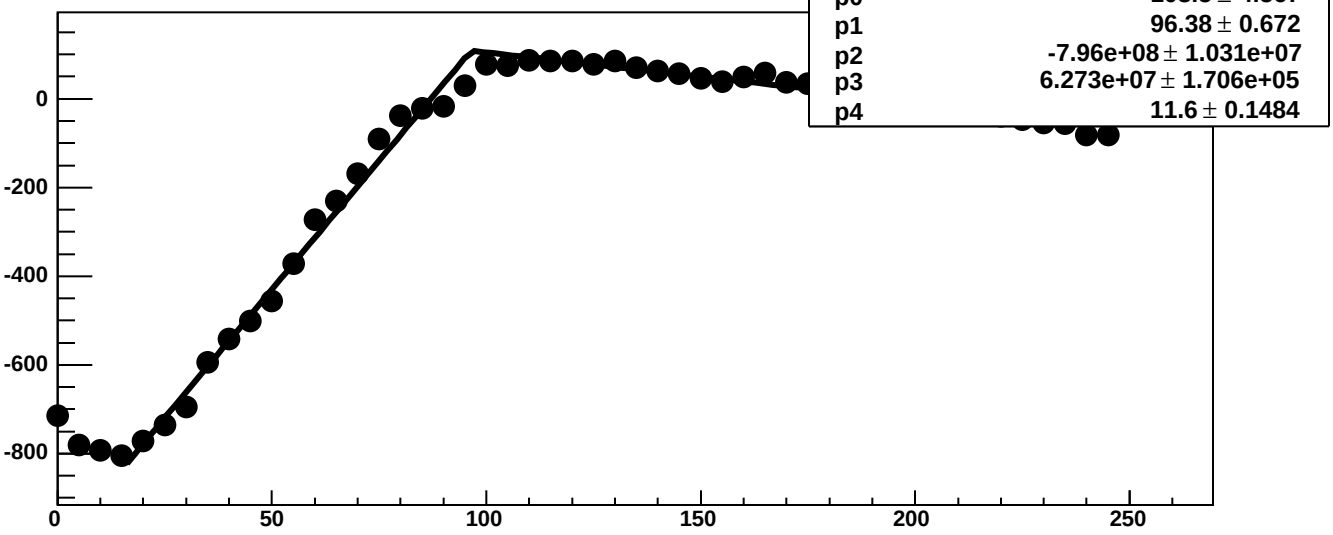
Chip 8, Channel 3, Enable 1!, DAC=1600, ADC Noise vs Hold



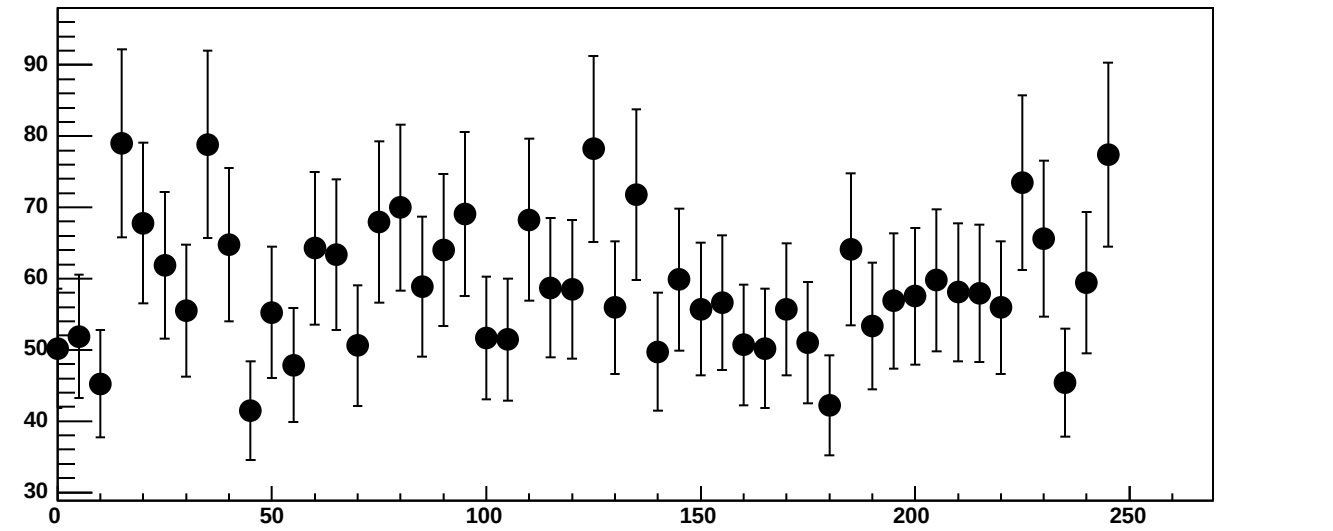
Chip 8, Channel 3, Enable 1!, DAC=1600, ADC Residuals vs Hold



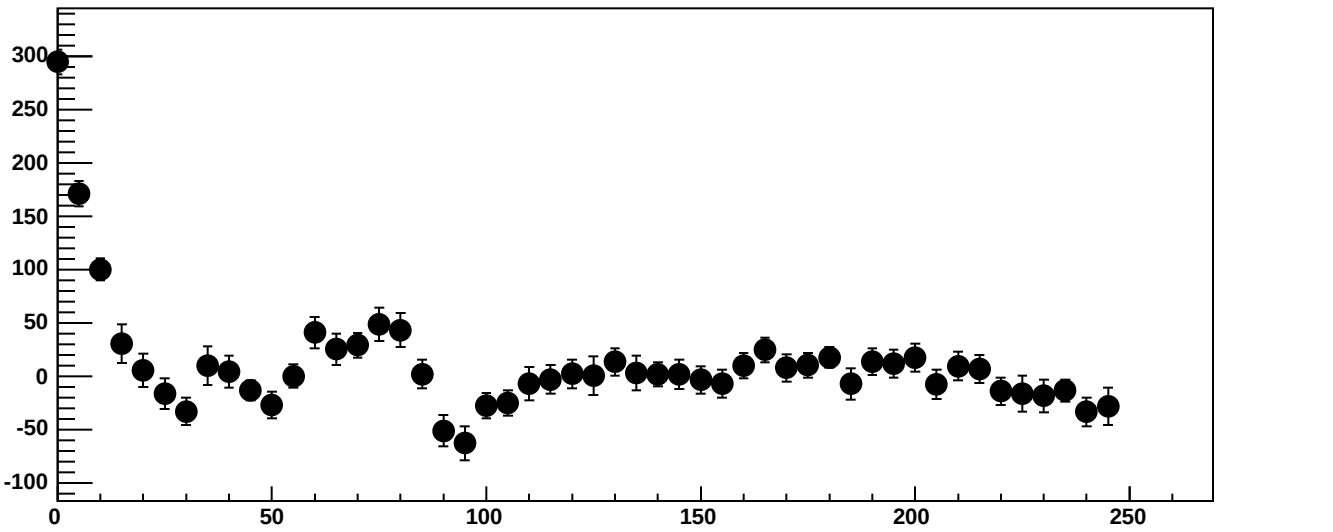
Chip 8, Channel 3, Enable 2, DAC=1600, ADC Mean vs Hold



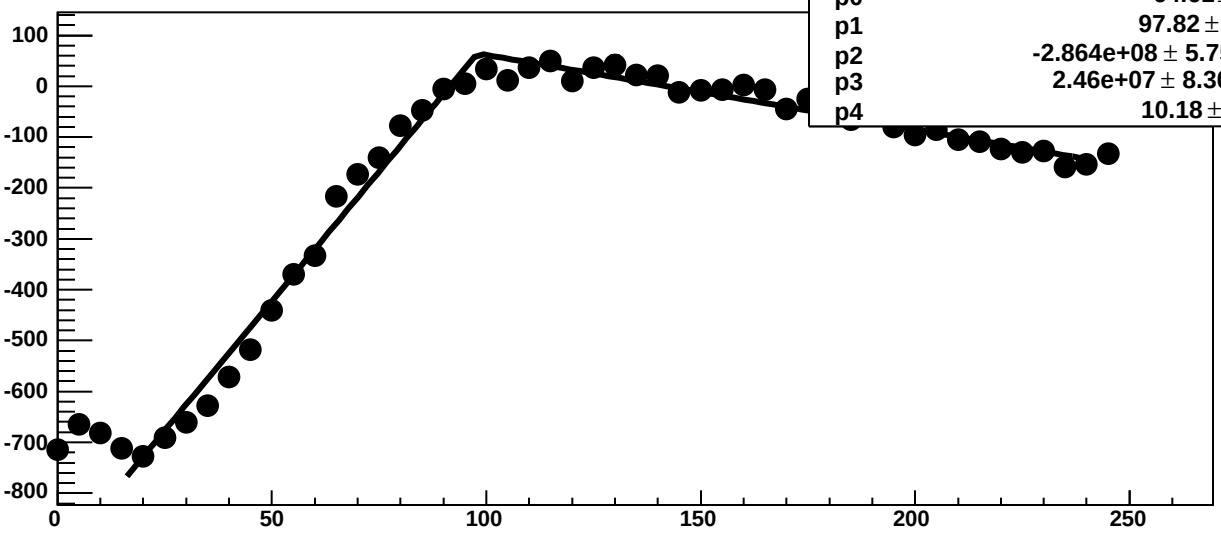
Chip 8, Channel 3, Enable 2, DAC=1600, ADC Noise vs Hold



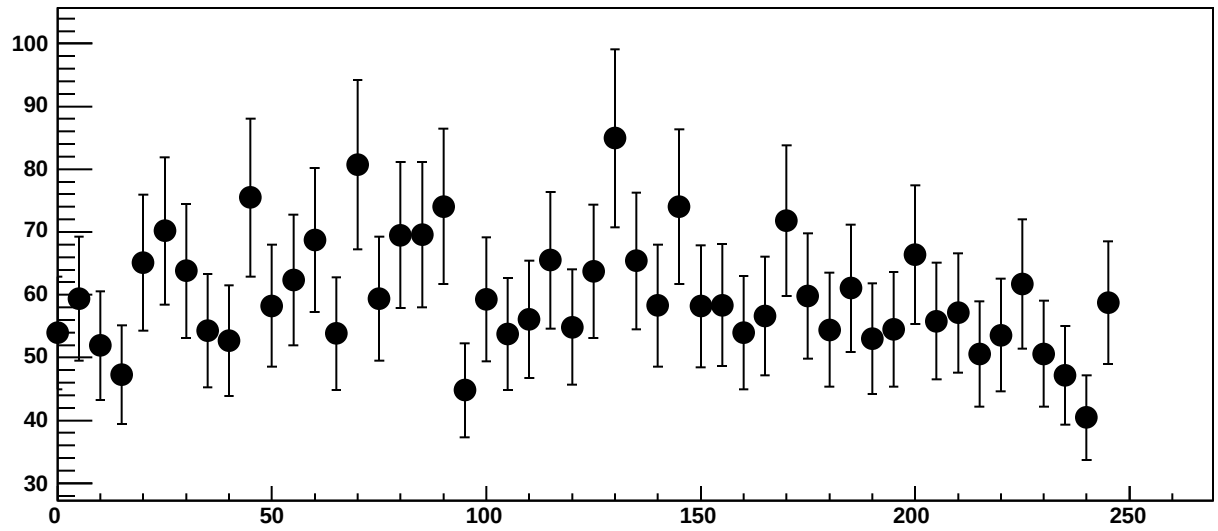
Chip 8, Channel 3, Enable 2, DAC=1600, ADC Residuals vs Hold



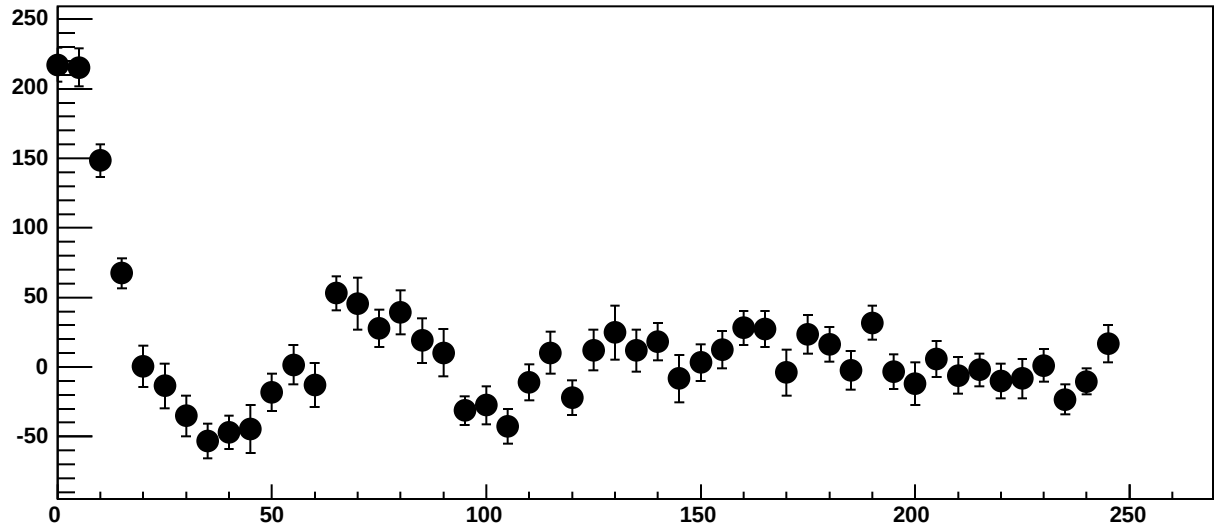
Chip 8, Channel 3, Enable 3, DAC=1600, ADC Mean vs Hold



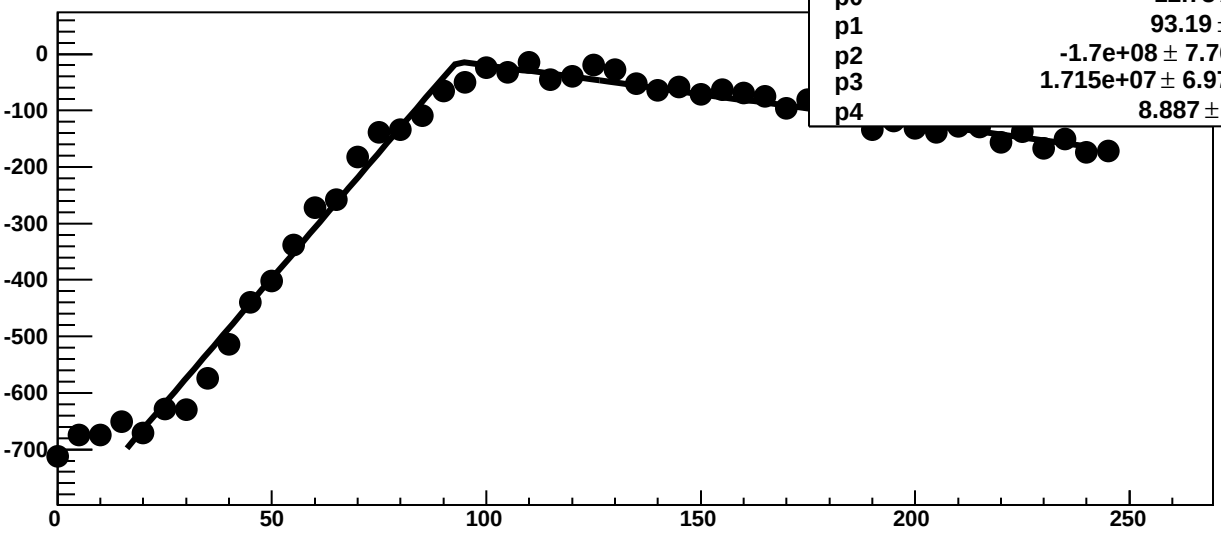
Chip 8, Channel 3, Enable 3, DAC=1600, ADC Noise vs Hold



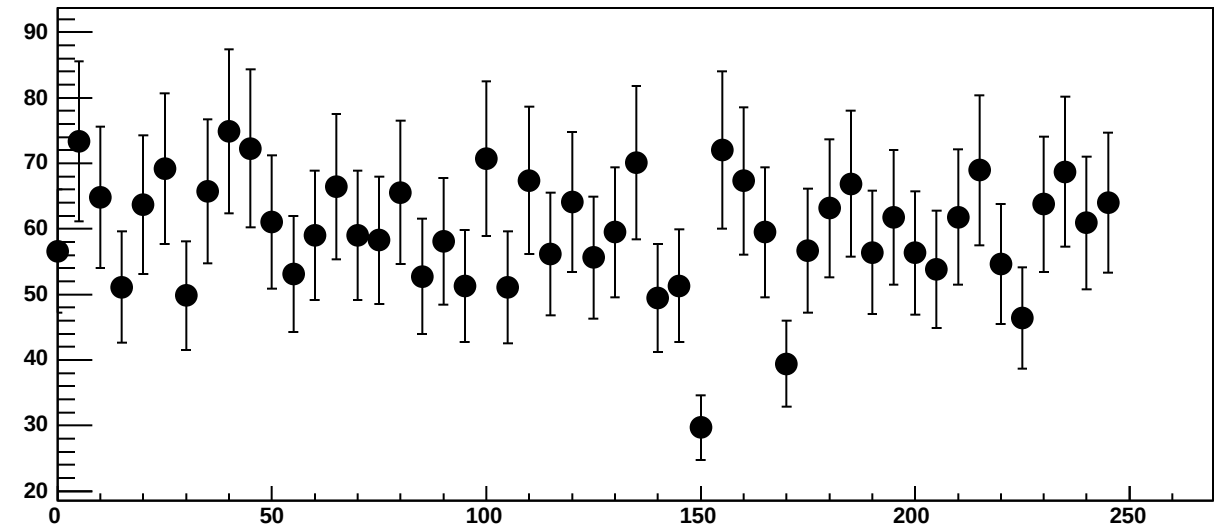
Chip 8, Channel 3, Enable 3, DAC=1600, ADC Residuals vs Hold



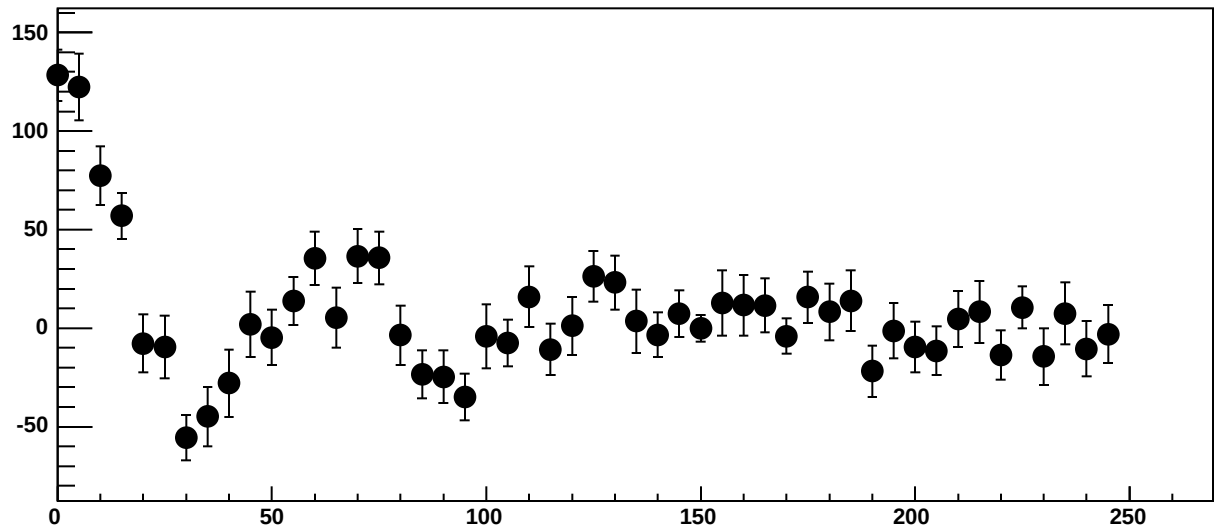
Chip 8, Channel 3, Enable 4, DAC=1600, ADC Mean vs Hold



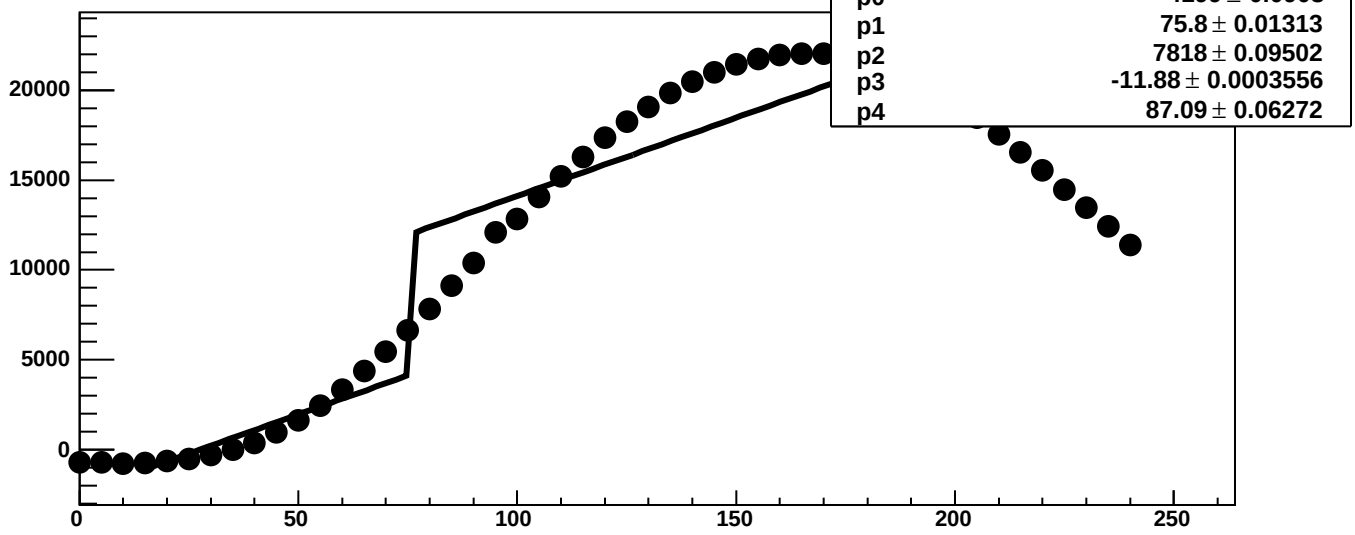
Chip 8, Channel 3, Enable 4, DAC=1600, ADC Noise vs Hold



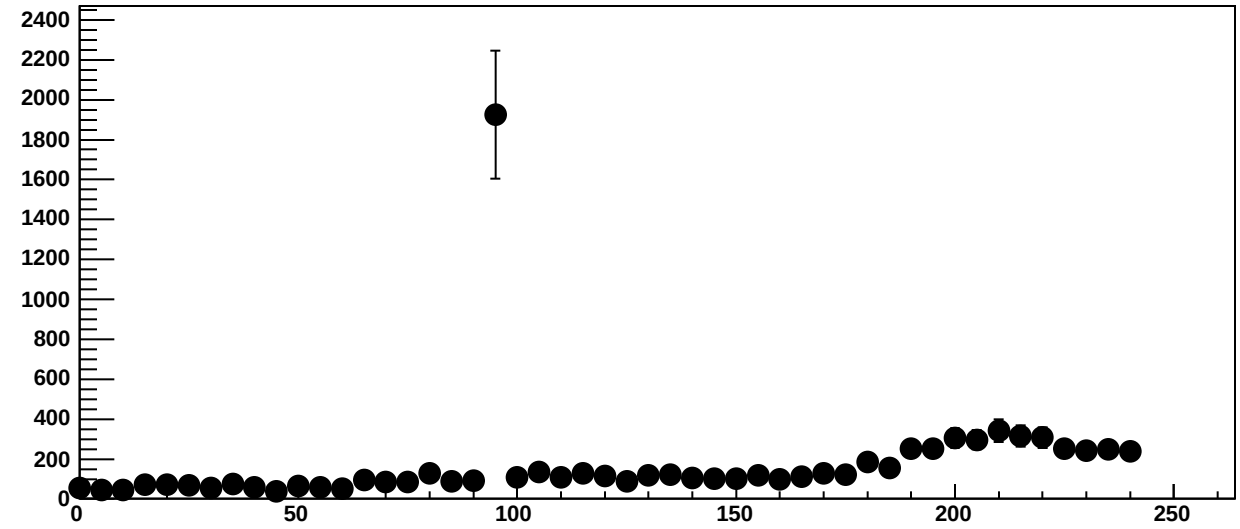
Chip 8, Channel 3, Enable 4, DAC=1600, ADC Residuals vs Hold



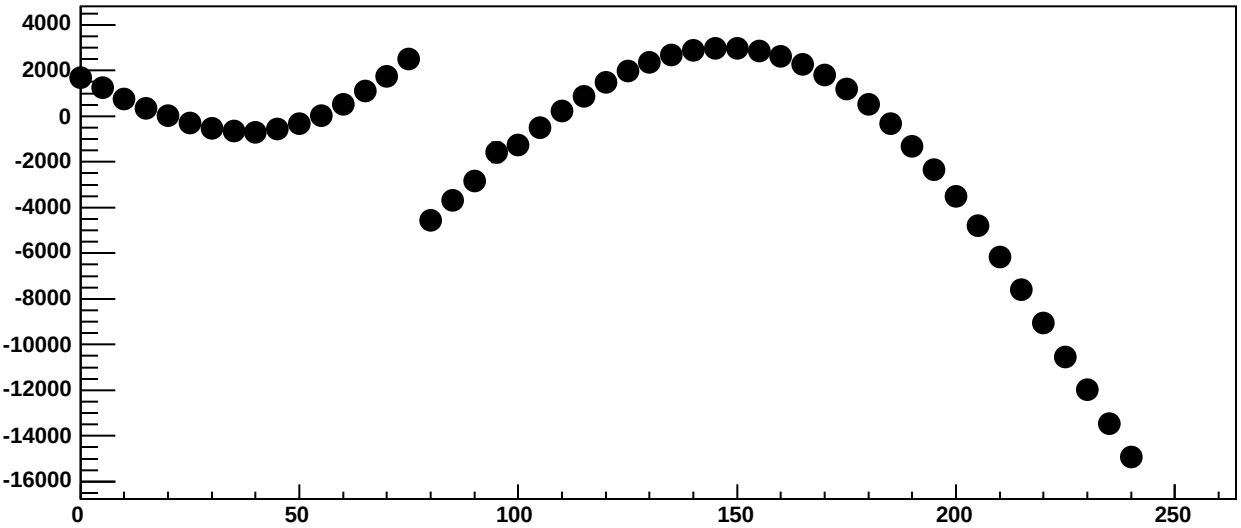
Chip 8, Channel 3, Enable 5, DAC=1600, ADC Mean vs Hold

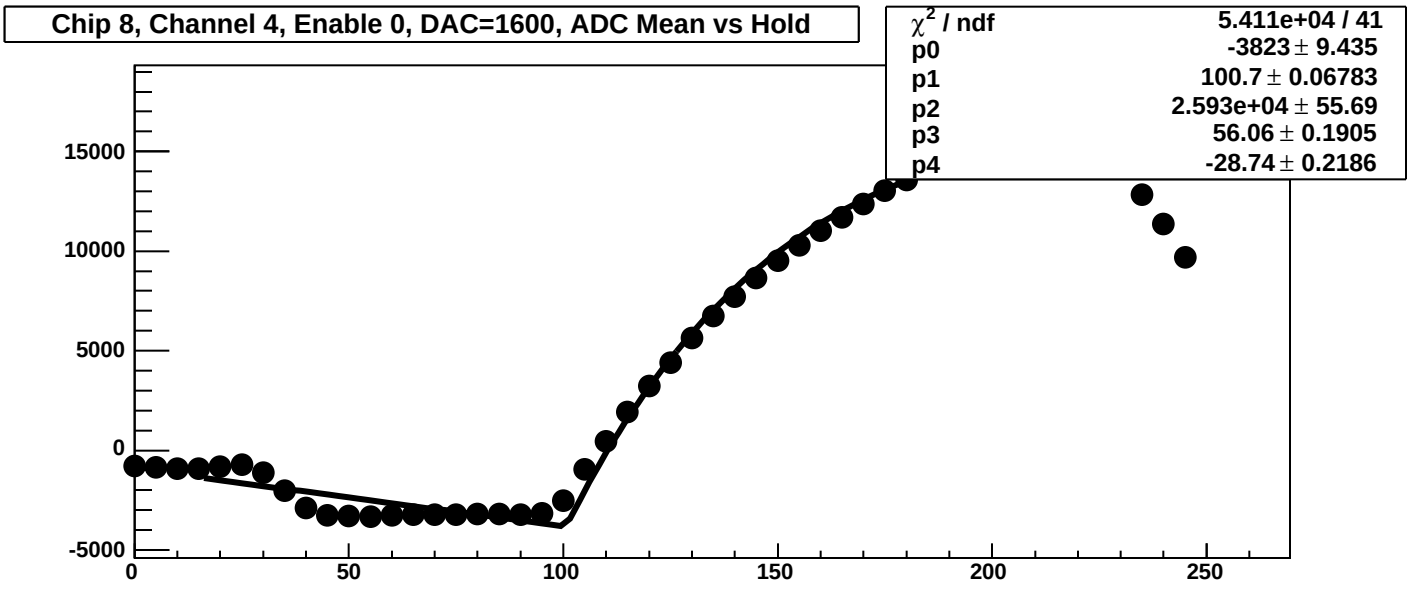


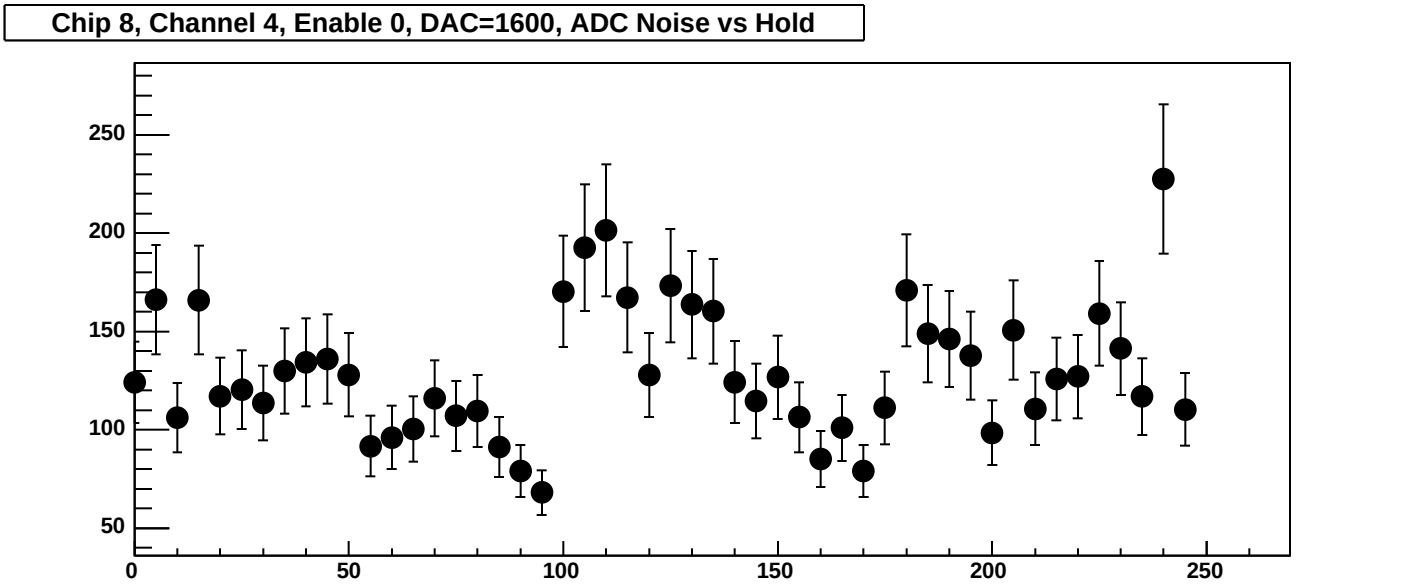
Chip 8, Channel 3, Enable 5, DAC=1600, ADC Noise vs Hold

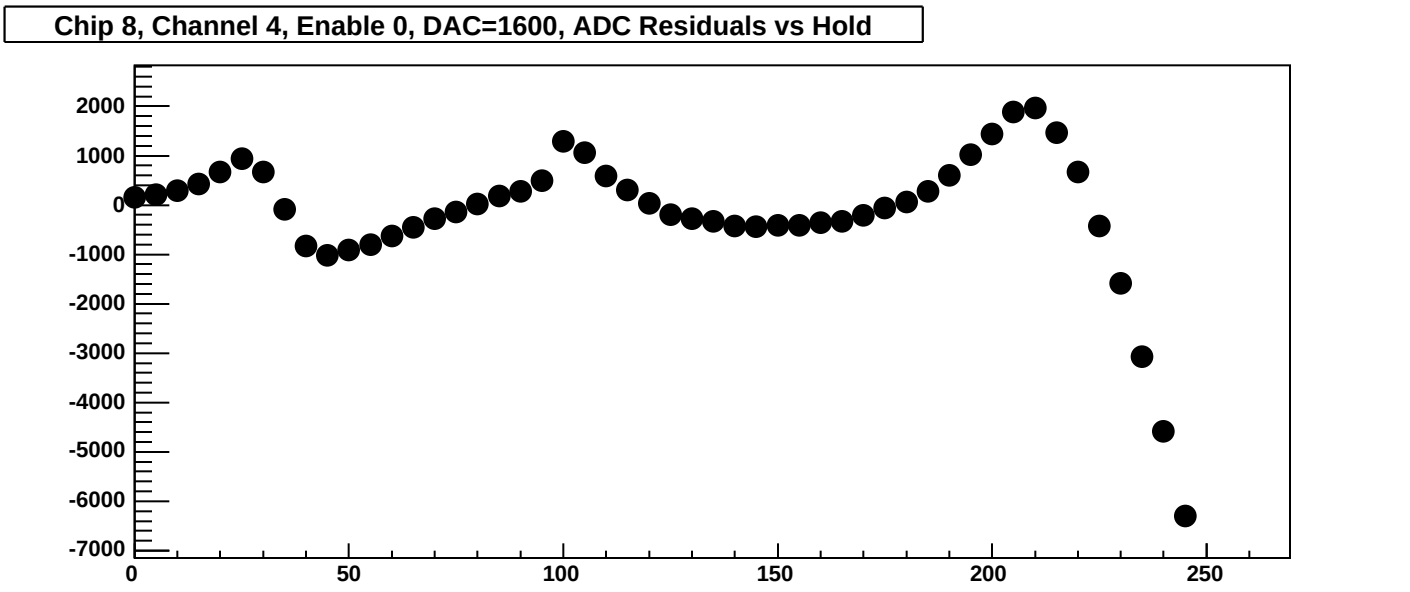


Chip 8, Channel 3, Enable 5, DAC=1600, ADC Residuals vs Hold

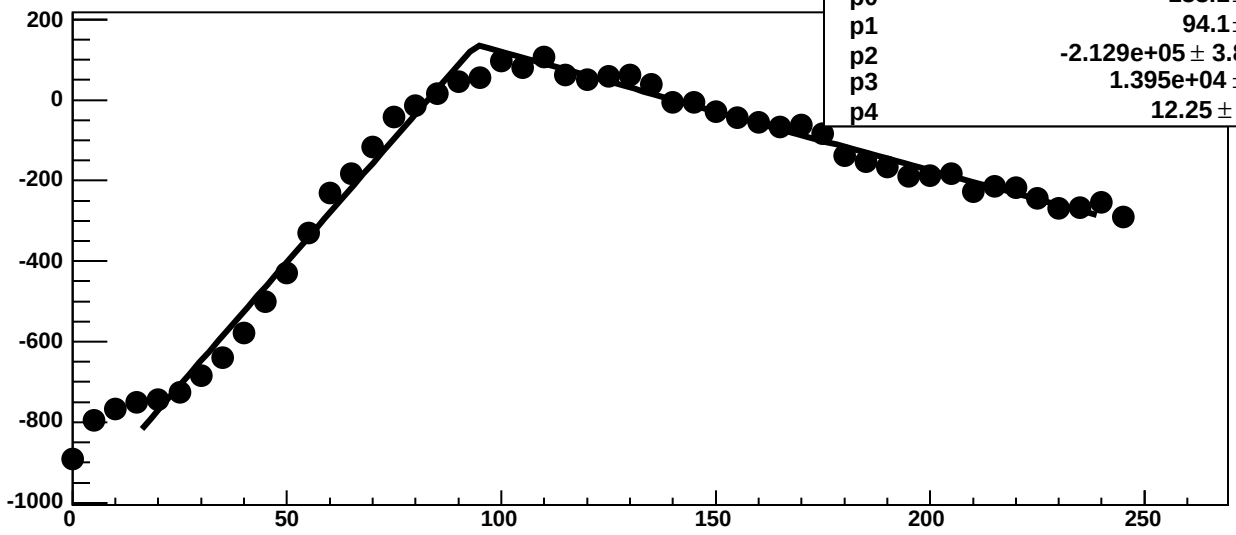



Chip 8, Channel 4, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.411e+04 / 41
p0 -3823 ± 9.435
p1 100.7 ± 0.06783
p2 2.593e+04 ± 55.69
p3 56.06 ± 0.1905
p4 -28.74 ± 0.2186
15000
10000
5000
0
-5000
0
50
100
150
200
250

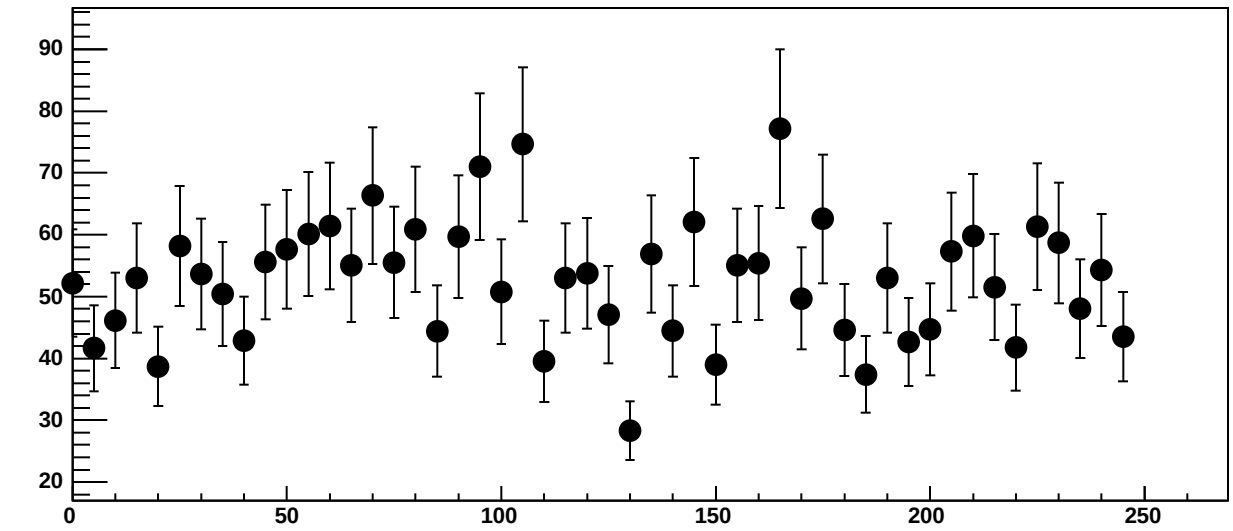

Chip 8, Channel 4, Enable 0, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
50
100
150
200
250


Chip 8, Channel 4, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
-7000
0
50
100
150
200
250

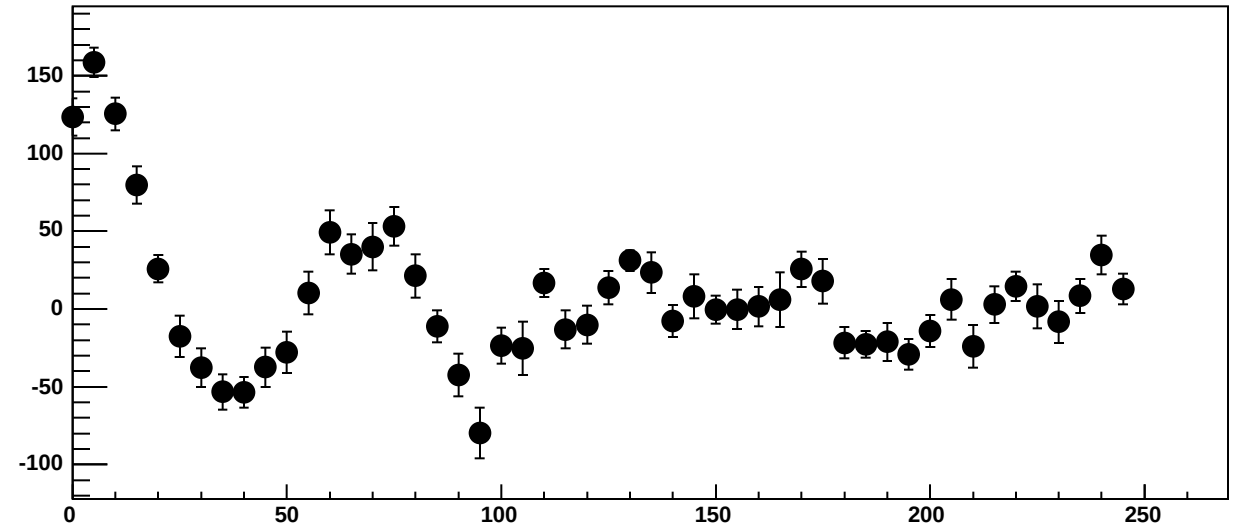
Chip 8, Channel 4, Enable 1, DAC=1600, ADC Mean vs Hold



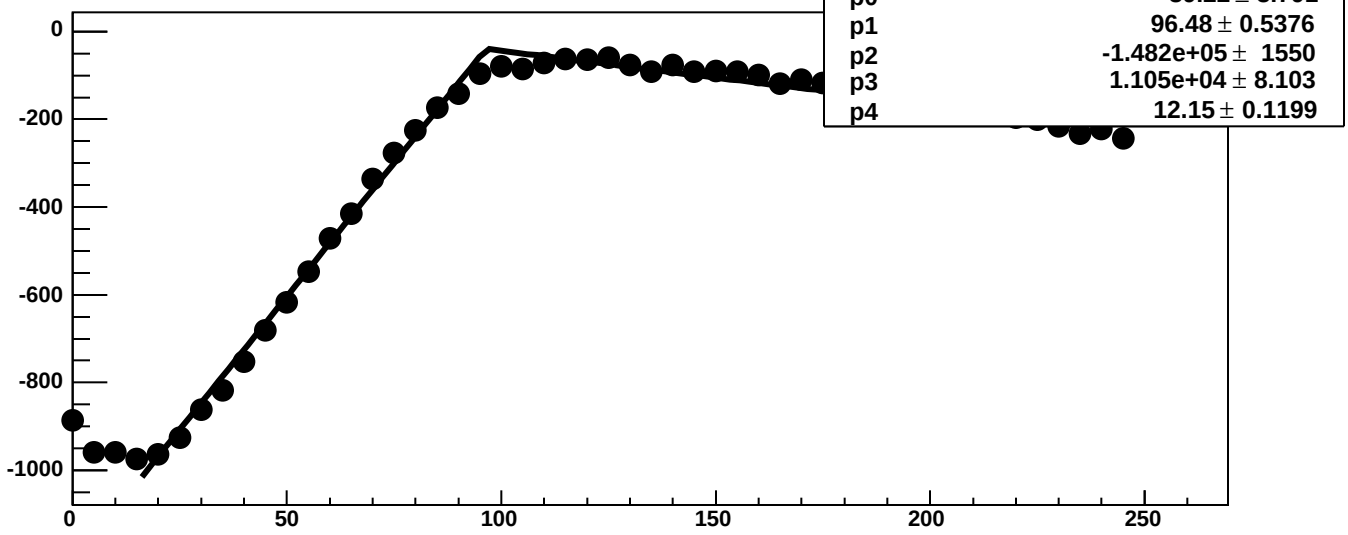
Chip 8, Channel 4, Enable 1, DAC=1600, ADC Noise vs Hold



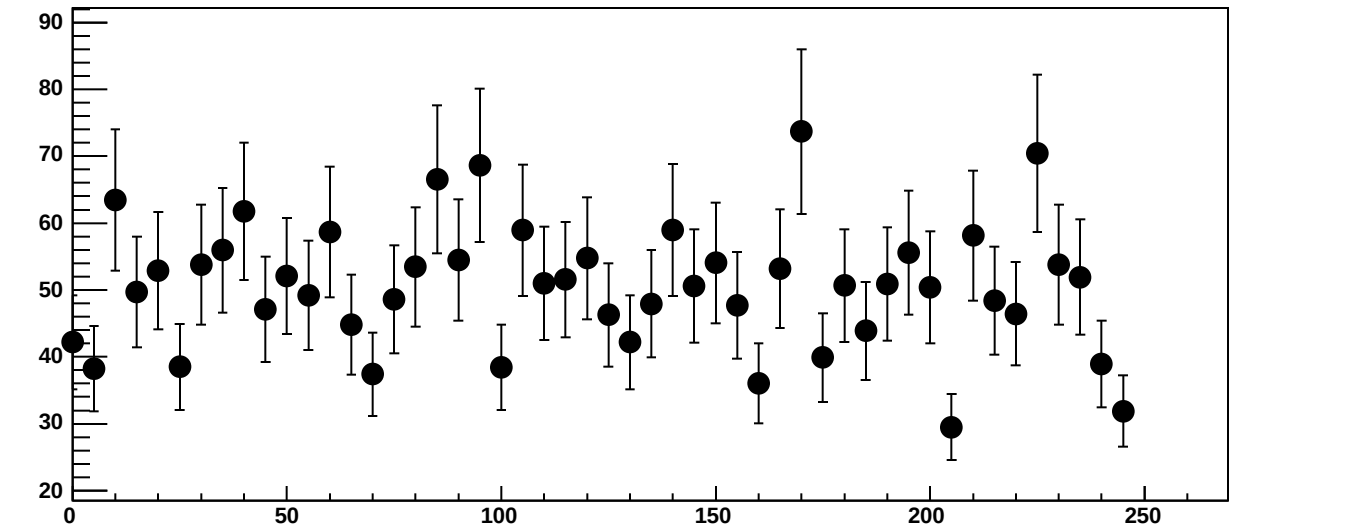
Chip 8, Channel 4, Enable 1, DAC=1600, ADC Residuals vs Hold



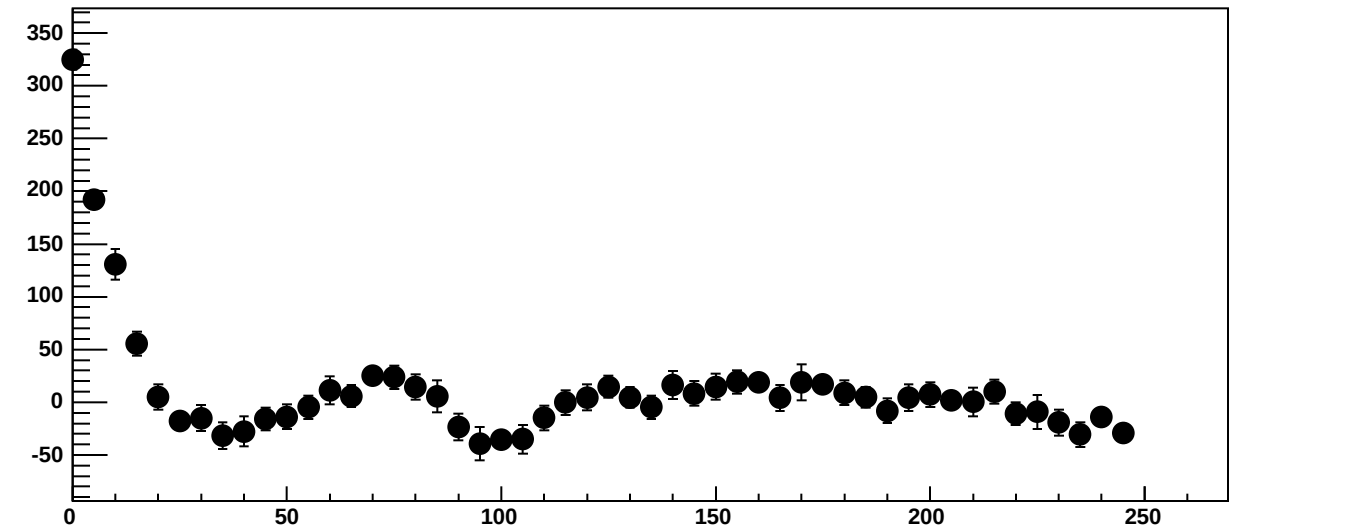
Chip 8, Channel 4, Enable 2, DAC=1600, ADC Mean vs Hold



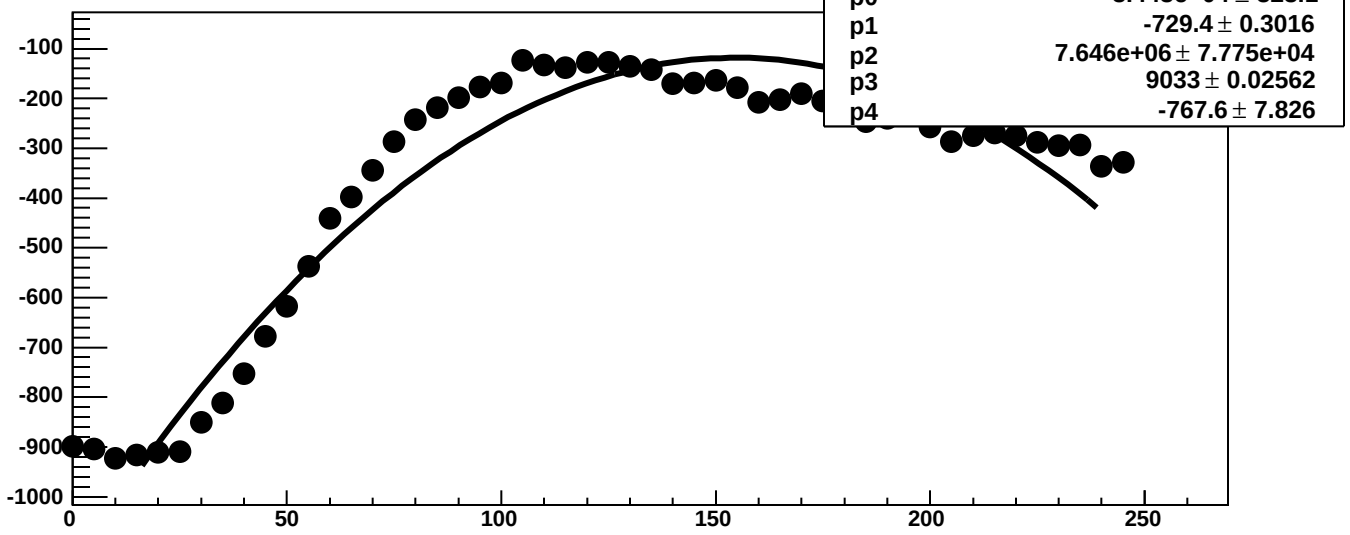
Chip 8, Channel 4, Enable 2, DAC=1600, ADC Noise vs Hold



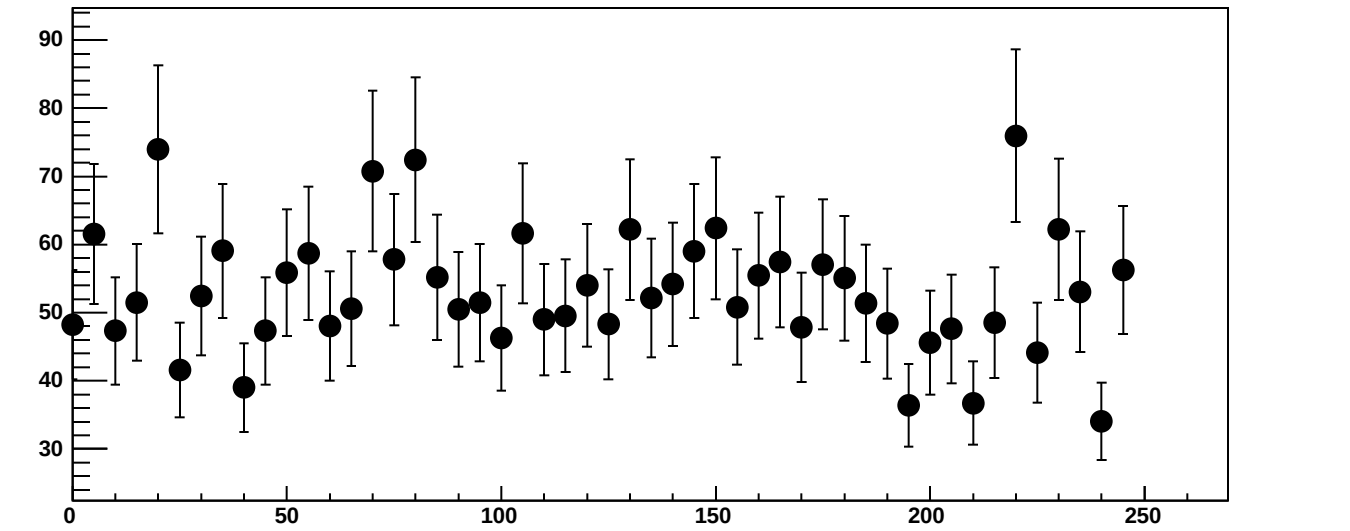
Chip 8, Channel 4, Enable 2, DAC=1600, ADC Residuals vs Hold



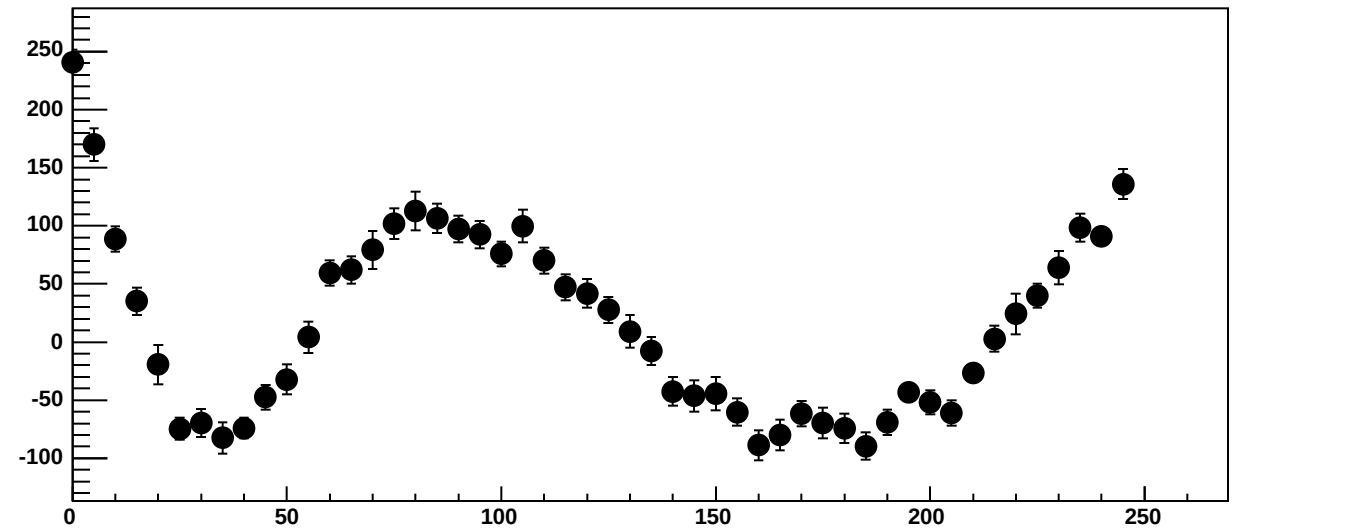
Chip 8, Channel 4, Enable 3, DAC=1600, ADC Mean vs Hold

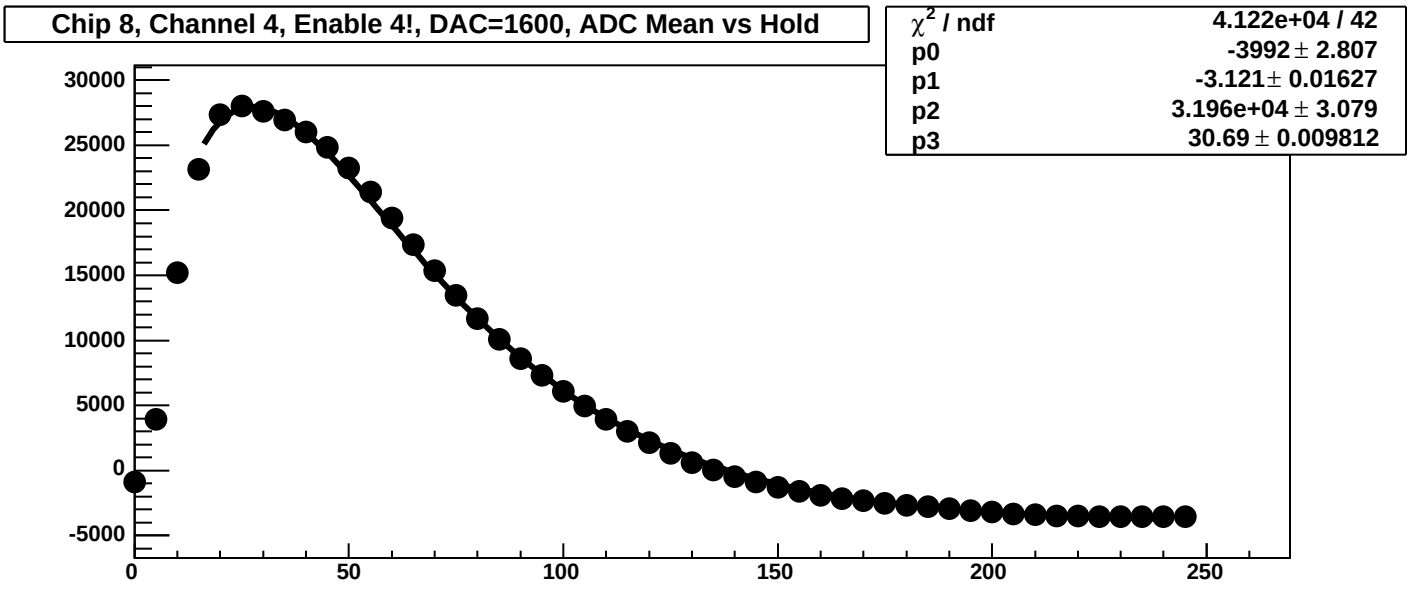


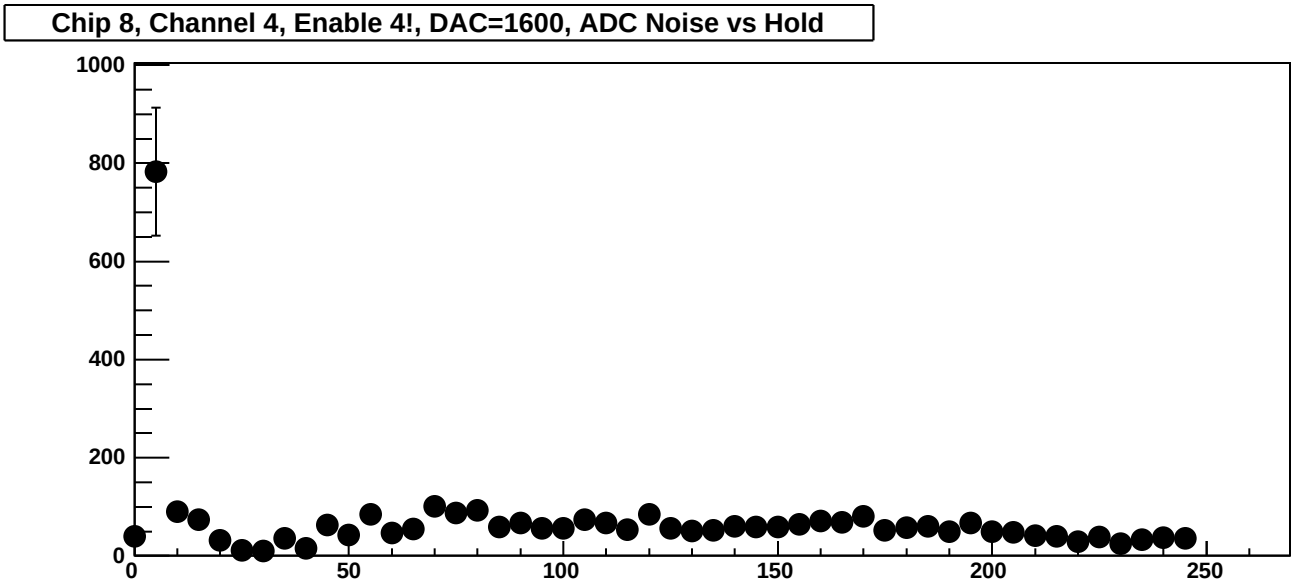
Chip 8, Channel 4, Enable 3, DAC=1600, ADC Noise vs Hold

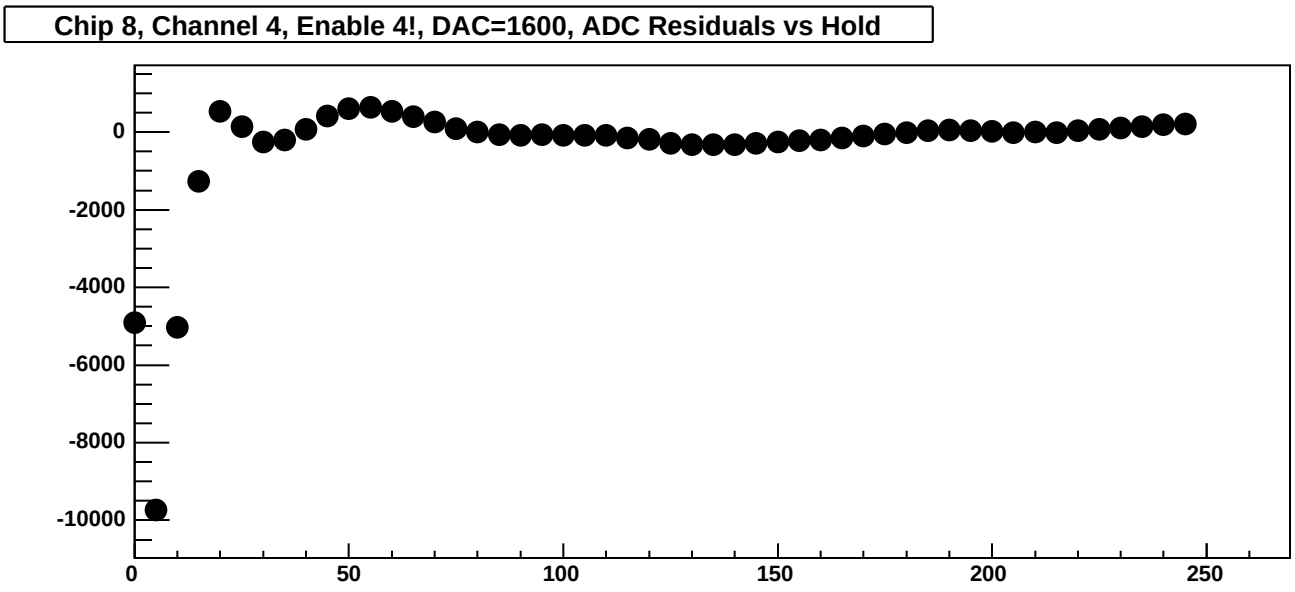


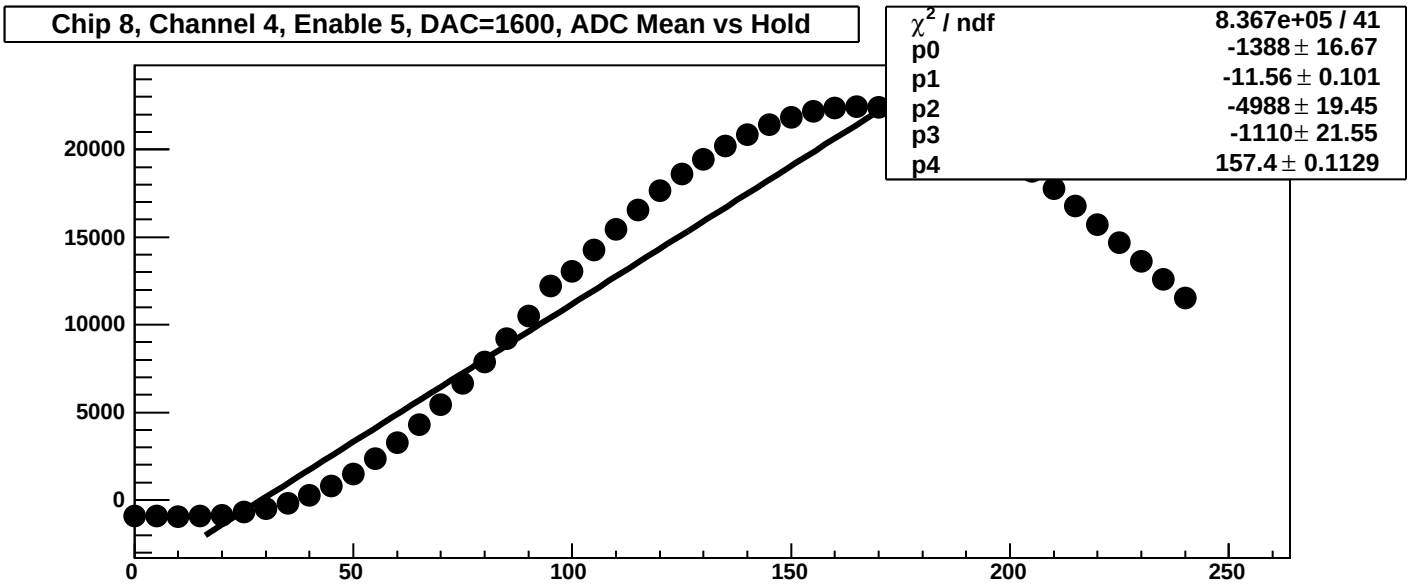
Chip 8, Channel 4, Enable 3, DAC=1600, ADC Residuals vs Hold

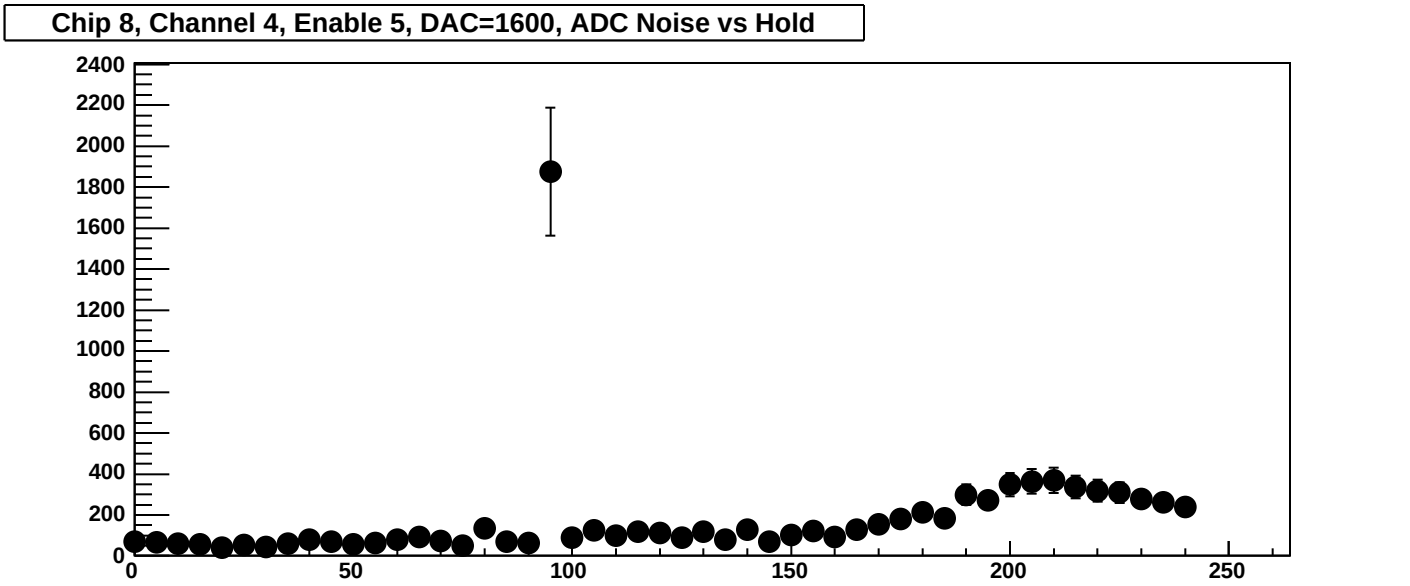


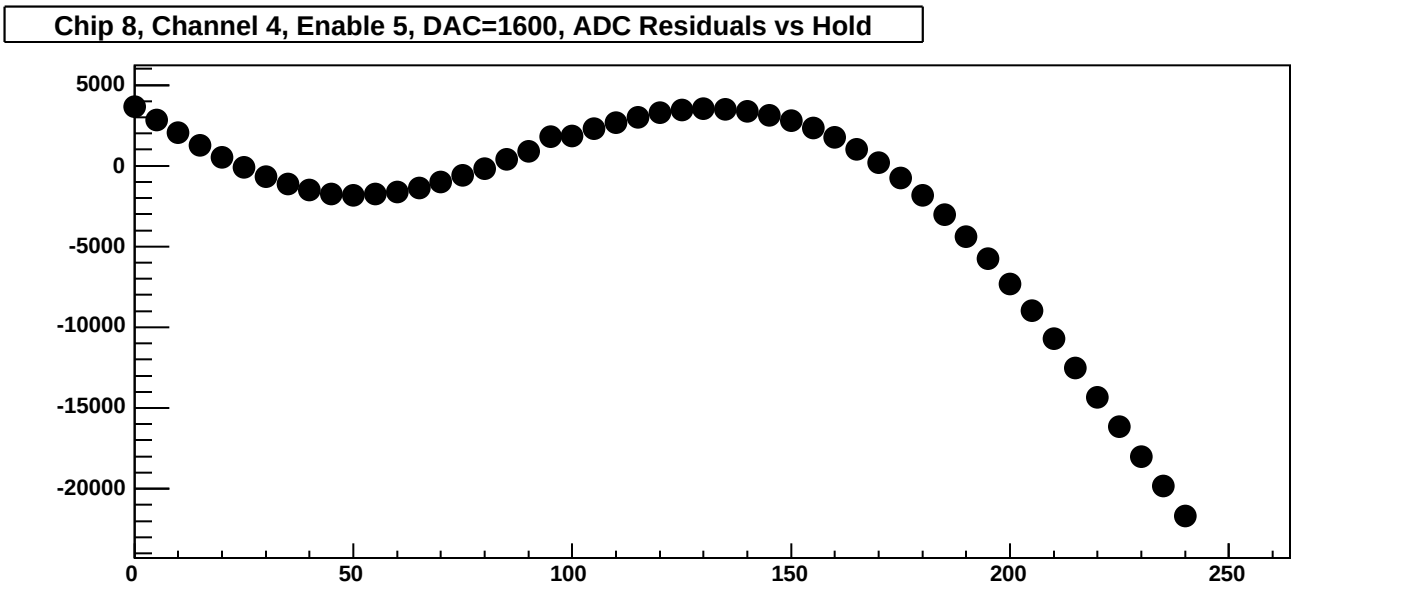

Chip 8, Channel 4, Enable 4!, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.122e+04 / 42
p0 -3992 ± 2.807
p1 -3.121 ± 0.01627
p2 3.196e+04 ± 3.079
p3 30.69 ± 0.009812
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

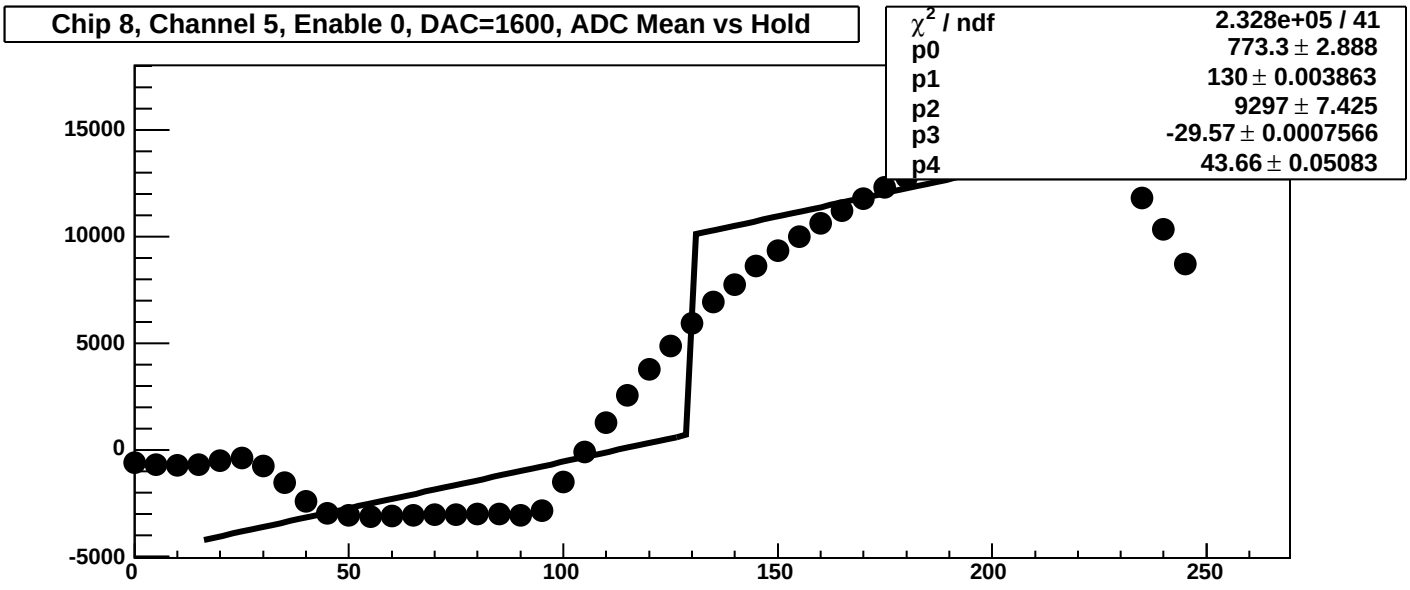

Chip 8, Channel 4, Enable 4!, DAC=1600, ADC Noise vs Hold
1000
800
600
400
200
0
0
50
100
150
200
250

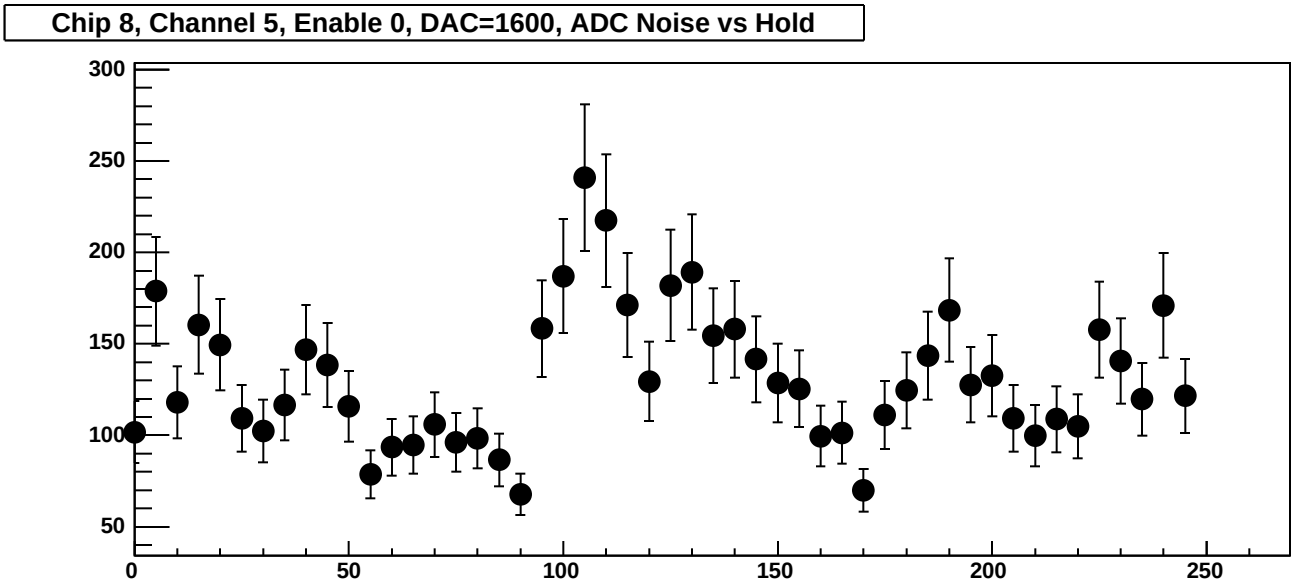

Chip 8, Channel 4, Enable 4!, DAC=1600, ADC Residuals vs Hold
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

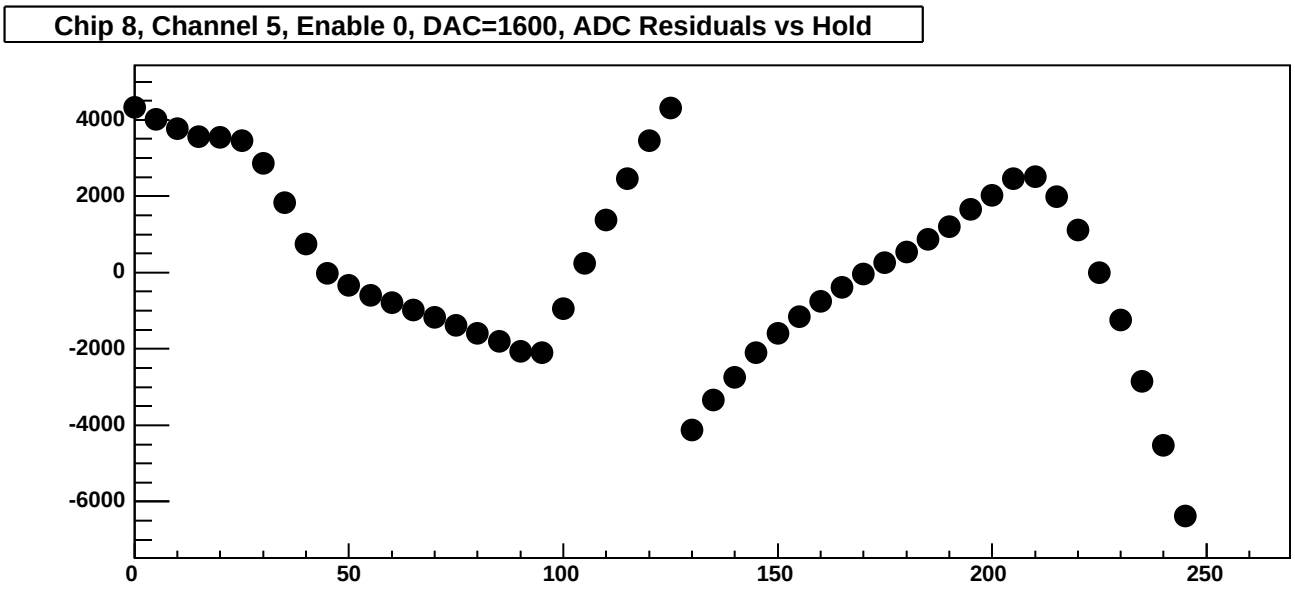

Chip 8, Channel 4, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 8.367e+05 / 41
p0 -1388 ± 16.67
p1 -11.56 ± 0.101
p2 -4988 ± 19.45
p3 -1110 ± 21.55
p4 157.4 ± 0.1129
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 8, Channel 4, Enable 5, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

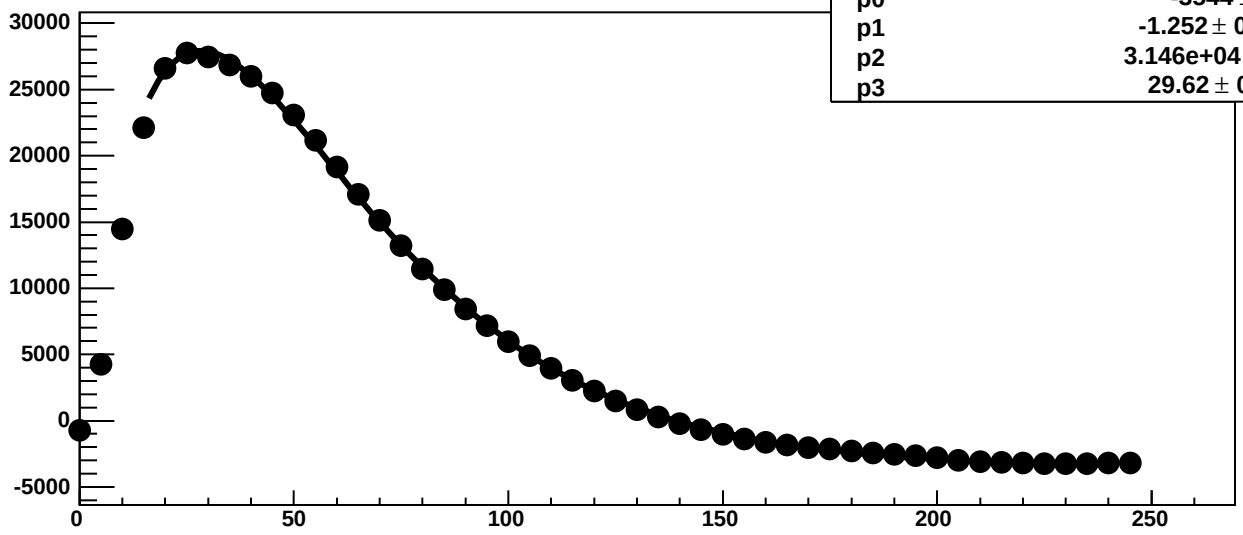

Chip 8, Channel 4, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250


Chip 8, Channel 5, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.328e+05 / 41
p0 773.3 ± 2.888
p1 130 ± 0.003863
p2 9297 ± 7.425
p3 -29.57 ± 0.0007566
p4 43.66 ± 0.05083
15000
10000
5000
0
-5000
0
50
100
150
200
250

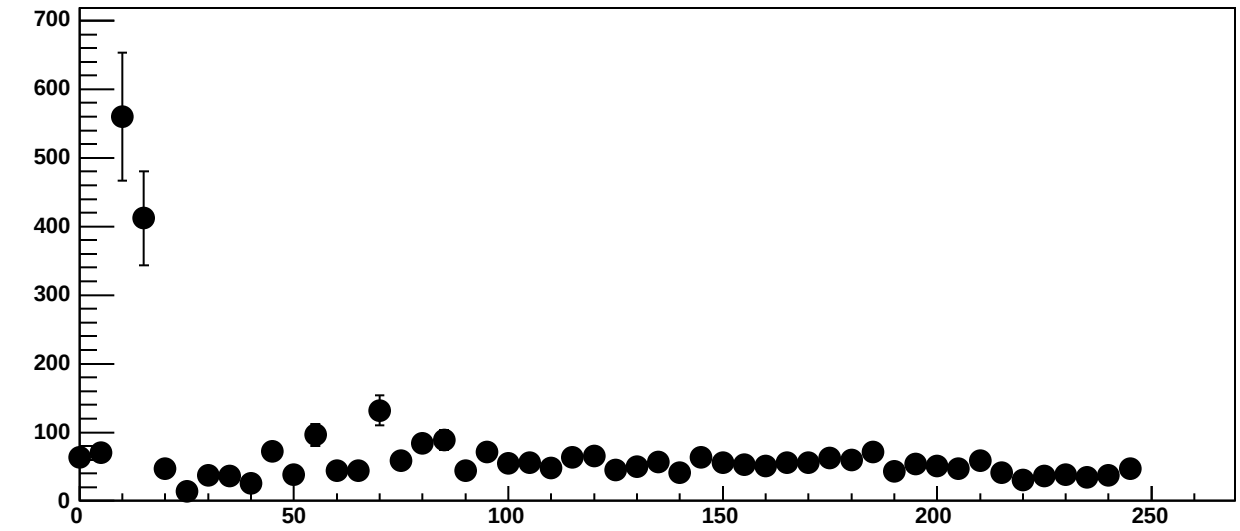

Chip 8, Channel 5, Enable 0, DAC=1600, ADC Noise vs Hold
300
250
200
150
100
50
0
50
100
150
200
250


Chip 8, Channel 5, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

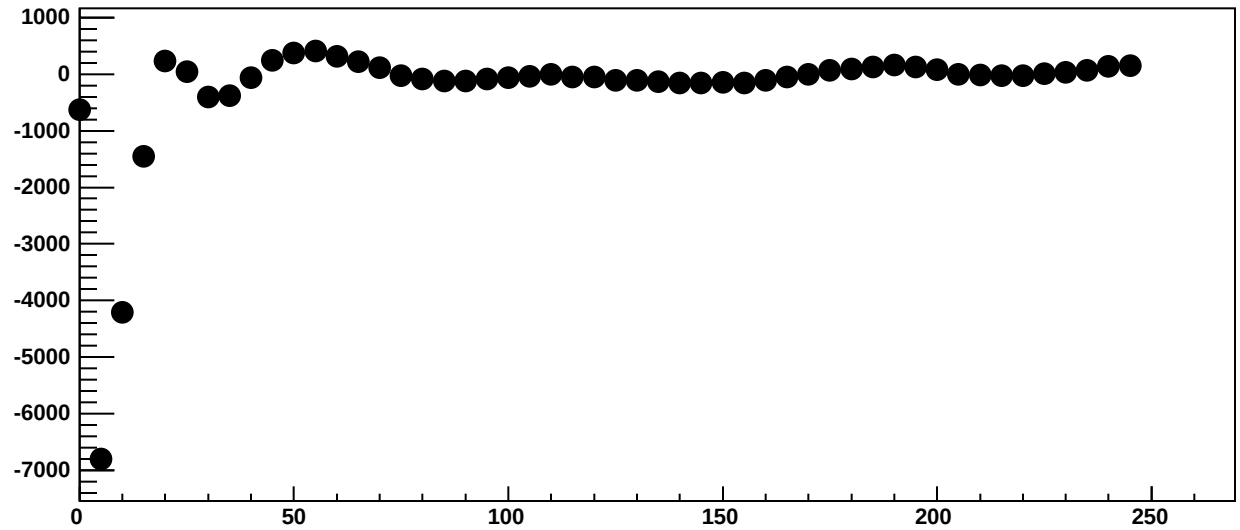
Chip 8, Channel 5, Enable 1!, DAC=1600, ADC Mean vs Hold

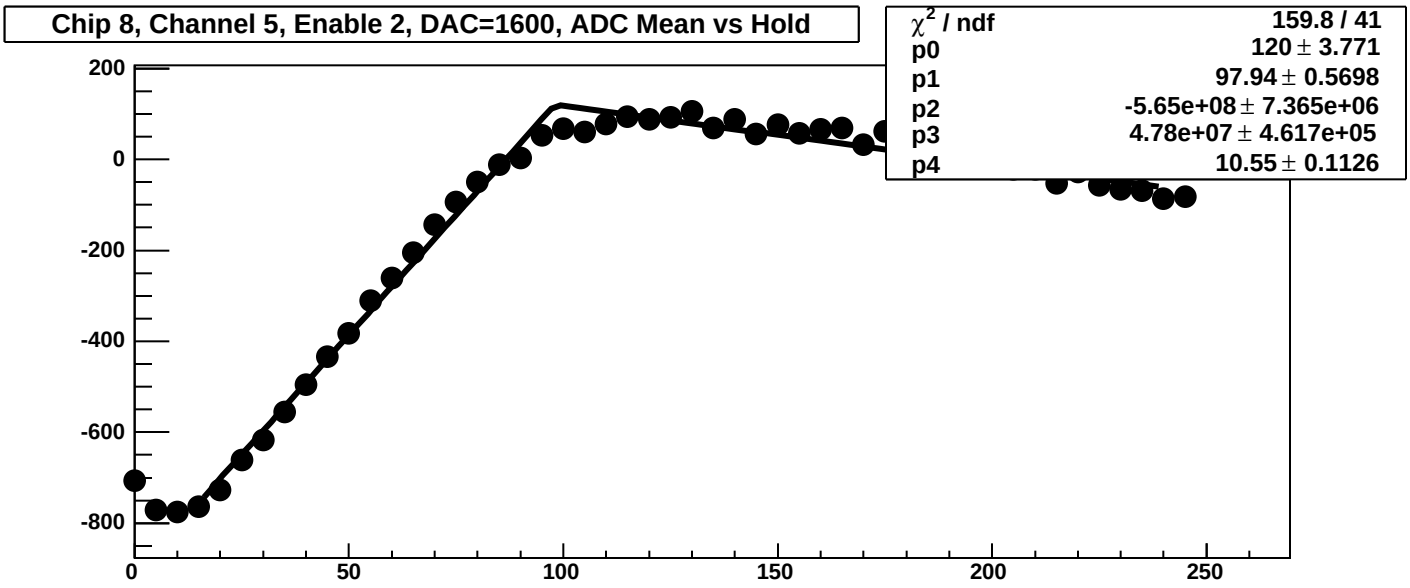


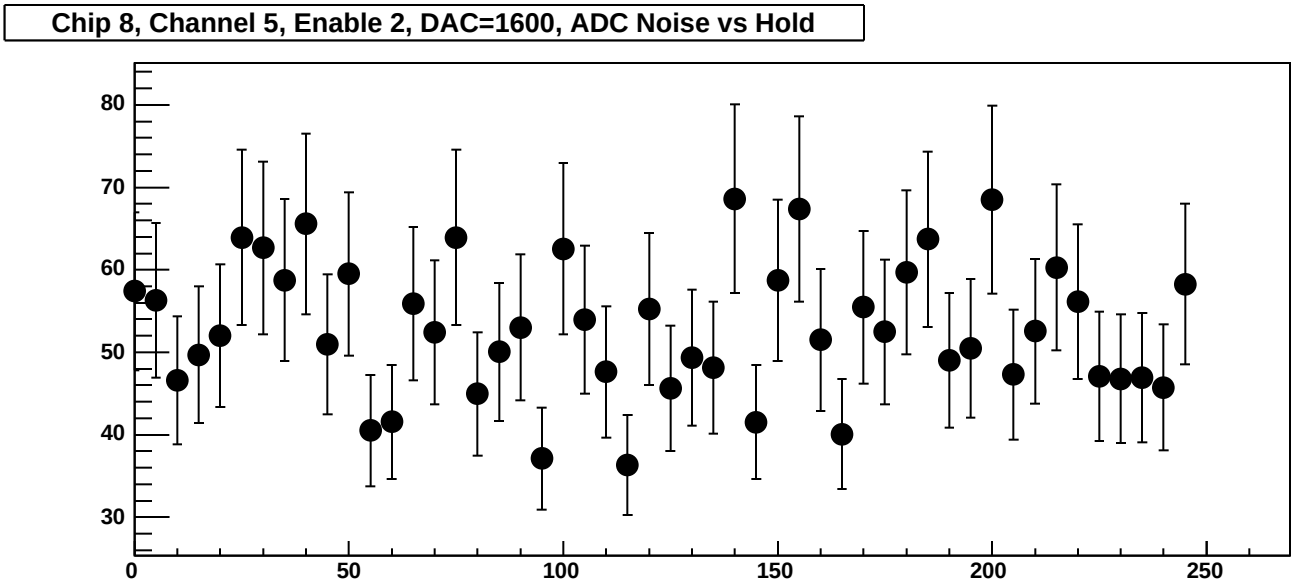
Chip 8, Channel 5, Enable 1!, DAC=1600, ADC Noise vs Hold

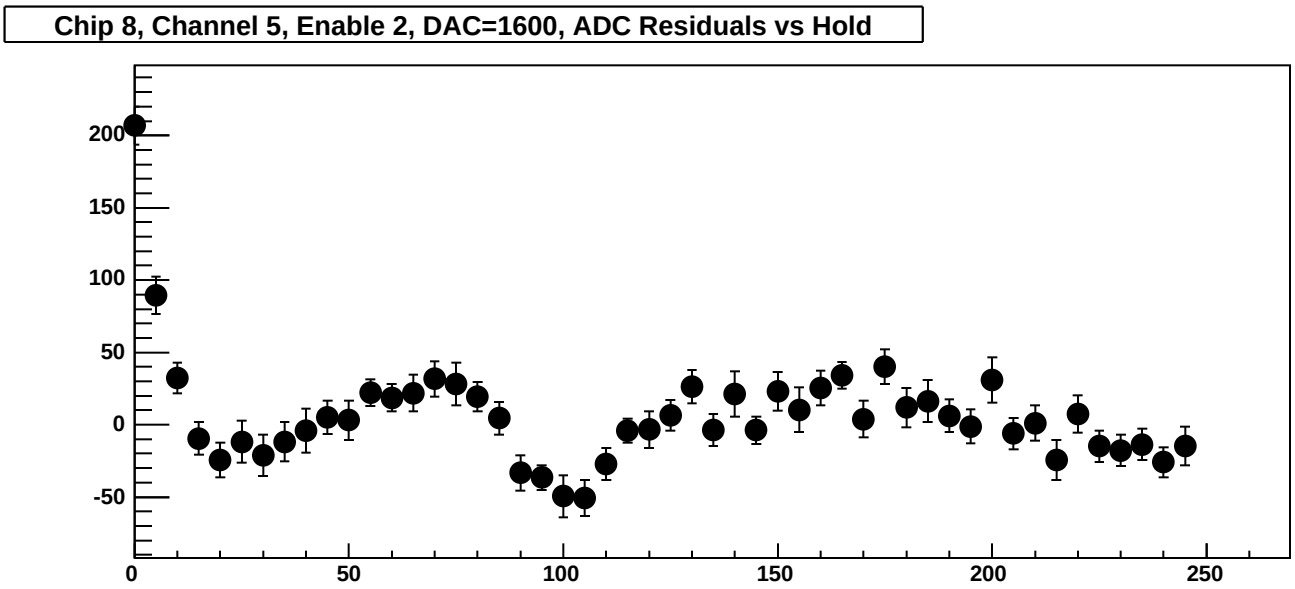


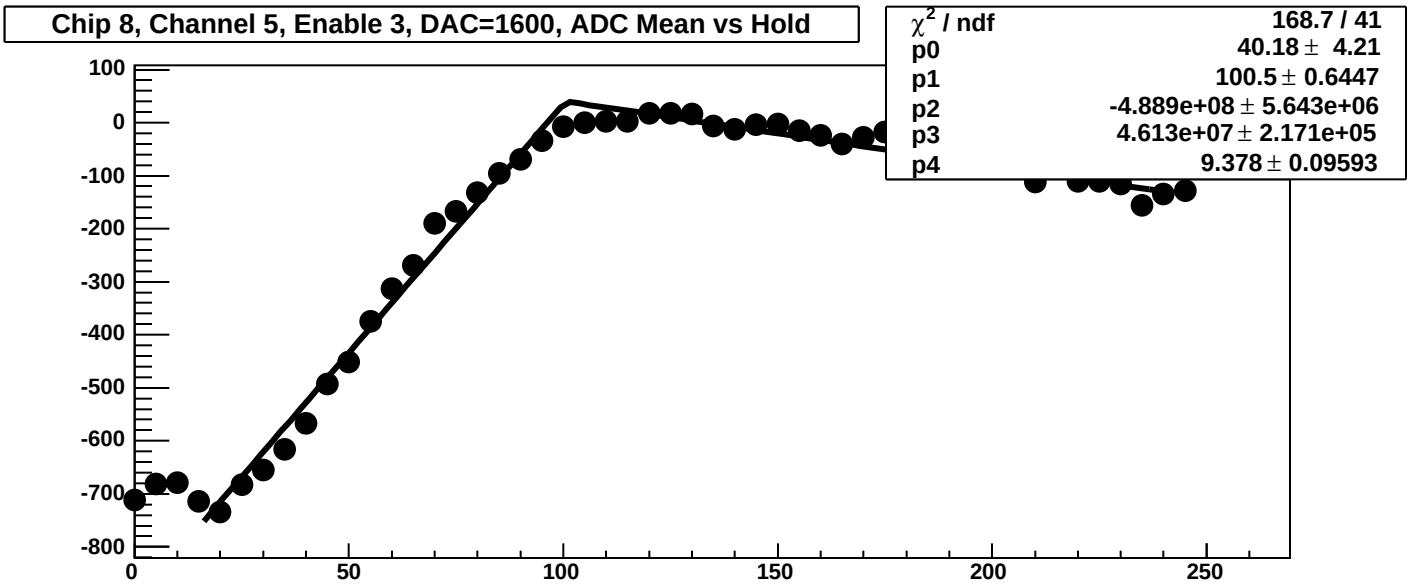
Chip 8, Channel 5, Enable 1!, DAC=1600, ADC Residuals vs Hold

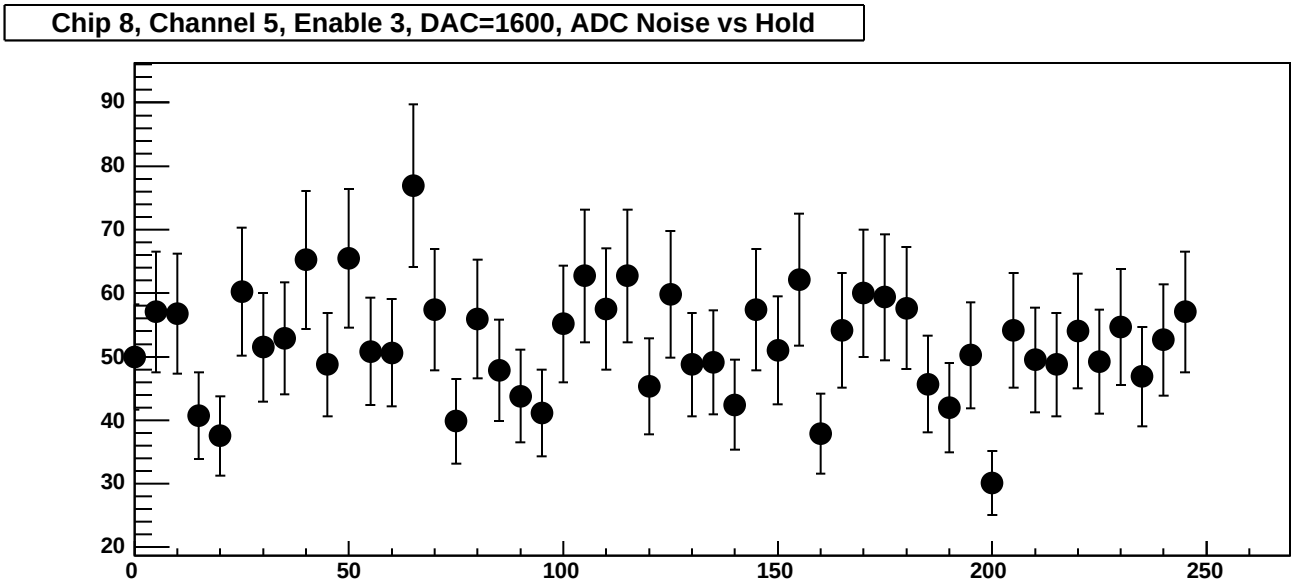


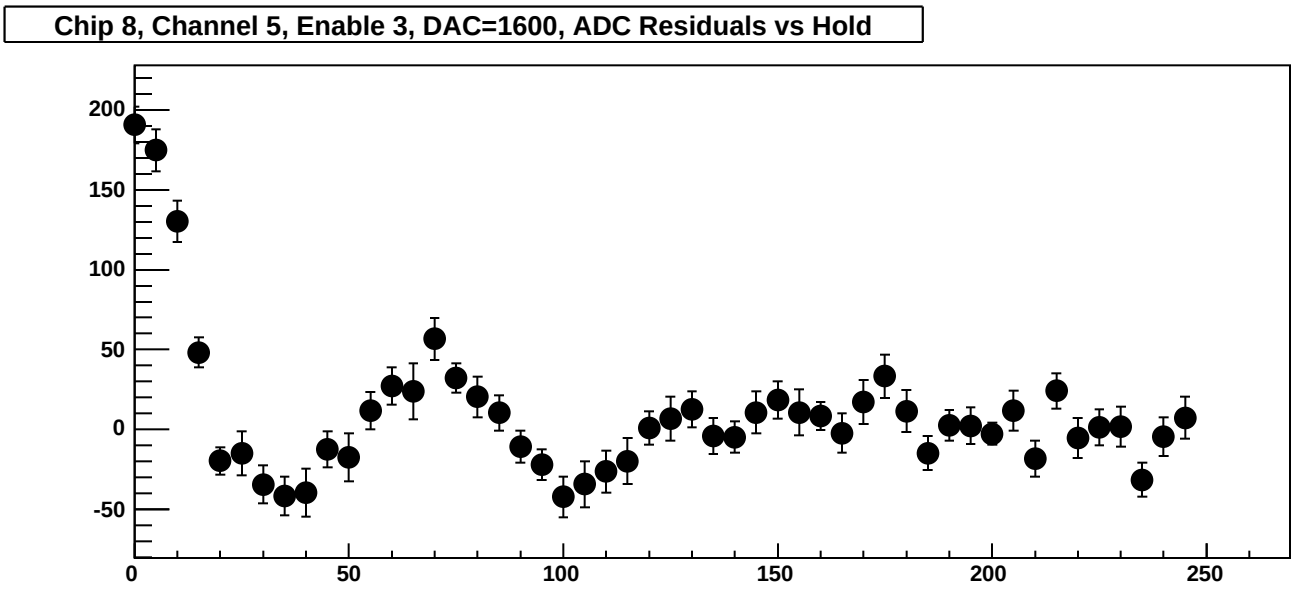

Chip 8, Channel 5, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 159.8 / 41
p0 120 ± 3.771
p1 97.94 ± 0.5698
p2 -5.65e+08 ± 7.365e+06
p3 4.78e+07 ± 4.617e+05
p4 10.55 ± 0.1126
200
0
-200
-400
-600
-800
0
50
100
150
200
250


Chip 8, Channel 5, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

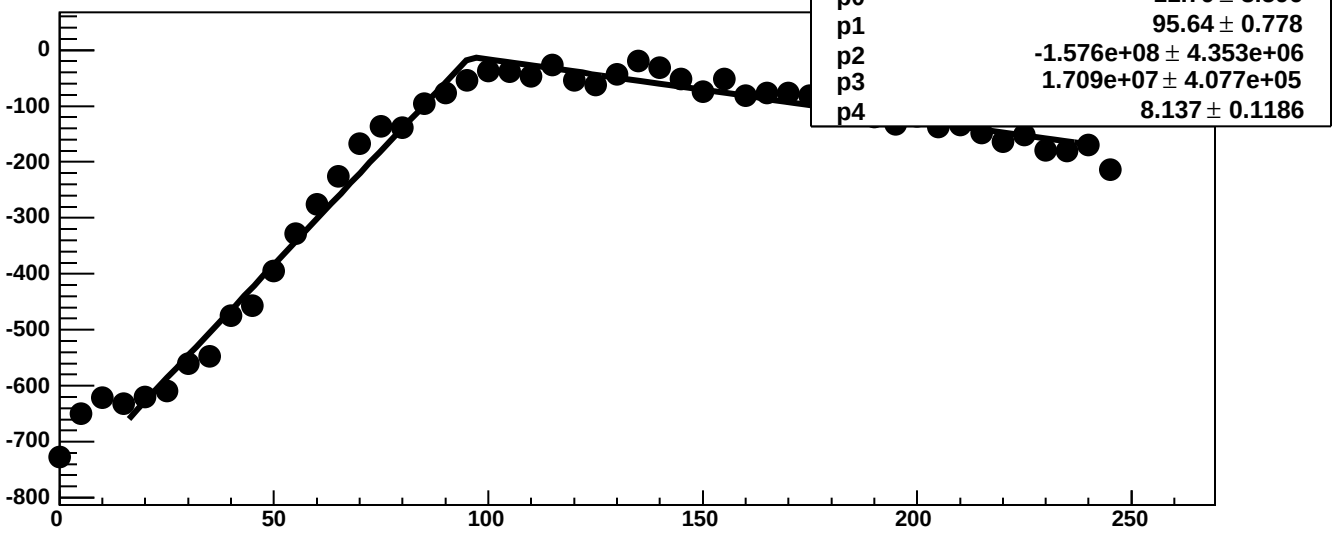

Chip 8, Channel 5, Enable 2, DAC=1600, ADC Residuals vs Hold
200
150
100
50
0
-50
0
50
100
150
200
250


Chip 8, Channel 5, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 168.7 / 41
p0 40.18 ± 4.21
p1 100.5 ± 0.6447
p2 -4.889e+08 ± 5.643e+06
p3 4.613e+07 ± 2.171e+05
p4 9.378 ± 0.09593
100
0
-100
-200
-300
-400
-500
-600
-700
-800
0
50
100
150
200
250

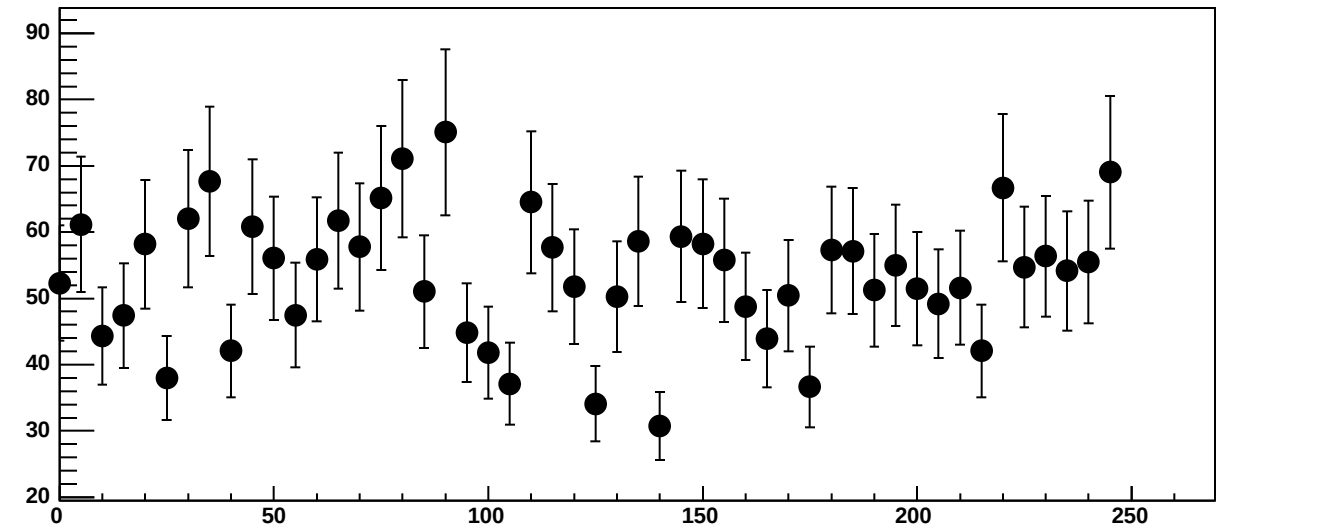

Chip 8, Channel 5, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 8, Channel 5, Enable 3, DAC=1600, ADC Residuals vs Hold
200
150
100
50
0
-50
0
50
100
150
200
250

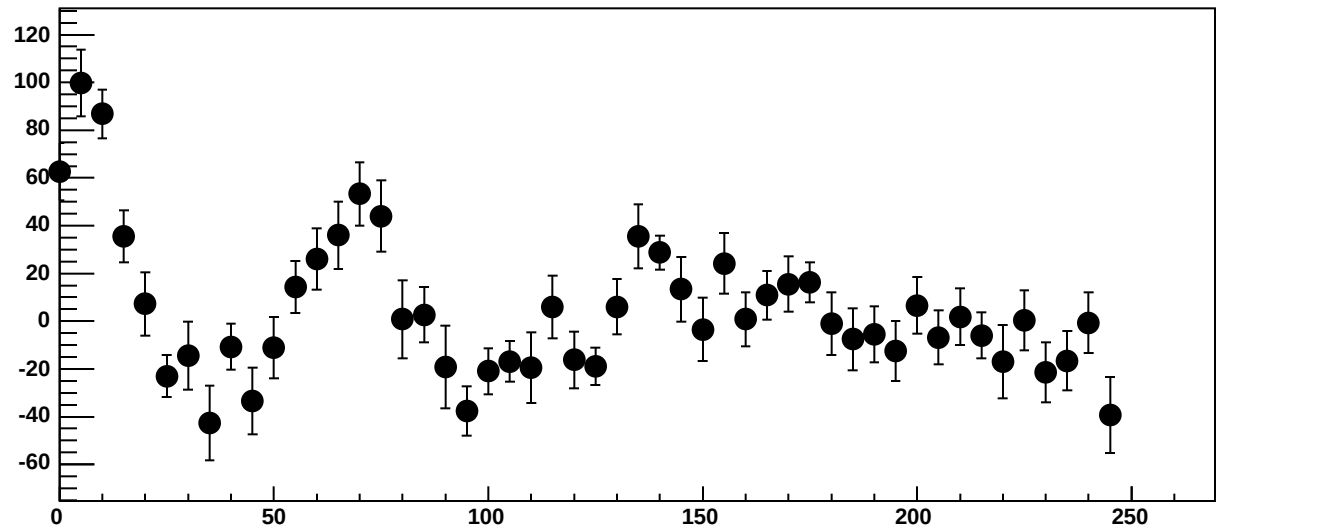
Chip 8, Channel 5, Enable 4, DAC=1600, ADC Mean vs Hold

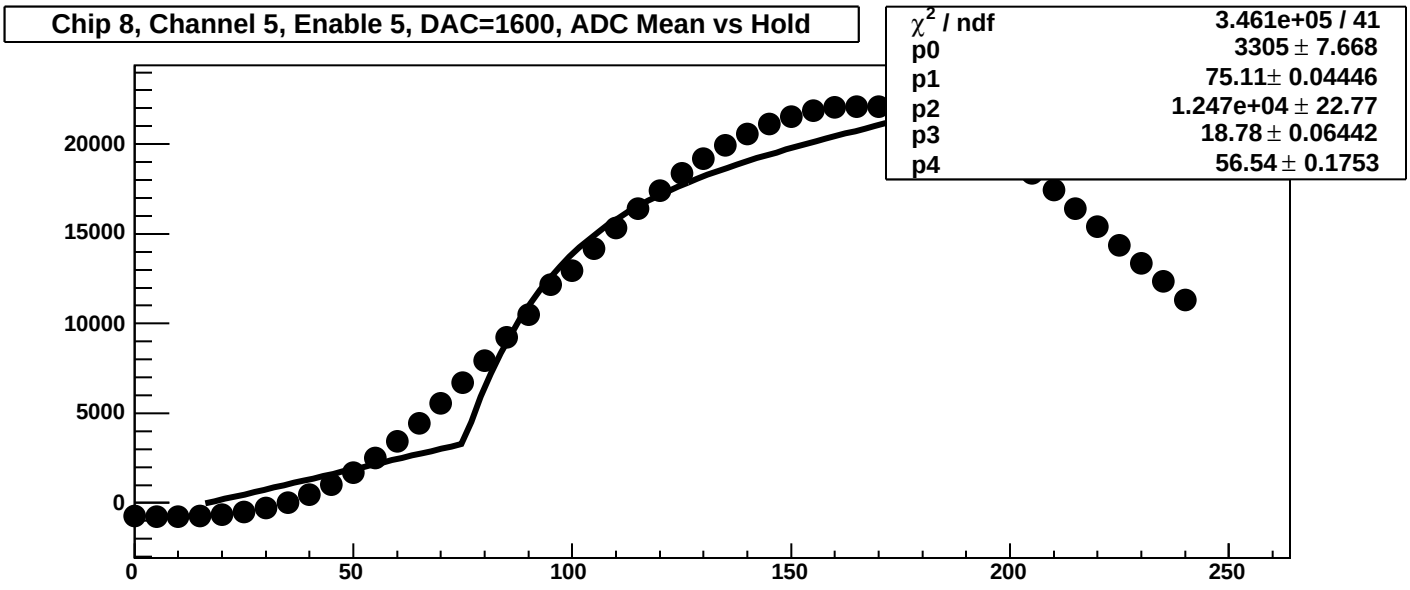


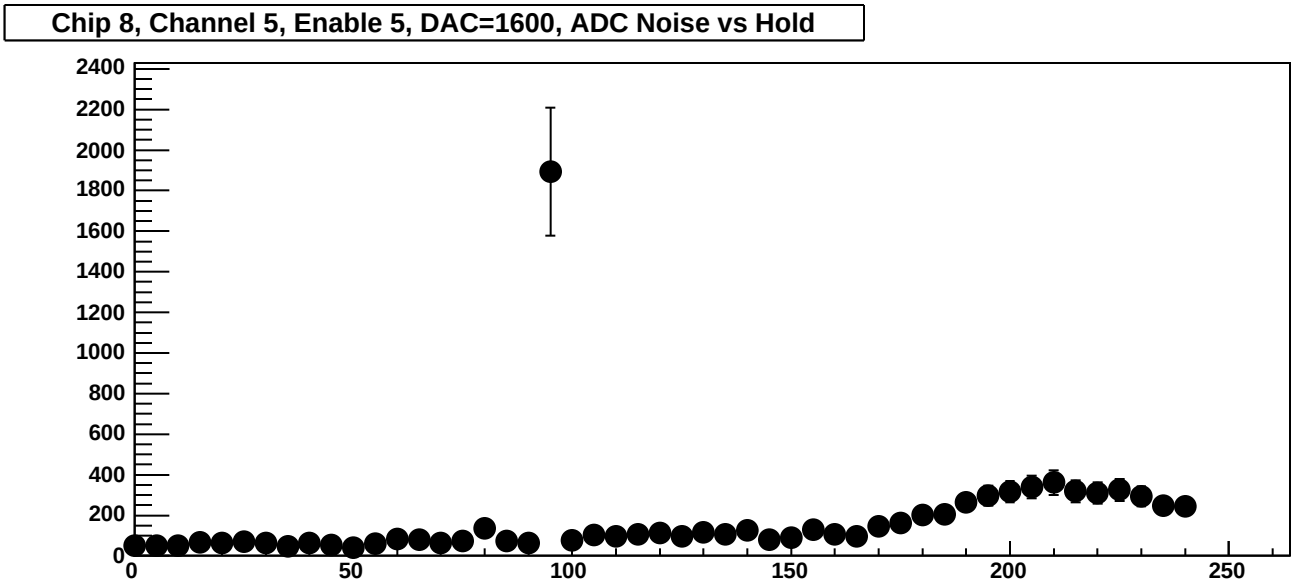
Chip 8, Channel 5, Enable 4, DAC=1600, ADC Noise vs Hold

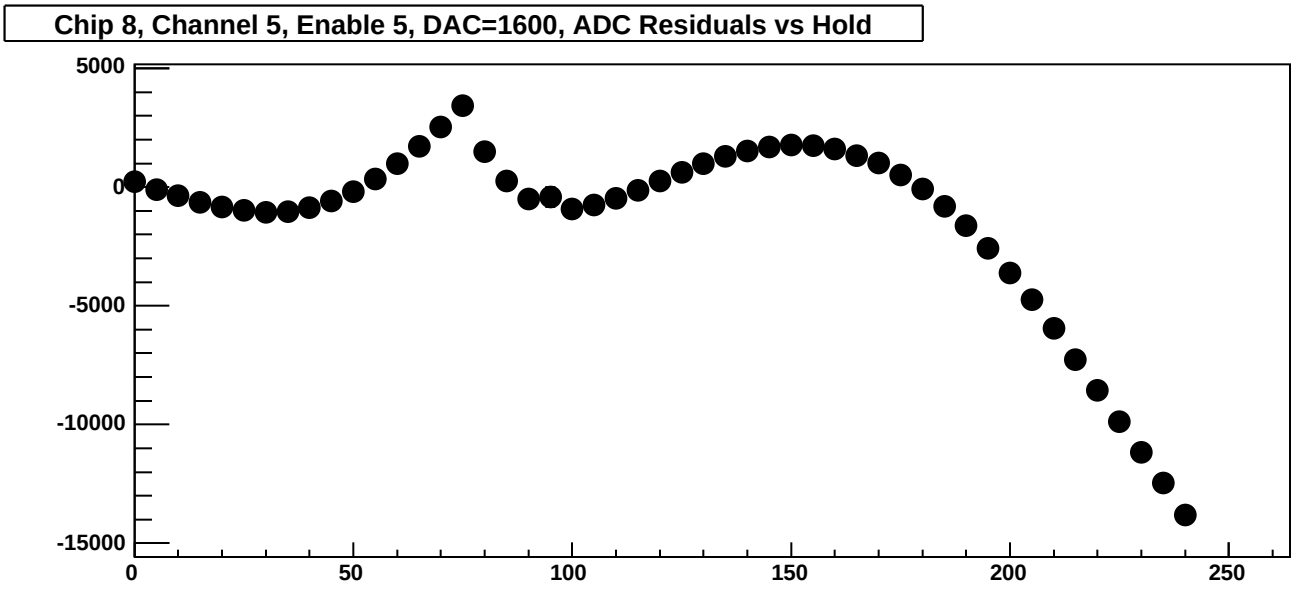


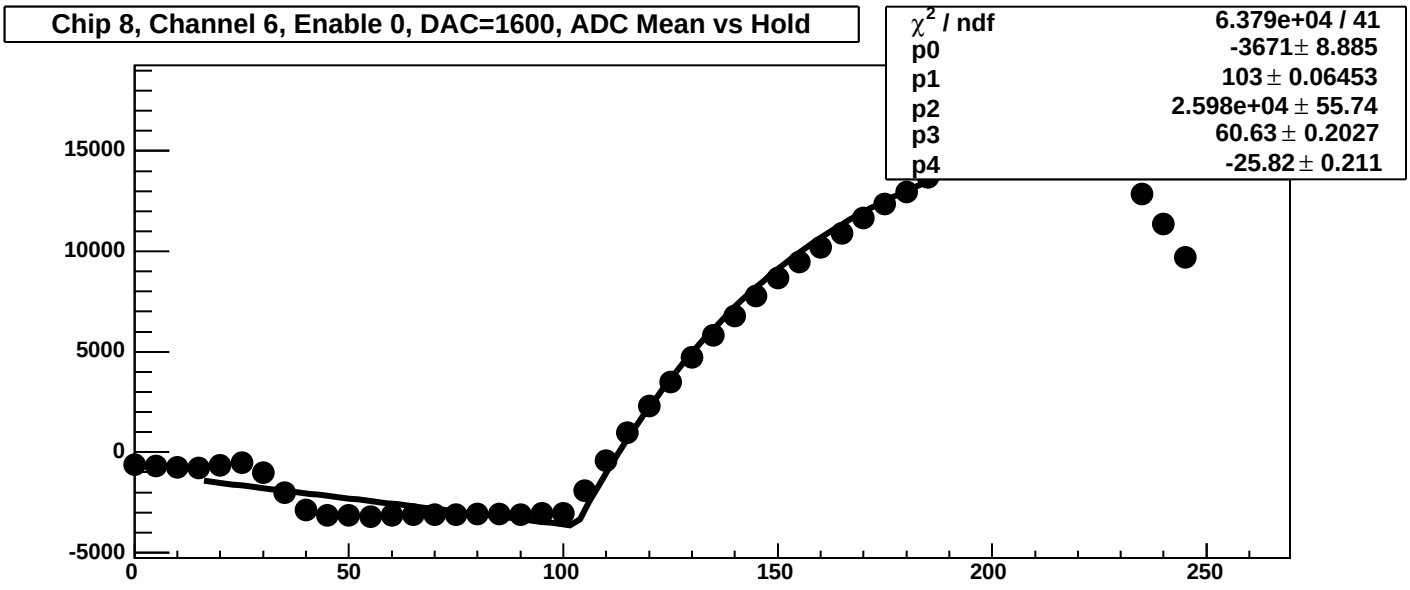
Chip 8, Channel 5, Enable 4, DAC=1600, ADC Residuals vs Hold

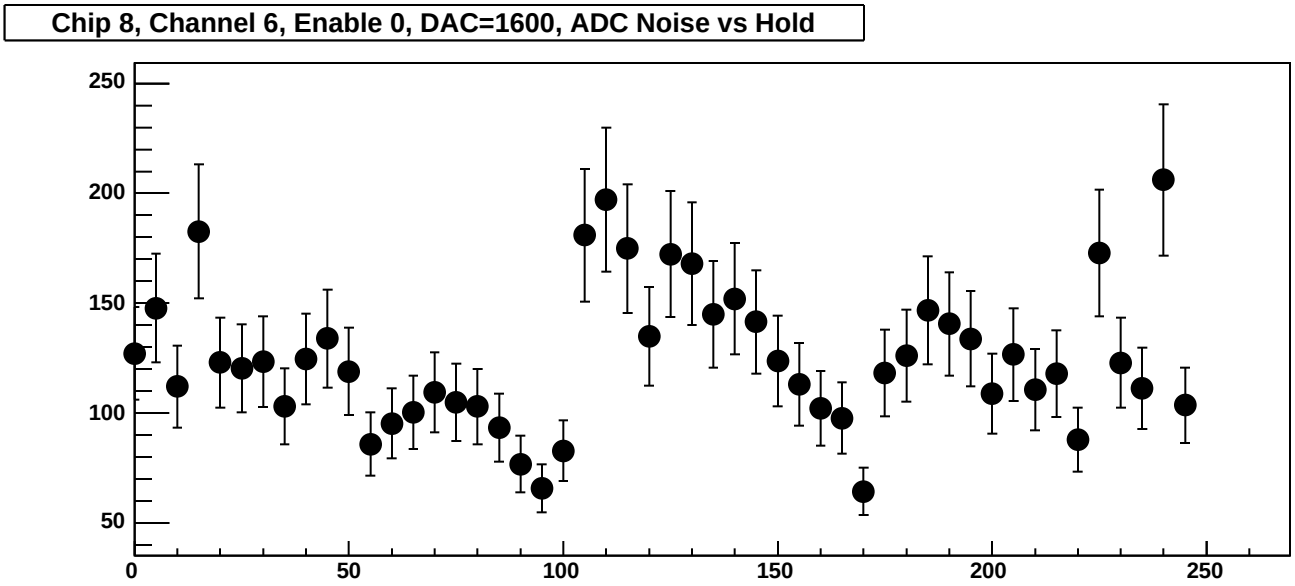


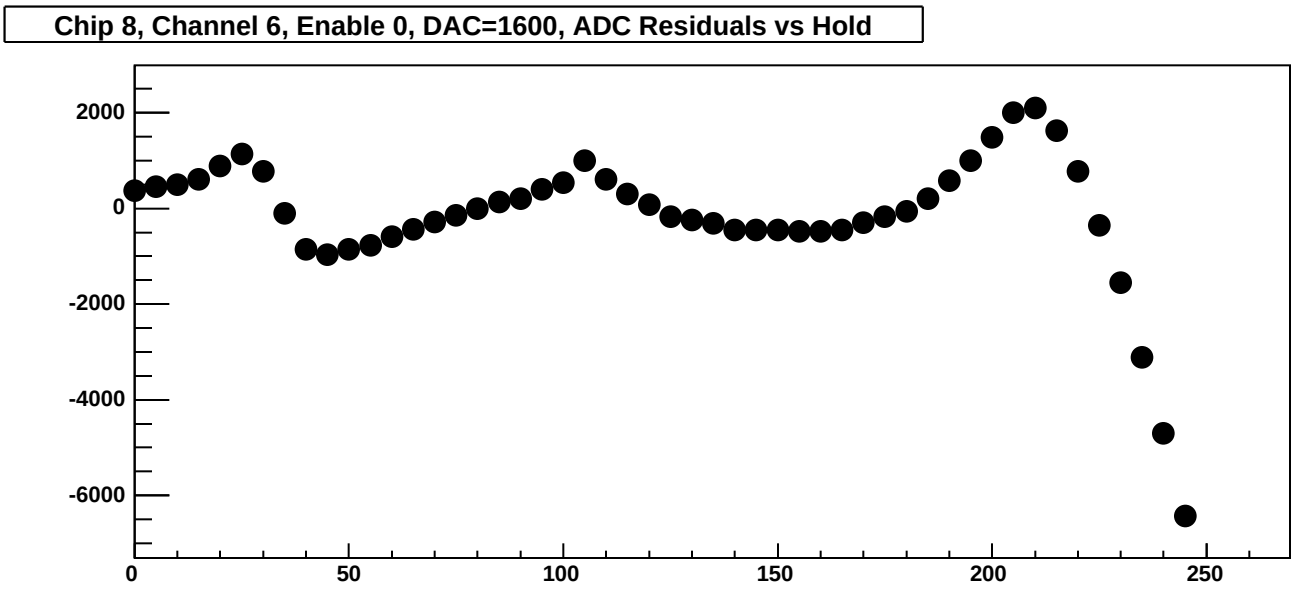

Chip 8, Channel 5, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.461e+05 / 41
p0 3305 ± 7.668
p1 75.11 ± 0.04446
p2 1.247e+04 ± 22.77
p3 18.78 ± 0.06442
p4 56.54 ± 0.1753
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 8, Channel 5, Enable 5, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

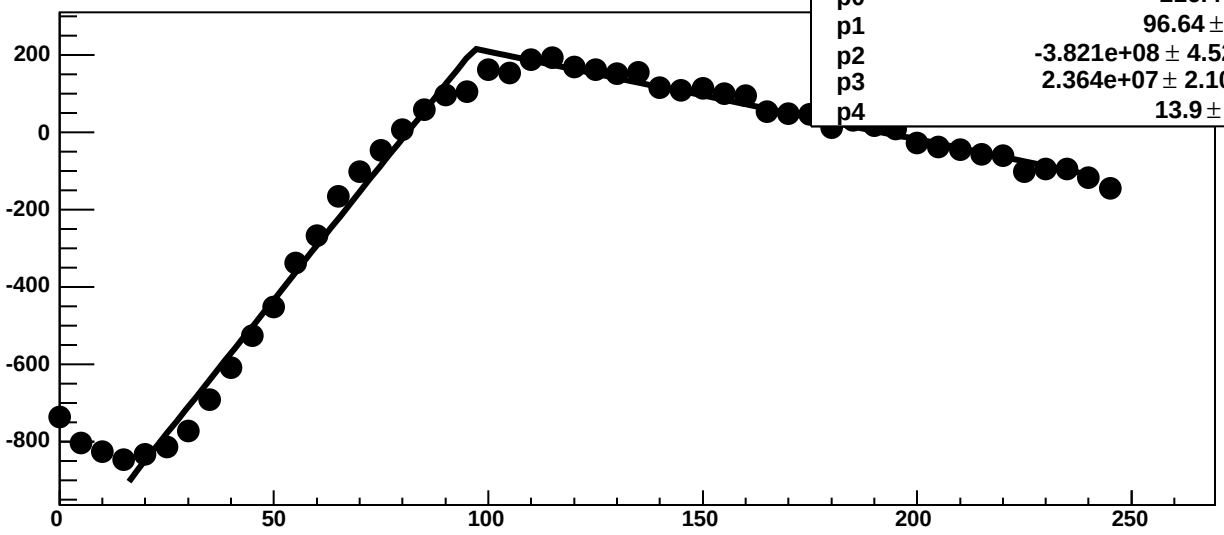

Chip 8, Channel 5, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
0
50
100
150
200
250


Chip 8, Channel 6, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 6.379e+04 / 41
p0 -3671 ± 8.885
p1 103 ± 0.06453
p2 2.598e+04 ± 55.74
p3 60.63 ± 0.2027
p4 -25.82 ± 0.211
15000
10000
5000
0
-5000
0
50
100
150
200
250

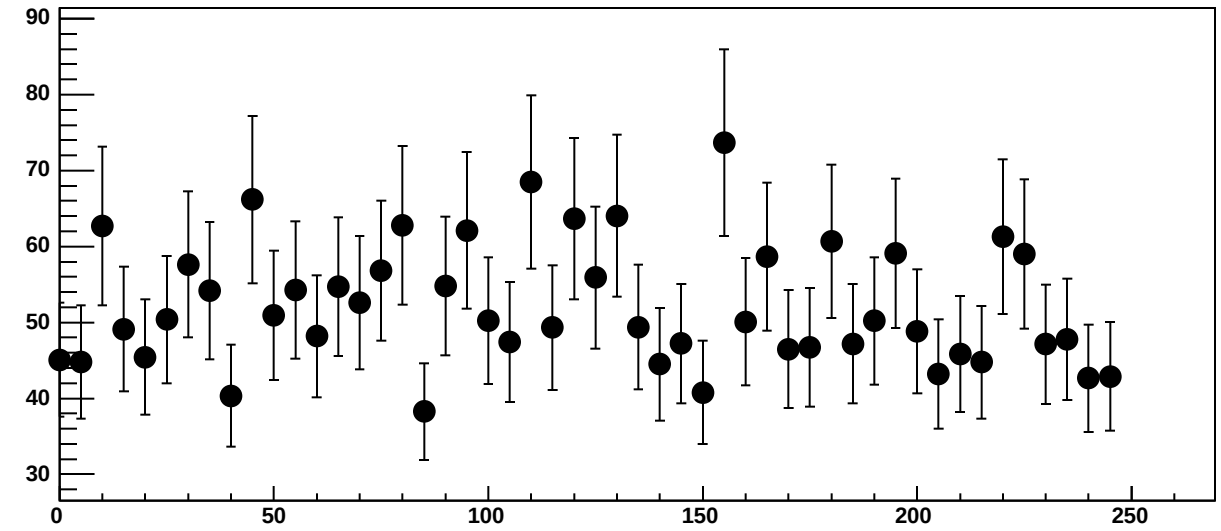

Chip 8, Channel 6, Enable 0, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
50
100
150
200
250


Chip 8, Channel 6, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

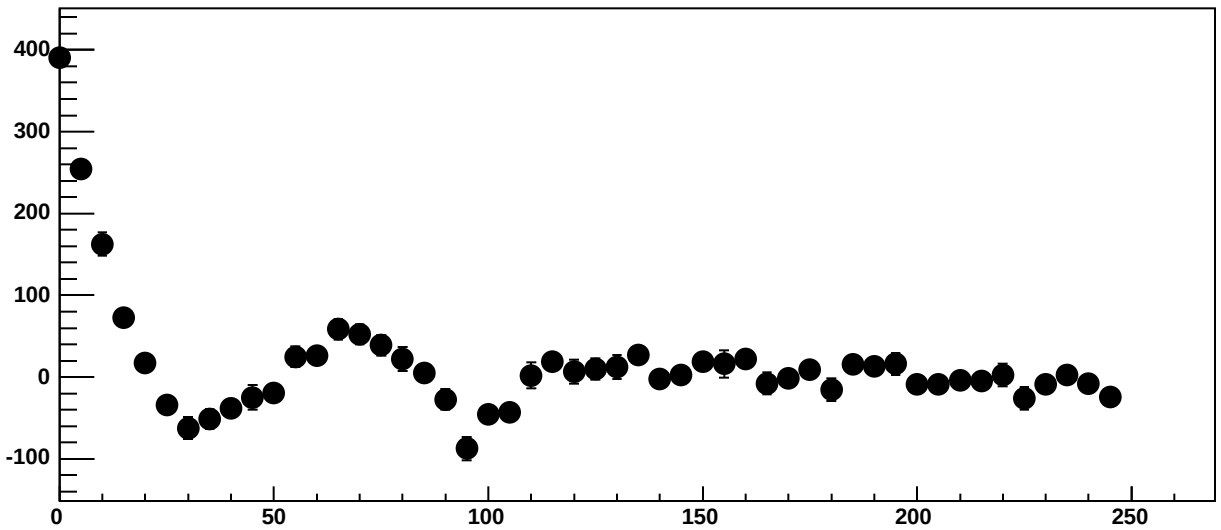
Chip 8, Channel 6, Enable 1, DAC=1600, ADC Mean vs Hold



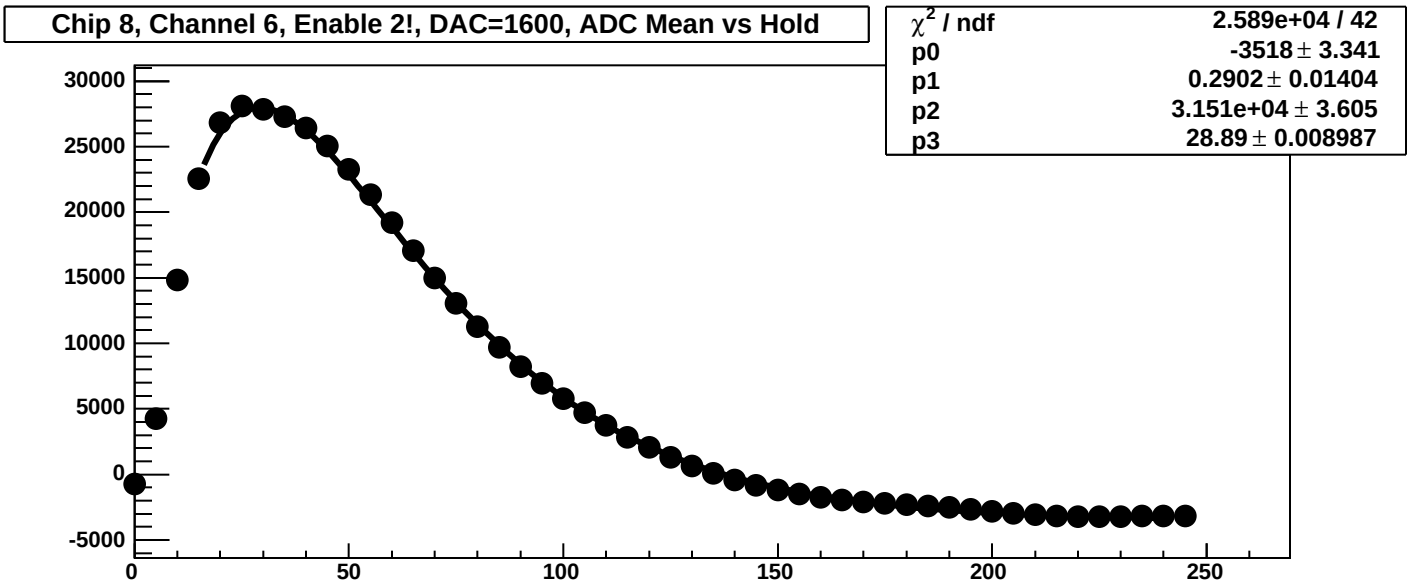
Chip 8, Channel 6, Enable 1, DAC=1600, ADC Noise vs Hold



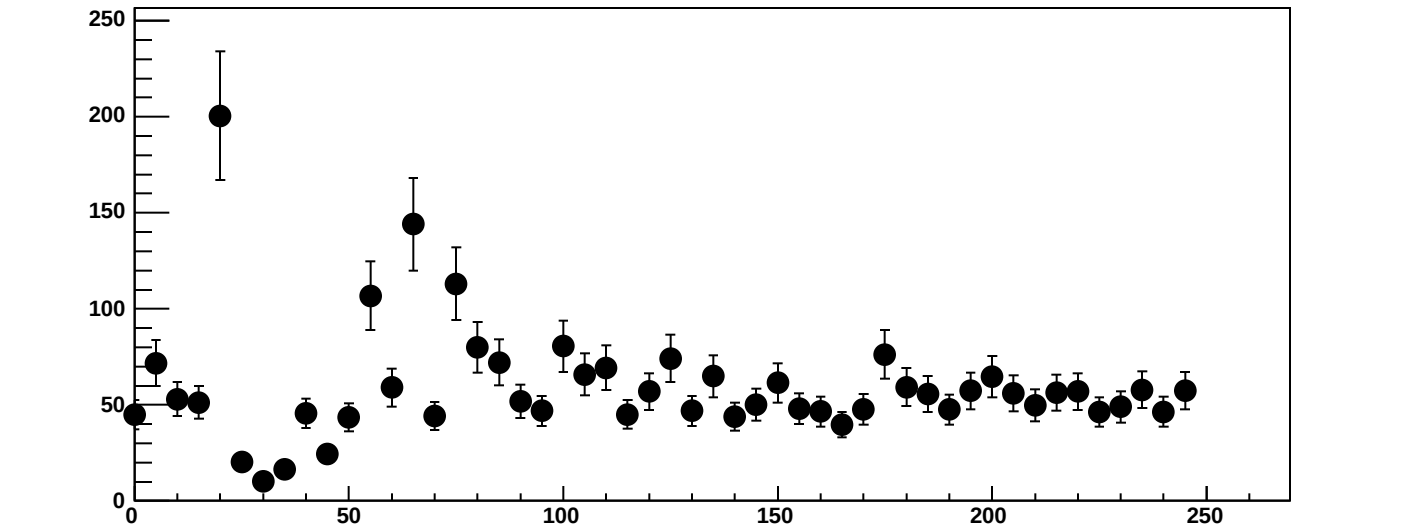
Chip 8, Channel 6, Enable 1, DAC=1600, ADC Residuals vs Hold



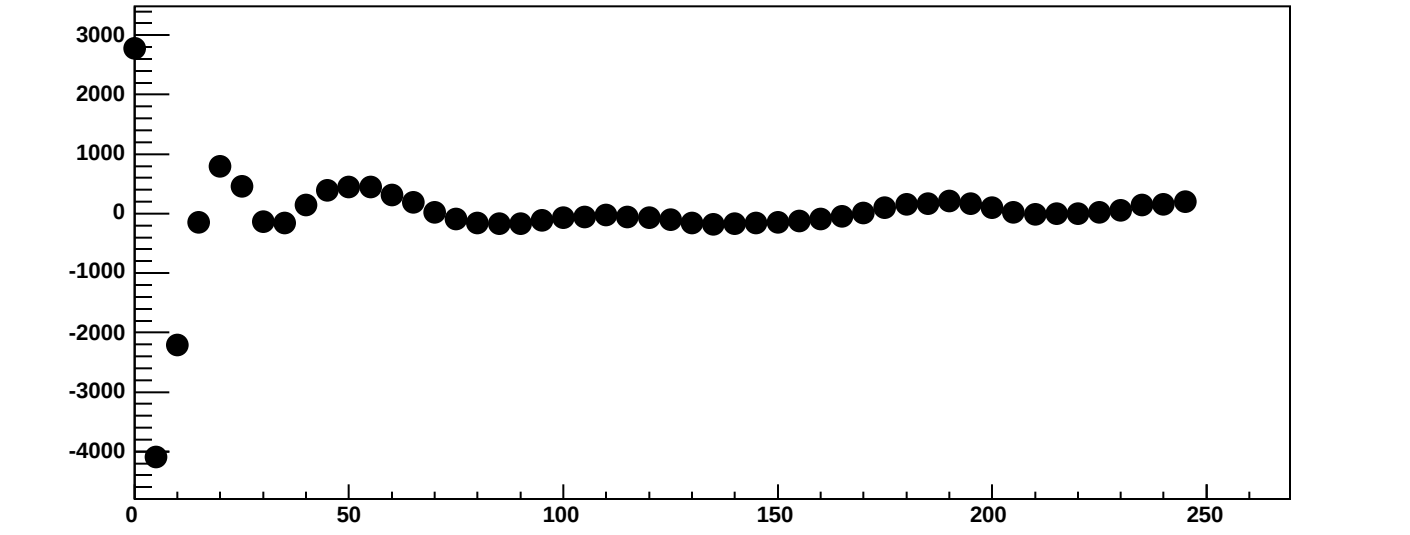
Chip 8, Channel 6, Enable 2!, DAC=1600, ADC Mean vs Hold



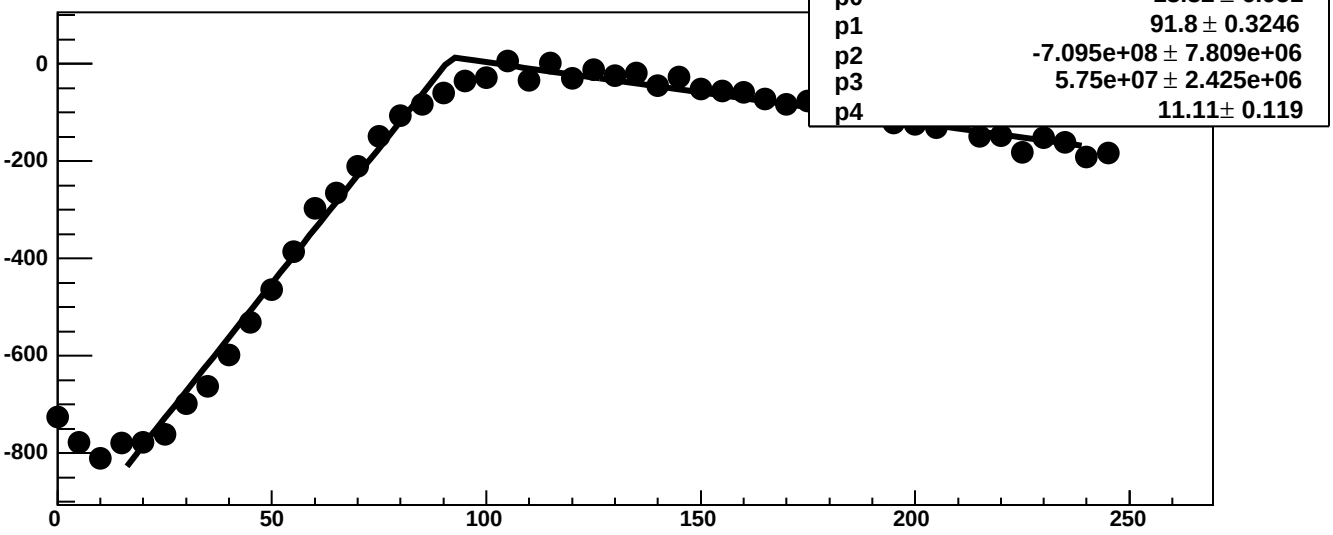
Chip 8, Channel 6, Enable 2!, DAC=1600, ADC Noise vs Hold



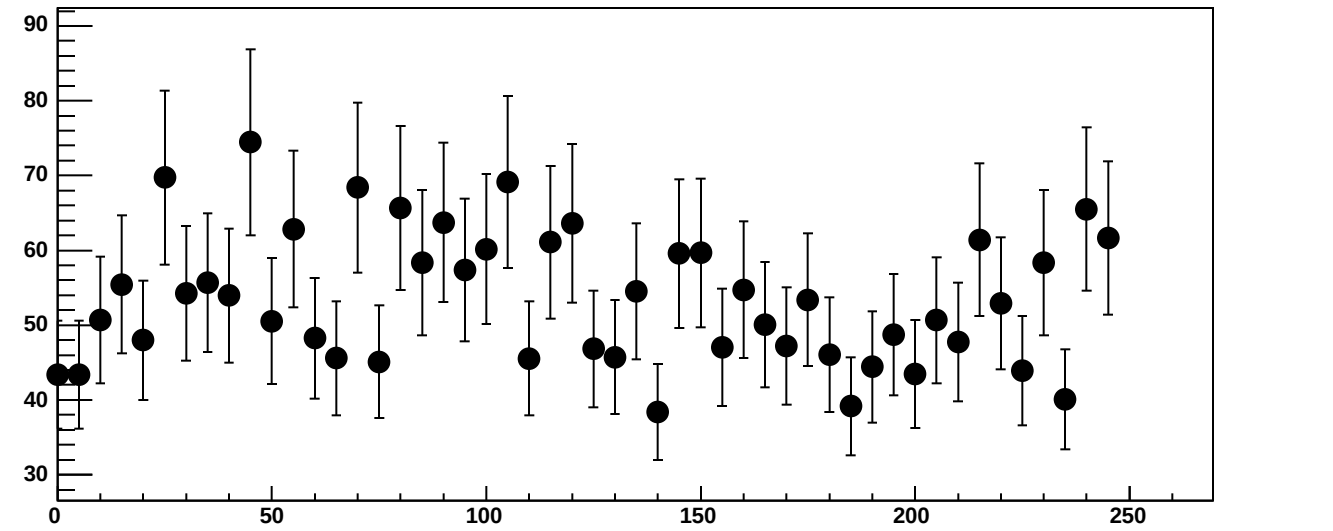
Chip 8, Channel 6, Enable 2!, DAC=1600, ADC Residuals vs Hold



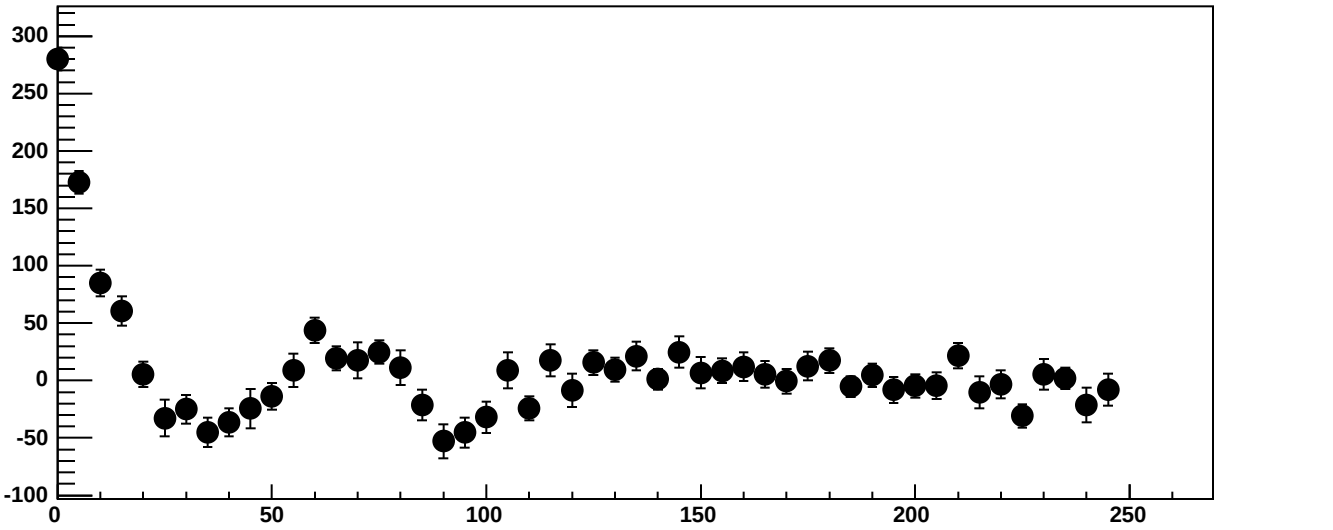
Chip 8, Channel 6, Enable 3, DAC=1600, ADC Mean vs Hold



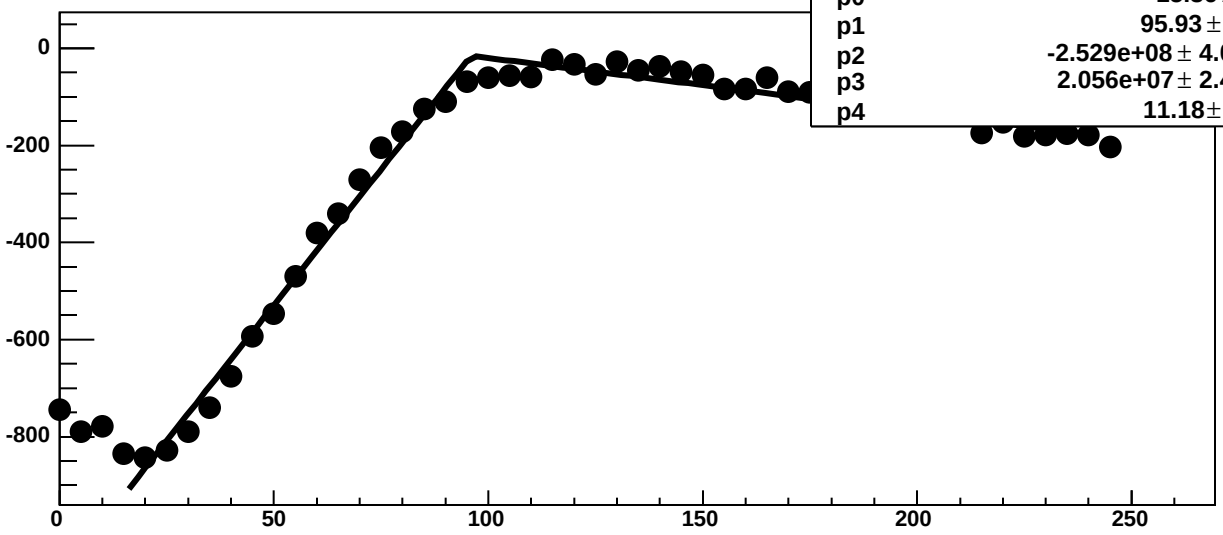
Chip 8, Channel 6, Enable 3, DAC=1600, ADC Noise vs Hold



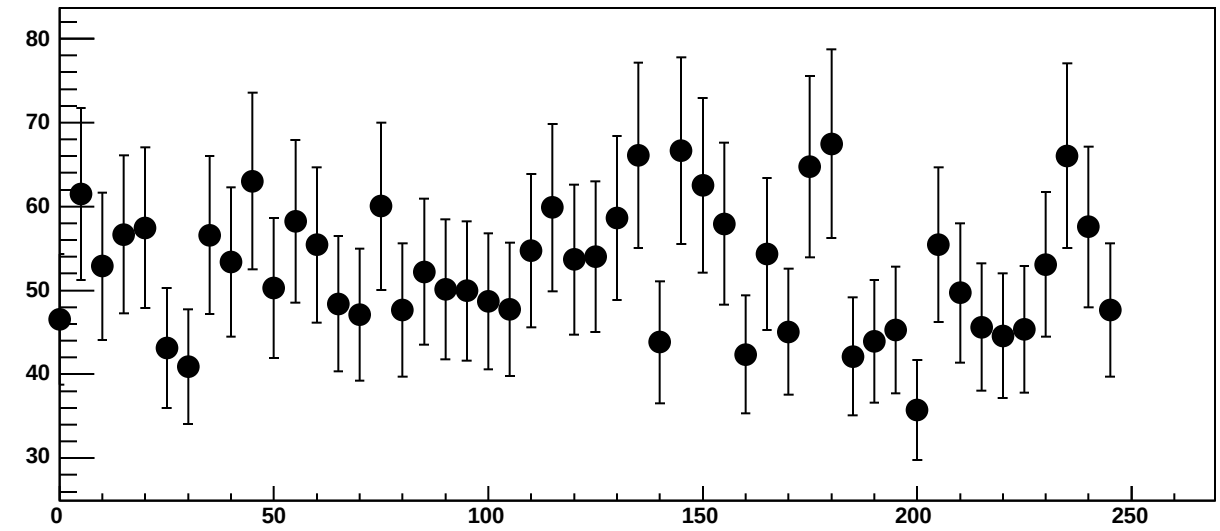
Chip 8, Channel 6, Enable 3, DAC=1600, ADC Residuals vs Hold



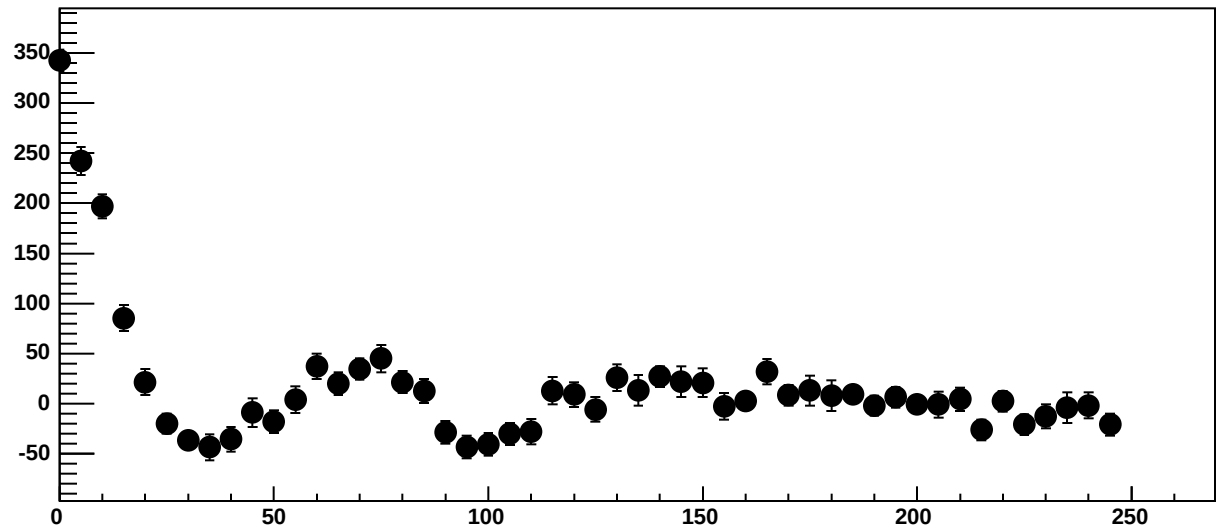
Chip 8, Channel 6, Enable 4, DAC=1600, ADC Mean vs Hold

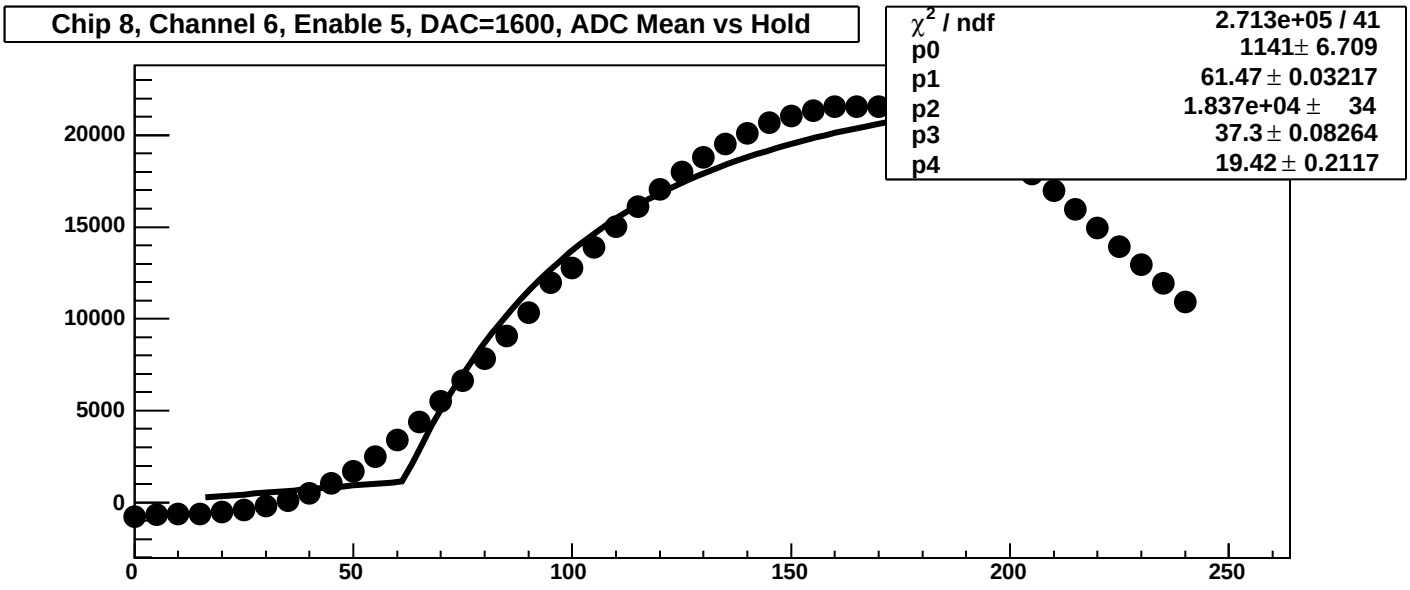


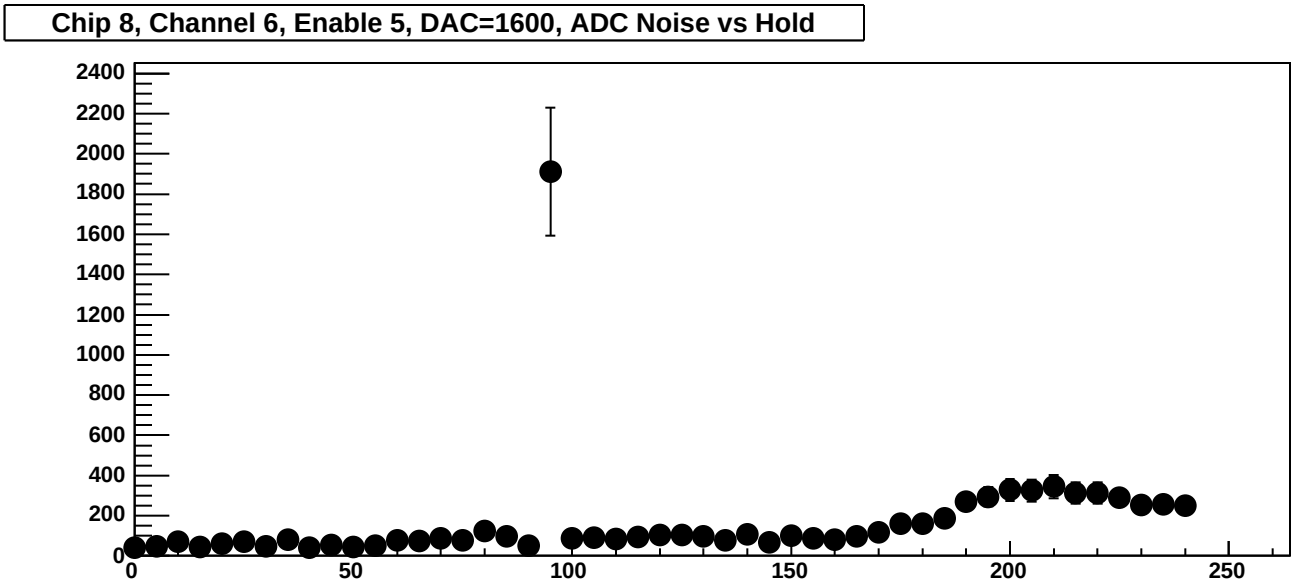
Chip 8, Channel 6, Enable 4, DAC=1600, ADC Noise vs Hold

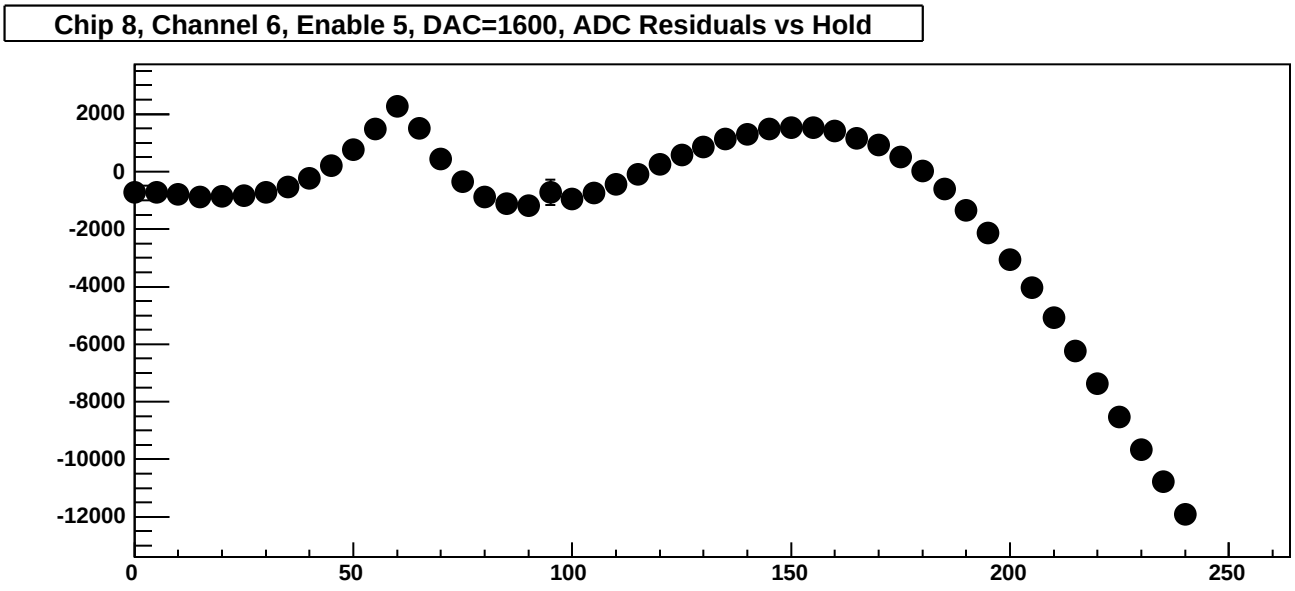


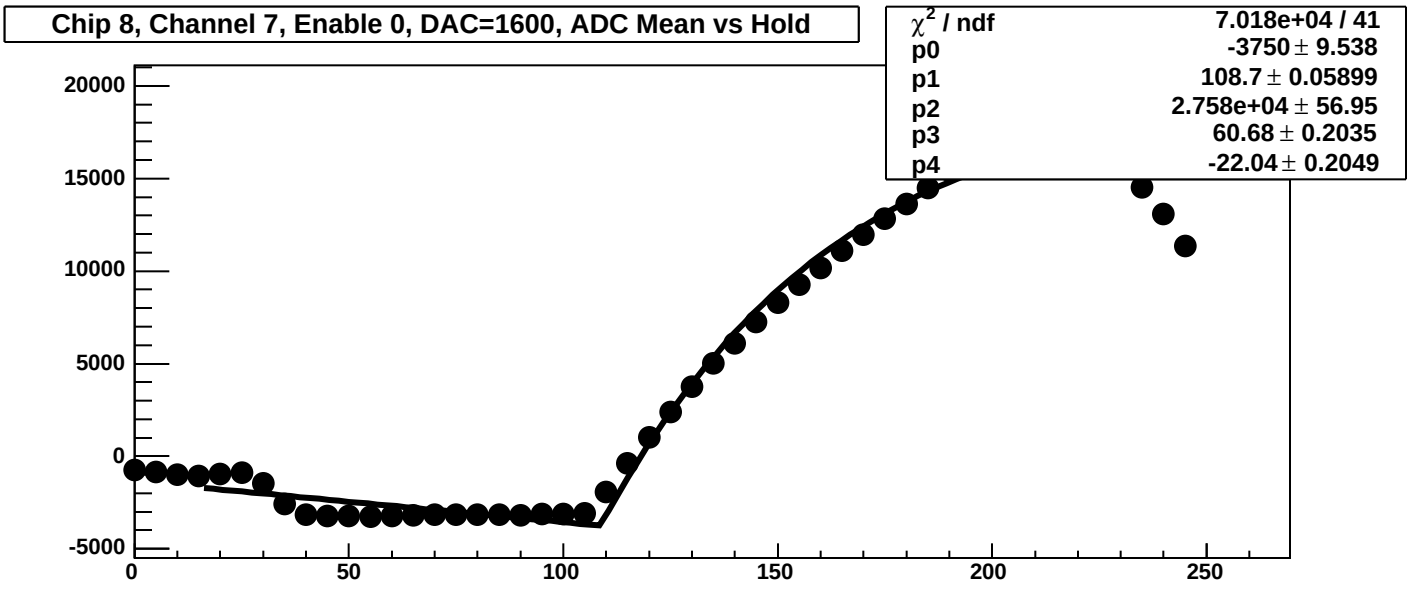
Chip 8, Channel 6, Enable 4, DAC=1600, ADC Residuals vs Hold

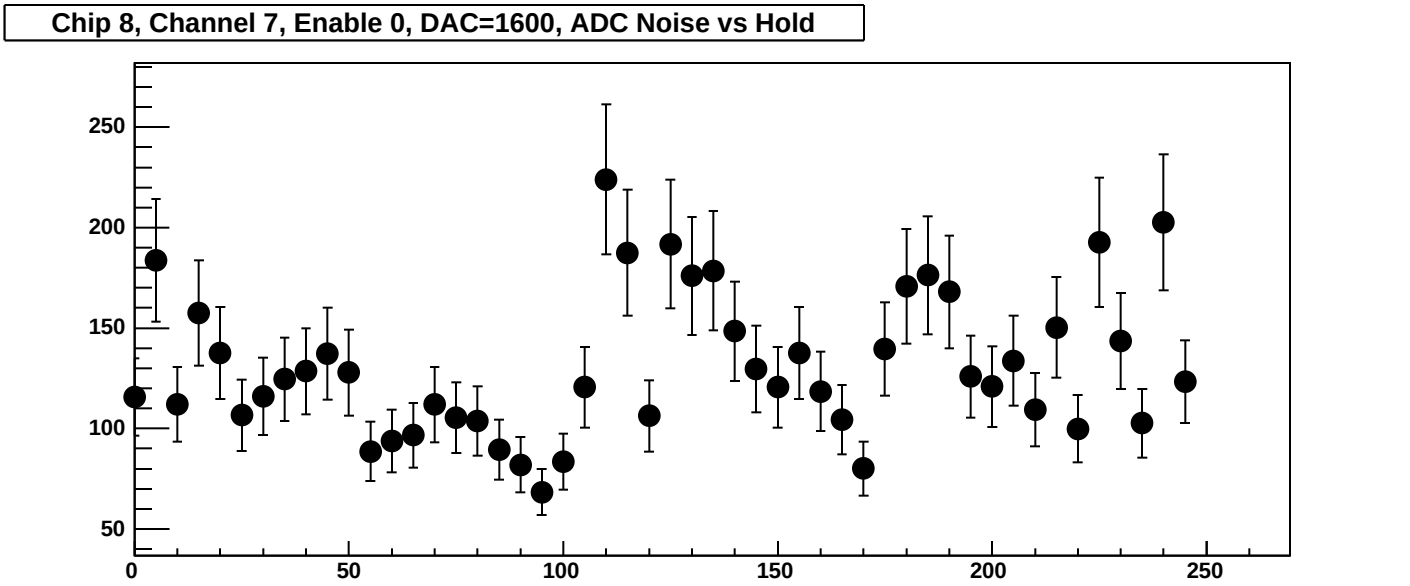


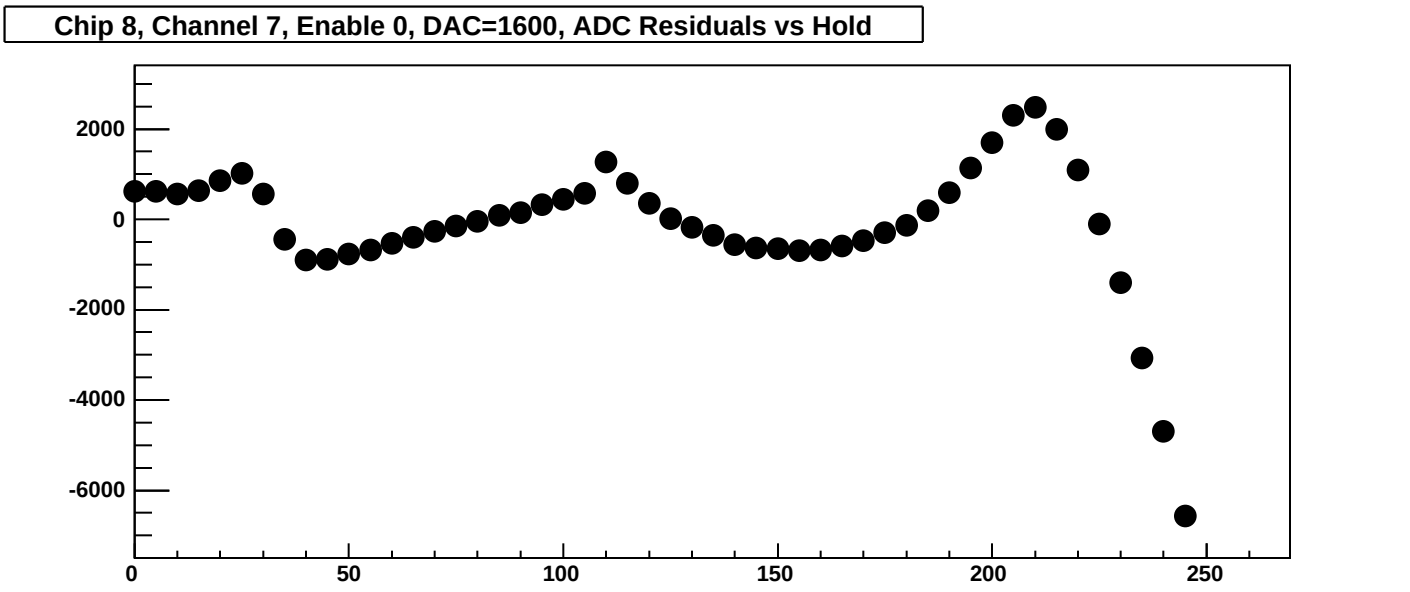

Chip 8, Channel 6, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.713e+05 / 41
p0 1141 ± 6.709
p1 61.47 ± 0.03217
p2 1.837e+04 ± 34
p3 37.3 ± 0.08264
p4 19.42 ± 0.2117
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 8, Channel 6, Enable 5, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

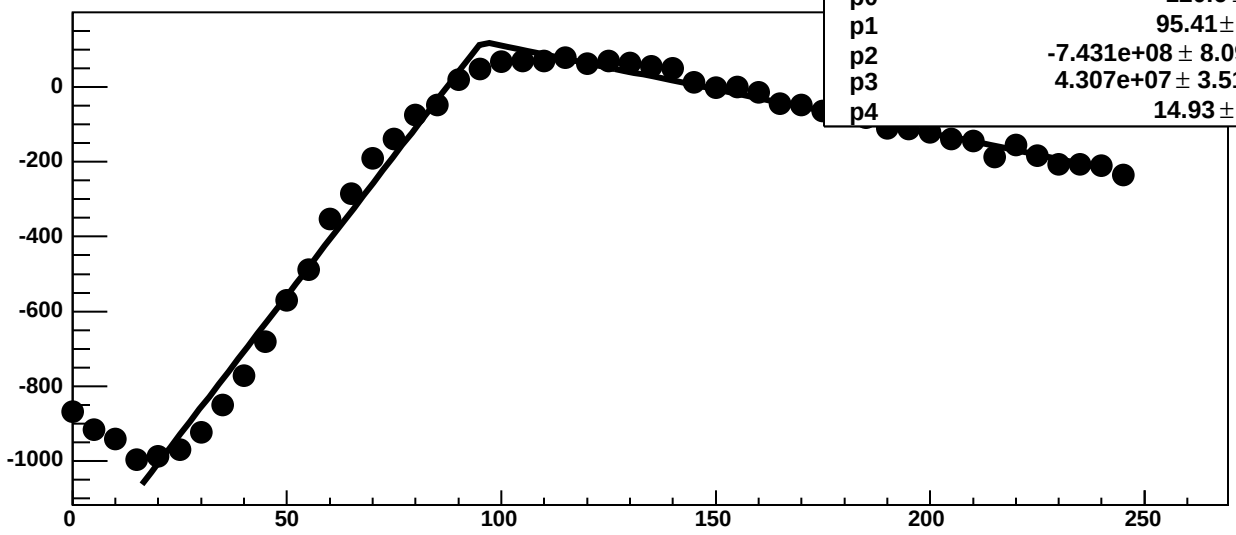

Chip 8, Channel 6, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
0
50
100
150
200
250


Chip 8, Channel 7, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.018e+04 / 41
p0 -3750 ± 9.538
p1 108.7 ± 0.05899
p2 2.758e+04 ± 56.95
p3 60.68 ± 0.2035
p4 -22.04 ± 0.2049
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

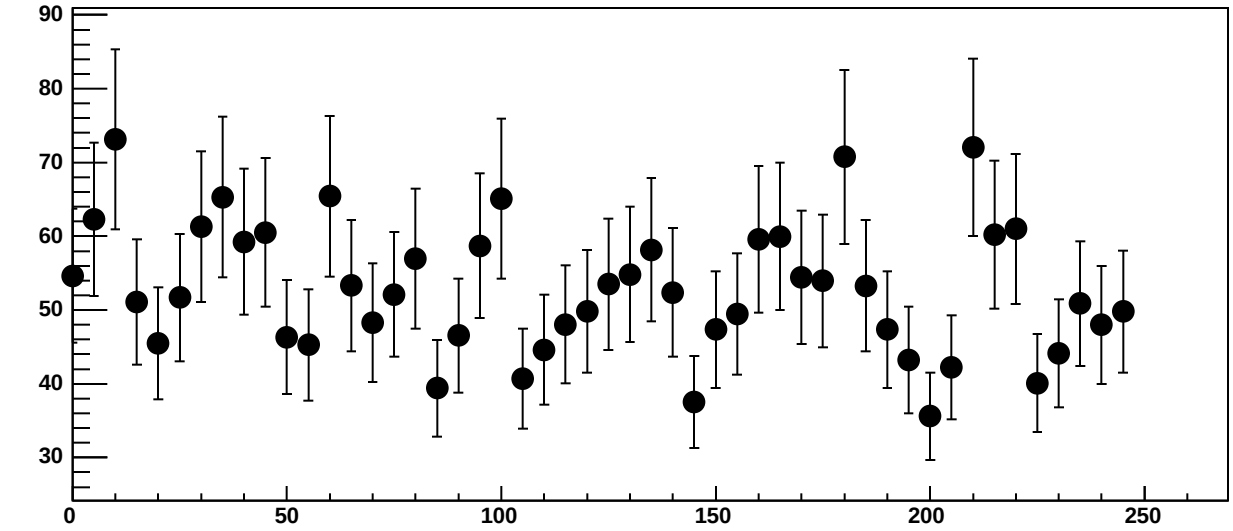

Chip 8, Channel 7, Enable 0, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
50
100
150
200
250


Chip 8, Channel 7, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

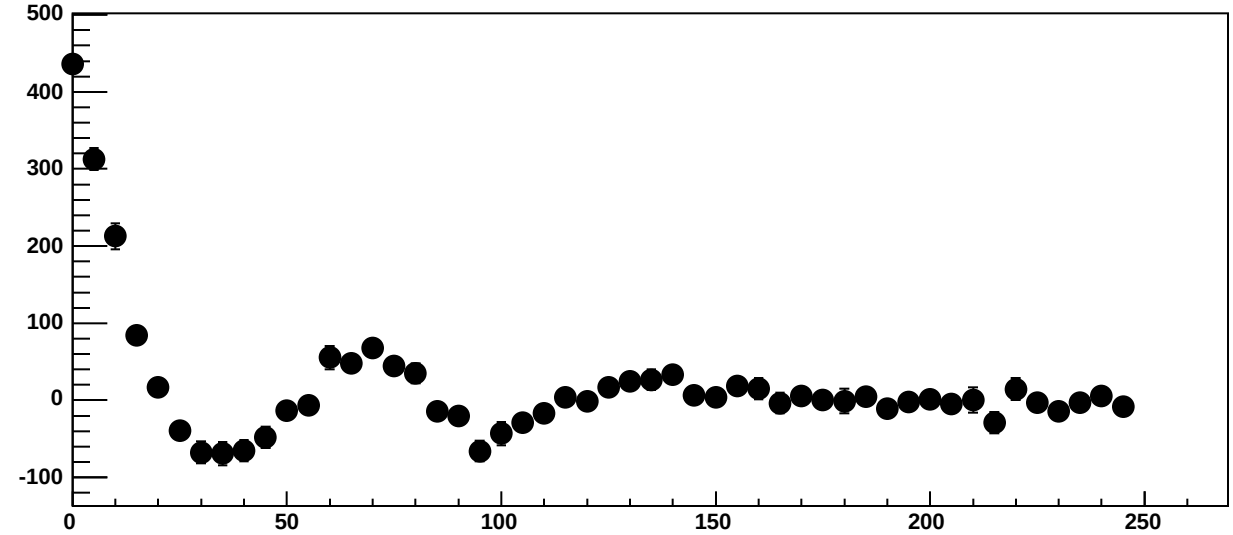
Chip 8, Channel 7, Enable 1, DAC=1600, ADC Mean vs Hold



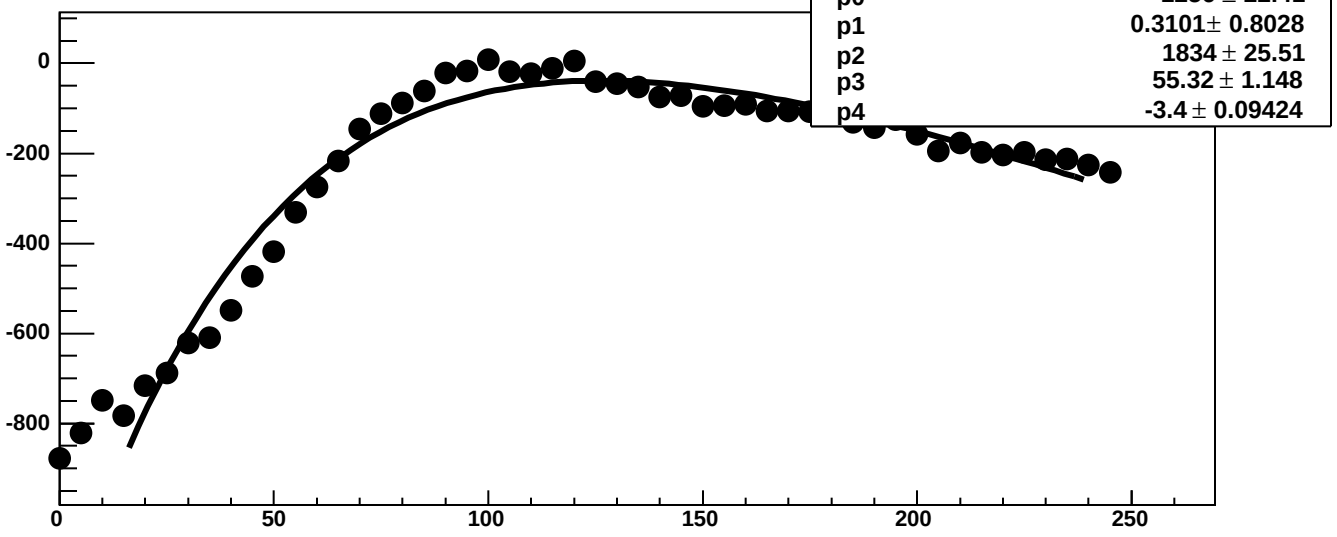
Chip 8, Channel 7, Enable 1, DAC=1600, ADC Noise vs Hold



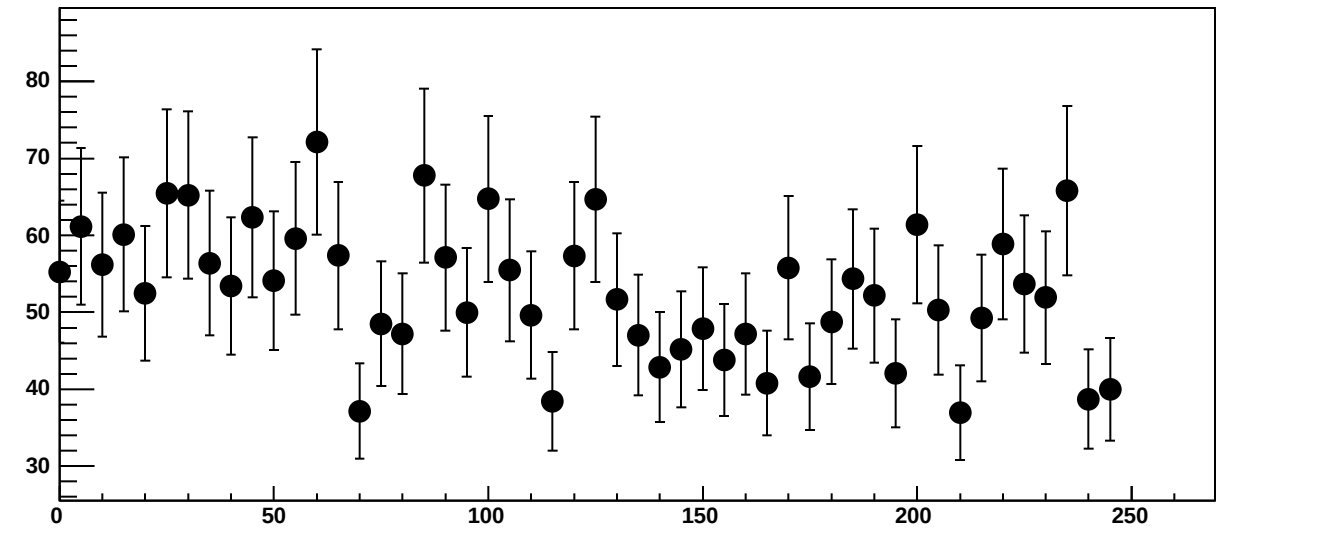
Chip 8, Channel 7, Enable 1, DAC=1600, ADC Residuals vs Hold



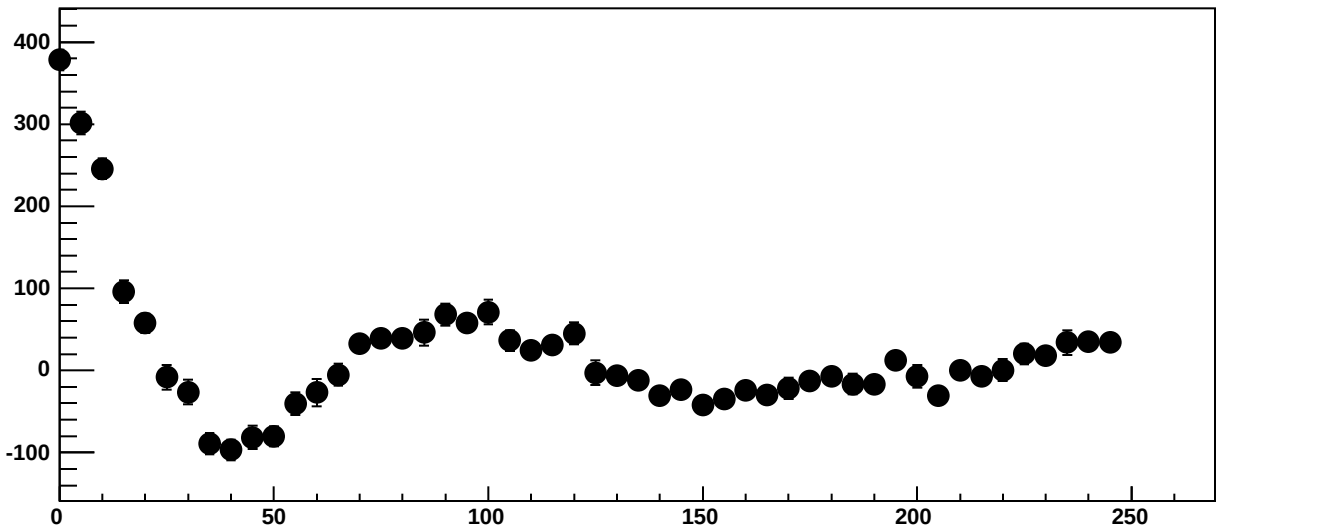
Chip 8, Channel 7, Enable 2, DAC=1600, ADC Mean vs Hold

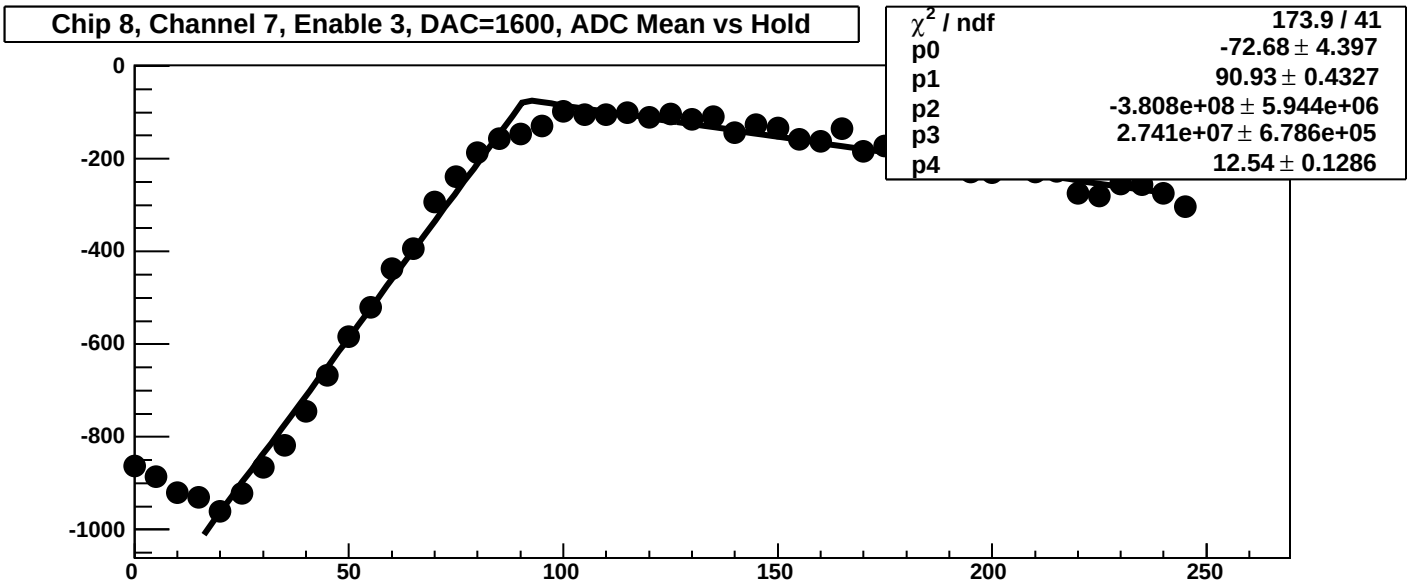


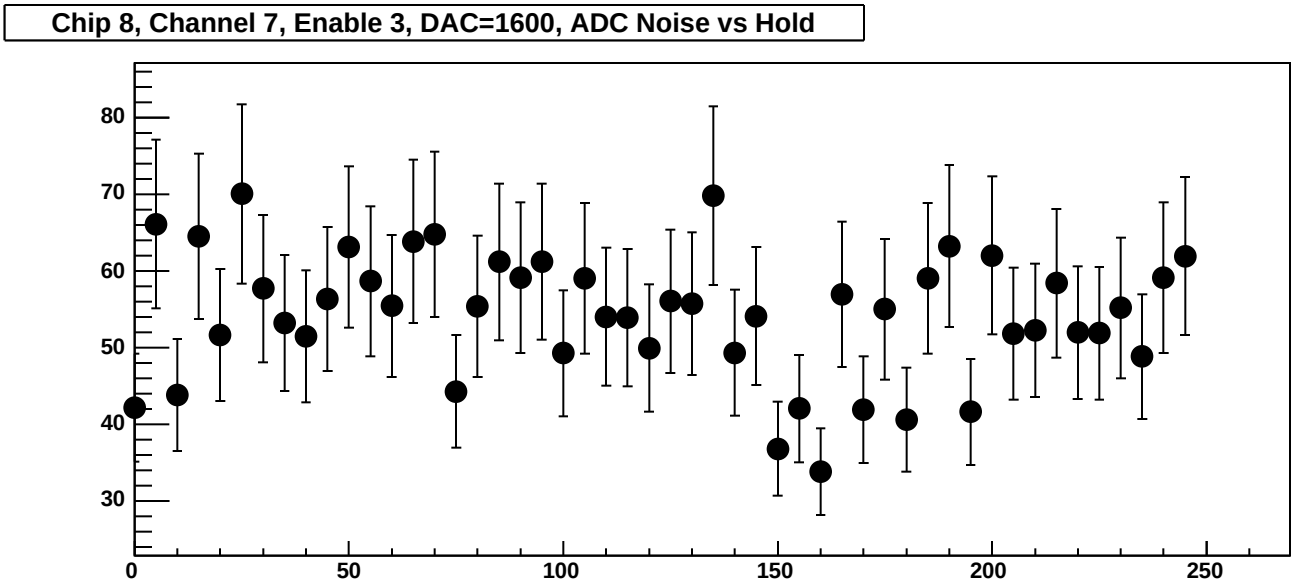
Chip 8, Channel 7, Enable 2, DAC=1600, ADC Noise vs Hold

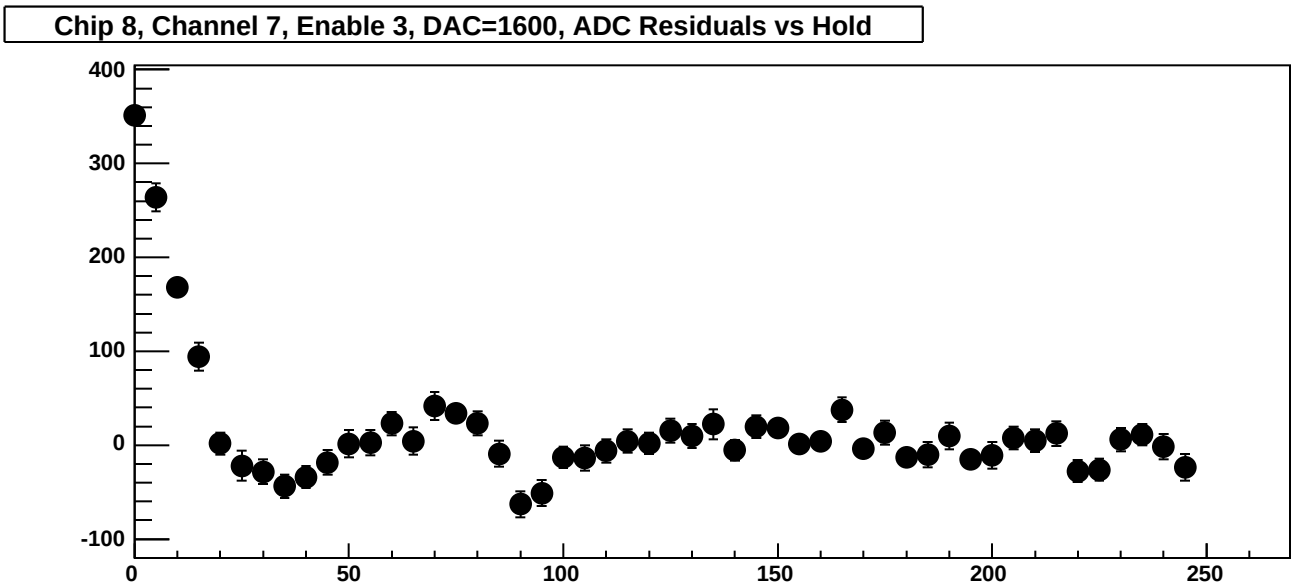


Chip 8, Channel 7, Enable 2, DAC=1600, ADC Residuals vs Hold

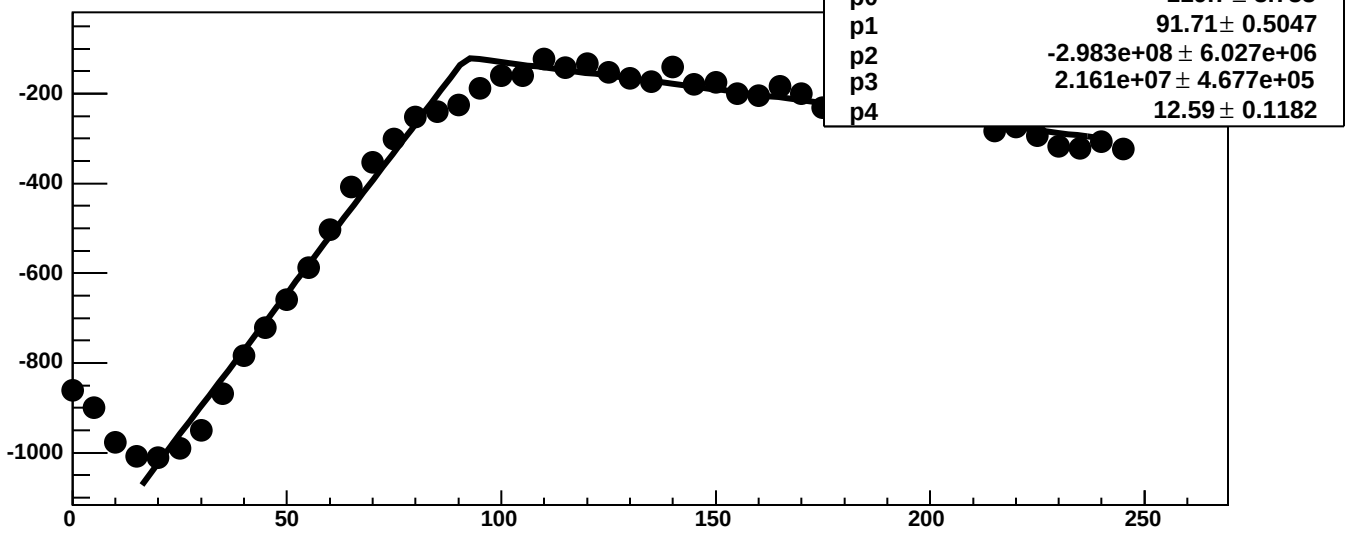



Chip 8, Channel 7, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 173.9 / 41
p0 -72.68 ± 4.397
p1 90.93 ± 0.4327
p2 -3.808e+08 ± 5.944e+06
p3 2.741e+07 ± 6.786e+05
p4 12.54 ± 0.1286
0
-200
-400
-600
-800
-1000
0
50
100
150
200
250

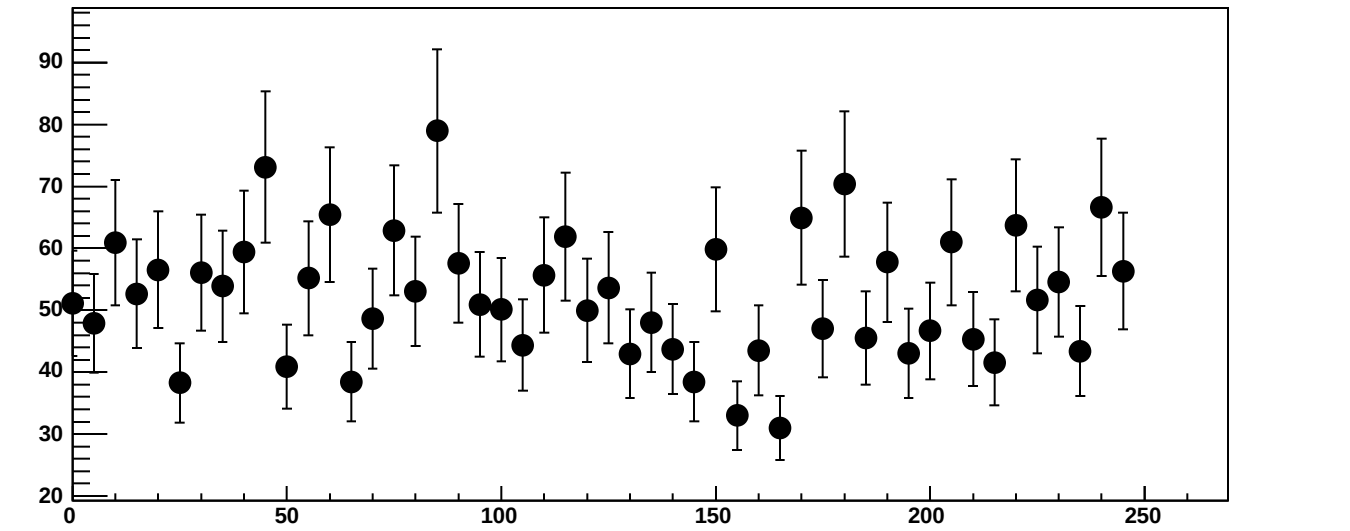

Chip 8, Channel 7, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 8, Channel 7, Enable 3, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

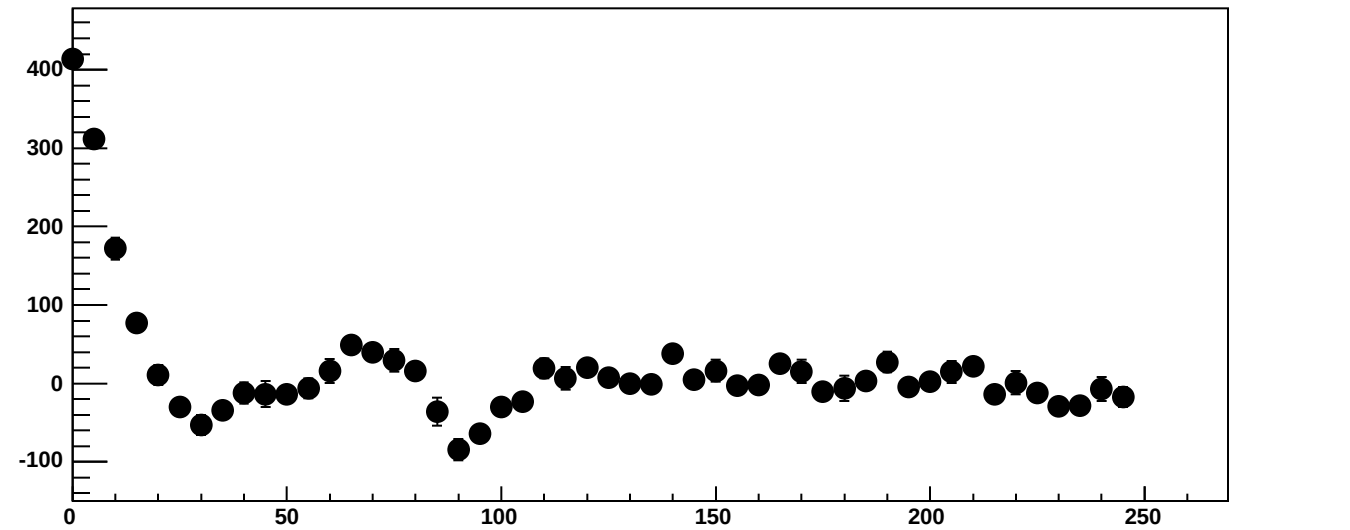
Chip 8, Channel 7, Enable 4, DAC=1600, ADC Mean vs Hold



Chip 8, Channel 7, Enable 4, DAC=1600, ADC Noise vs Hold

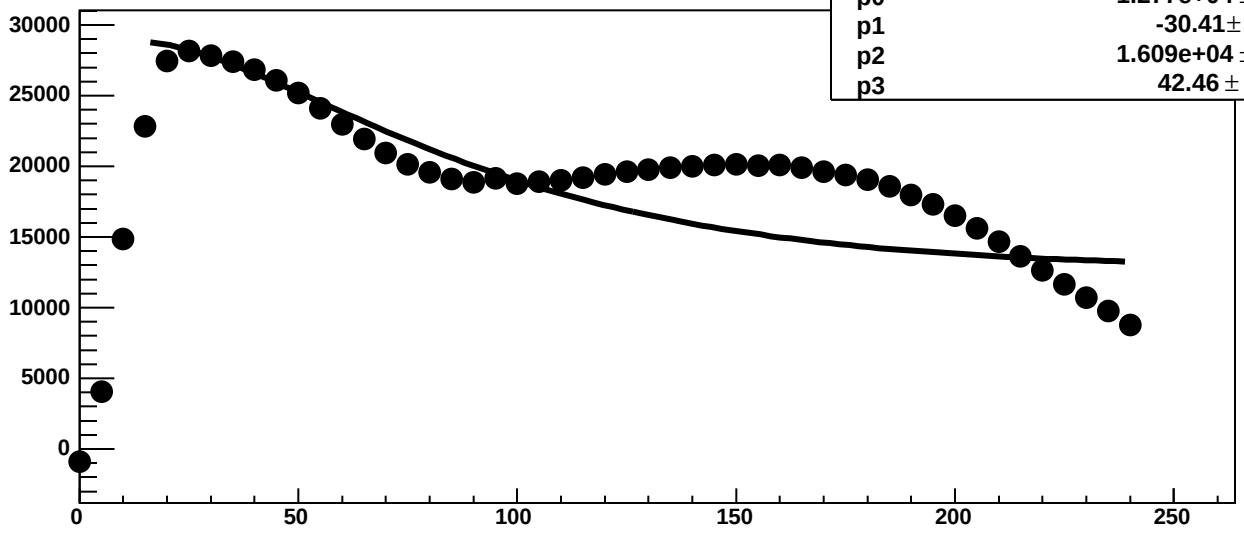


Chip 8, Channel 7, Enable 4, DAC=1600, ADC Residuals vs Hold

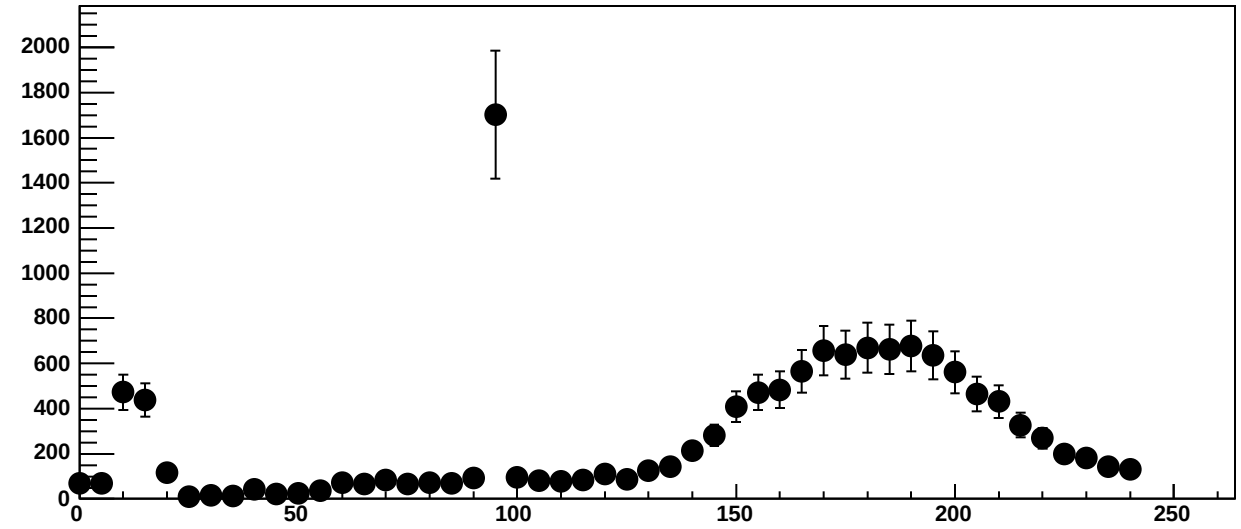


Chip 8, Channel 7, Enable 5!, DAC=1600, ADC Mean vs Hold

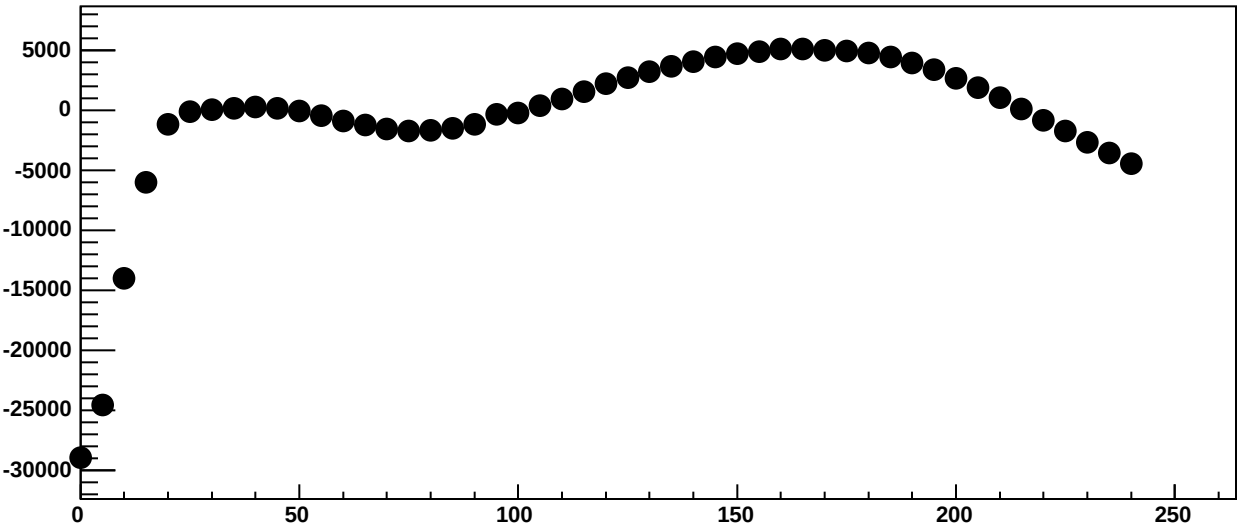
χ^2 / ndf	1.928e+05 / 42
p0	1.277e+04 ± 32.56
p1	-30.41± 0.4119
p2	1.609e+04 ± 47.67
p3	42.46 ± 0.1946

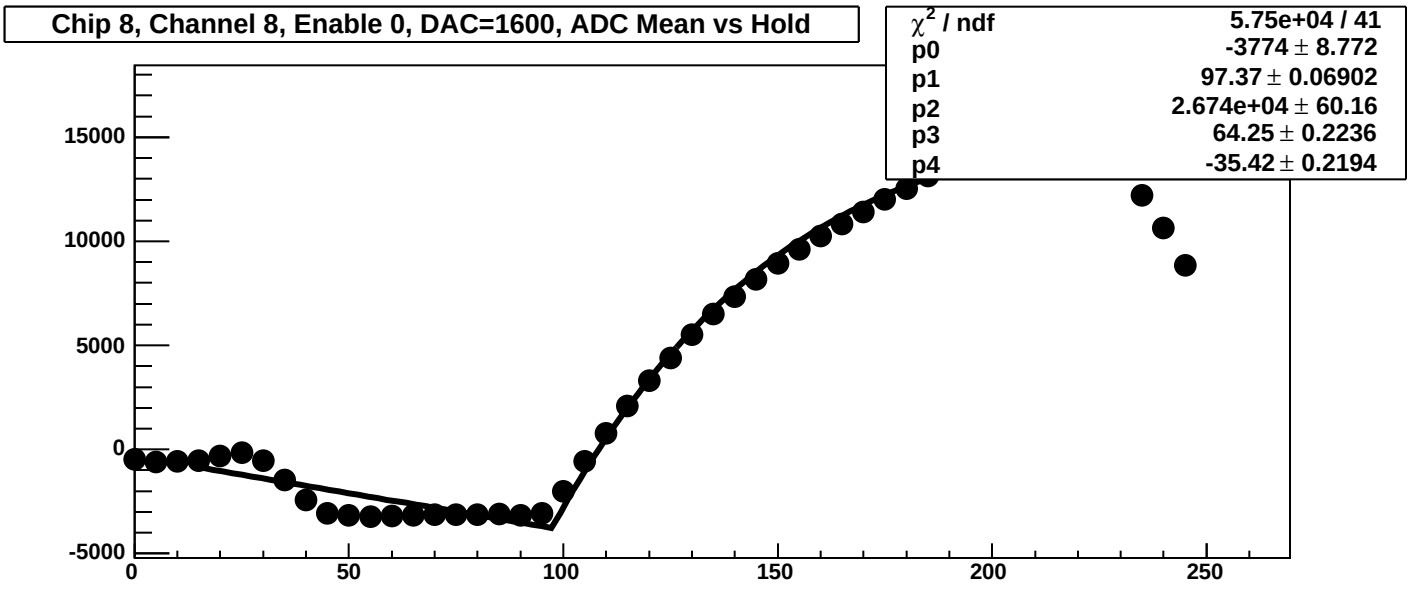


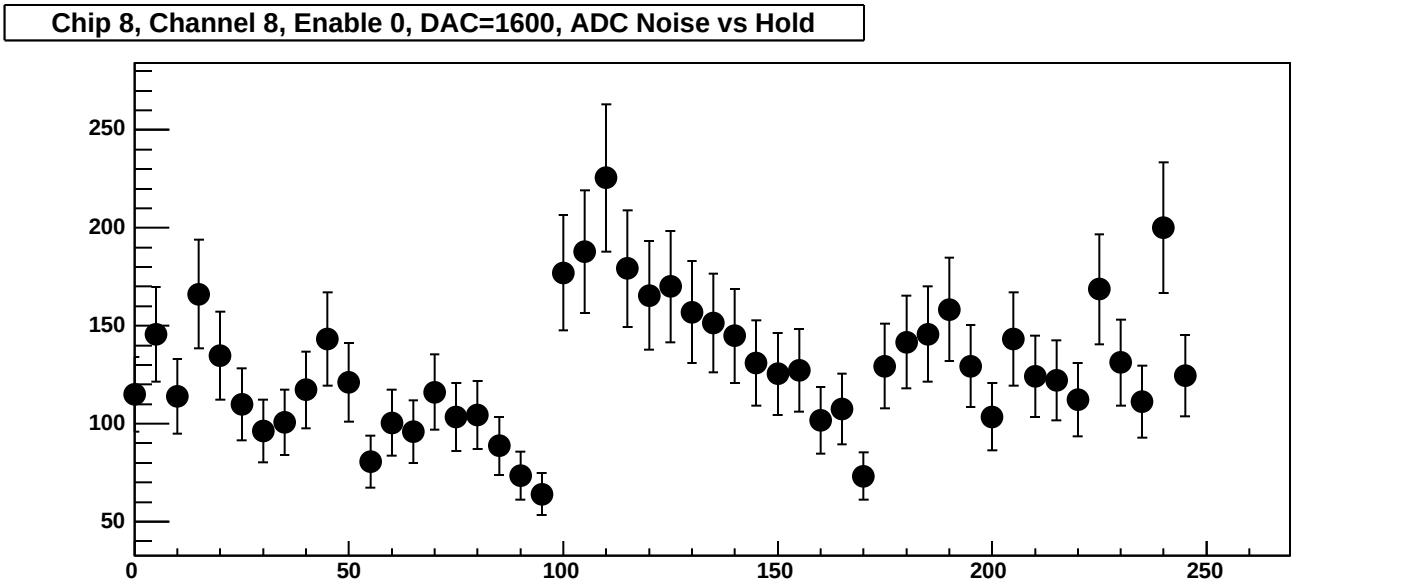
Chip 8, Channel 7, Enable 5!, DAC=1600, ADC Noise vs Hold

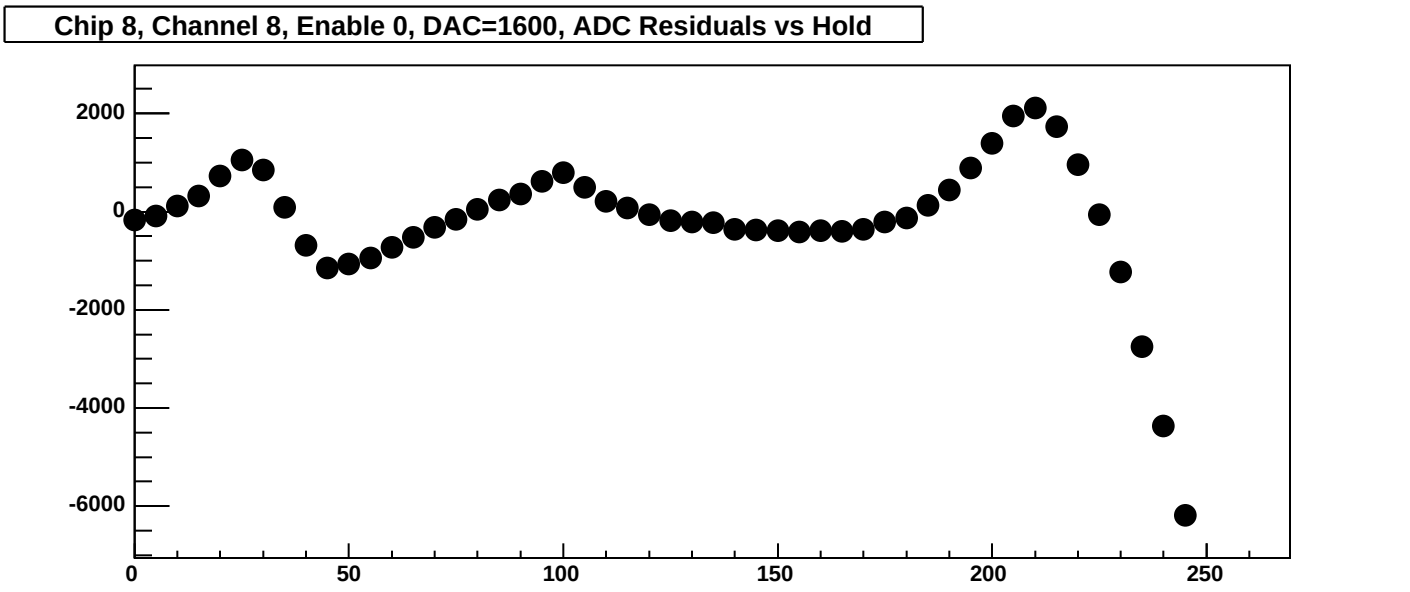


Chip 8, Channel 7, Enable 5!, DAC=1600, ADC Residuals vs Hold

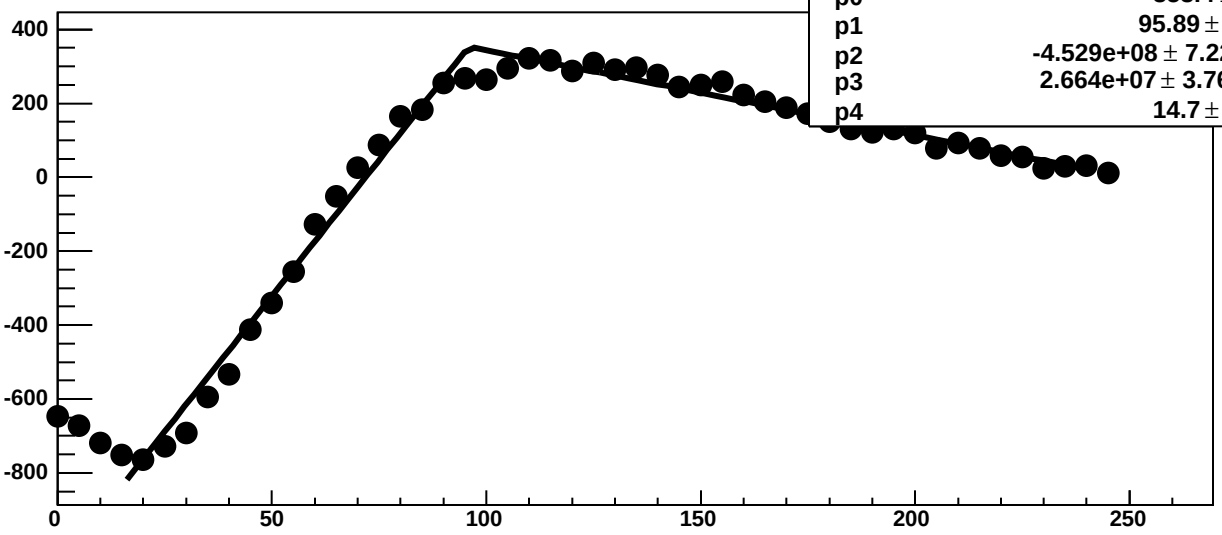



Chip 8, Channel 8, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.75e+04 / 41
p0 -3774 ± 8.772
p1 97.37 ± 0.06902
p2 2.674e+04 ± 60.16
p3 64.25 ± 0.2236
p4 -35.42 ± 0.2194
15000
10000
5000
0
-5000
0
50
100
150
200
250

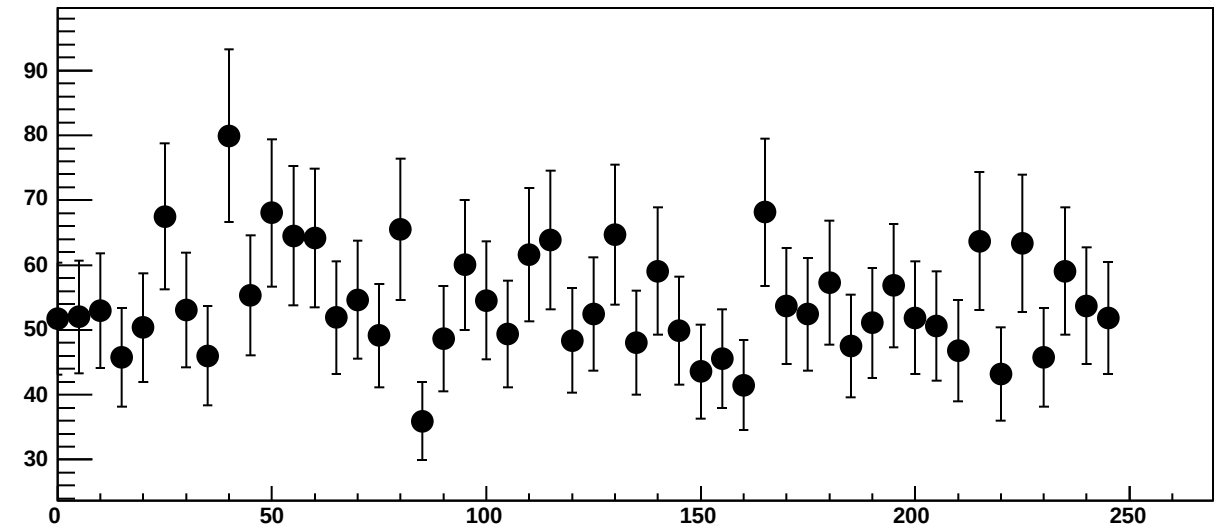

Chip 8, Channel 8, Enable 0, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
50
100
150
200
250


Chip 8, Channel 8, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

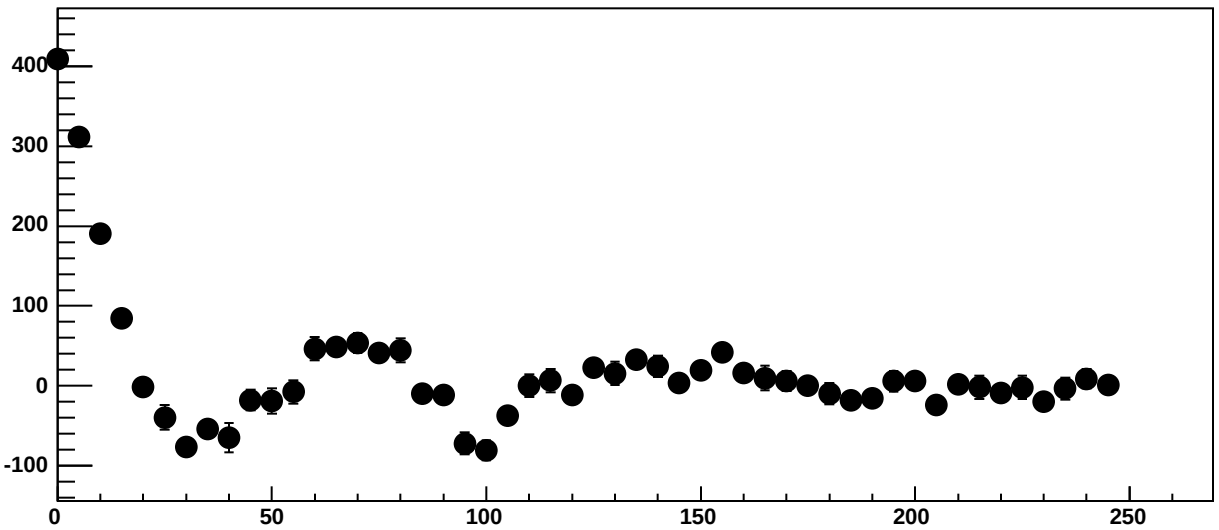
Chip 8, Channel 8, Enable 1, DAC=1600, ADC Mean vs Hold



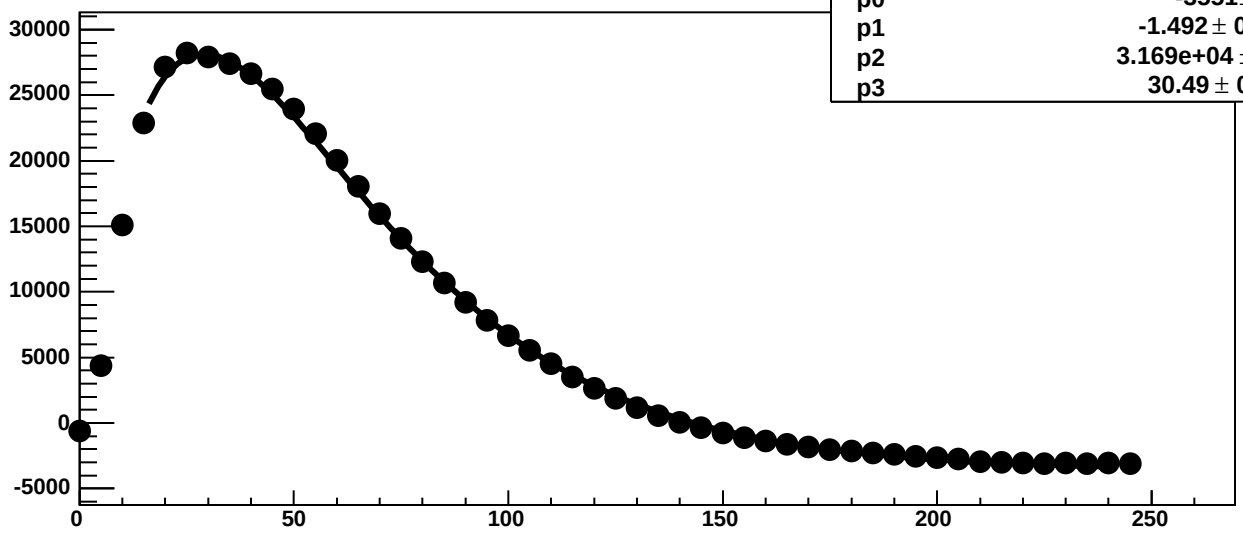
Chip 8, Channel 8, Enable 1, DAC=1600, ADC Noise vs Hold



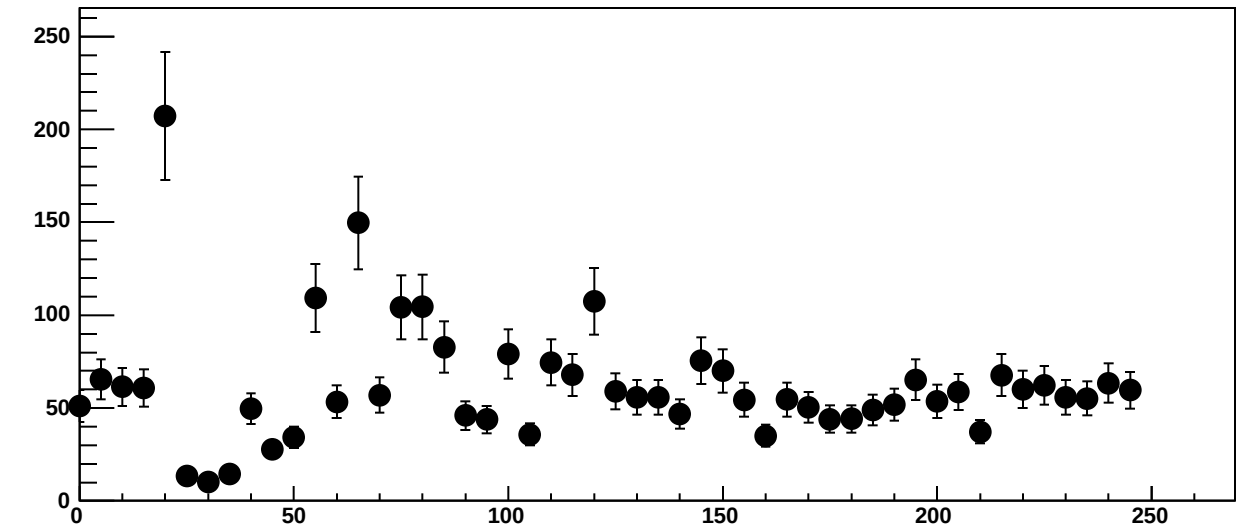
Chip 8, Channel 8, Enable 1, DAC=1600, ADC Residuals vs Hold



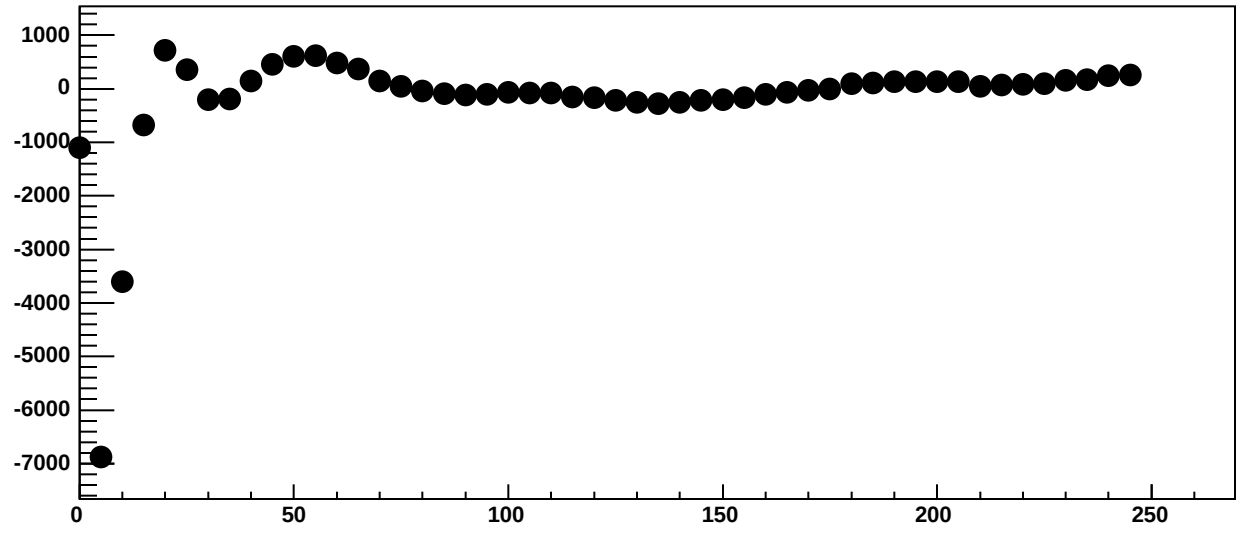
Chip 8, Channel 8, Enable 2!, DAC=1600, ADC Mean vs Hold

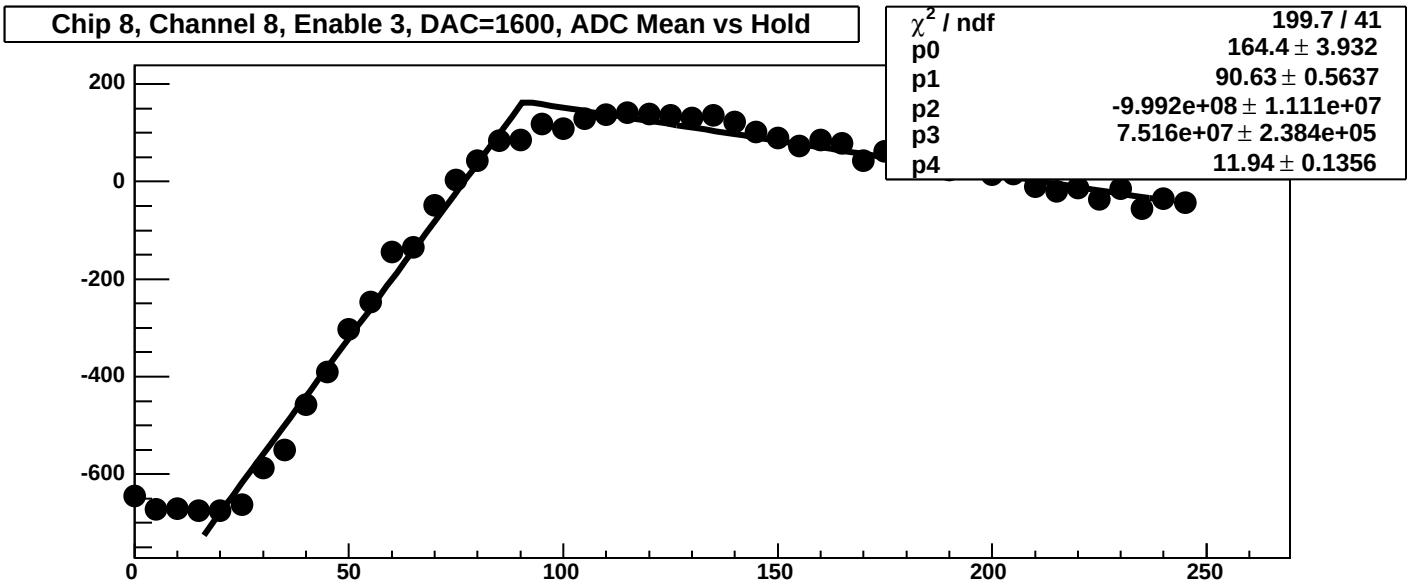


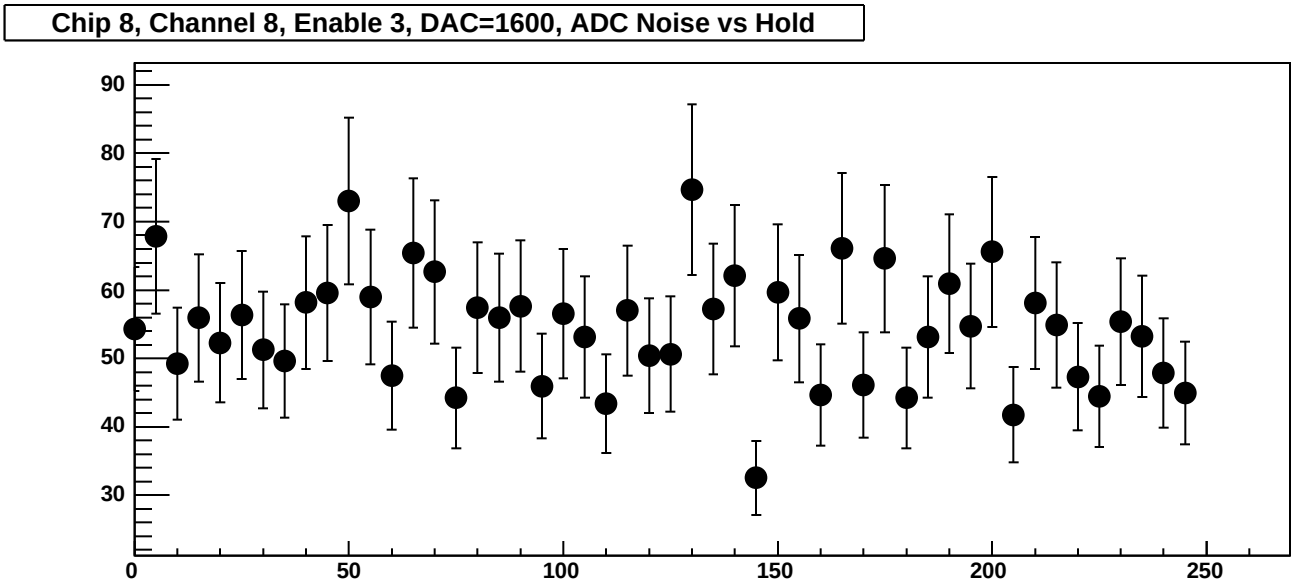
Chip 8, Channel 8, Enable 2!, DAC=1600, ADC Noise vs Hold

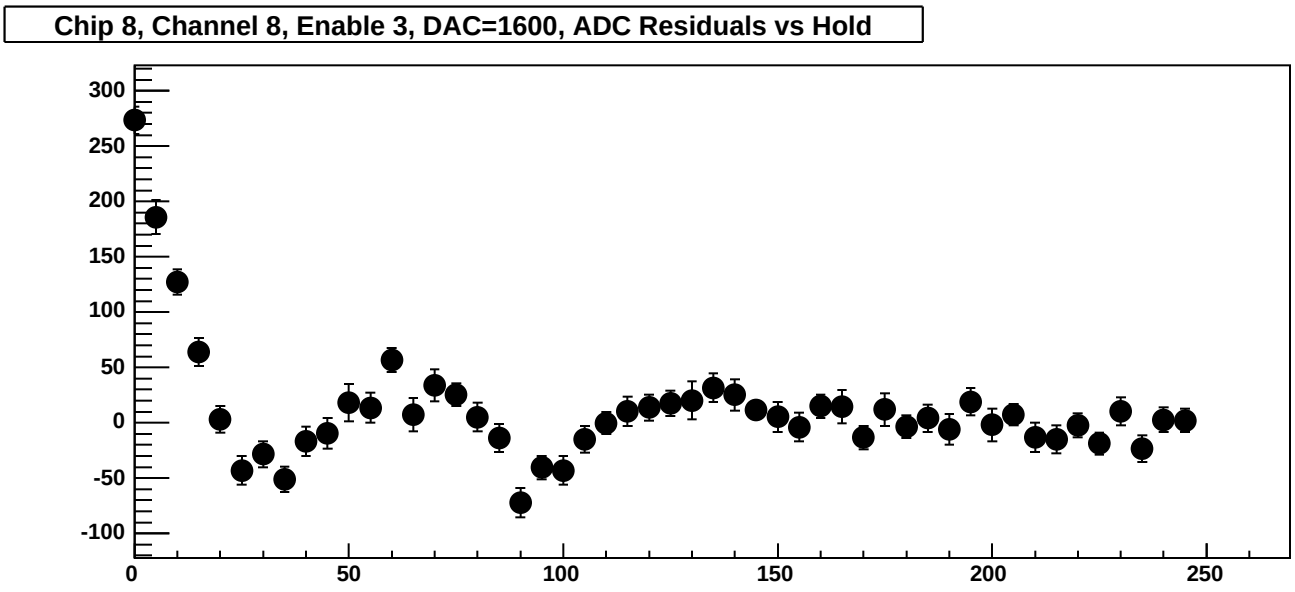


Chip 8, Channel 8, Enable 2!, DAC=1600, ADC Residuals vs Hold

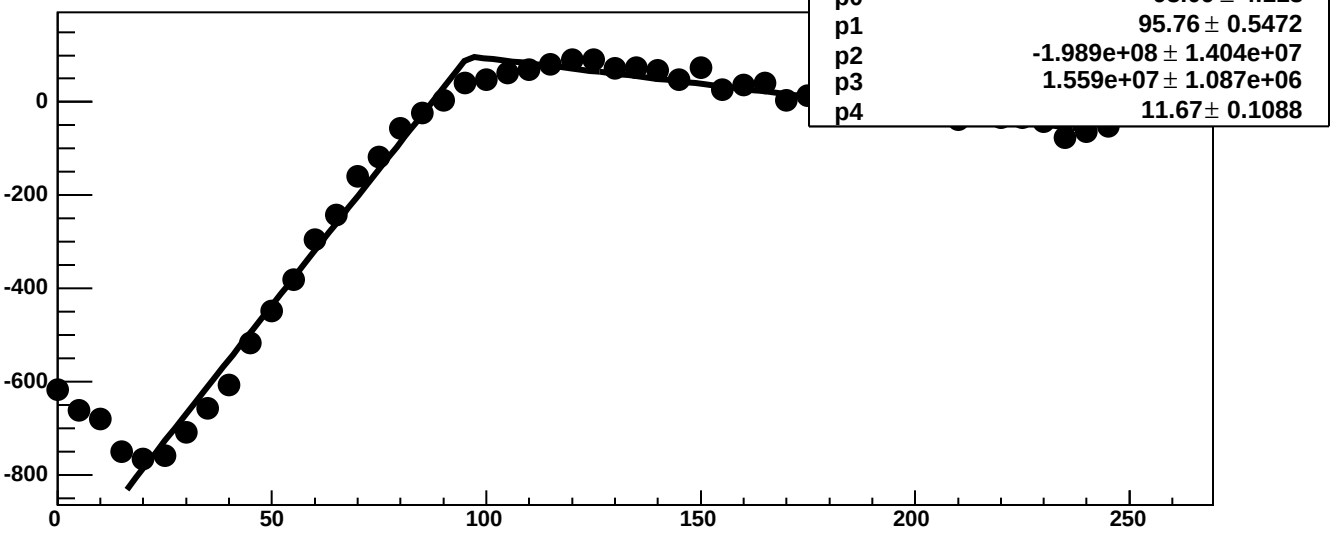



Chip 8, Channel 8, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 199.7 / 41
p0 164.4 ± 3.932
p1 90.63 ± 0.5637
p2 -9.992e+08 ± 1.111e+07
p3 7.516e+07 ± 2.384e+05
p4 11.94 ± 0.1356
200
0
-200
-400
-600
0
50
100
150
200
250

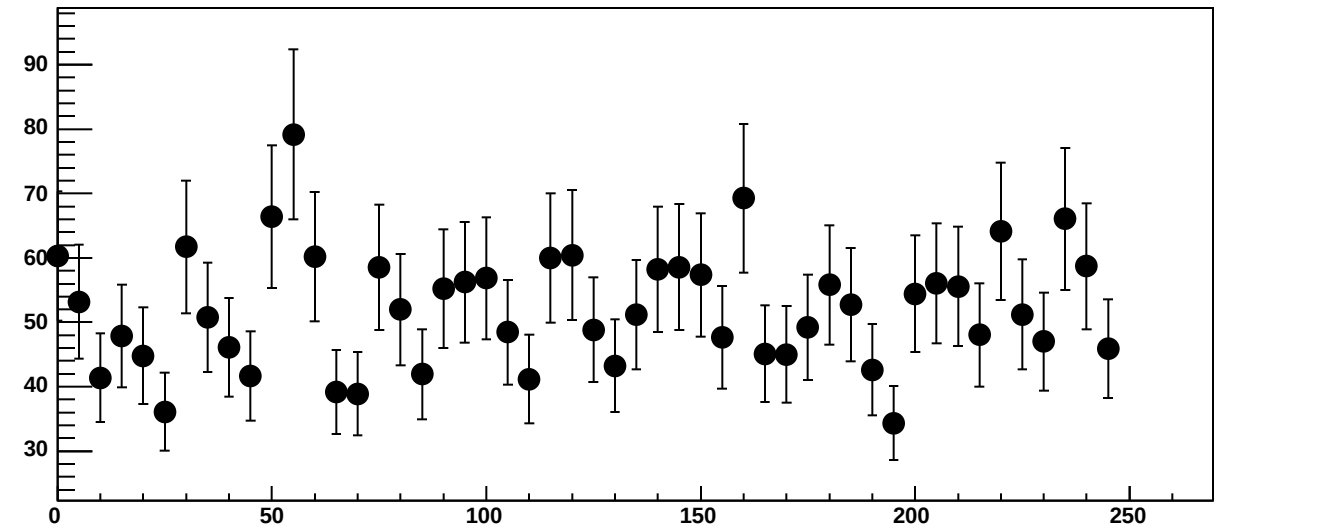

Chip 8, Channel 8, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 8, Channel 8, Enable 3, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

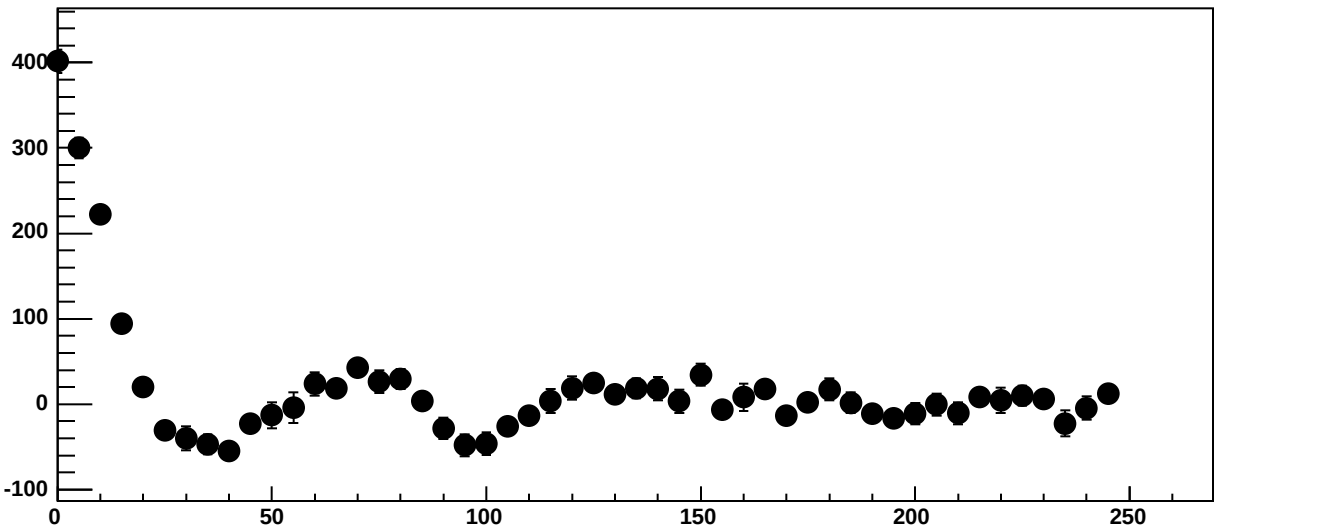
Chip 8, Channel 8, Enable 4, DAC=1600, ADC Mean vs Hold

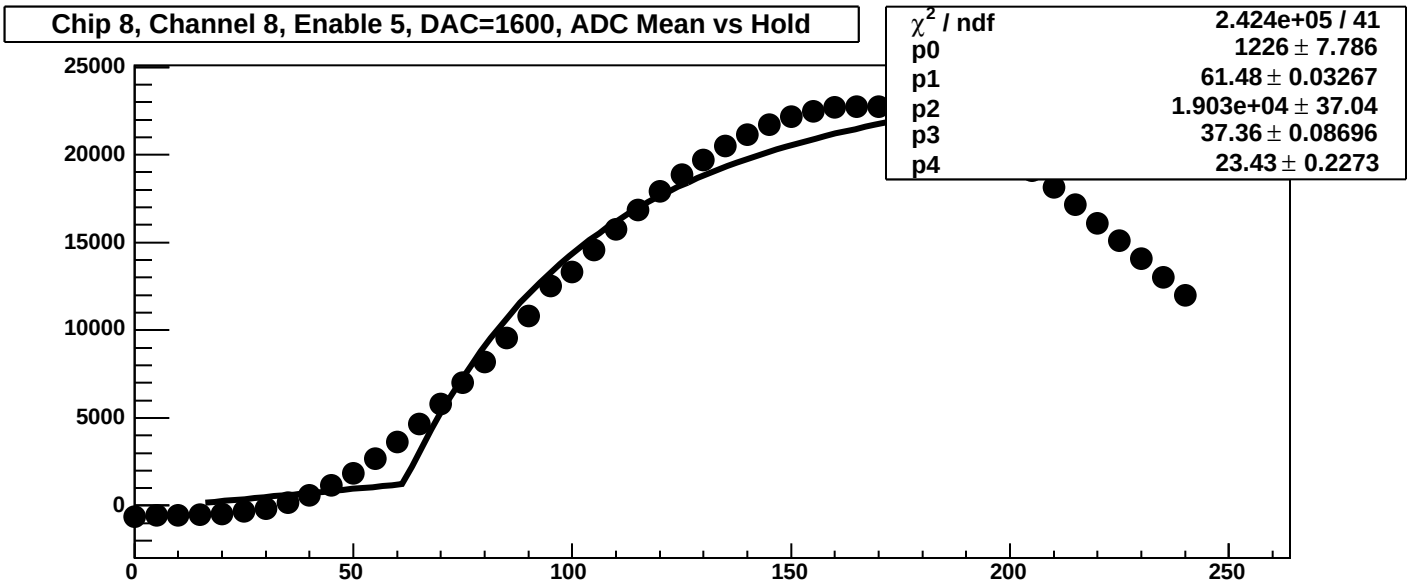


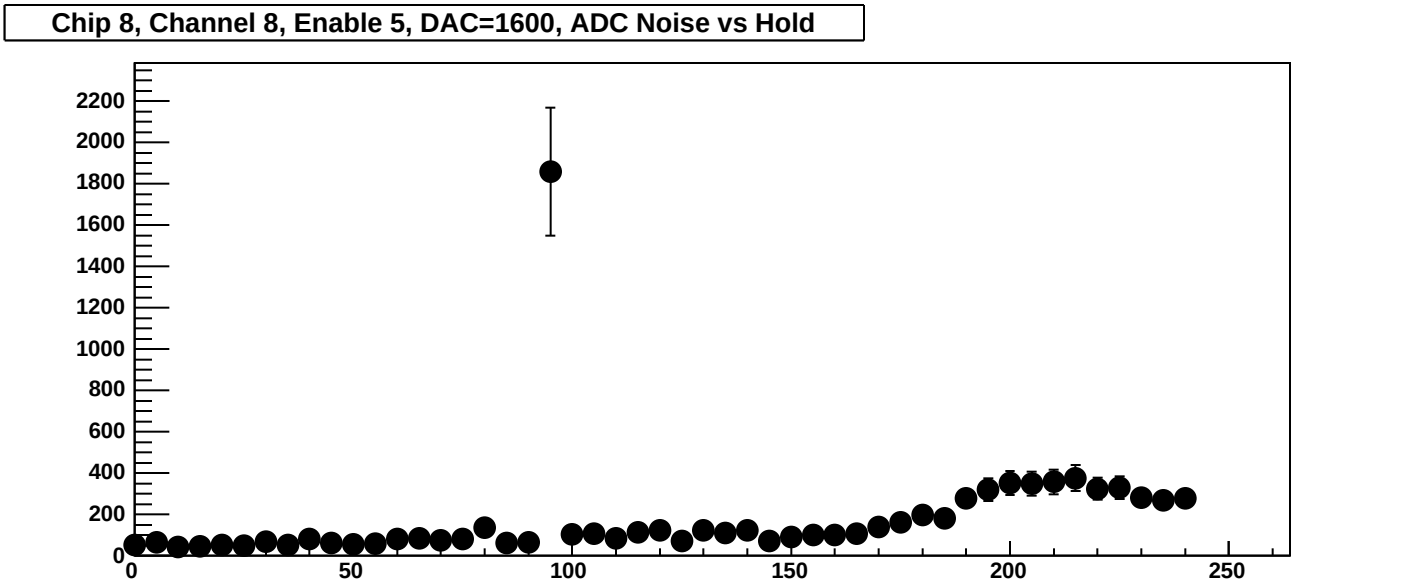
Chip 8, Channel 8, Enable 4, DAC=1600, ADC Noise vs Hold

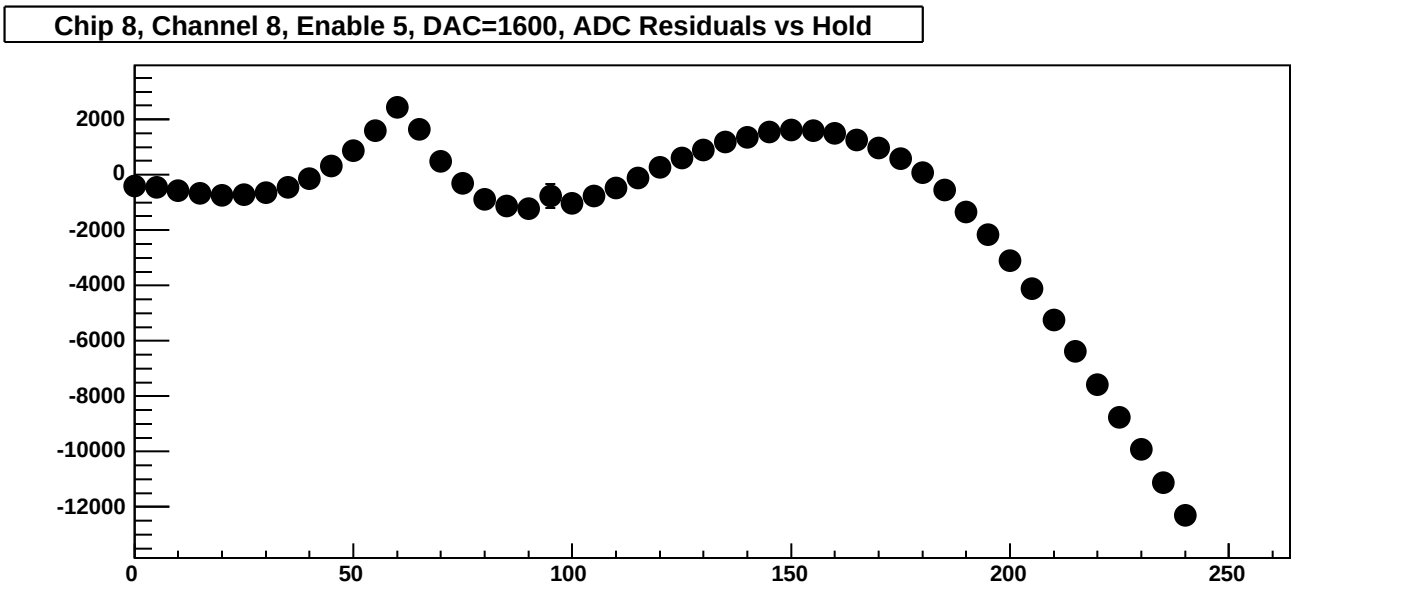


Chip 8, Channel 8, Enable 4, DAC=1600, ADC Residuals vs Hold

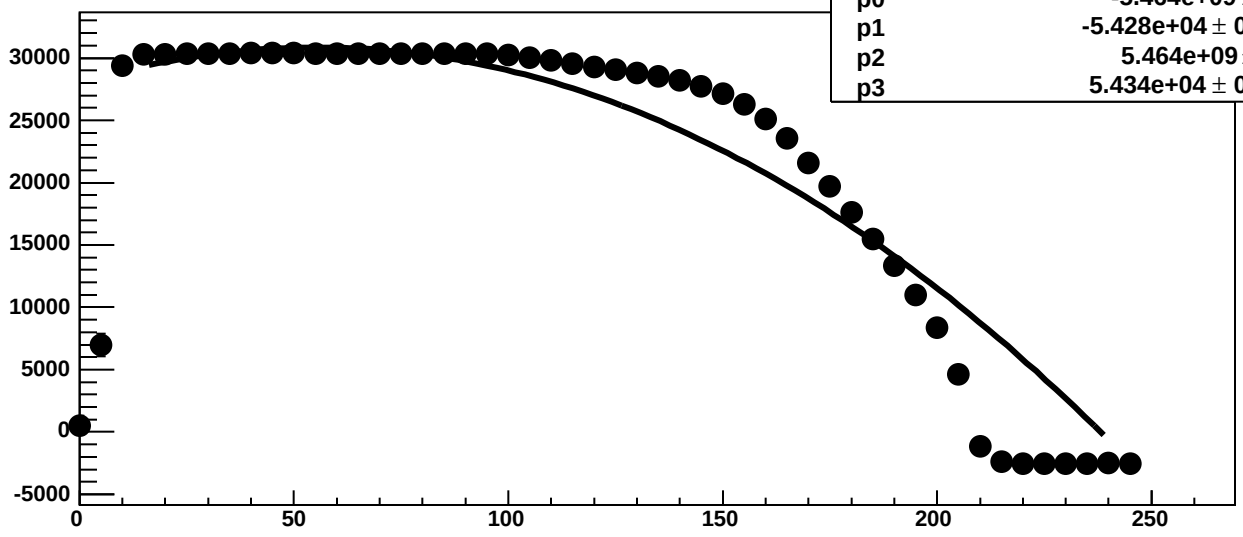



Chip 8, Channel 8, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.424e+05 / 41
p0 1226 ± 7.786
p1 61.48 ± 0.03267
p2 1.903e+04 ± 37.04
p3 37.36 ± 0.08696
p4 23.43 ± 0.2273
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

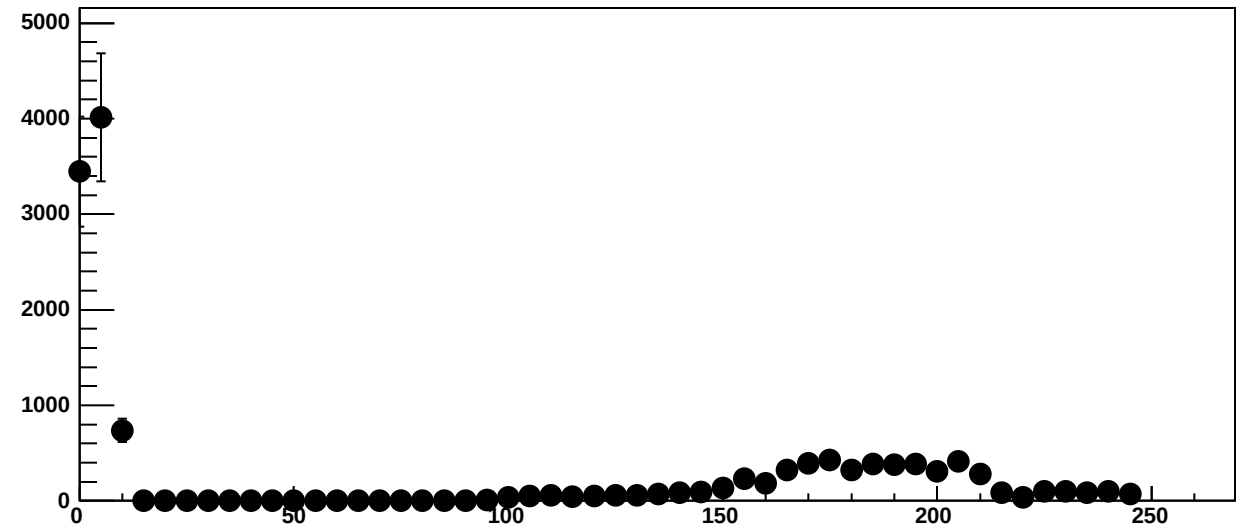

Chip 8, Channel 8, Enable 5, DAC=1600, ADC Noise vs Hold
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 8, Channel 8, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
0
50
100
150
200
250

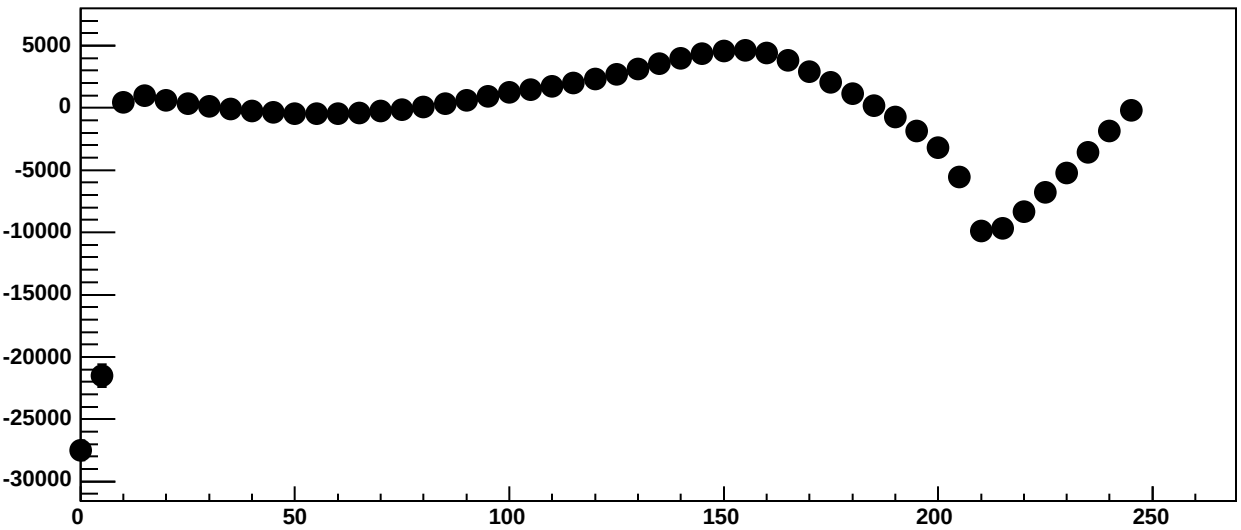
Chip 8, Channel 9, Enable 0!, DAC=1600, ADC Mean vs Hold



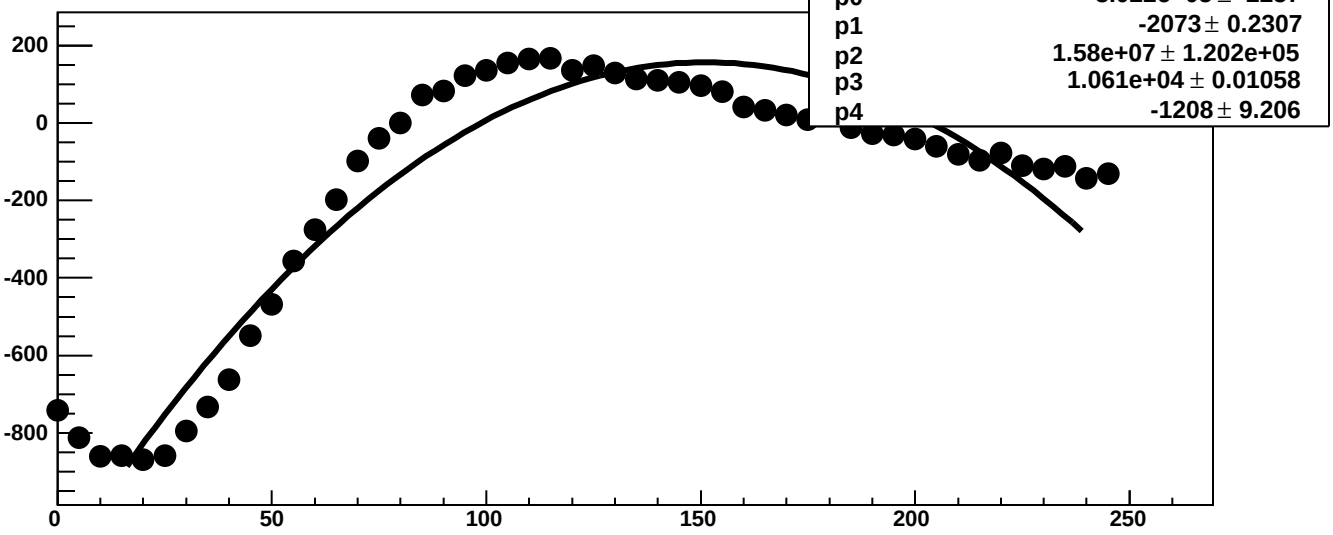
Chip 8, Channel 9, Enable 0!, DAC=1600, ADC Noise vs Hold



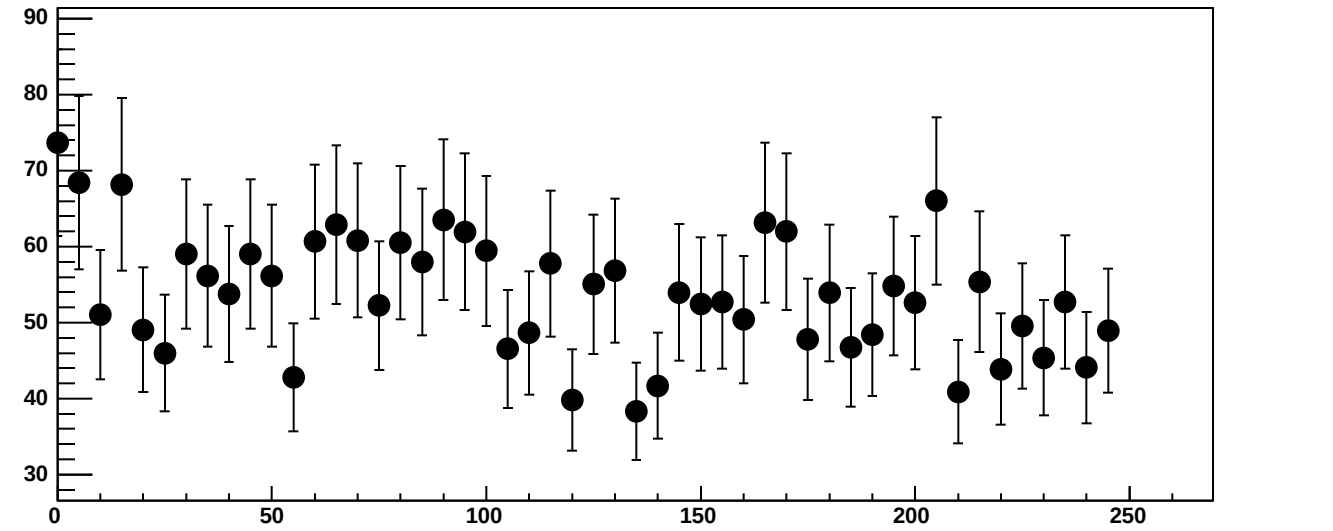
Chip 8, Channel 9, Enable 0!, DAC=1600, ADC Residuals vs Hold



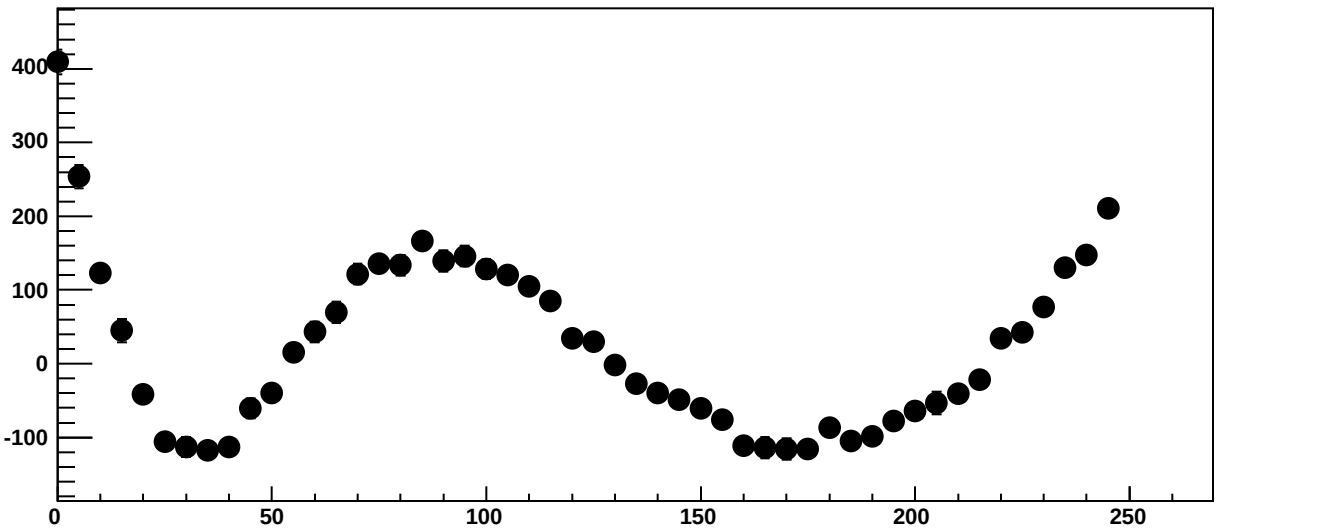
Chip 8, Channel 9, Enable 1, DAC=1600, ADC Mean vs Hold



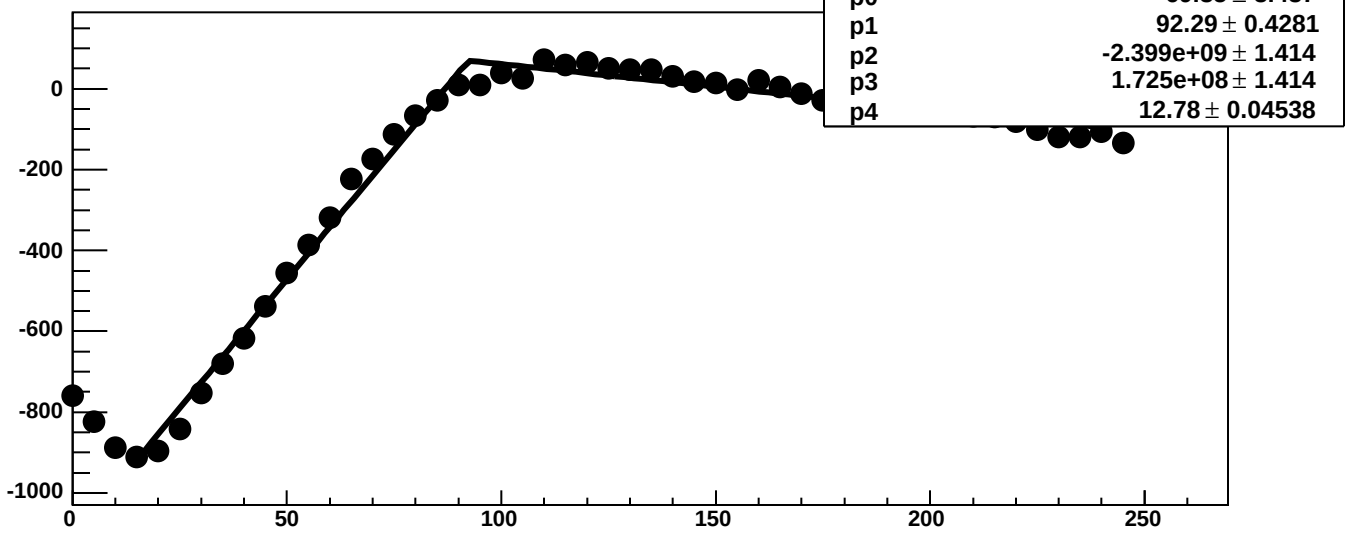
Chip 8, Channel 9, Enable 1, DAC=1600, ADC Noise vs Hold



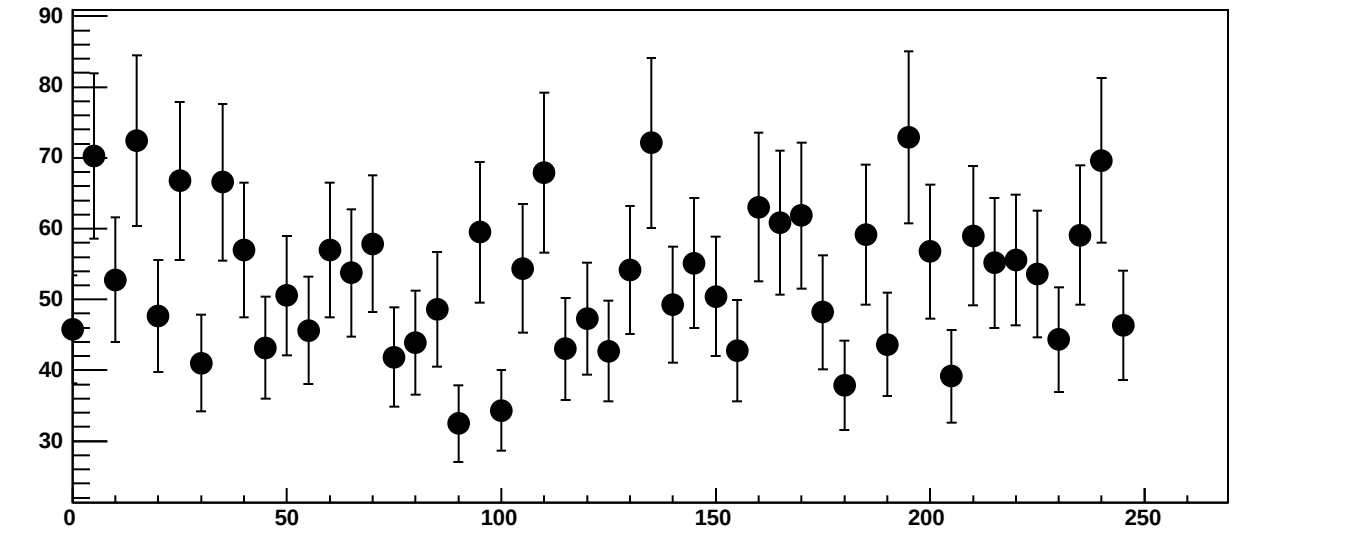
Chip 8, Channel 9, Enable 1, DAC=1600, ADC Residuals vs Hold



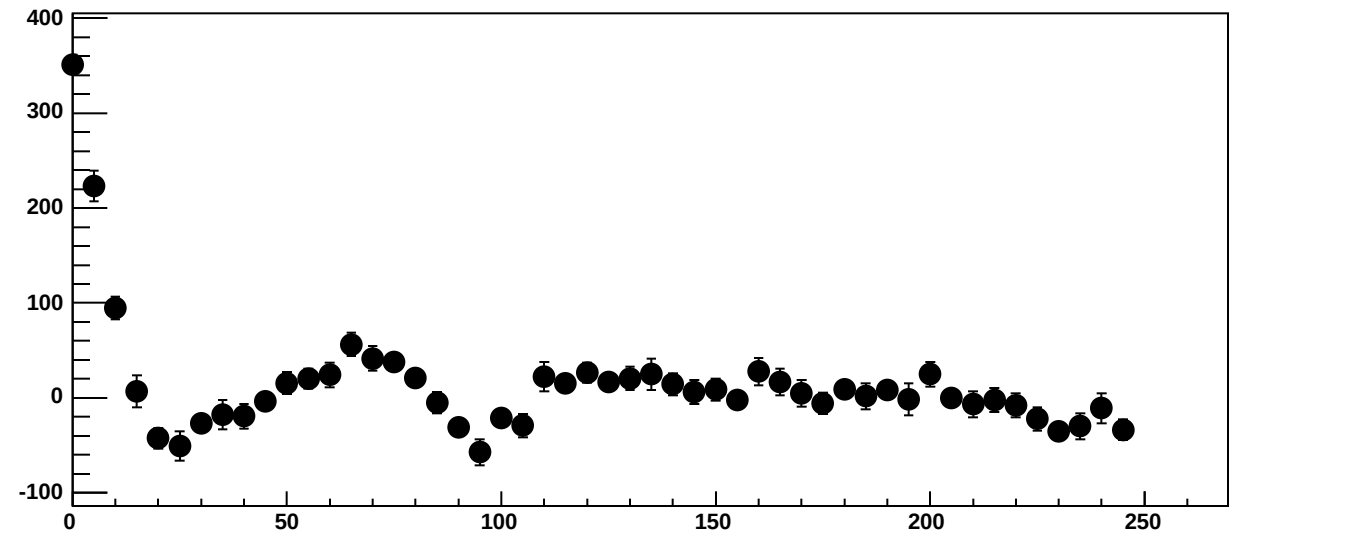
Chip 8, Channel 9, Enable 2, DAC=1600, ADC Mean vs Hold



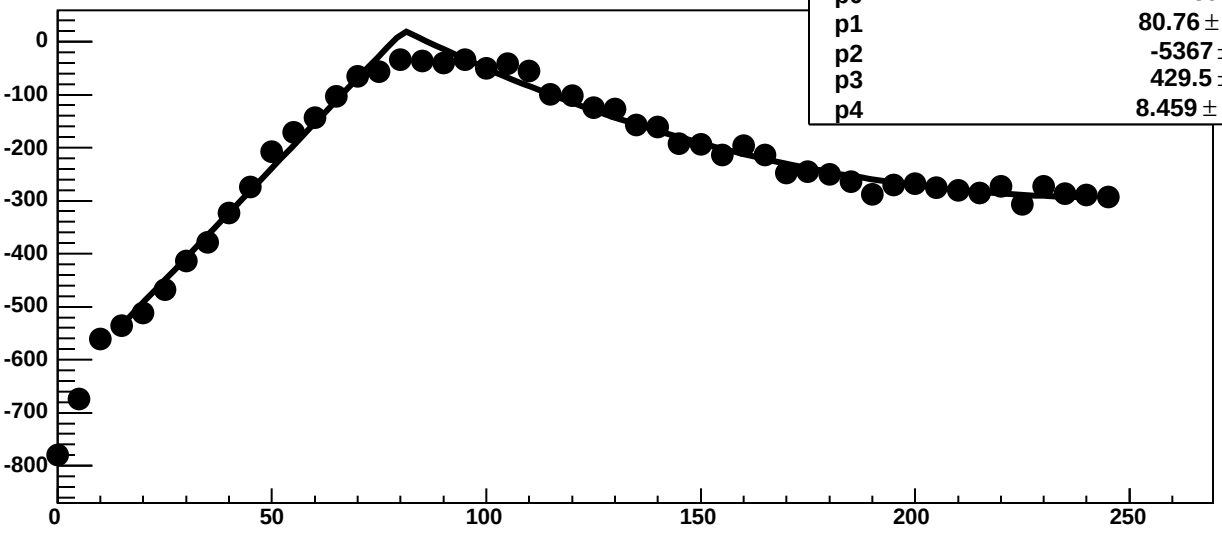
Chip 8, Channel 9, Enable 2, DAC=1600, ADC Noise vs Hold



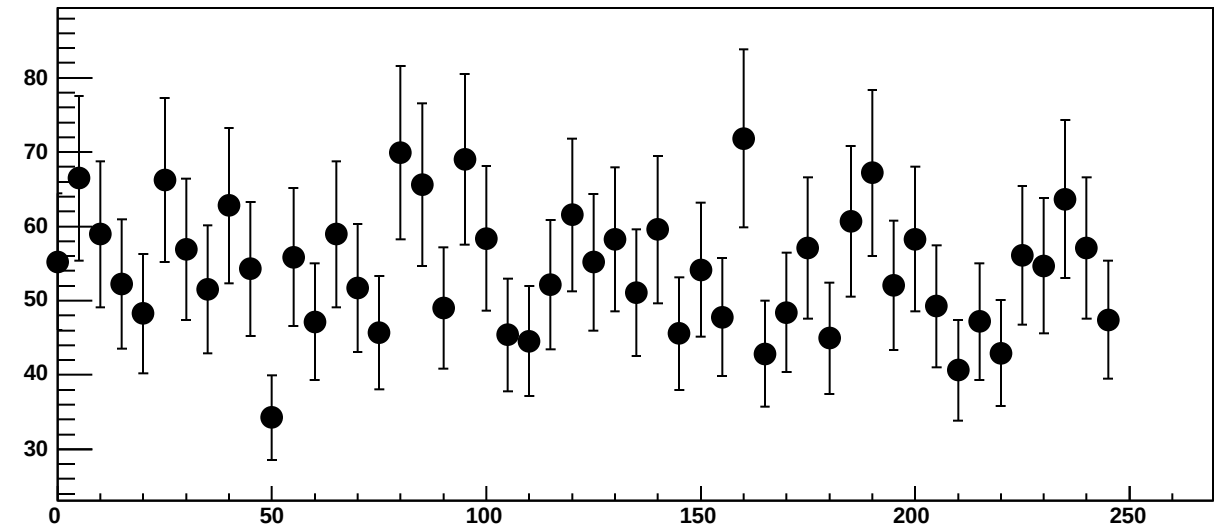
Chip 8, Channel 9, Enable 2, DAC=1600, ADC Residuals vs Hold



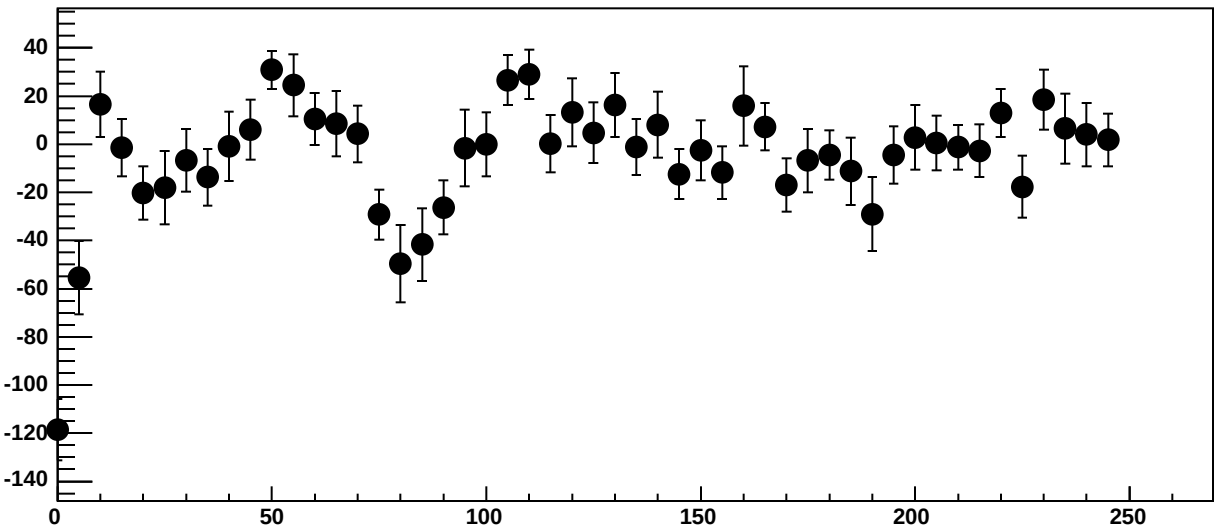
Chip 8, Channel 9, Enable 3, DAC=1600, ADC Mean vs Hold

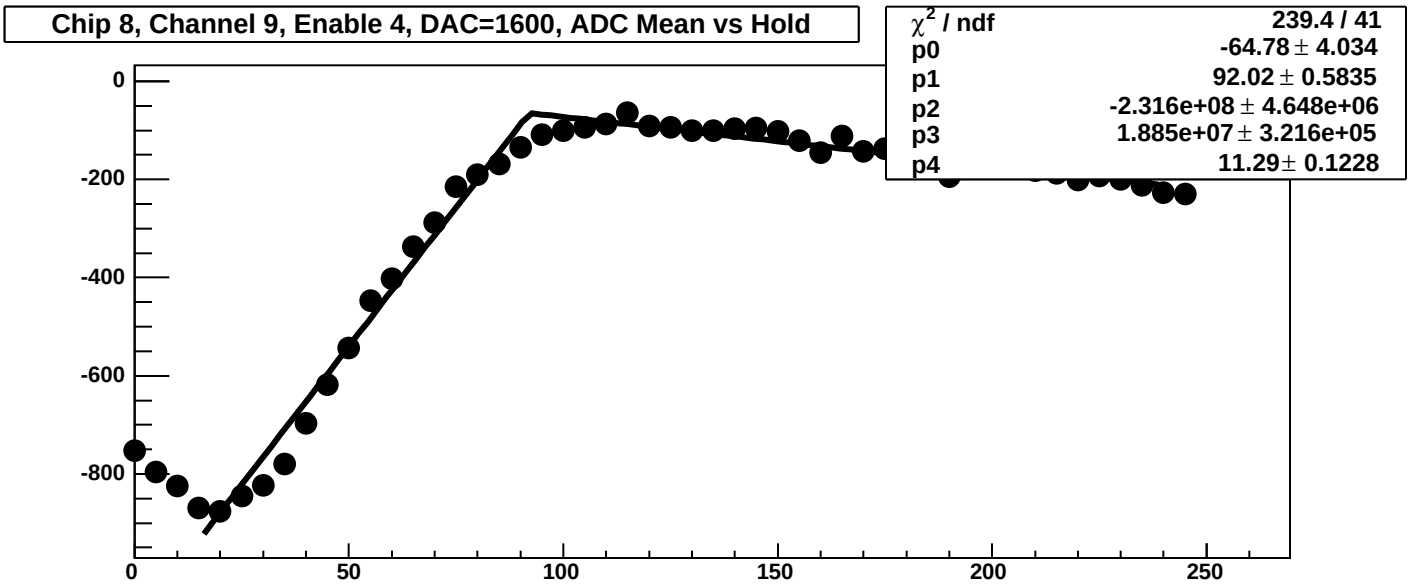


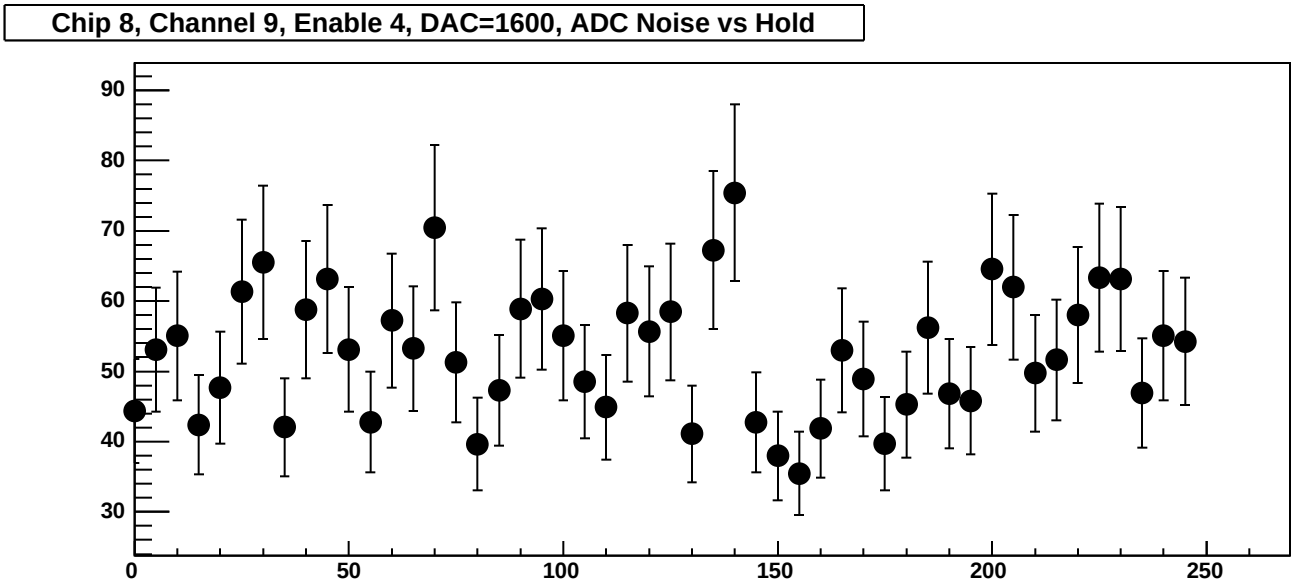
Chip 8, Channel 9, Enable 3, DAC=1600, ADC Noise vs Hold

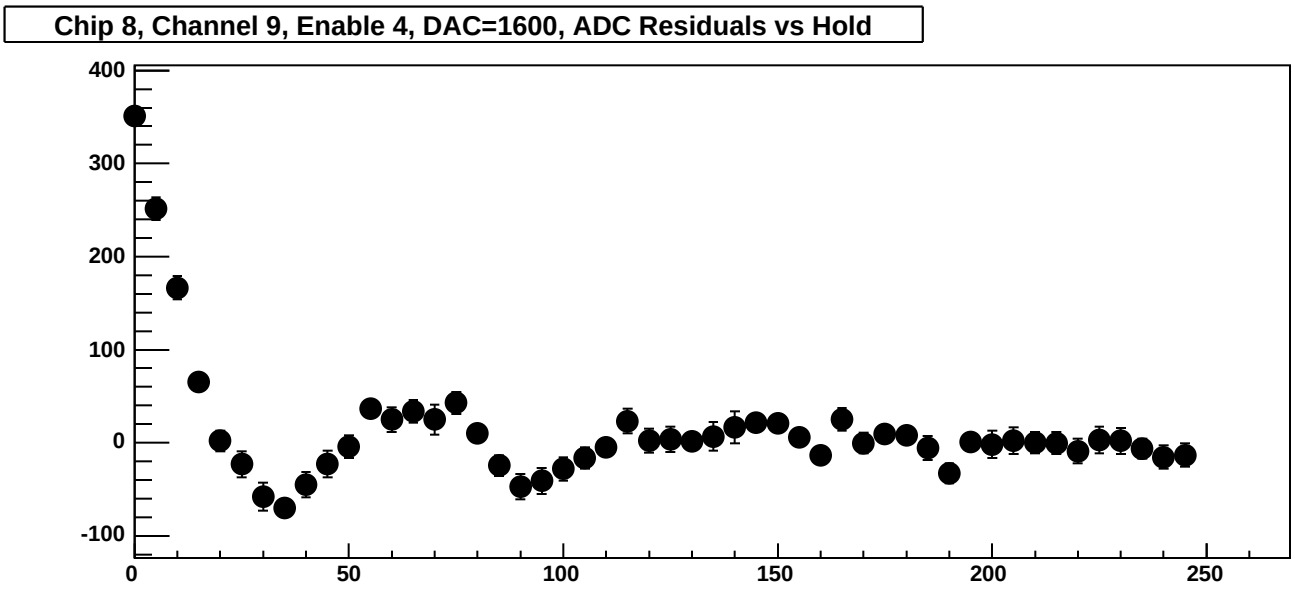


Chip 8, Channel 9, Enable 3, DAC=1600, ADC Residuals vs Hold

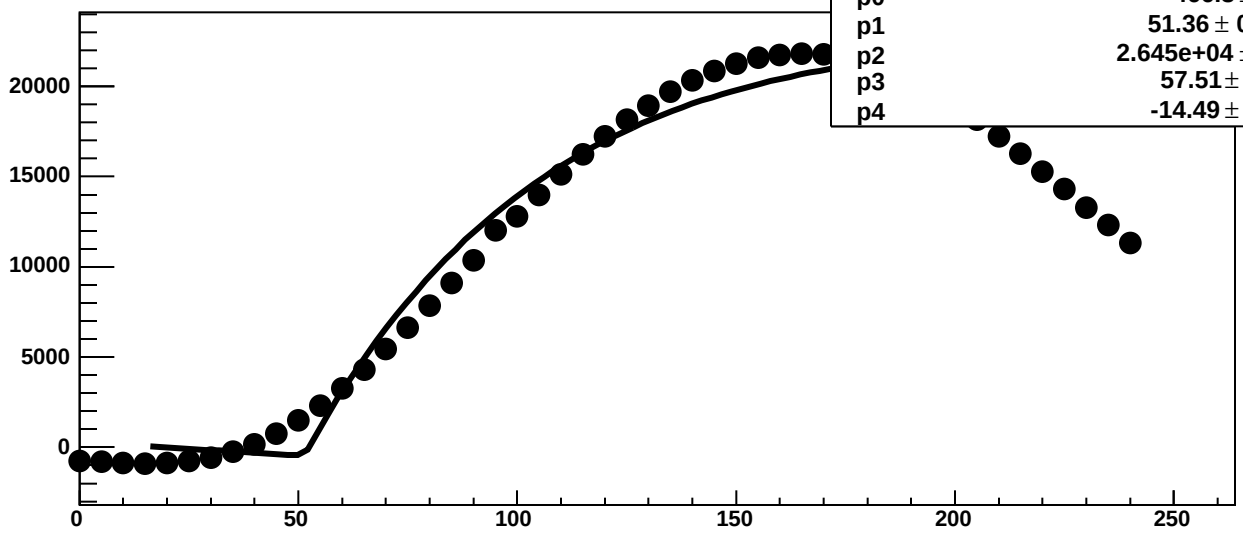



Chip 8, Channel 9, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 239.4 / 41
p0 -64.78 ± 4.034
p1 92.02 ± 0.5835
p2 -2.316e+08 ± 4.648e+06
p3 1.885e+07 ± 3.216e+05
p4 11.29 ± 0.1228
0
-200
-400
-600
-800
0
50
100
150
200
250

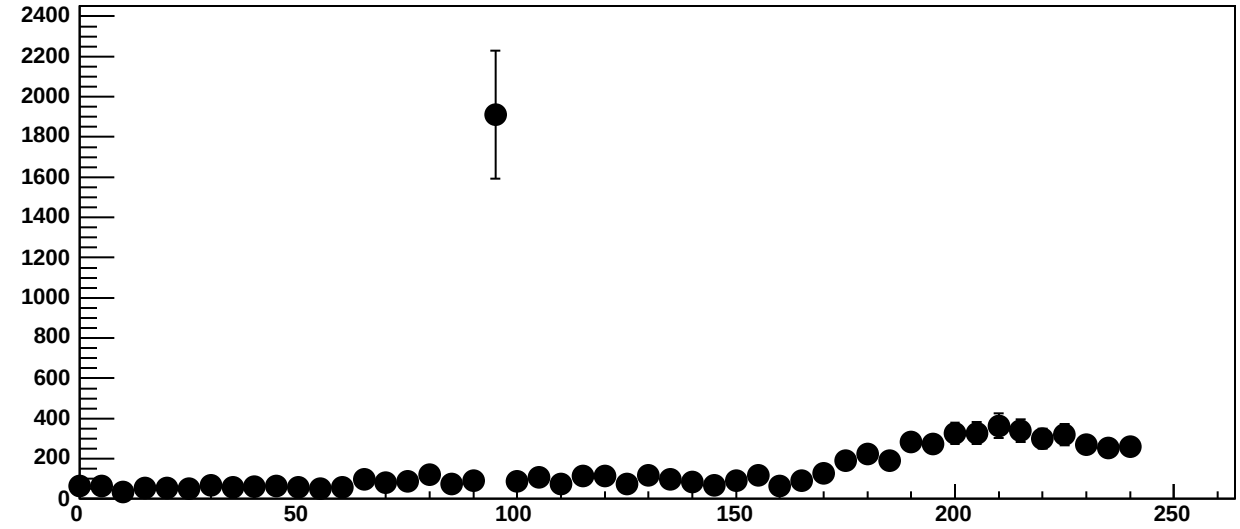

Chip 8, Channel 9, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 8, Channel 9, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

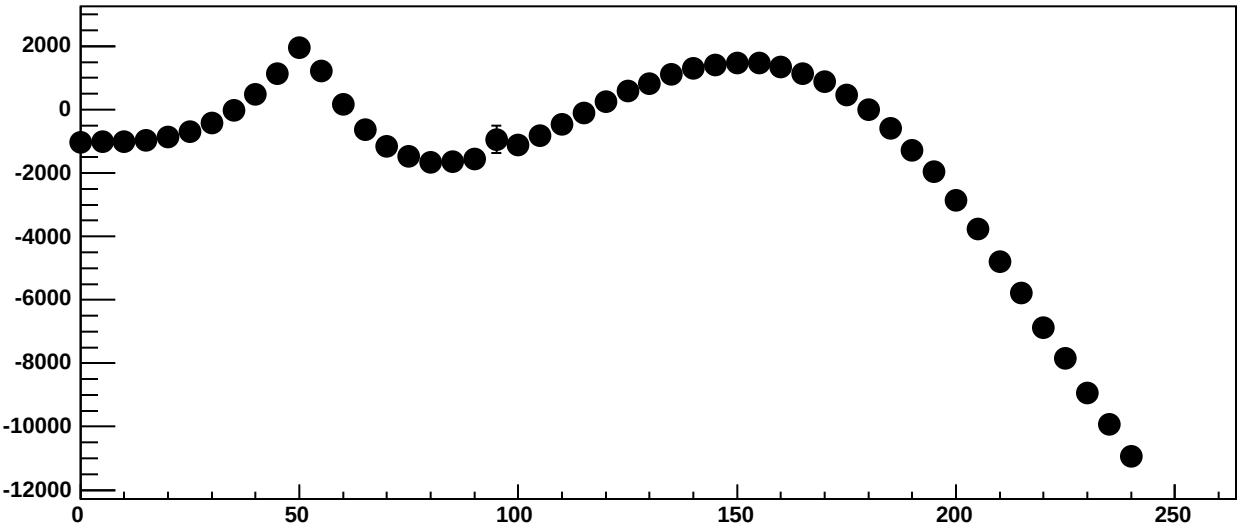
Chip 8, Channel 9, Enable 5, DAC=1600, ADC Mean vs Hold



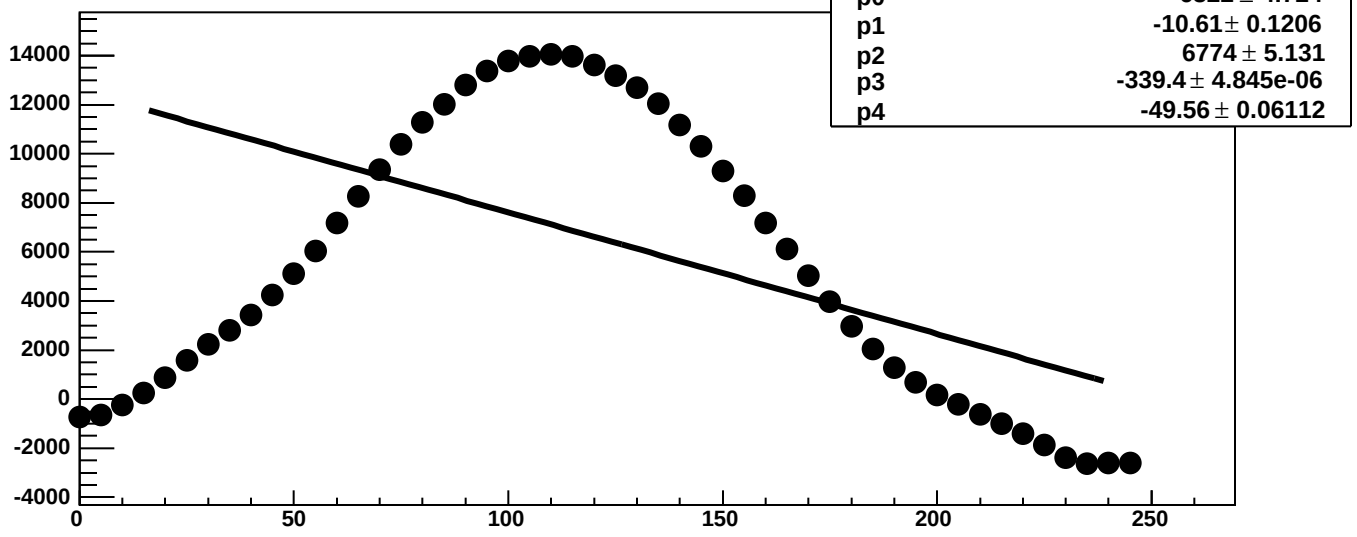
Chip 8, Channel 9, Enable 5, DAC=1600, ADC Noise vs Hold



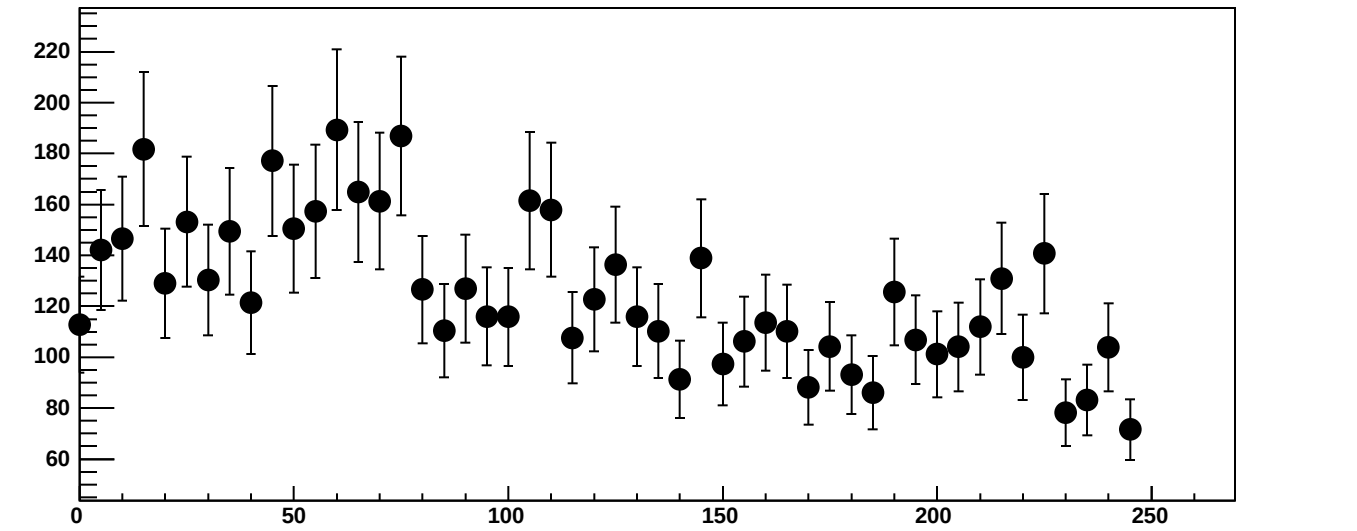
Chip 8, Channel 9, Enable 5, DAC=1600, ADC Residuals vs Hold



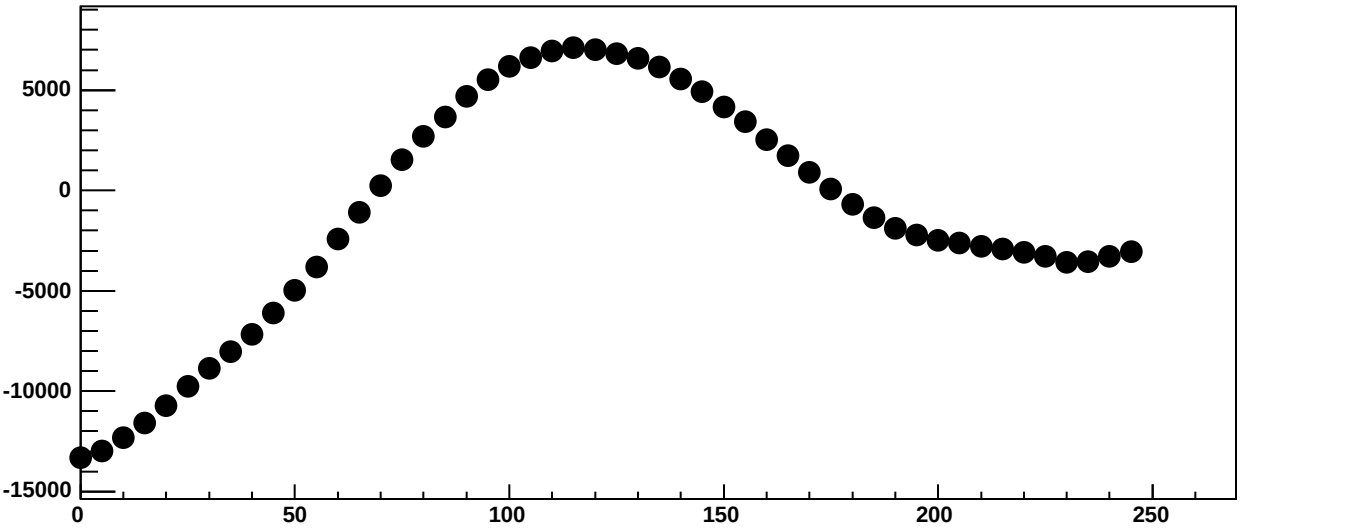
Chip 8, Channel 10, Enable 0, DAC=1600, ADC Mean vs Hold



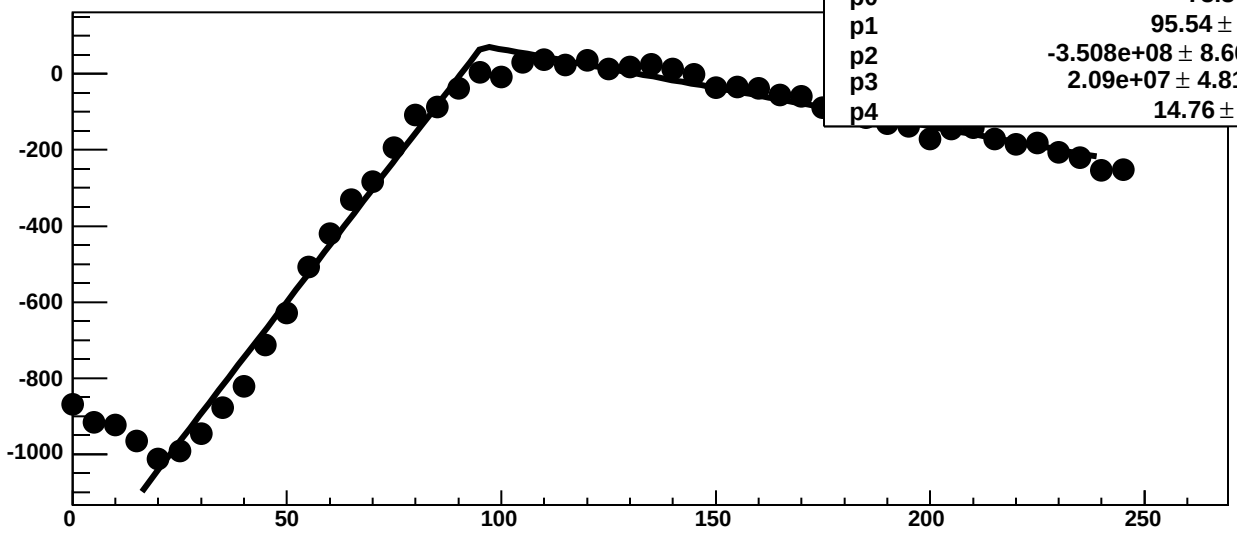
Chip 8, Channel 10, Enable 0, DAC=1600, ADC Noise vs Hold



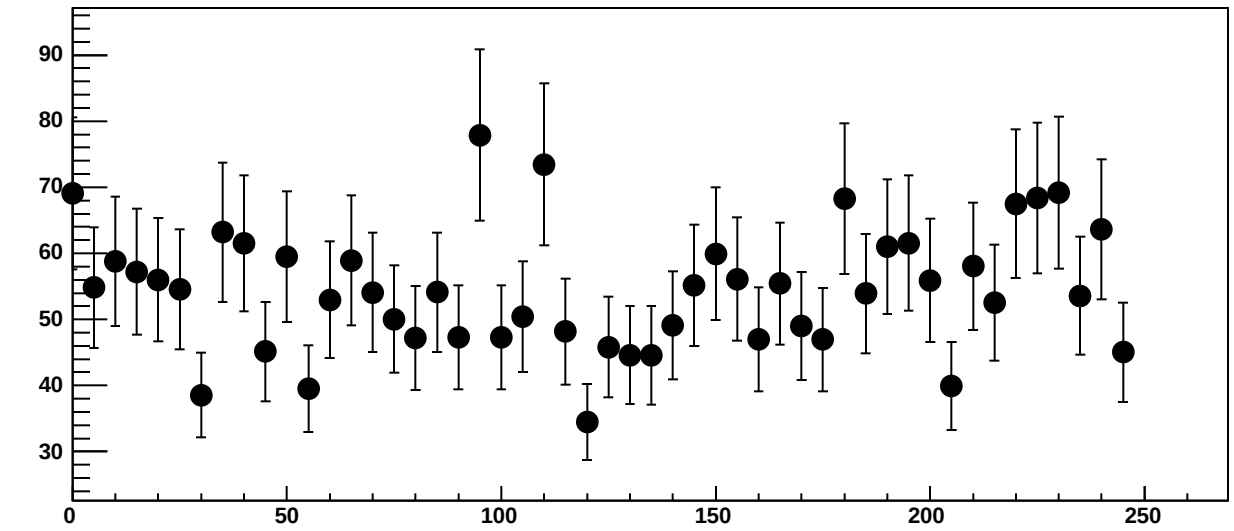
Chip 8, Channel 10, Enable 0, DAC=1600, ADC Residuals vs Hold



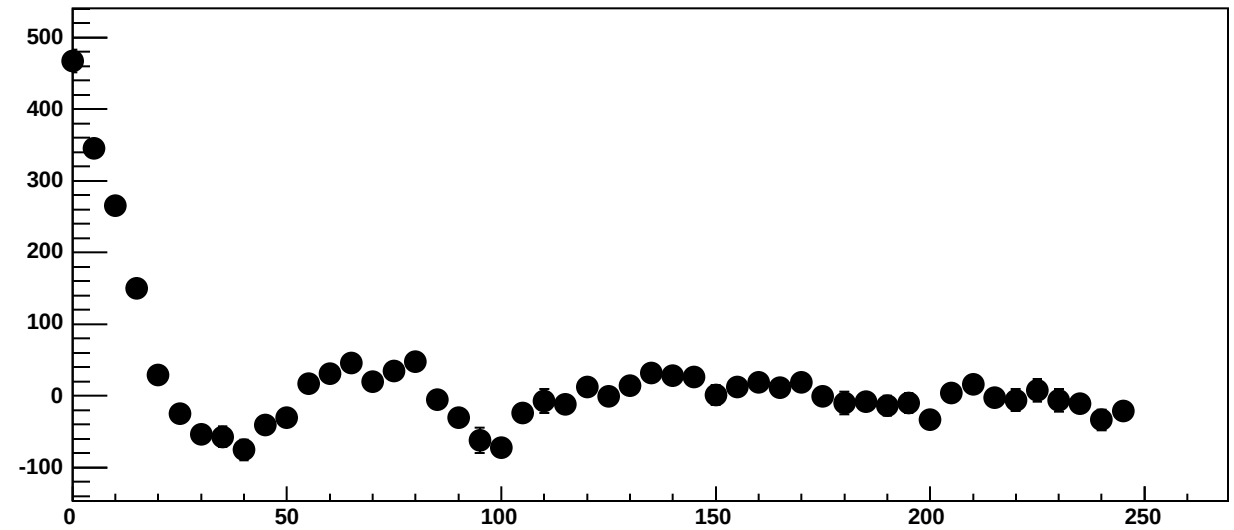
Chip 8, Channel 10, Enable 1, DAC=1600, ADC Mean vs Hold



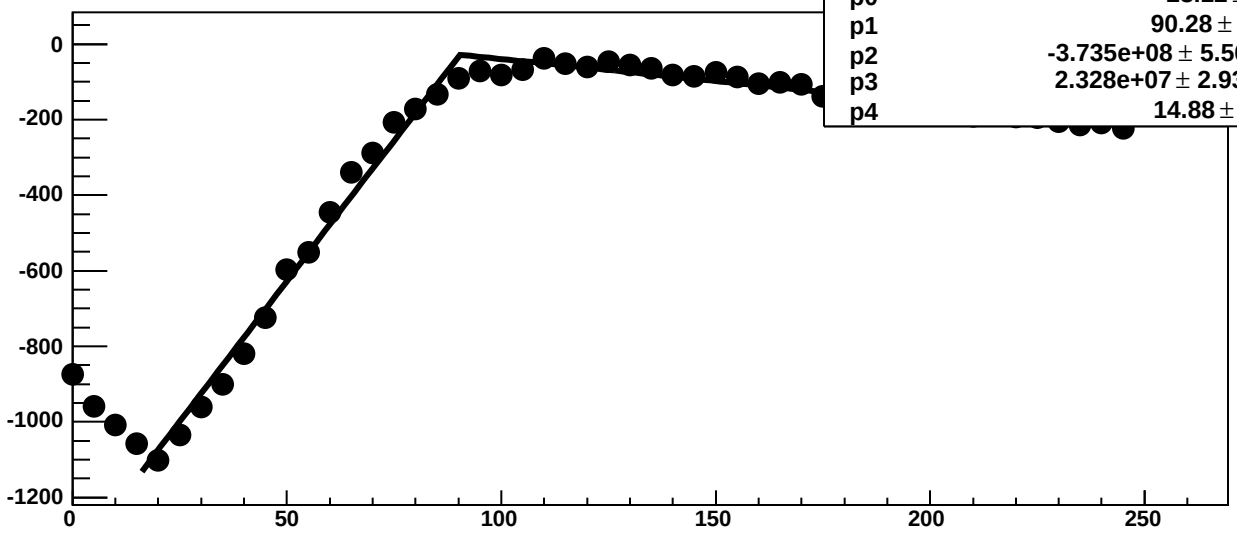
Chip 8, Channel 10, Enable 1, DAC=1600, ADC Noise vs Hold



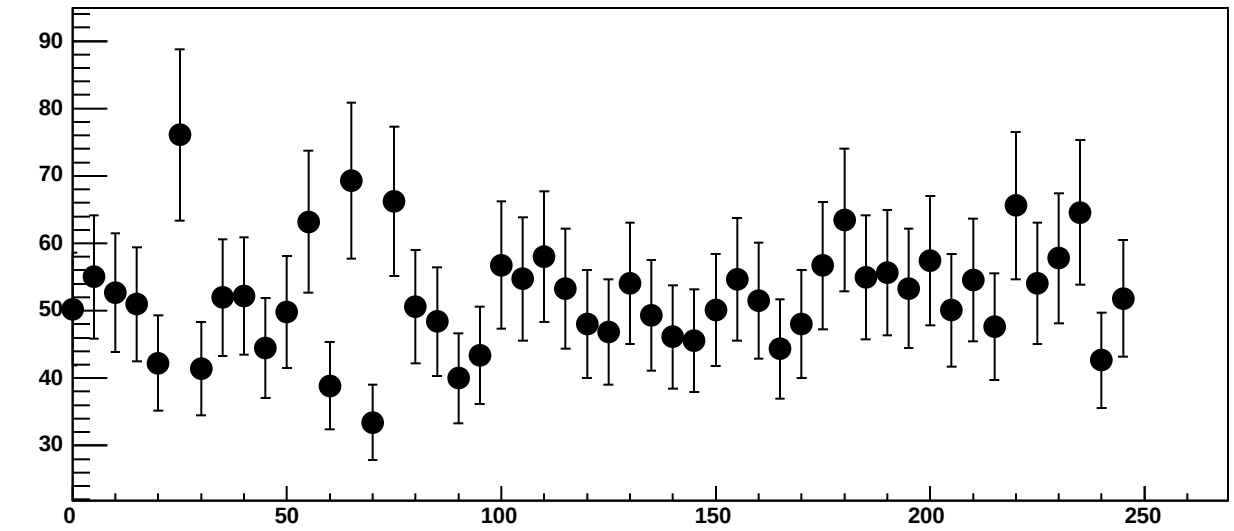
Chip 8, Channel 10, Enable 1, DAC=1600, ADC Residuals vs Hold



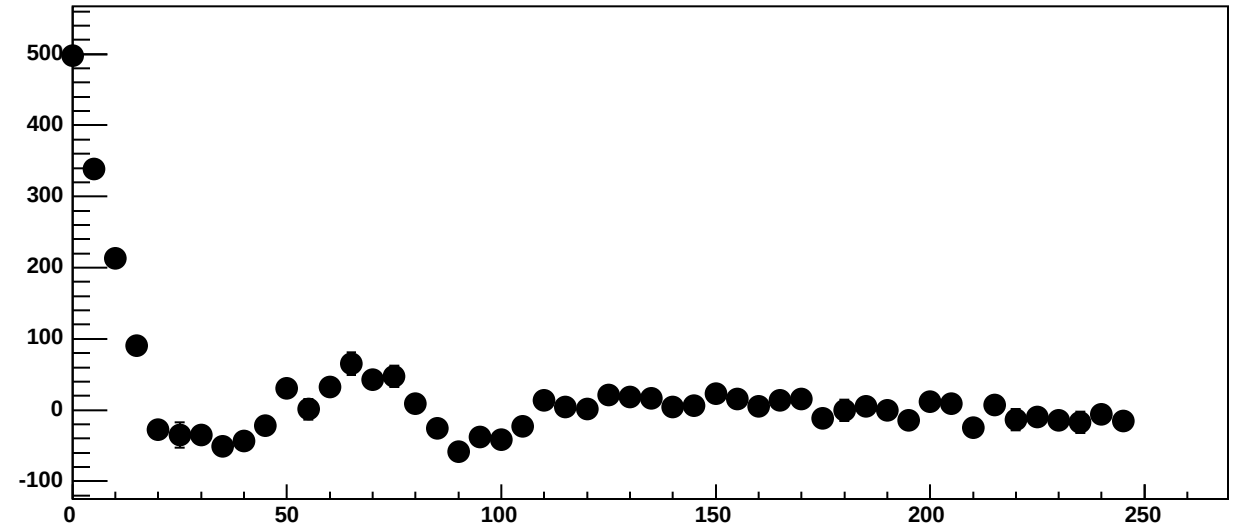
Chip 8, Channel 10, Enable 2, DAC=1600, ADC Mean vs Hold

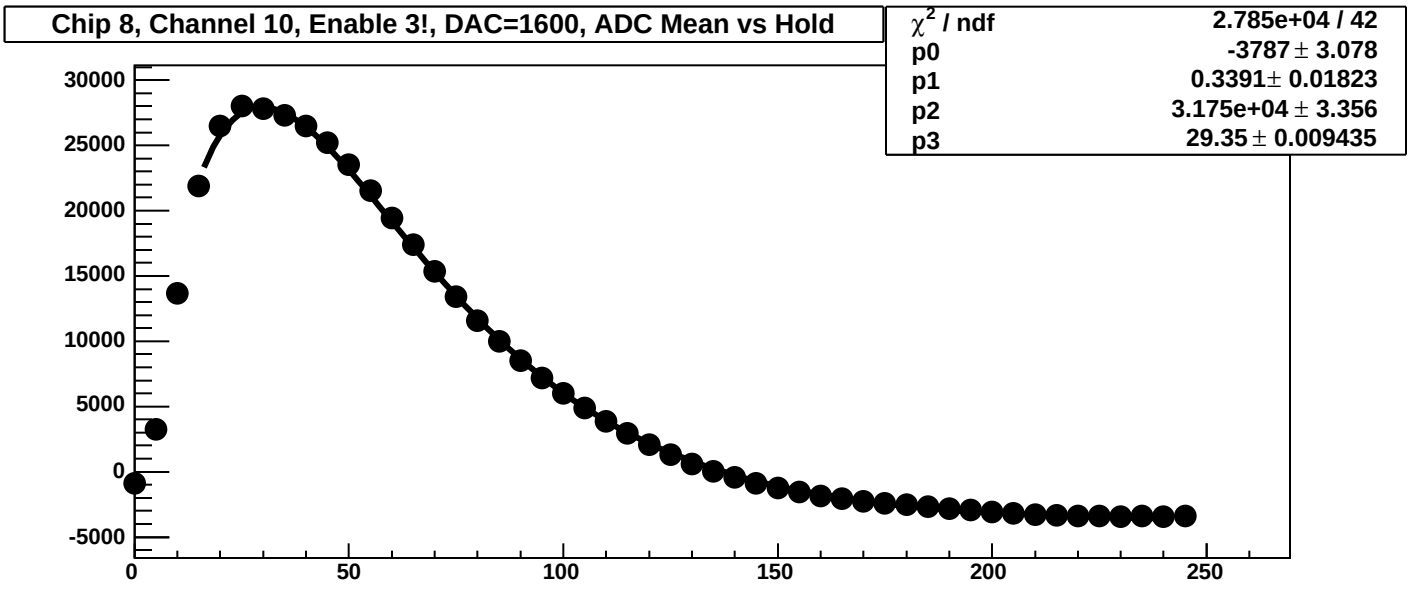


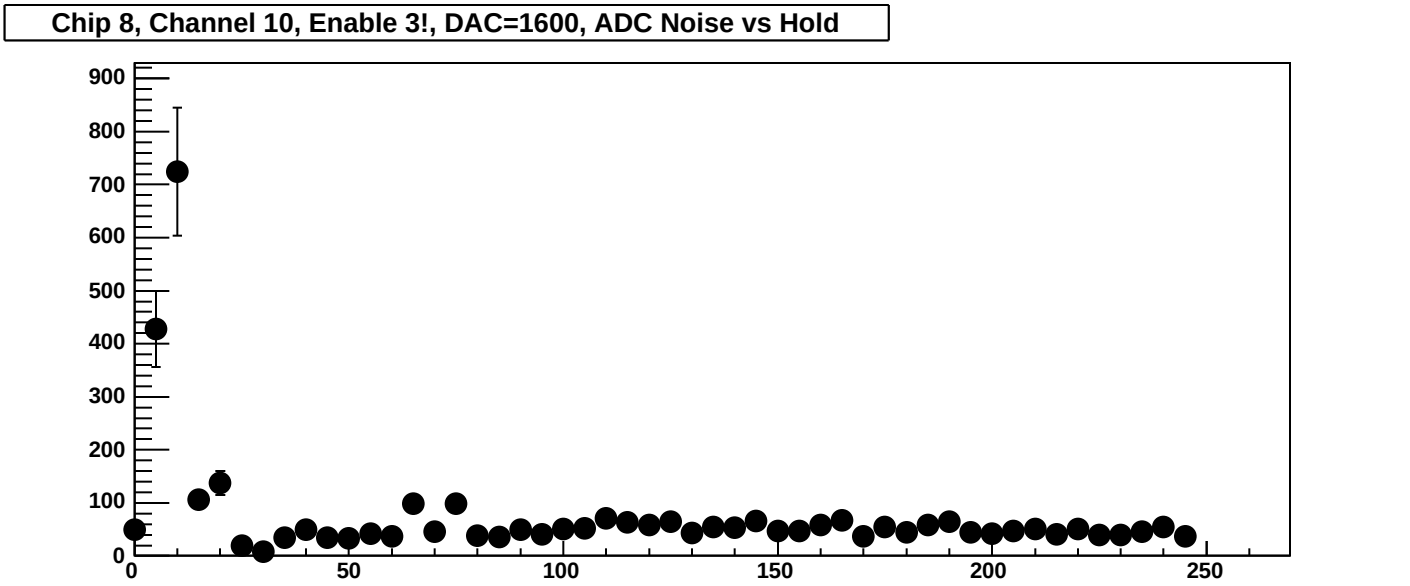
Chip 8, Channel 10, Enable 2, DAC=1600, ADC Noise vs Hold

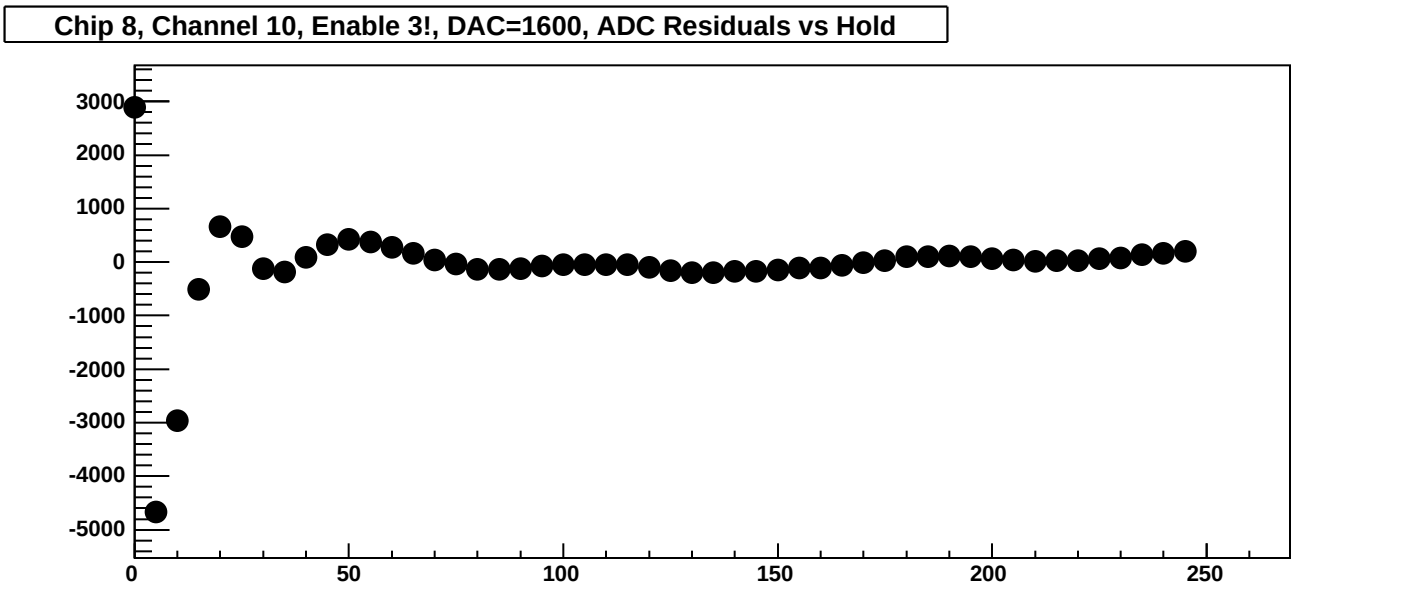


Chip 8, Channel 10, Enable 2, DAC=1600, ADC Residuals vs Hold

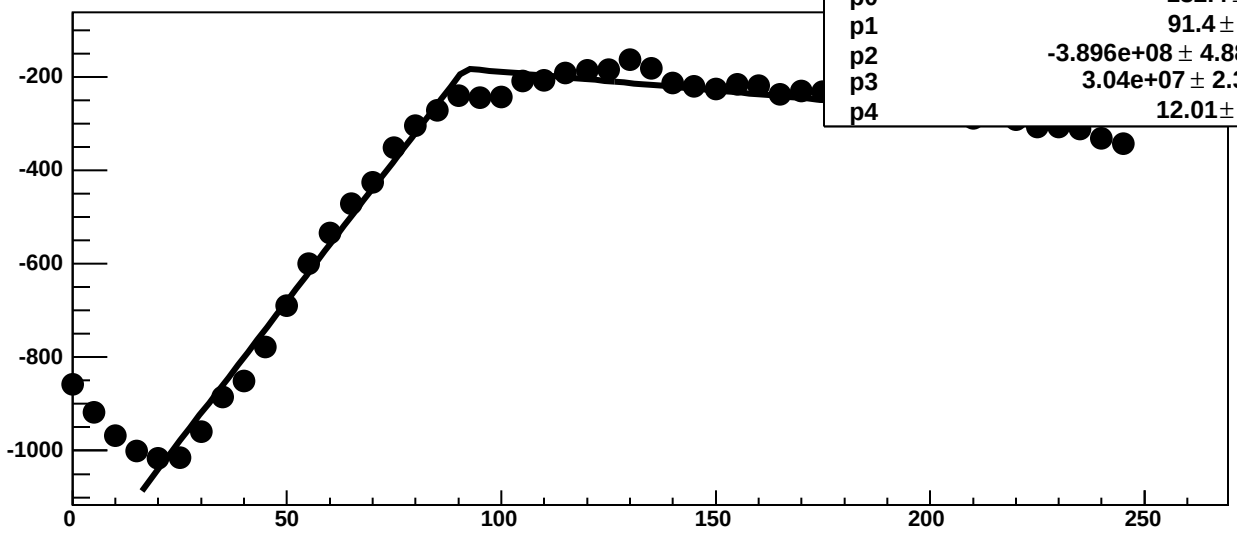



Chip 8, Channel 10, Enable 3!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.785e+04 / 42
p0 -3787 ± 3.078
p1 0.3391 ± 0.01823
p2 3.175e+04 ± 3.356
p3 29.35 ± 0.009435
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

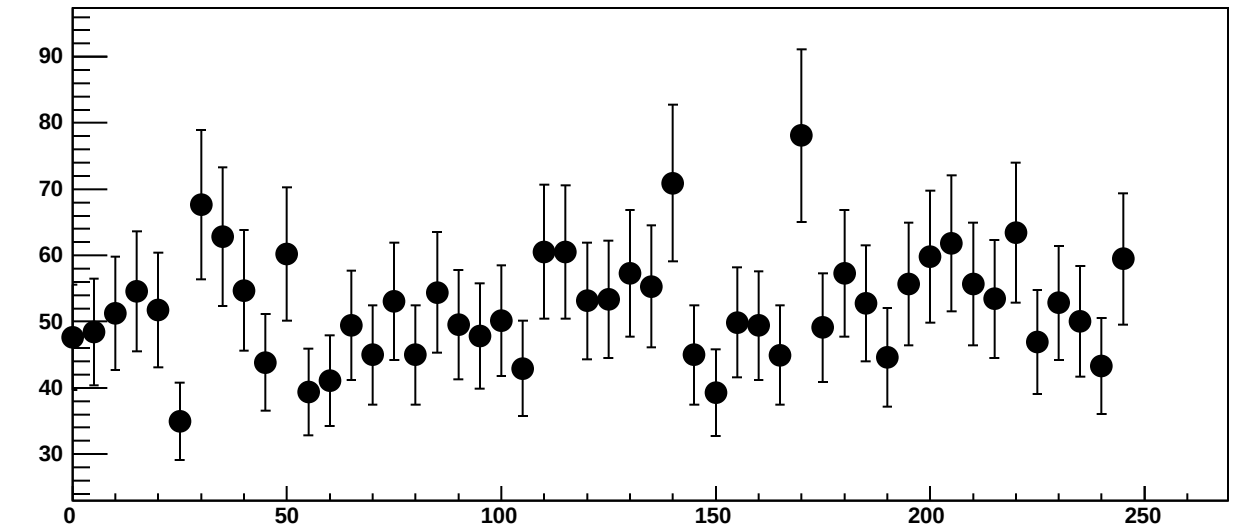

Chip 8, Channel 10, Enable 3!, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 8, Channel 10, Enable 3!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

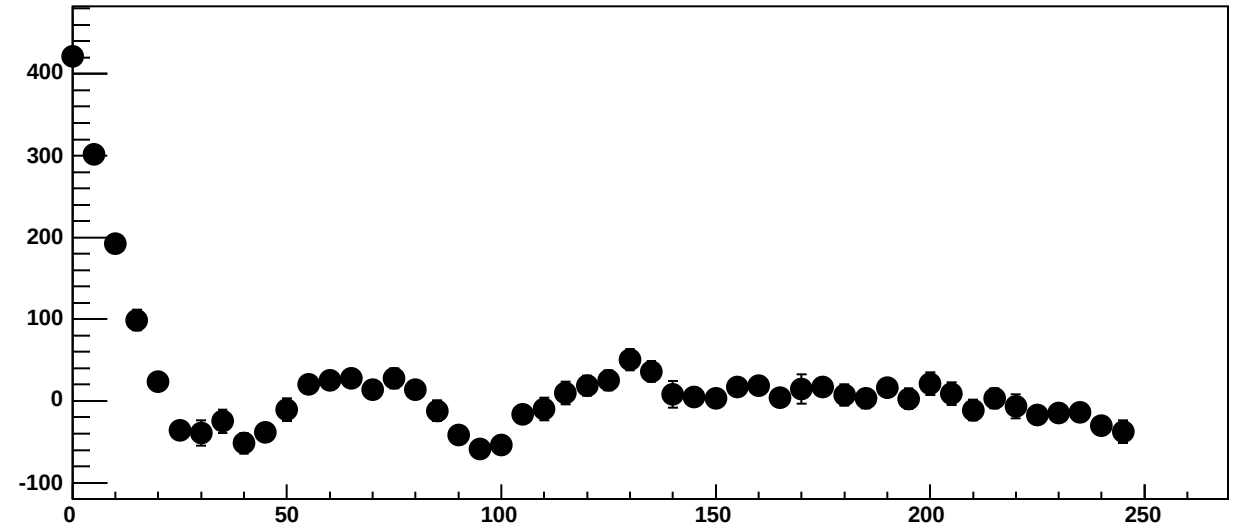
Chip 8, Channel 10, Enable 4, DAC=1600, ADC Mean vs Hold

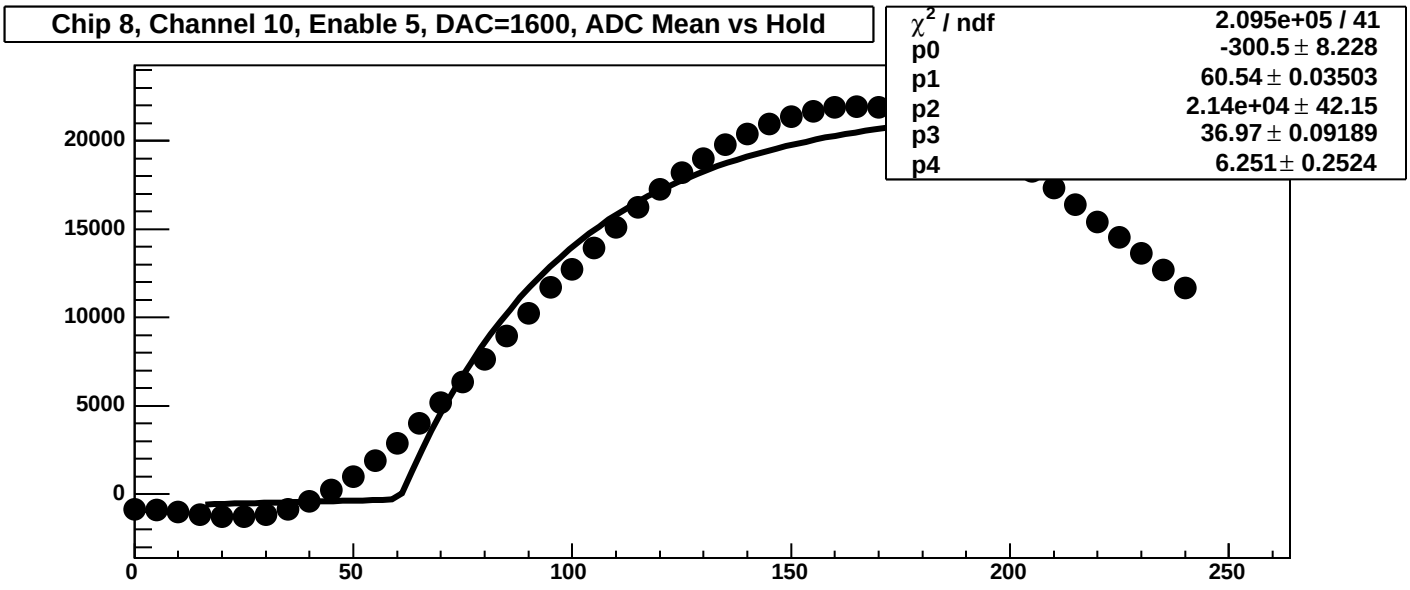


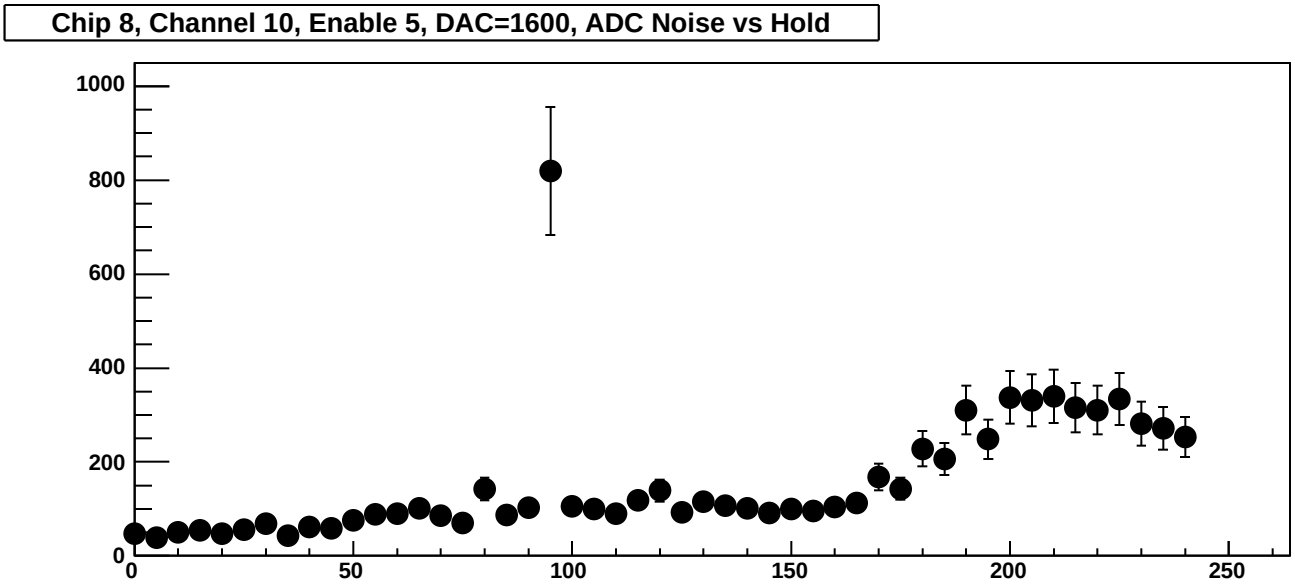
Chip 8, Channel 10, Enable 4, DAC=1600, ADC Noise vs Hold

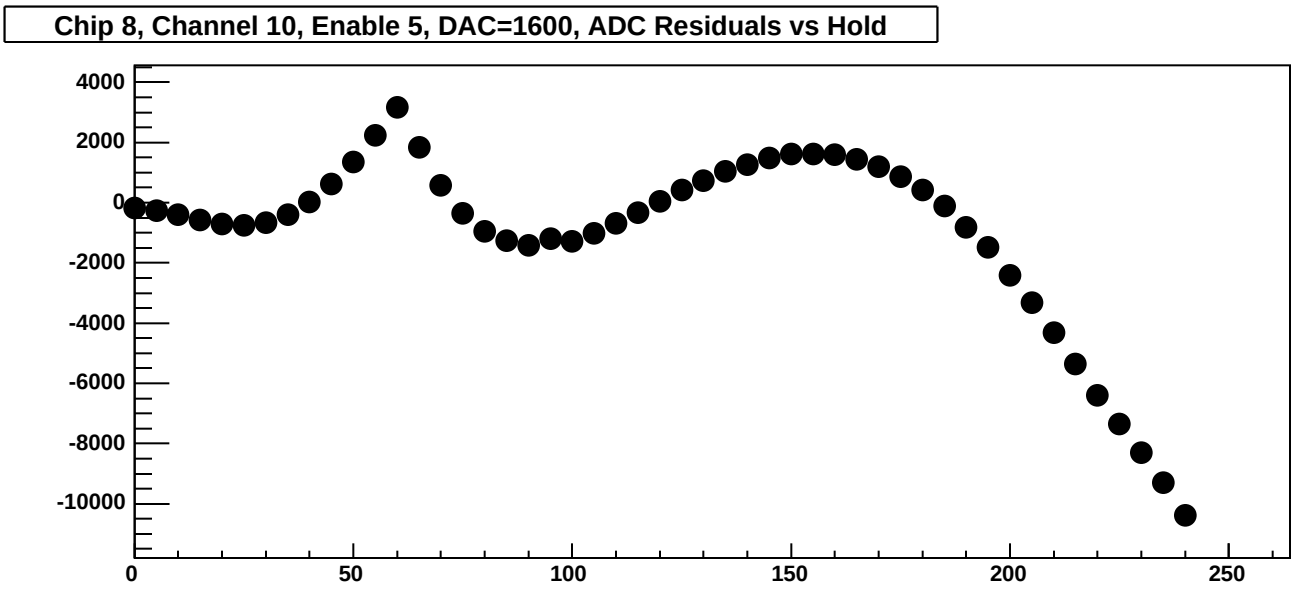


Chip 8, Channel 10, Enable 4, DAC=1600, ADC Residuals vs Hold

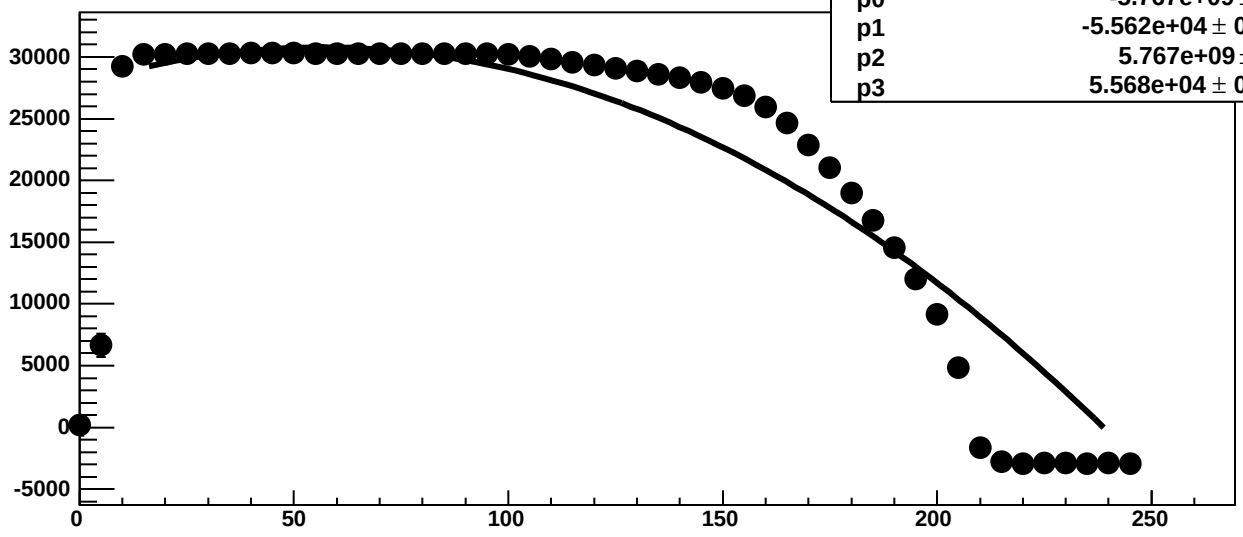



Chip 8, Channel 10, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.095e+05 / 41
p0 -300.5 ± 8.228
p1 60.54 ± 0.03503
p2 2.14e+04 ± 42.15
p3 36.97 ± 0.09189
p4 6.251 ± 0.2524
20000
15000
10000
5000
0
0
50
100
150
200
250

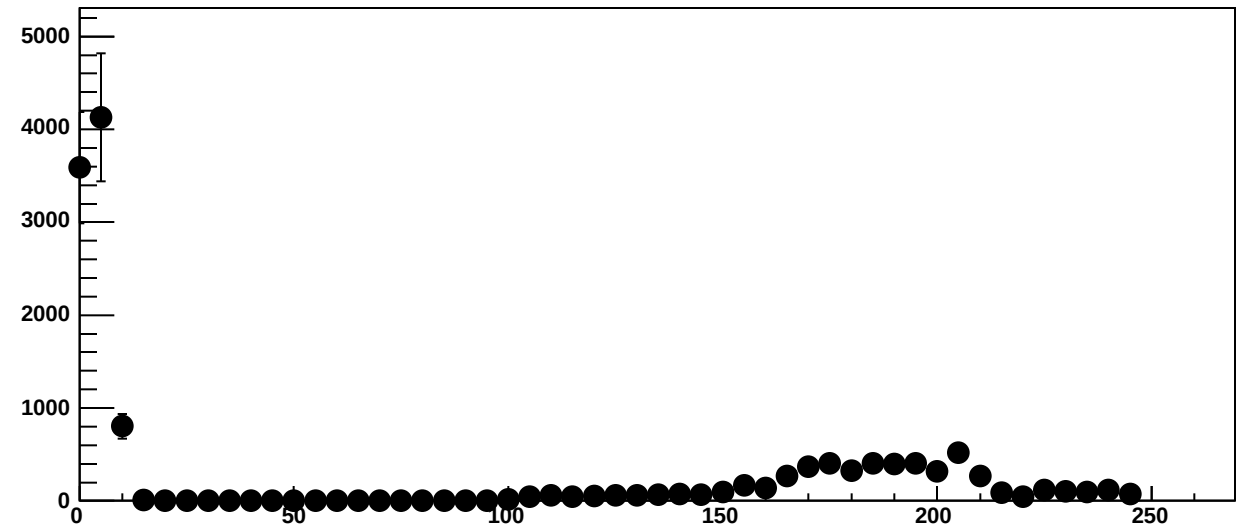

Chip 8, Channel 10, Enable 5, DAC=1600, ADC Noise vs Hold
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 8, Channel 10, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

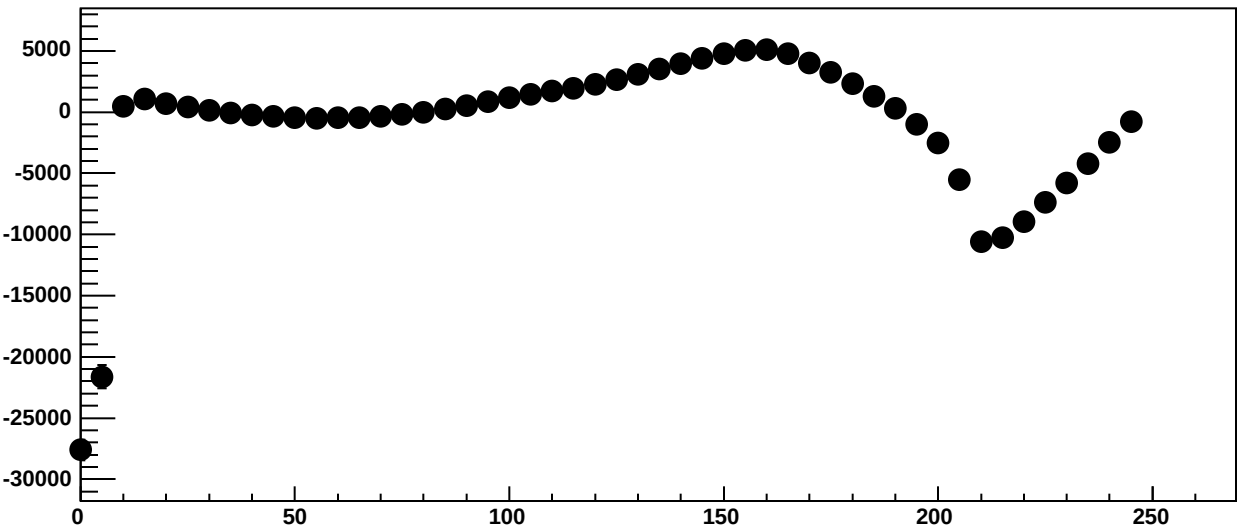
Chip 8, Channel 11, Enable 0!, DAC=1600, ADC Mean vs Hold



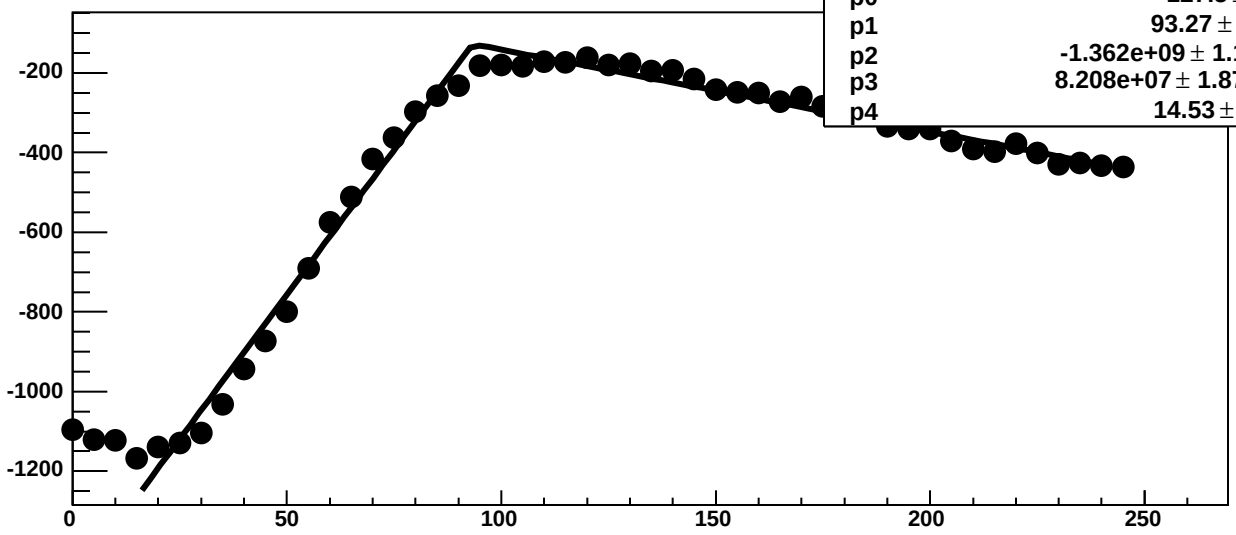
Chip 8, Channel 11, Enable 0!, DAC=1600, ADC Noise vs Hold



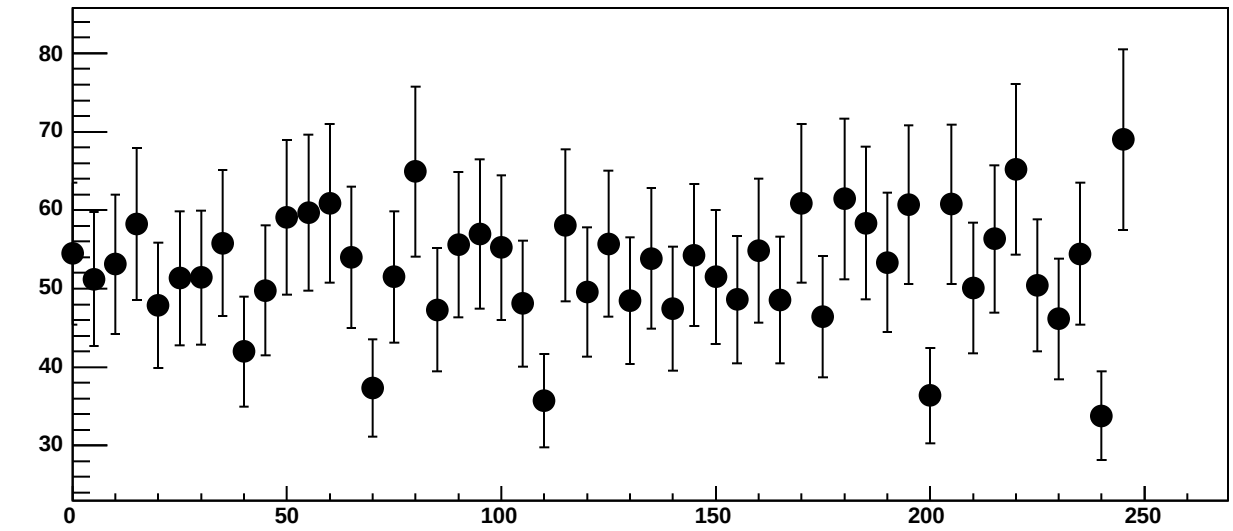
Chip 8, Channel 11, Enable 0!, DAC=1600, ADC Residuals vs Hold



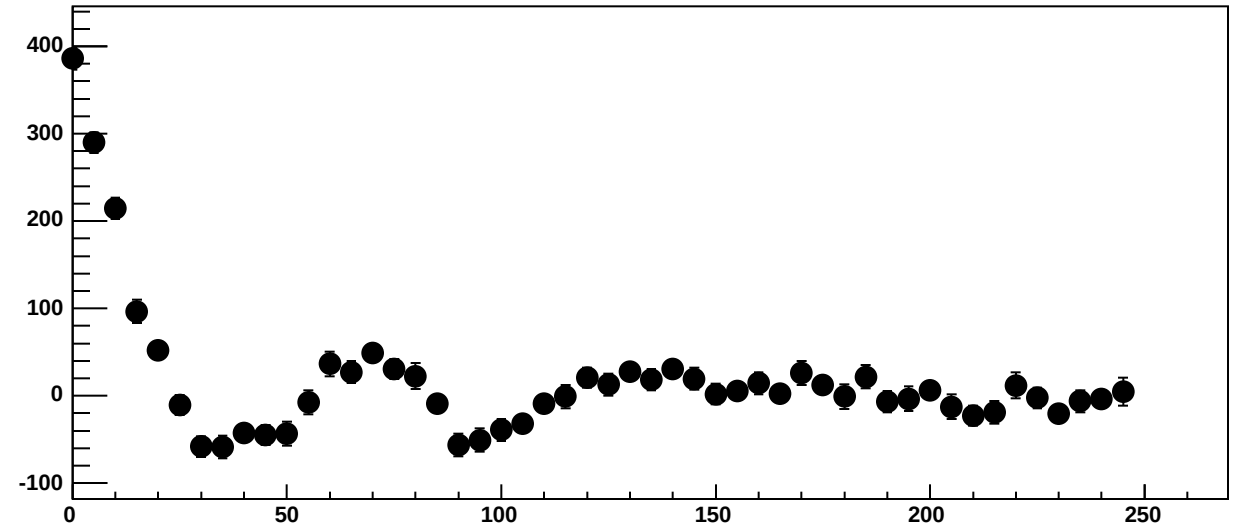
Chip 8, Channel 11, Enable 1, DAC=1600, ADC Mean vs Hold



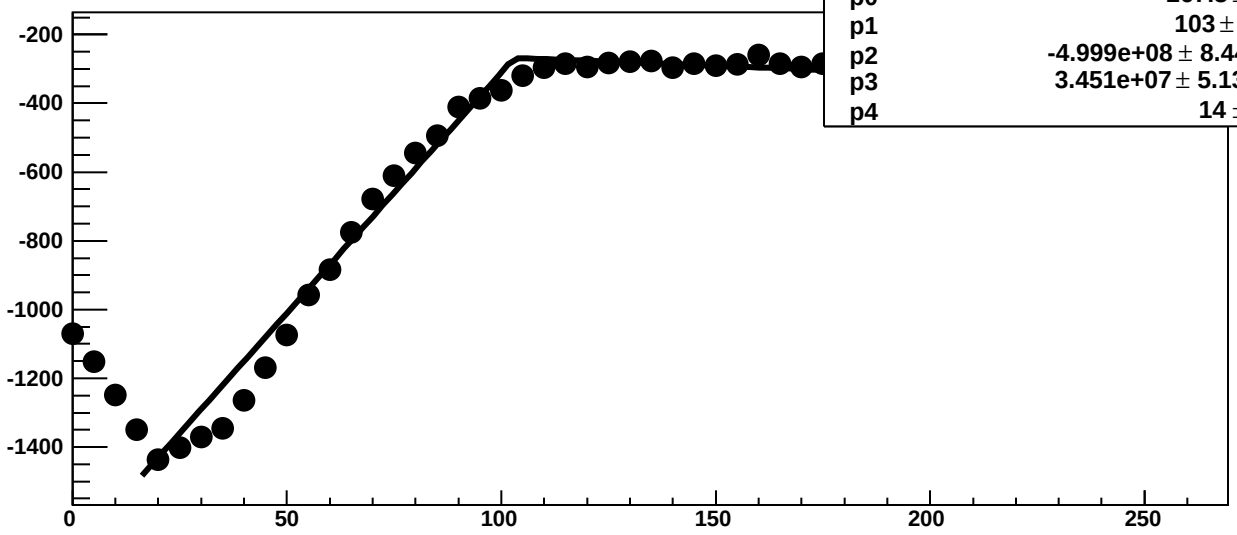
Chip 8, Channel 11, Enable 1, DAC=1600, ADC Noise vs Hold



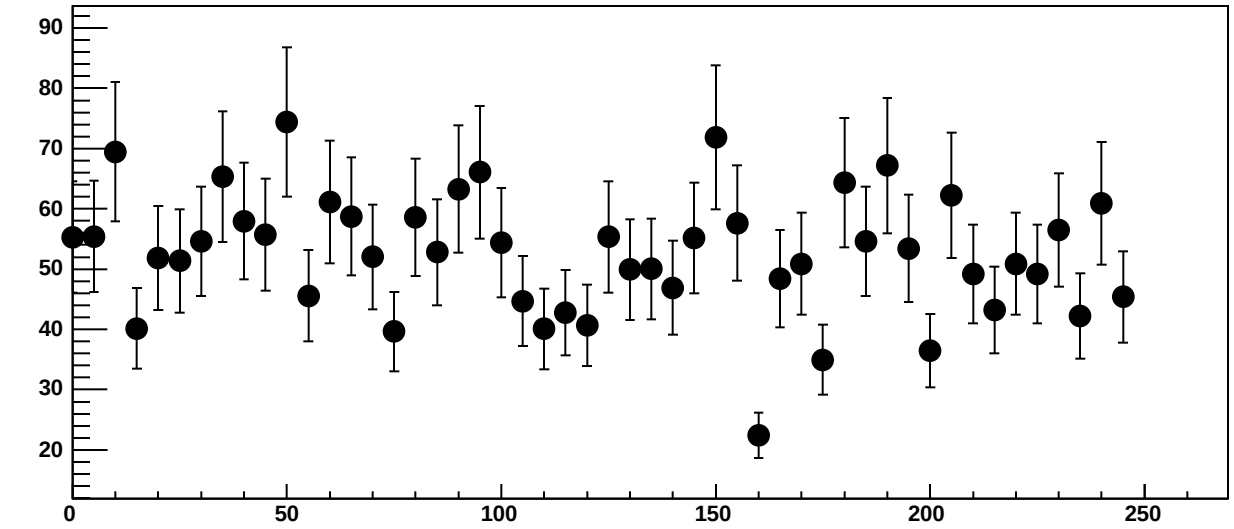
Chip 8, Channel 11, Enable 1, DAC=1600, ADC Residuals vs Hold



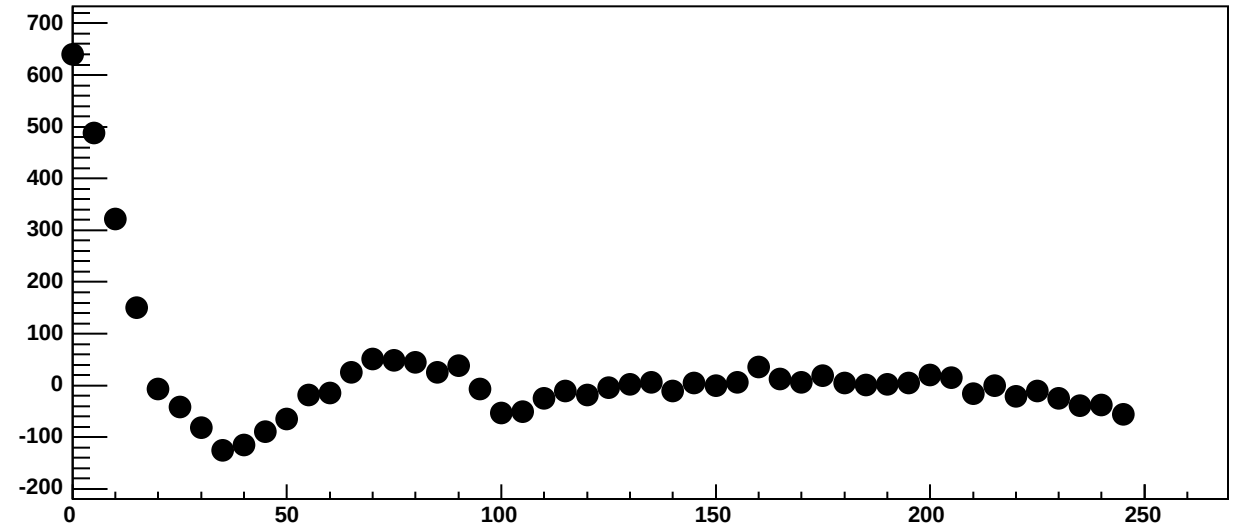
Chip 8, Channel 11, Enable 2, DAC=1600, ADC Mean vs Hold



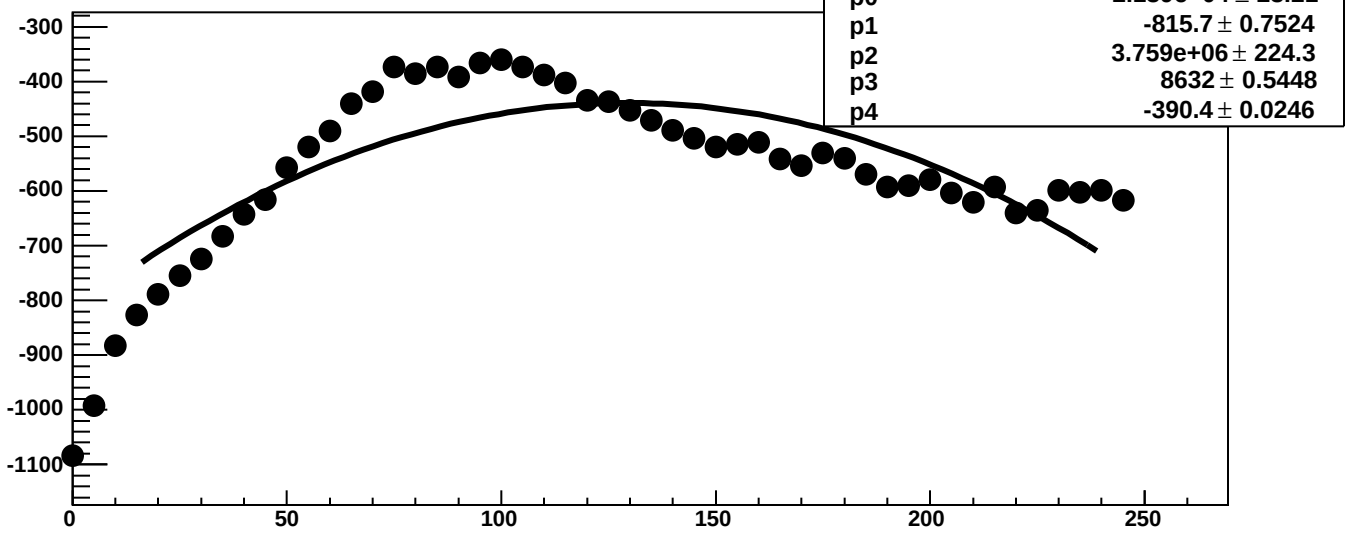
Chip 8, Channel 11, Enable 2, DAC=1600, ADC Noise vs Hold



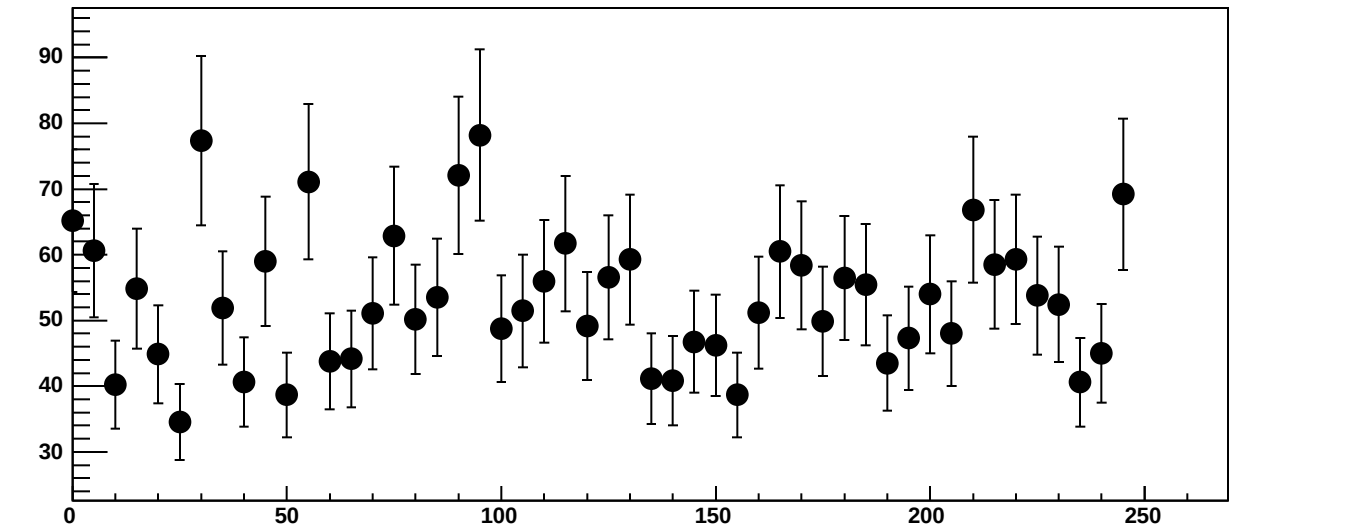
Chip 8, Channel 11, Enable 2, DAC=1600, ADC Residuals vs Hold



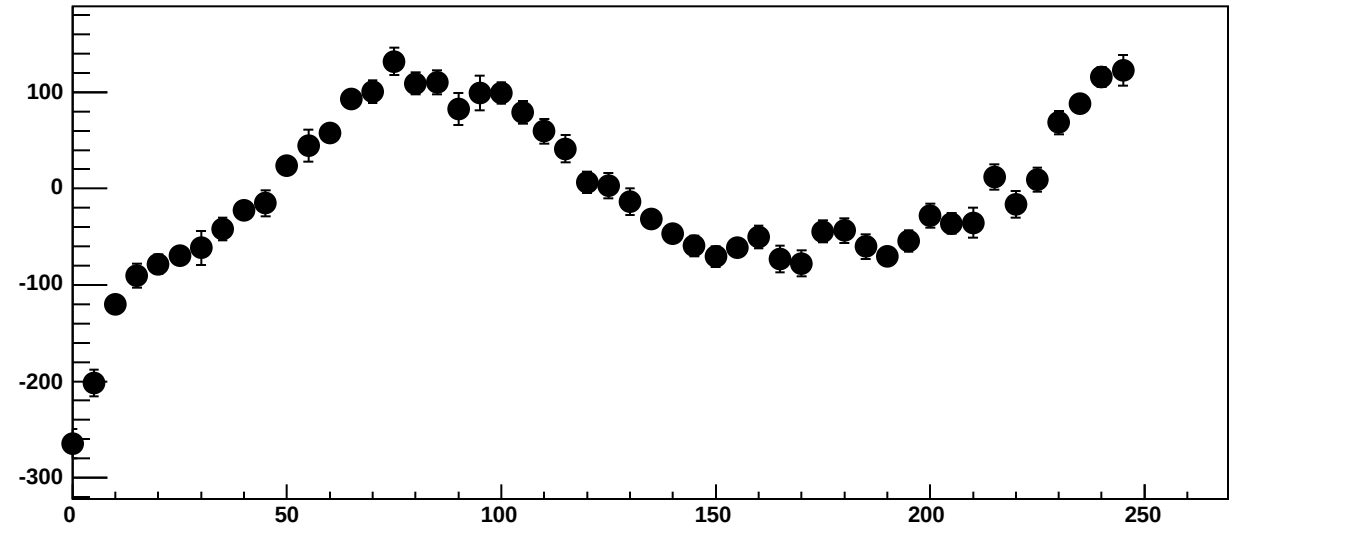
Chip 8, Channel 11, Enable 3, DAC=1600, ADC Mean vs Hold

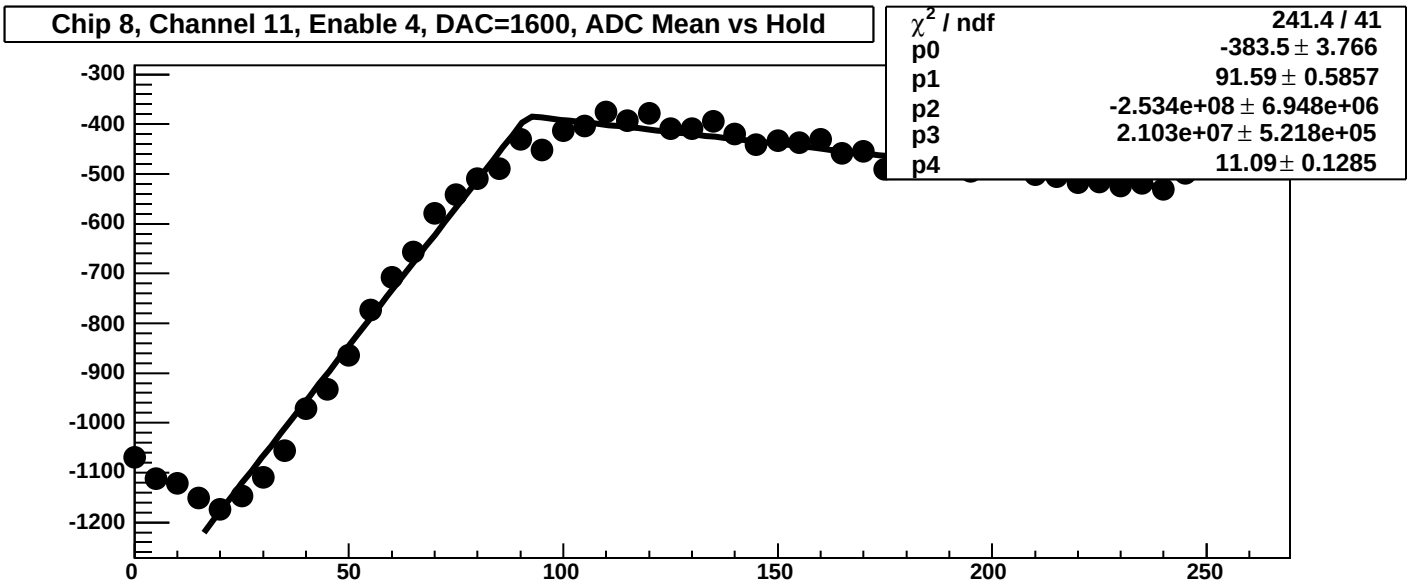


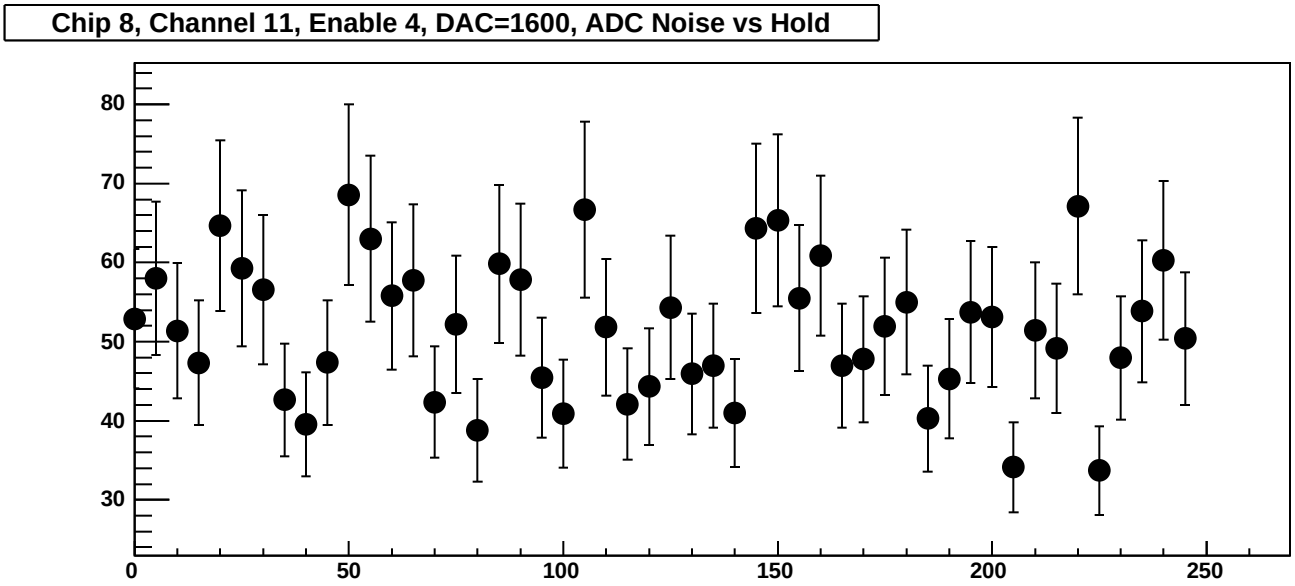
Chip 8, Channel 11, Enable 3, DAC=1600, ADC Noise vs Hold

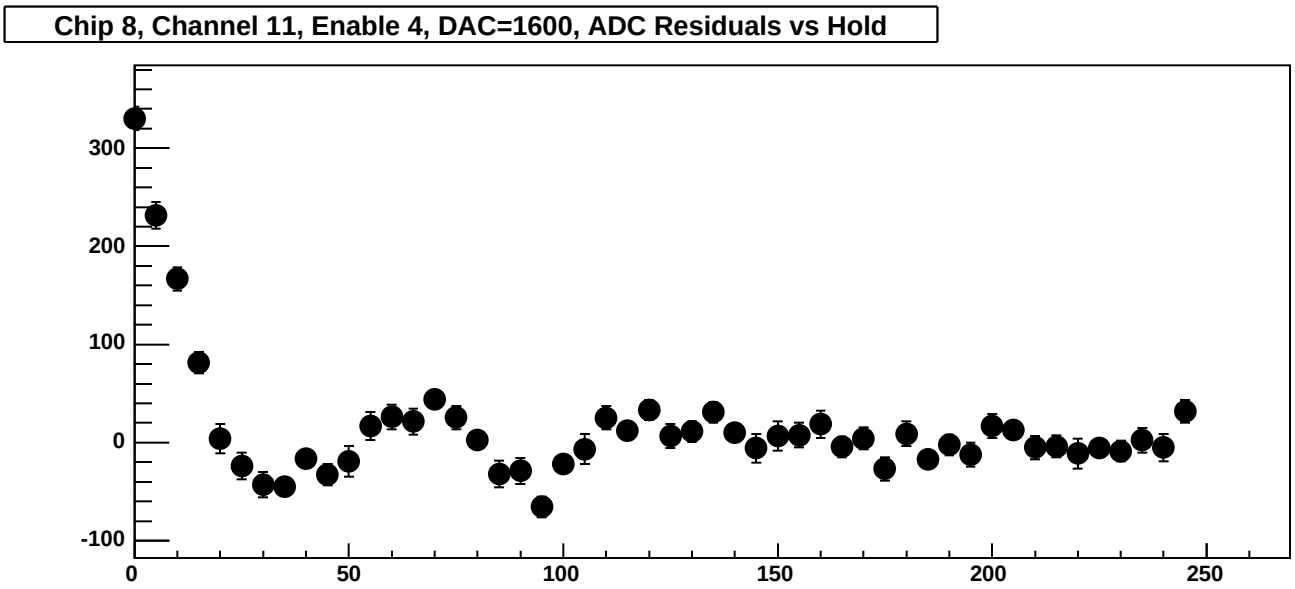


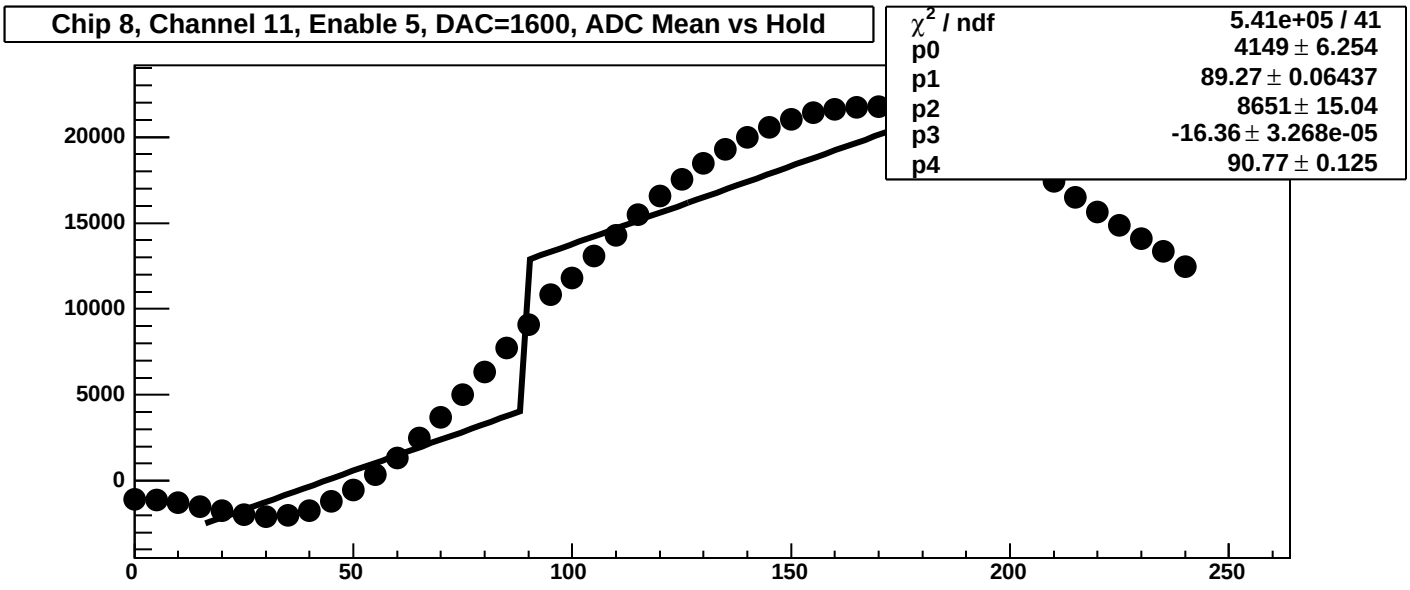
Chip 8, Channel 11, Enable 3, DAC=1600, ADC Residuals vs Hold

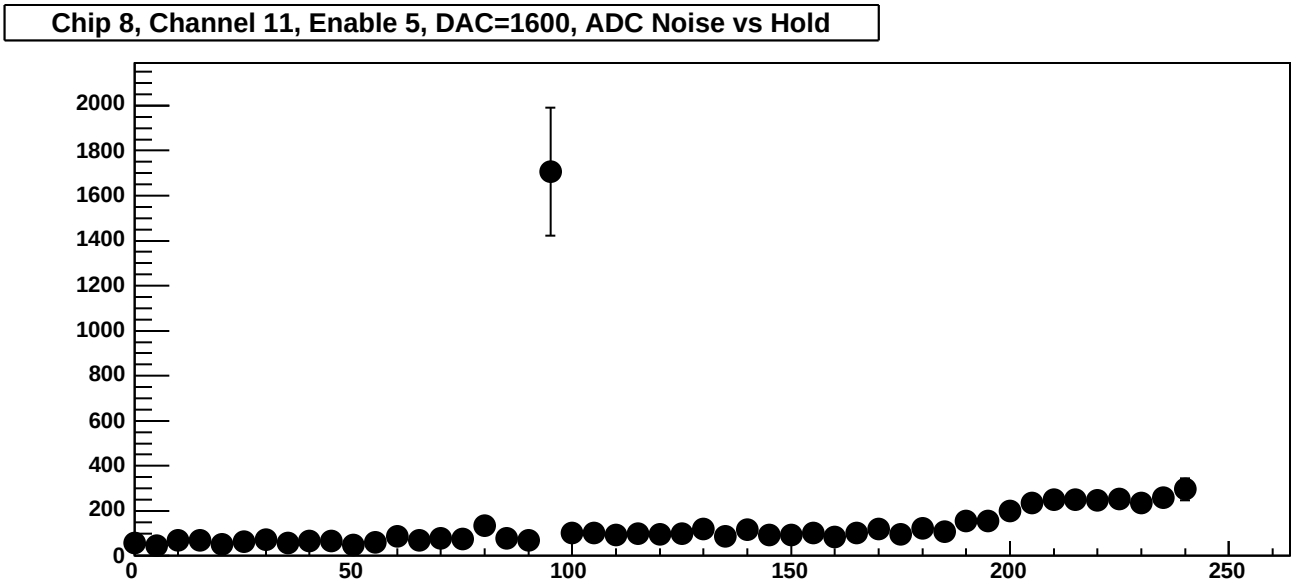


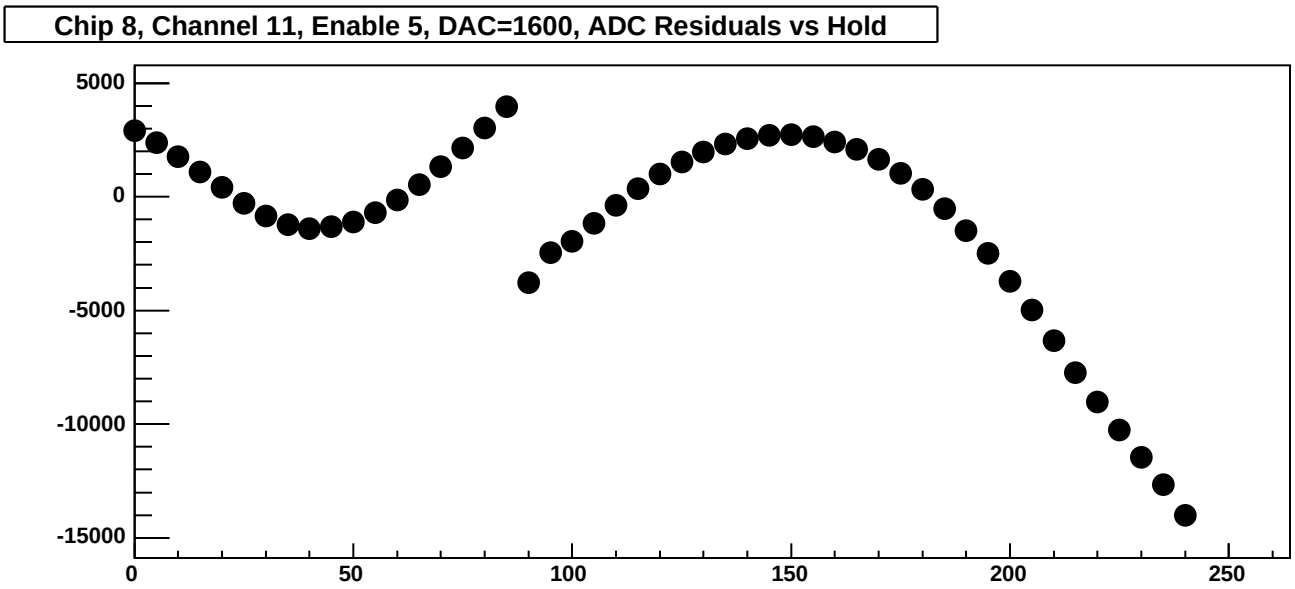

Chip 8, Channel 11, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 241.4 / 41
p0 -383.5 ± 3.766
p1 91.59 ± 0.5857
p2 -2.534e+08 ± 6.948e+06
p3 2.103e+07 ± 5.218e+05
p4 11.09 ± 0.1285
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
50
100
150
200
250

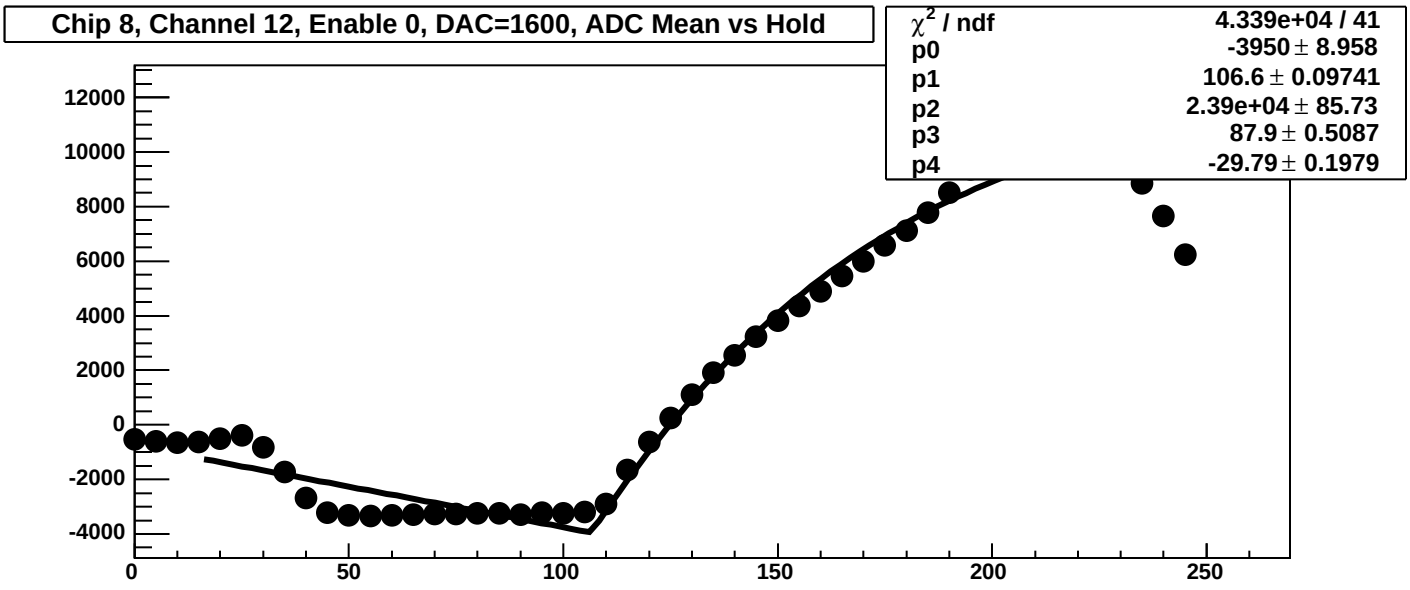

Chip 8, Channel 11, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

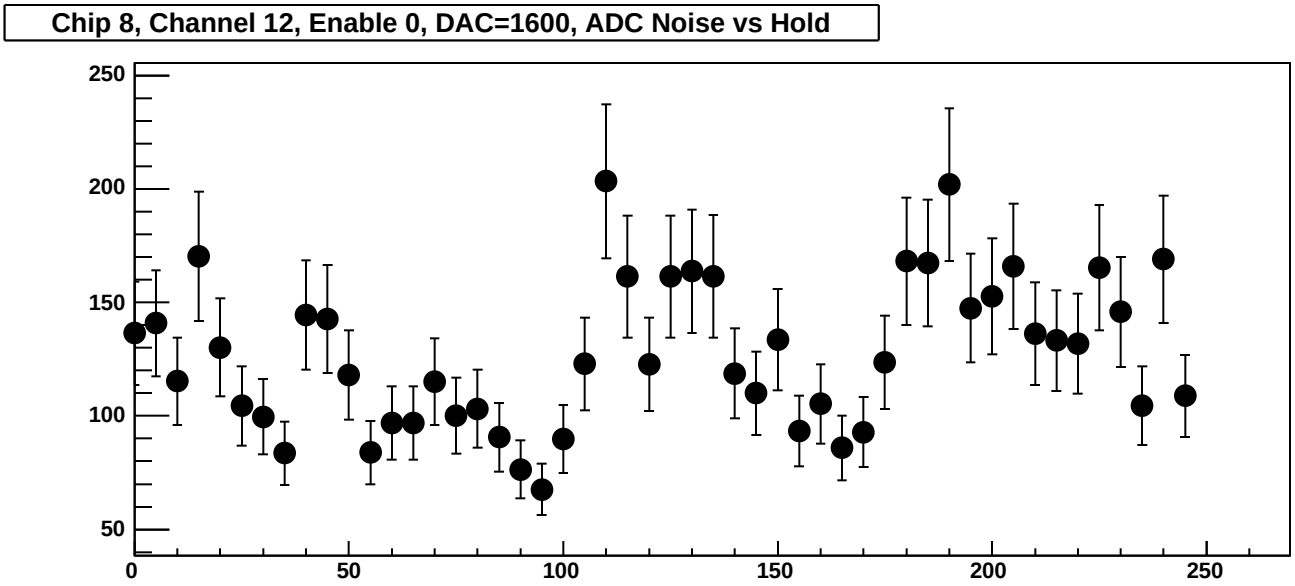

Chip 8, Channel 11, Enable 4, DAC=1600, ADC Residuals vs Hold
300
200
100
0
-100
0
50
100
150
200
250

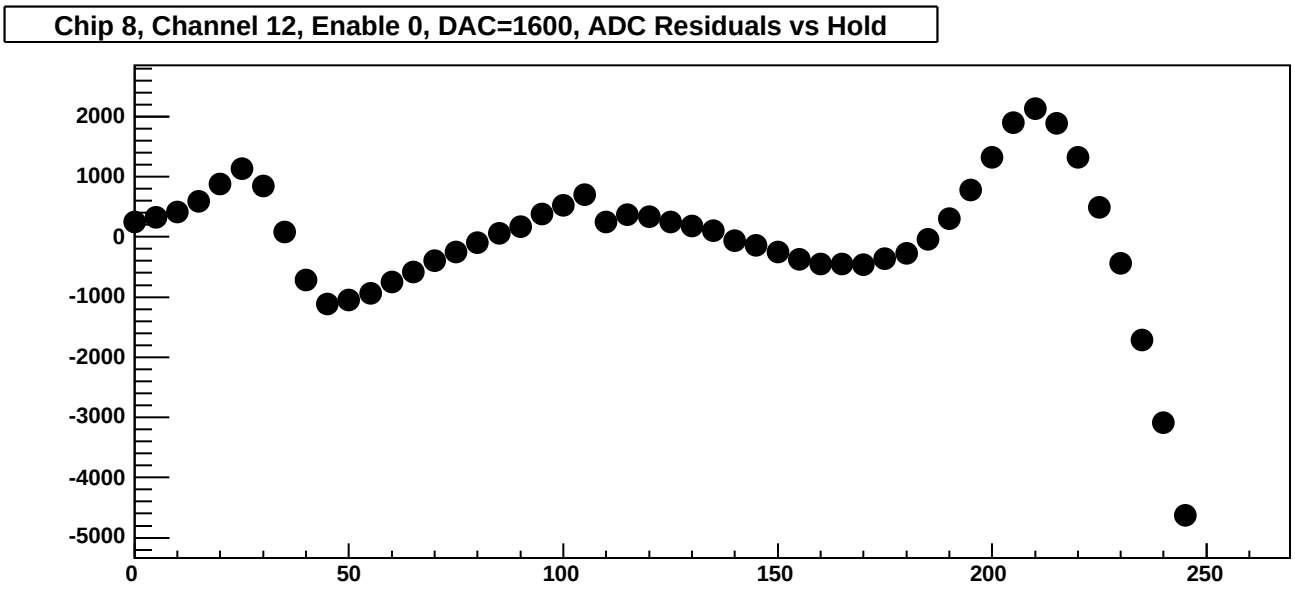

Chip 8, Channel 11, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.41e+05 / 41
p0 4149 ± 6.254
p1 89.27 ± 0.06437
p2 8651 ± 15.04
p3 -16.36 ± 3.268e-05
p4 90.77 ± 0.125
20000
15000
10000
5000
0
0
50
100
150
200
250

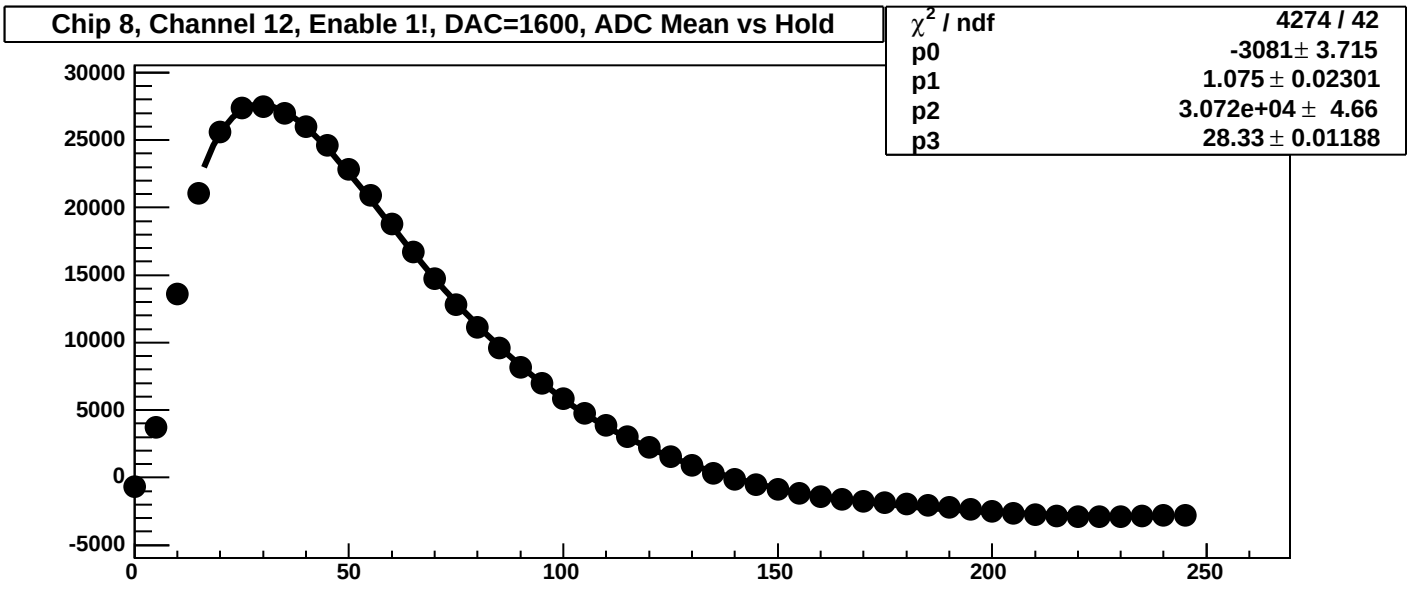

Chip 8, Channel 11, Enable 5, DAC=1600, ADC Noise vs Hold
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

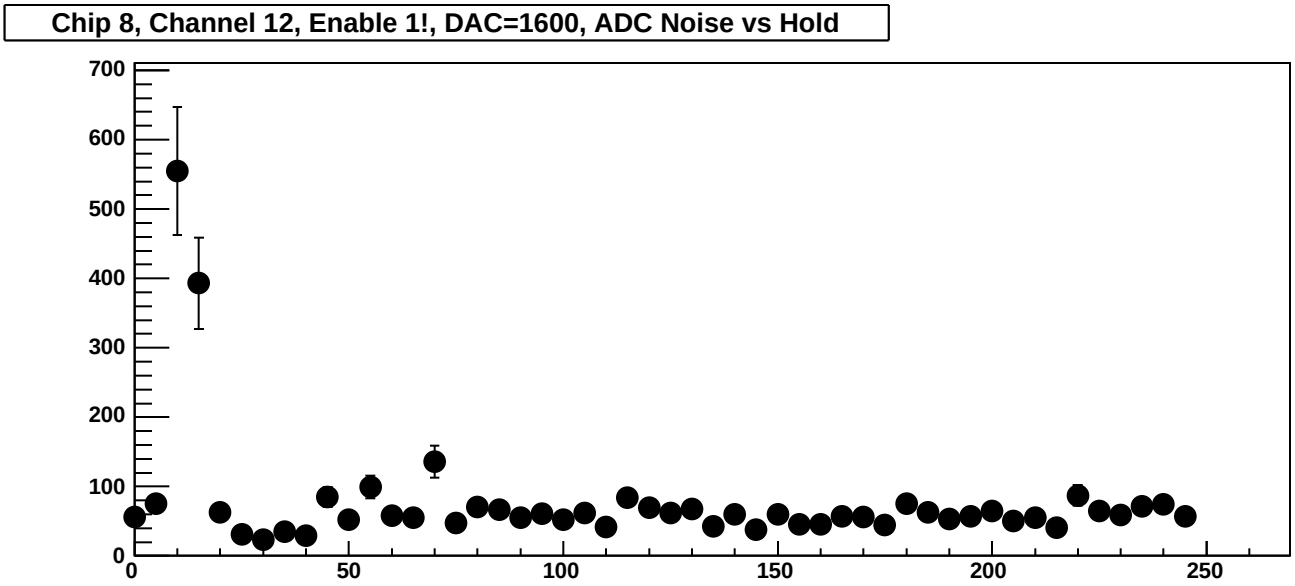

Chip 8, Channel 11, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
0
50
100
150
200
250

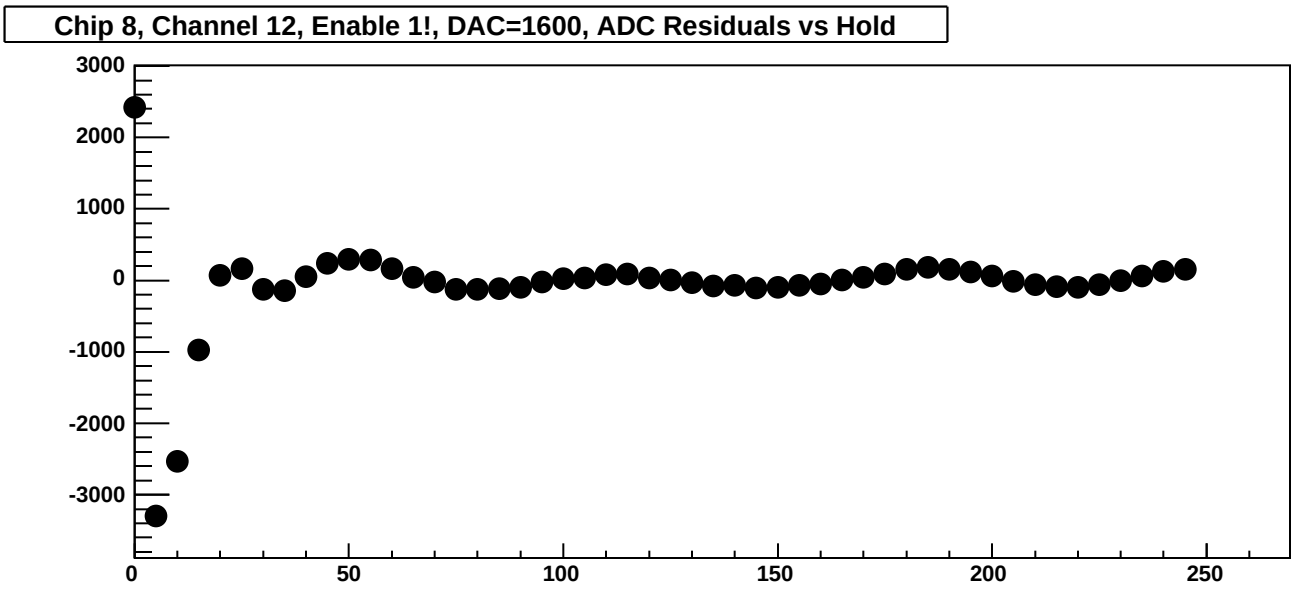

Chip 8, Channel 12, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.339e+04 / 41
p0 -3950 ± 8.958
p1 106.6 ± 0.09741
p2 2.39e+04 ± 85.73
p3 87.9 ± 0.5087
p4 -29.79 ± 0.1979
12000
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250


Chip 8, Channel 12, Enable 0, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
50
100
150
200
250

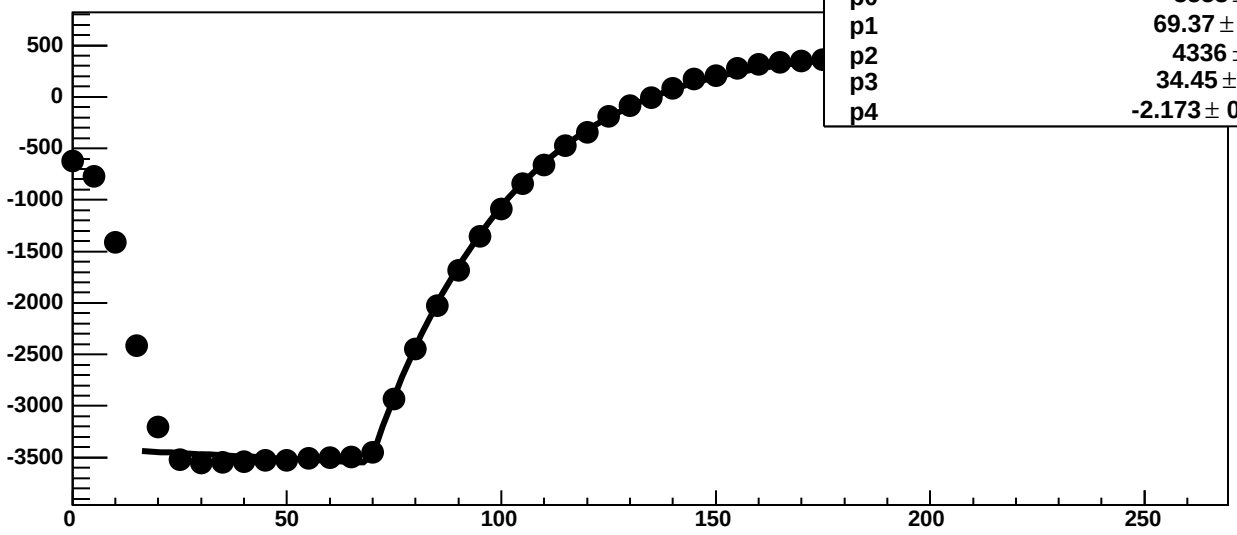

Chip 8, Channel 12, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250


Chip 8, Channel 12, Enable 1!, DAC=1600, ADC Mean vs Hold
χ² / ndf 4274 / 42
p0 -3081 ± 3.715
p1 1.075 ± 0.02301
p2 3.072e+04 ± 4.66
p3 28.33 ± 0.01188
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

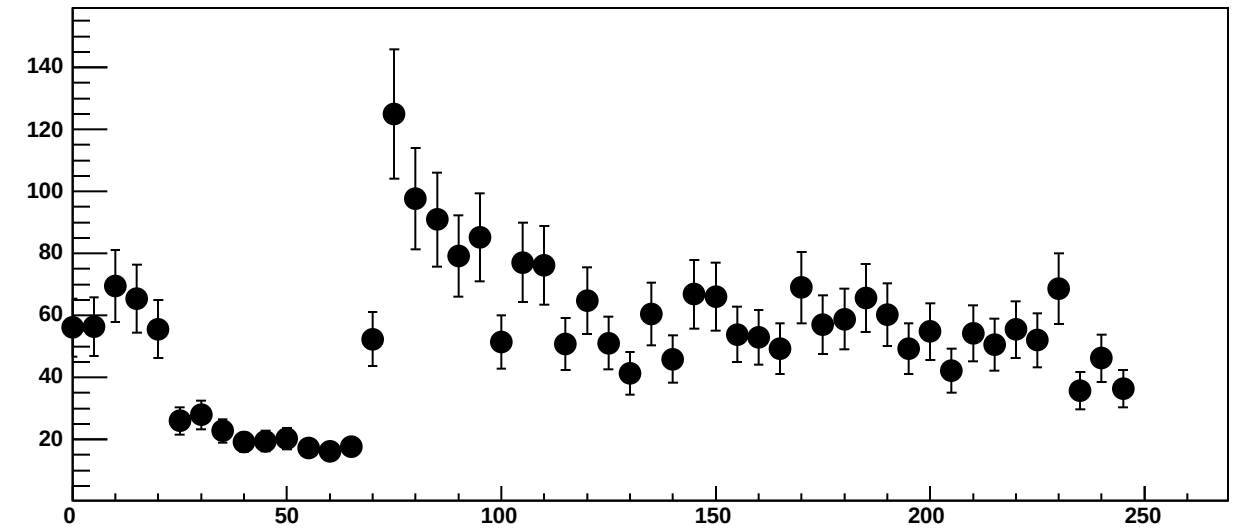

Chip 8, Channel 12, Enable 1!, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 8, Channel 12, Enable 1!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

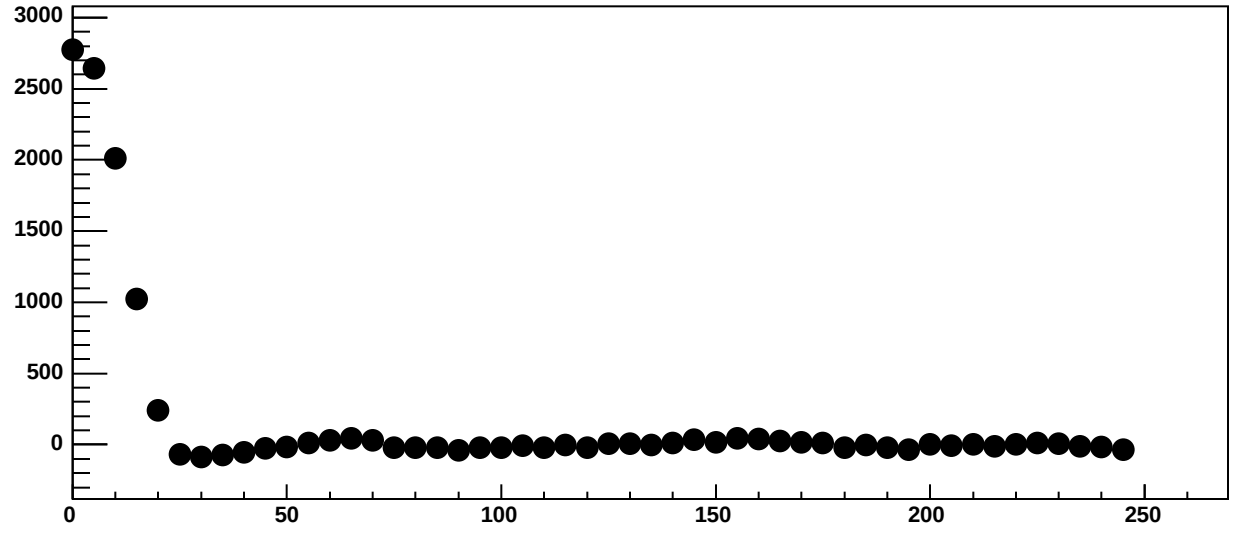
Chip 8, Channel 12, Enable 2, DAC=1600, ADC Mean vs Hold

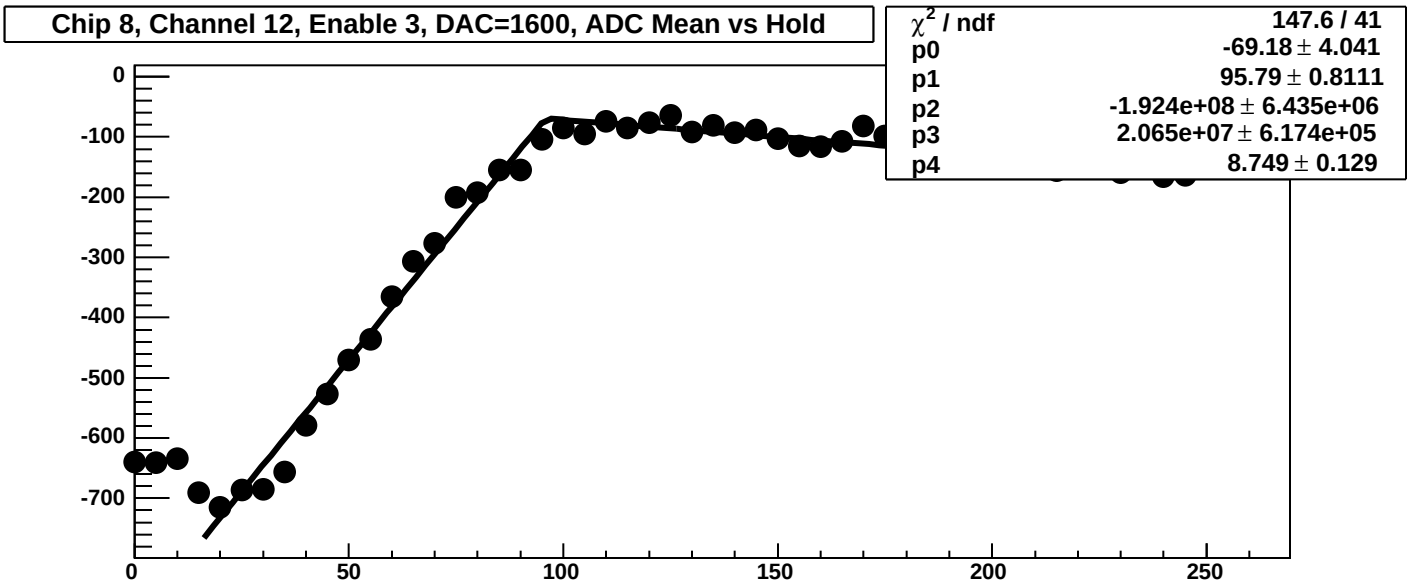


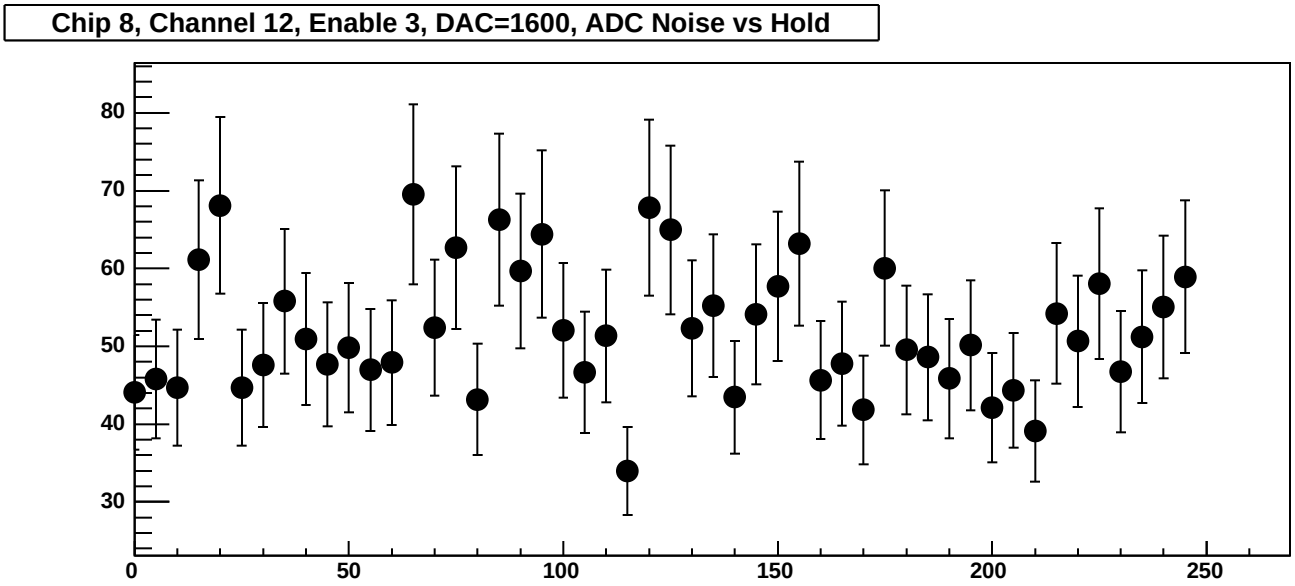
Chip 8, Channel 12, Enable 2, DAC=1600, ADC Noise vs Hold

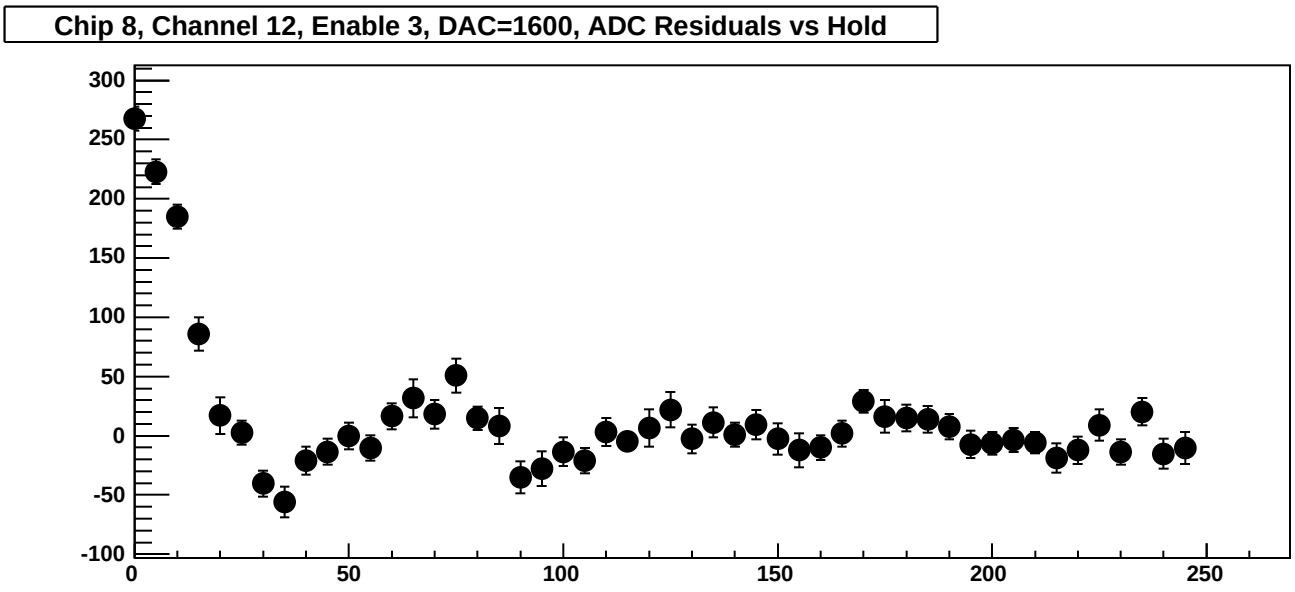


Chip 8, Channel 12, Enable 2, DAC=1600, ADC Residuals vs Hold

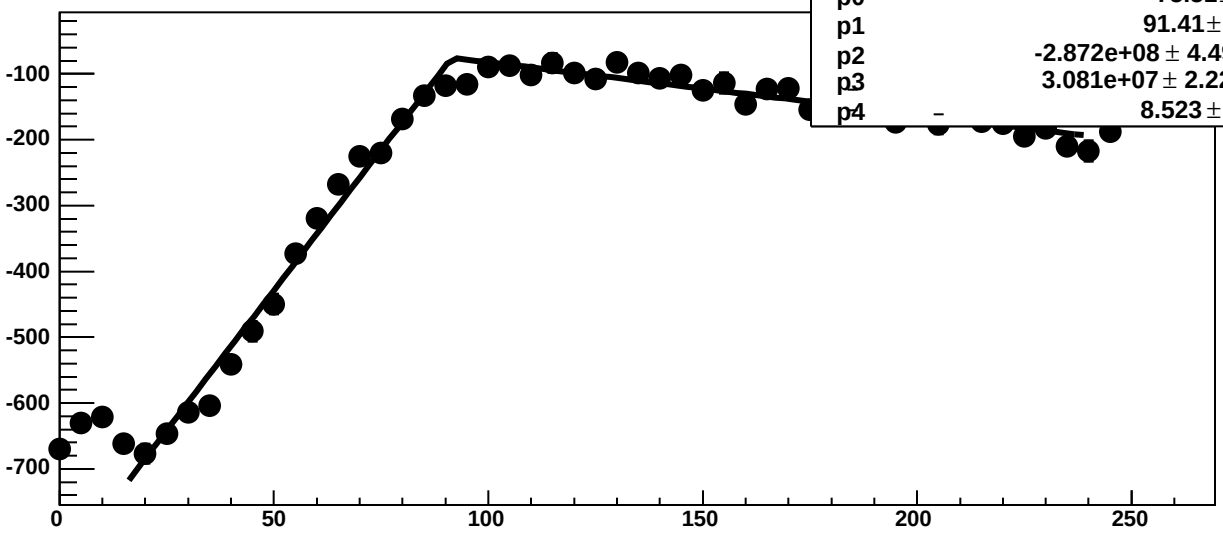



Chip 8, Channel 12, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 147.6 / 41
p0 -69.18 ± 4.041
p1 95.79 ± 0.8111
p2 -1.924e+08 ± 6.435e+06
p3 2.065e+07 ± 6.174e+05
p4 8.749 ± 0.129
0
-100
-200
-300
-400
-500
-600
-700
0
50
100
150
200
250

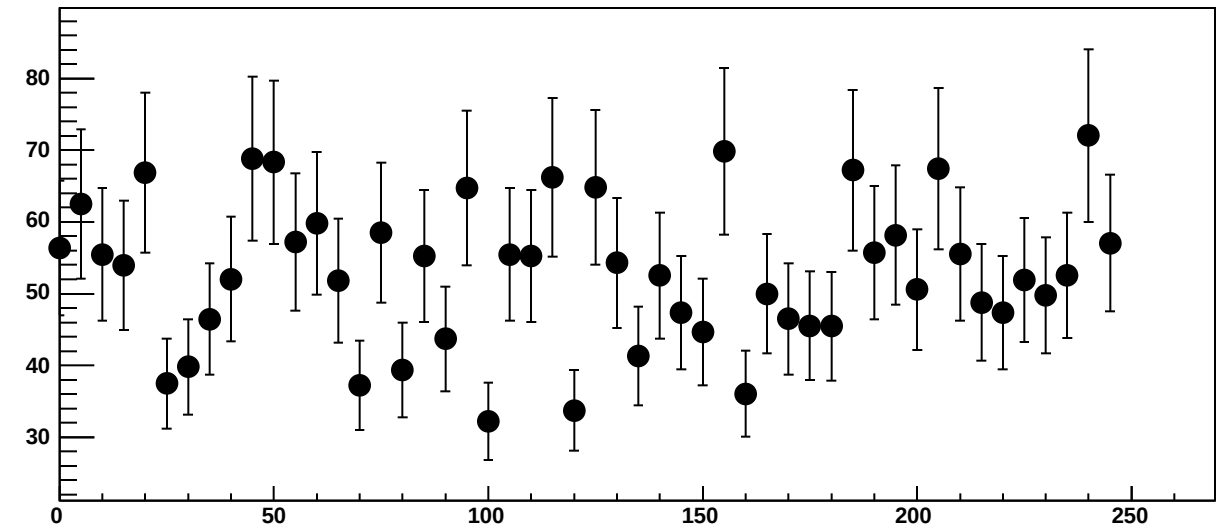

Chip 8, Channel 12, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 8, Channel 12, Enable 3, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

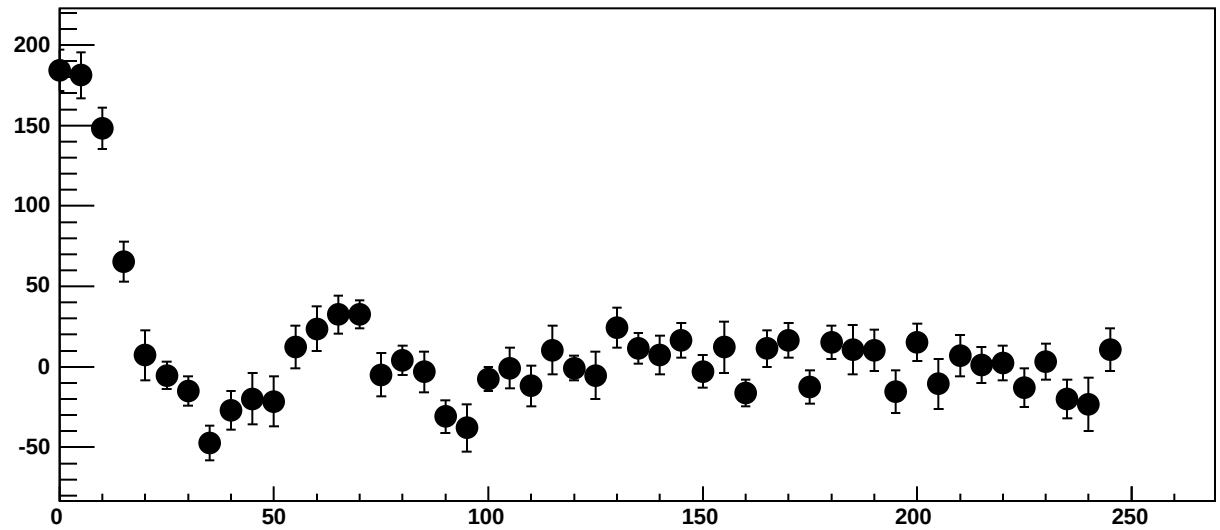
Chip 8, Channel 12, Enable 4, DAC=1600, ADC Mean vs Hold

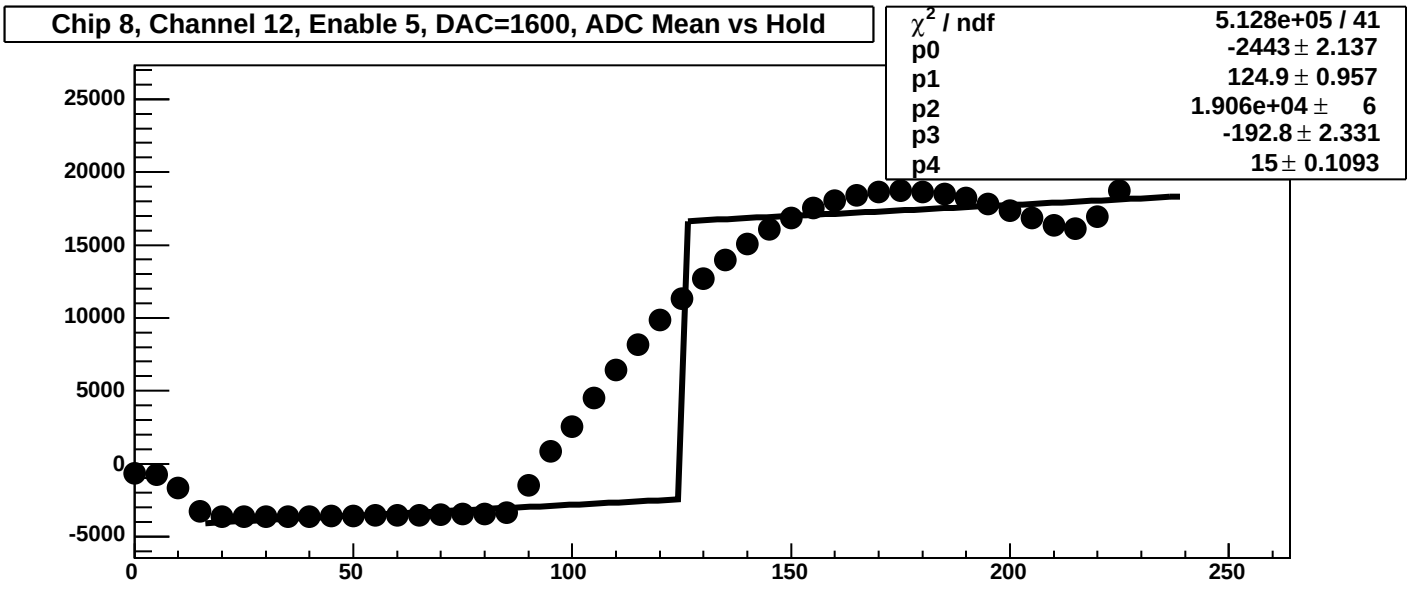


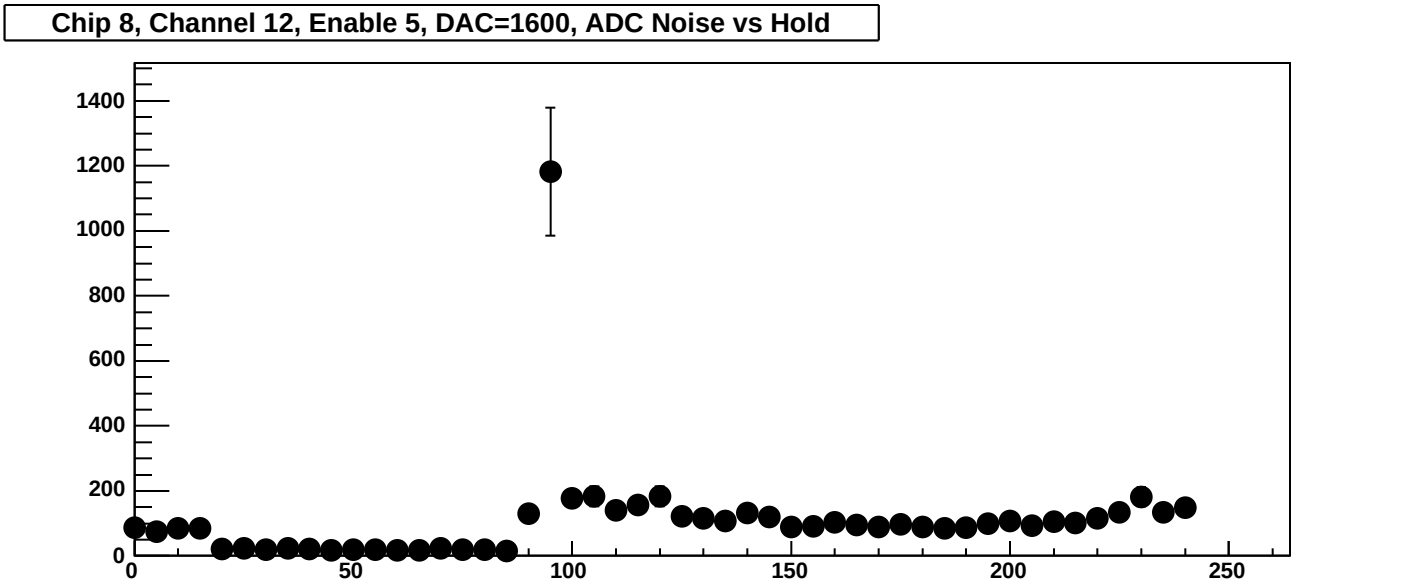
Chip 8, Channel 12, Enable 4, DAC=1600, ADC Noise vs Hold

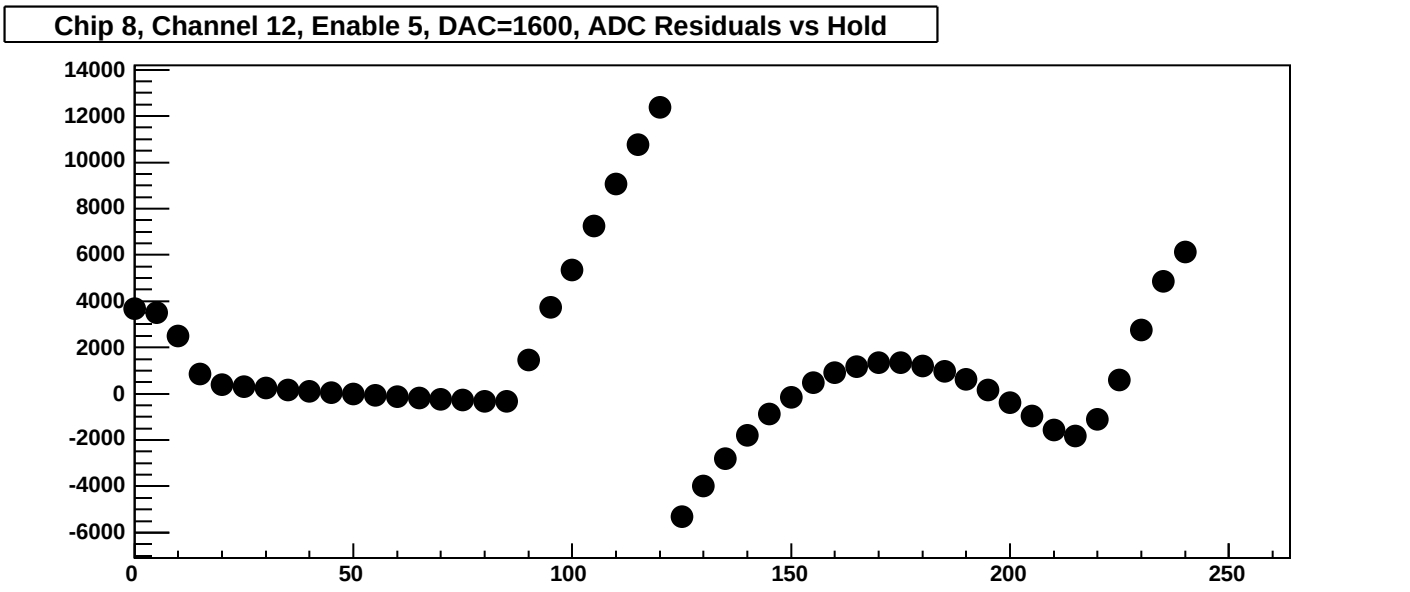


Chip 8, Channel 12, Enable 4, DAC=1600, ADC Residuals vs Hold

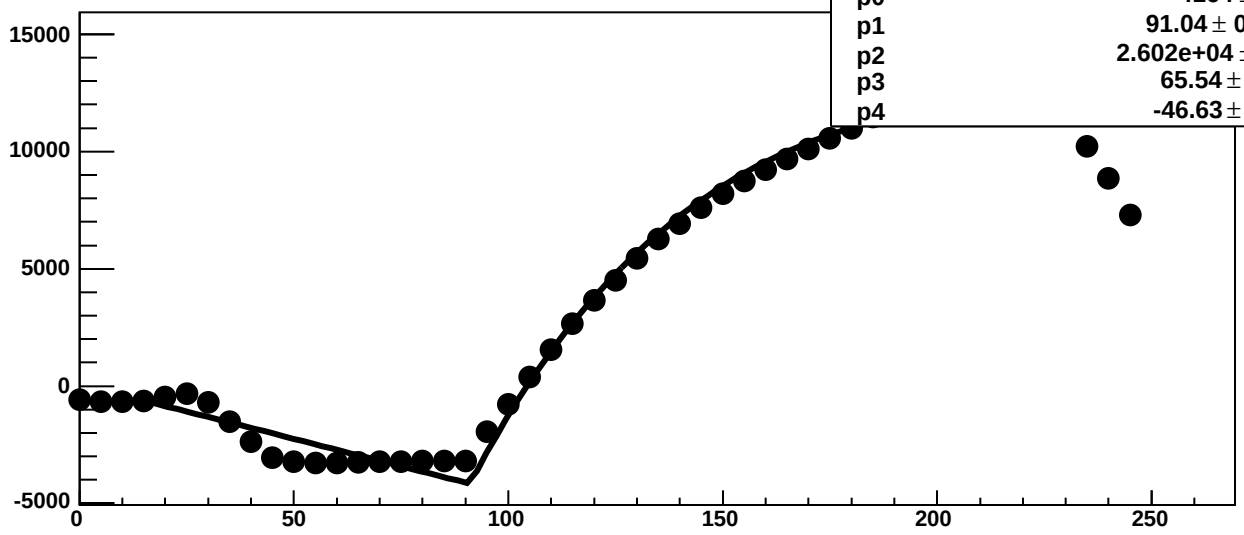



Chip 8, Channel 12, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.128e+05 / 41
p0 -2443 ± 2.137
p1 124.9 ± 0.957
p2 1.906e+04 ± 6
p3 -192.8 ± 2.331
p4 15 ± 0.1093
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

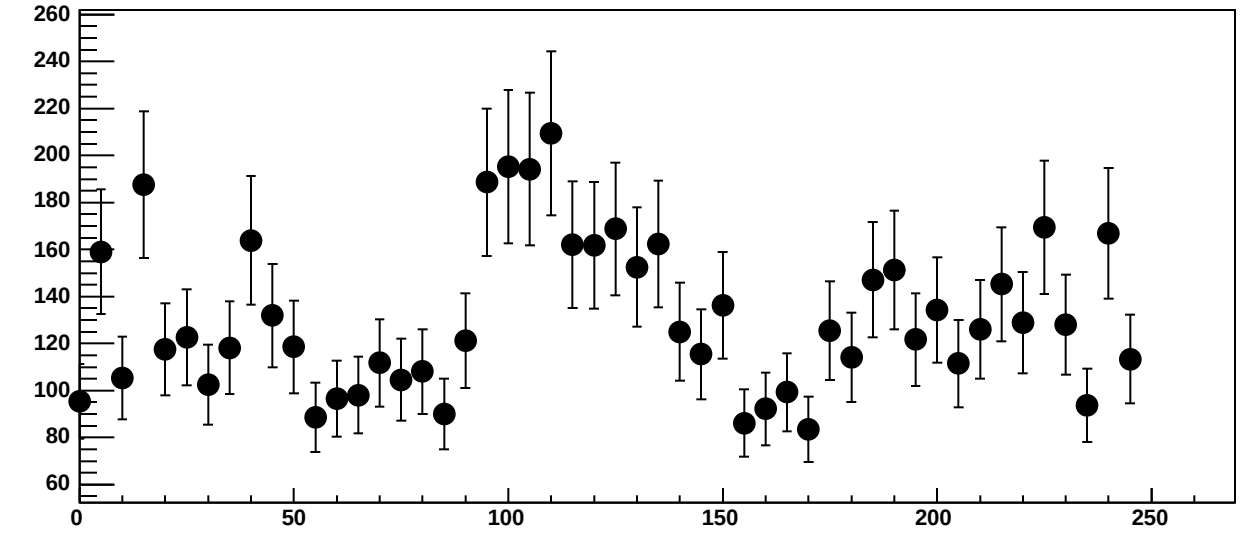

Chip 8, Channel 12, Enable 5, DAC=1600, ADC Noise vs Hold
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 8, Channel 12, Enable 5, DAC=1600, ADC Residuals vs Hold
14000
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

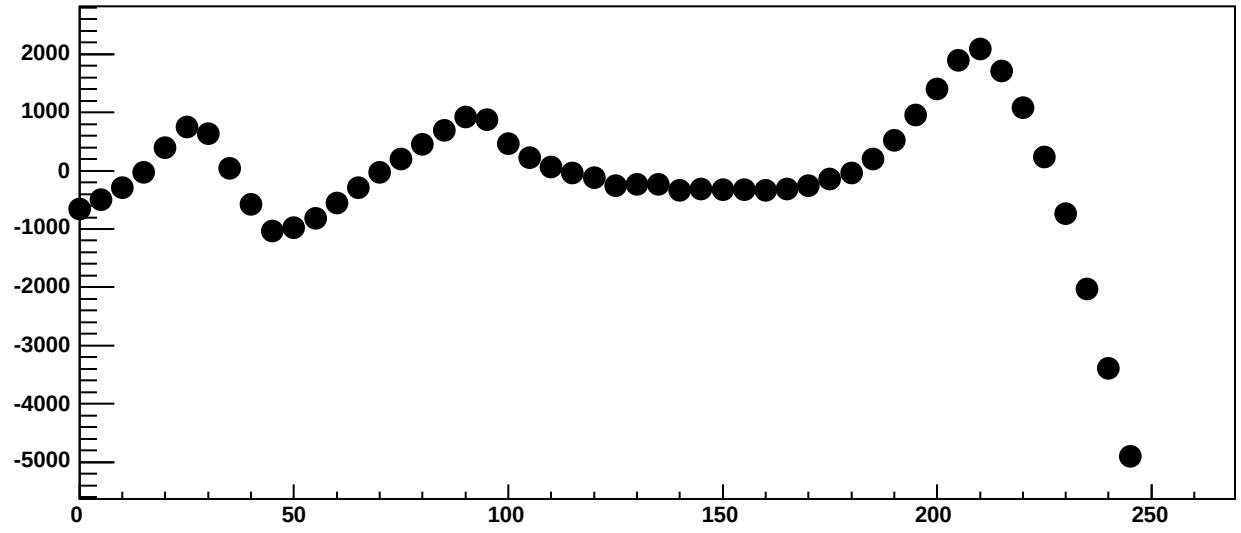
Chip 8, Channel 13, Enable 0, DAC=1600, ADC Mean vs Hold



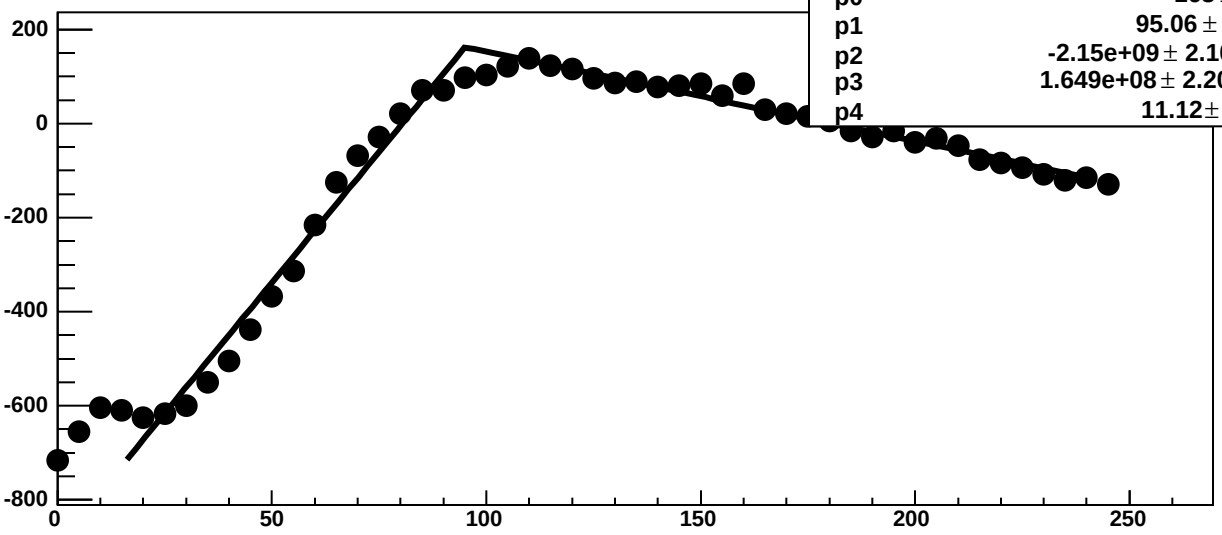
Chip 8, Channel 13, Enable 0, DAC=1600, ADC Noise vs Hold



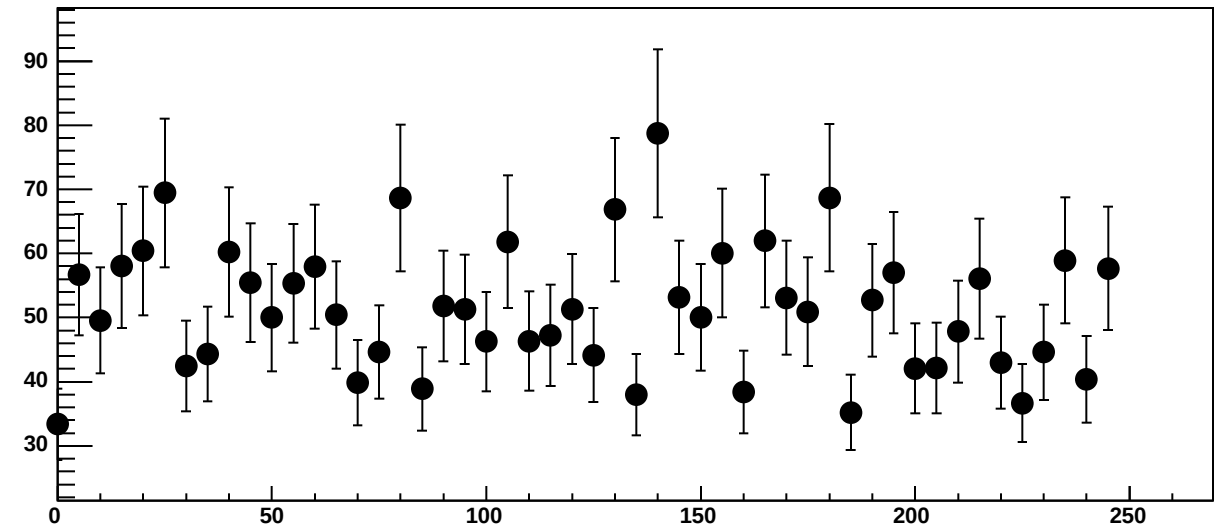
Chip 8, Channel 13, Enable 0, DAC=1600, ADC Residuals vs Hold



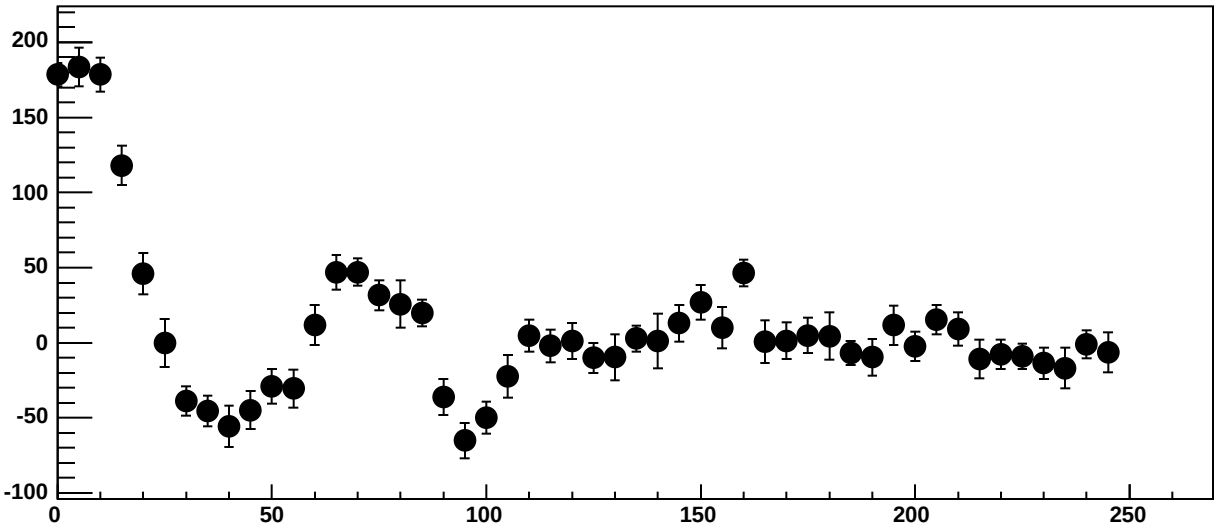
Chip 8, Channel 13, Enable 1, DAC=1600, ADC Mean vs Hold

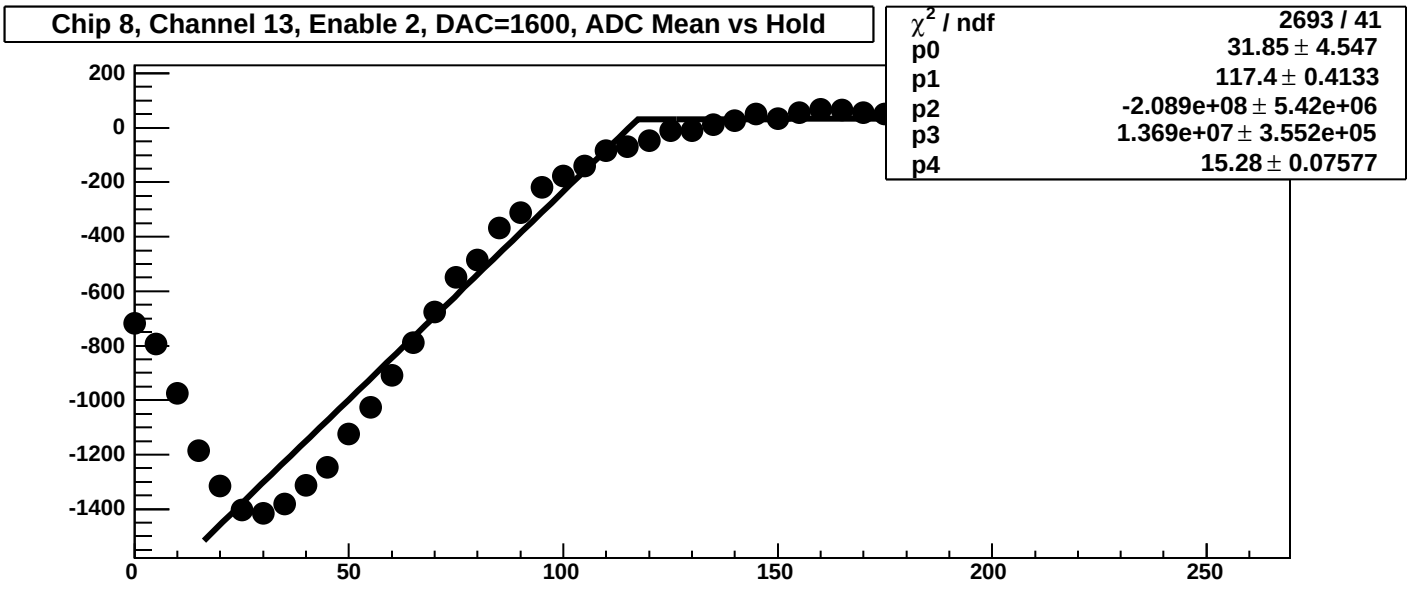


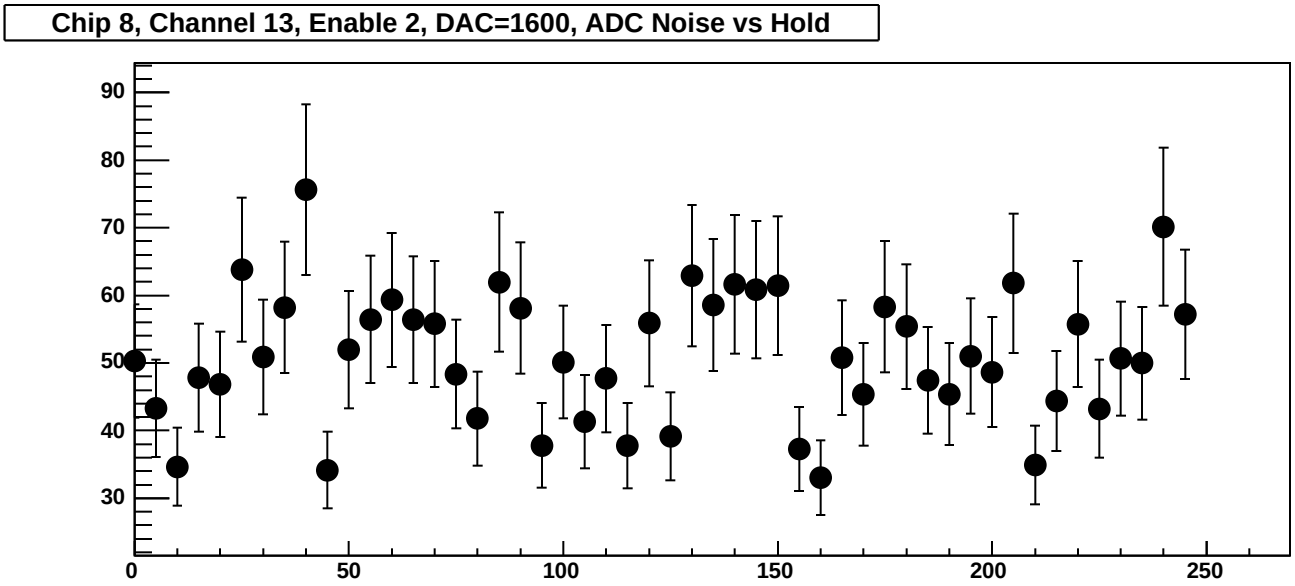
Chip 8, Channel 13, Enable 1, DAC=1600, ADC Noise vs Hold

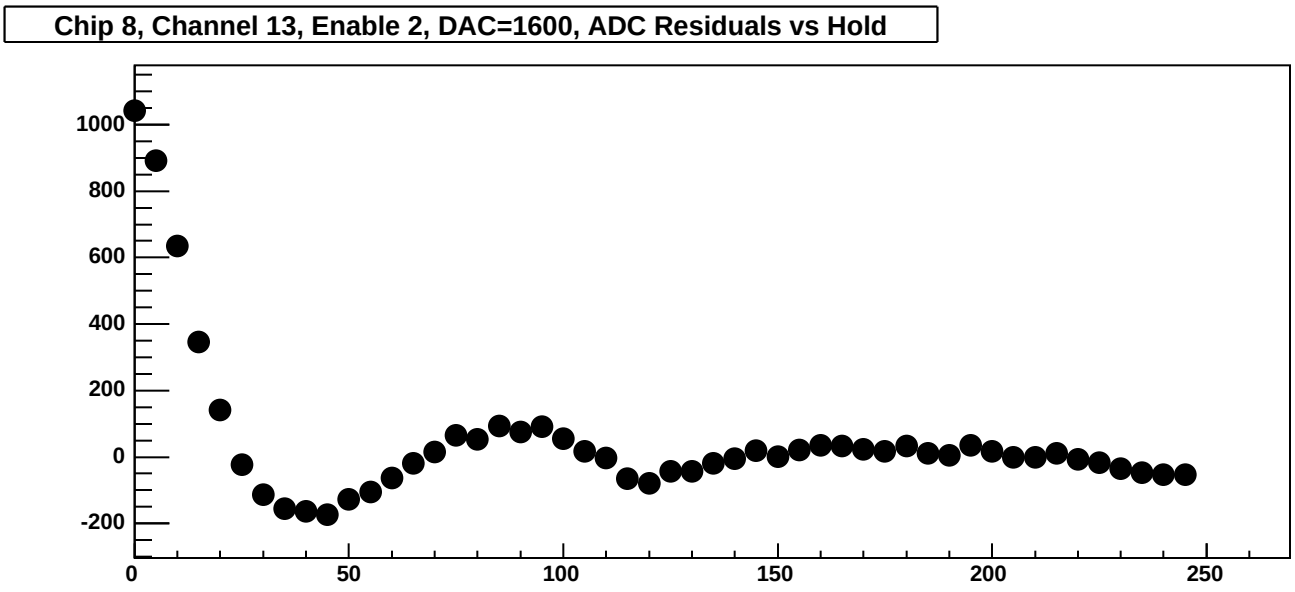


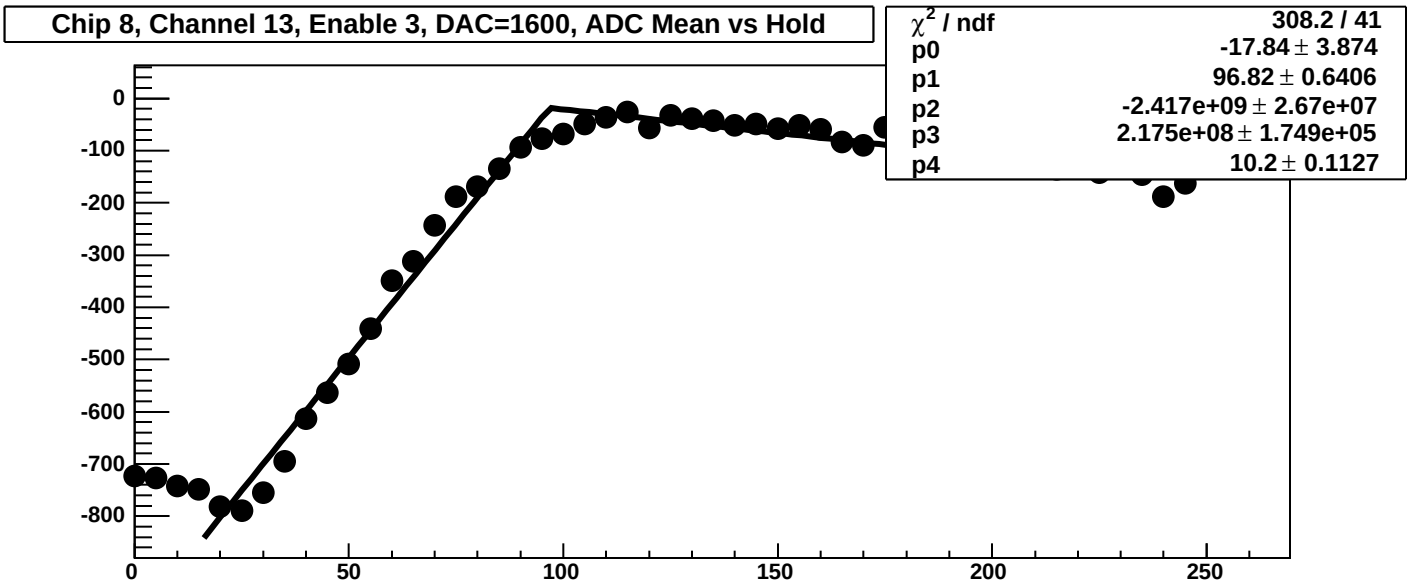
Chip 8, Channel 13, Enable 1, DAC=1600, ADC Residuals vs Hold

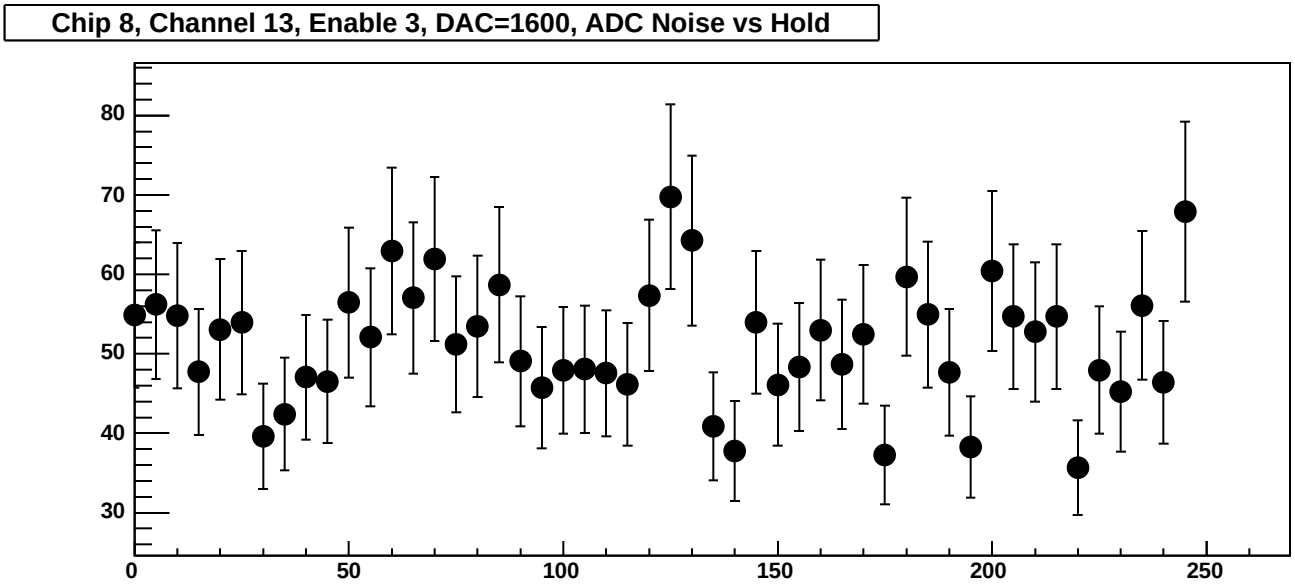


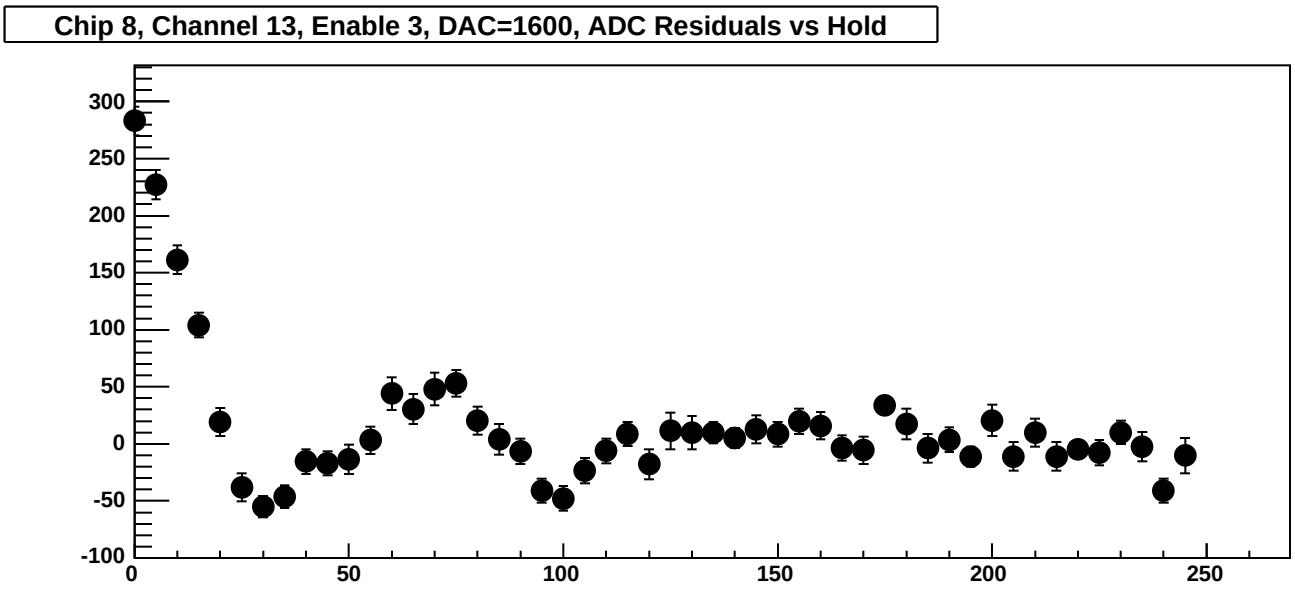

Chip 8, Channel 13, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 2693 / 41
p0 31.85 ± 4.547
p1 117.4 ± 0.4133
p2 -2.089e+08 ± 5.42e+06
p3 1.369e+07 ± 3.552e+05
p4 15.28 ± 0.07577
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

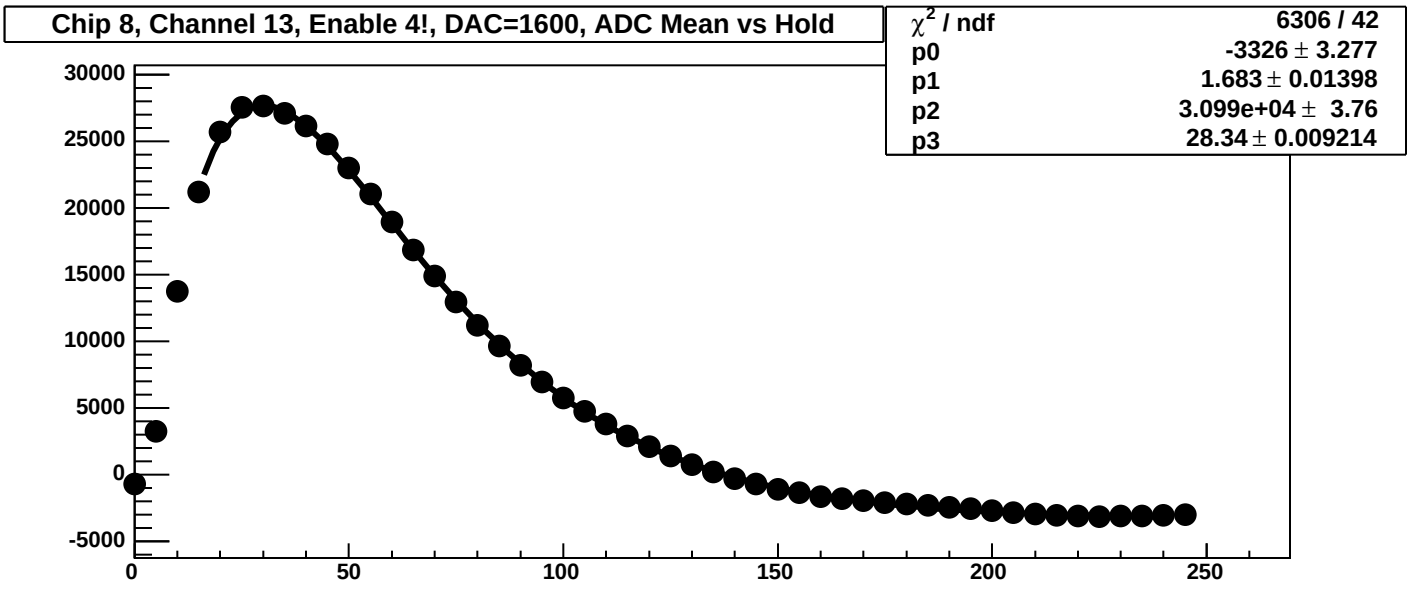

Chip 8, Channel 13, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

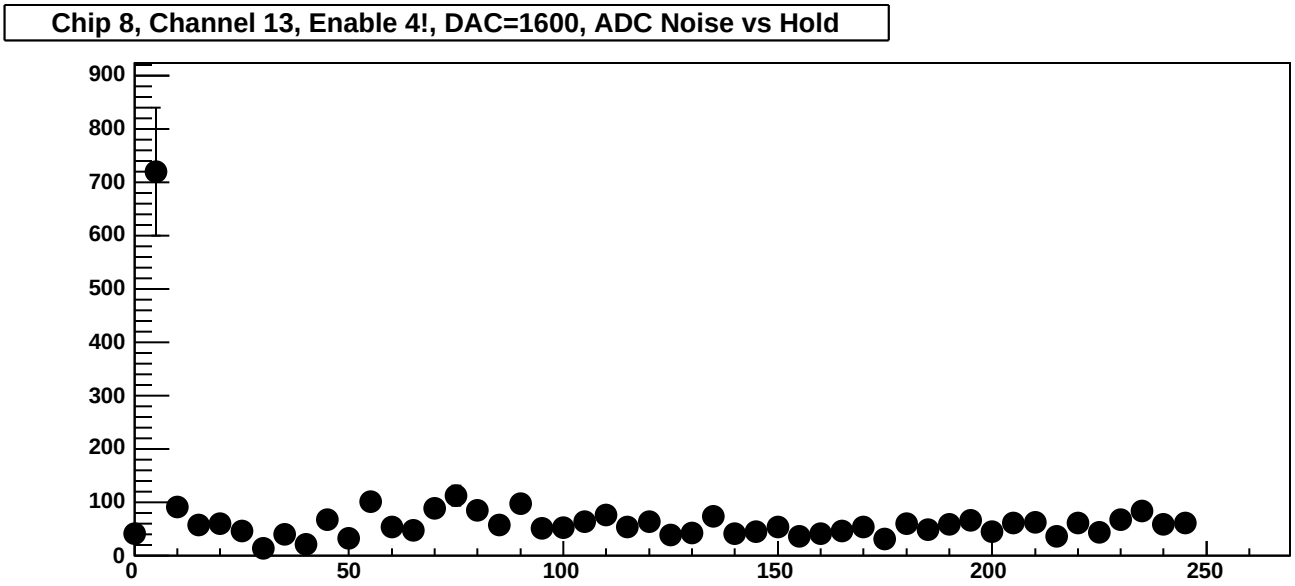

Chip 8, Channel 13, Enable 2, DAC=1600, ADC Residuals vs Hold
1000
800
600
400
200
0
-200
0
50
100
150
200
250

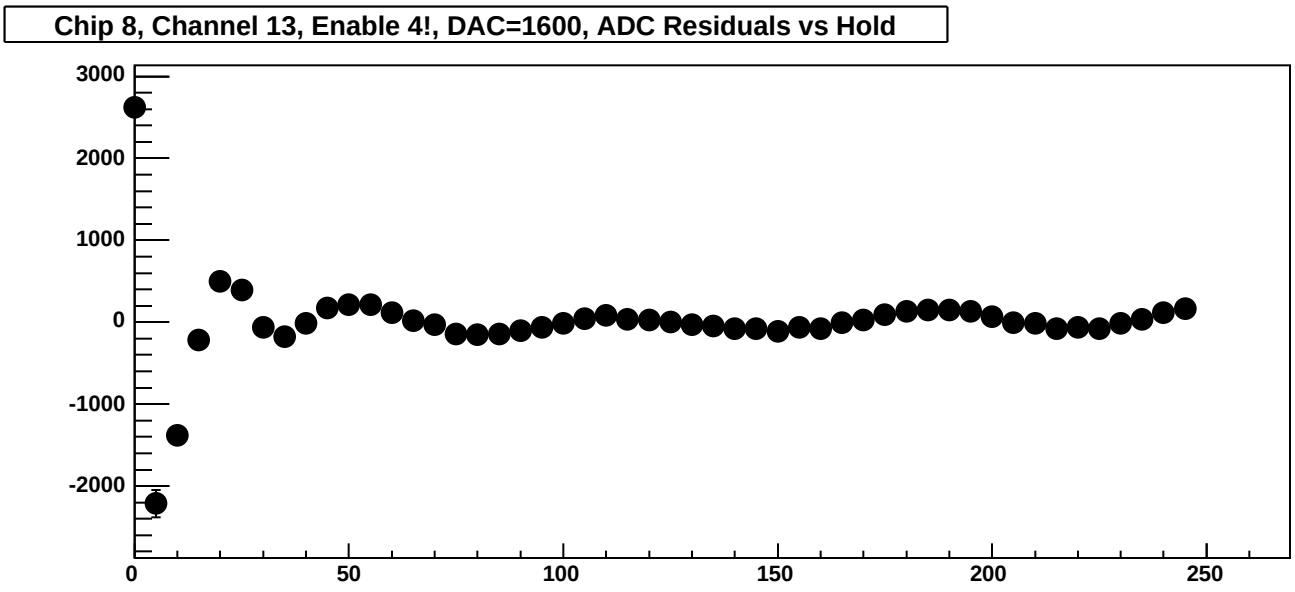

Chip 8, Channel 13, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 308.2 / 41
p0 -17.84 ± 3.874
p1 96.82 ± 0.6406
p2 -2.417e+09 ± 2.67e+07
p3 2.175e+08 ± 1.749e+05
p4 10.2 ± 0.1127
0
-100
-200
-300
-400
-500
-600
-700
-800
0
50
100
150
200
250


Chip 8, Channel 13, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

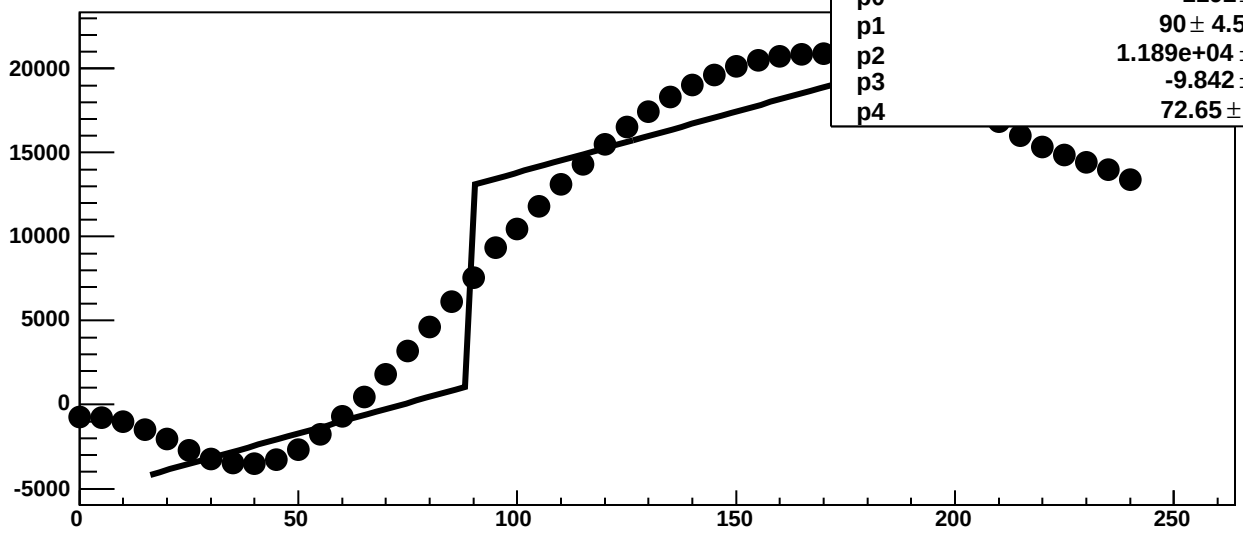

Chip 8, Channel 13, Enable 3, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250


Chip 8, Channel 13, Enable 4!, DAC=1600, ADC Mean vs Hold
χ² / ndf 6306 / 42
p0 -3326 ± 3.277
p1 1.683 ± 0.01398
p2 3.099e+04 ± 3.76
p3 28.34 ± 0.009214
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

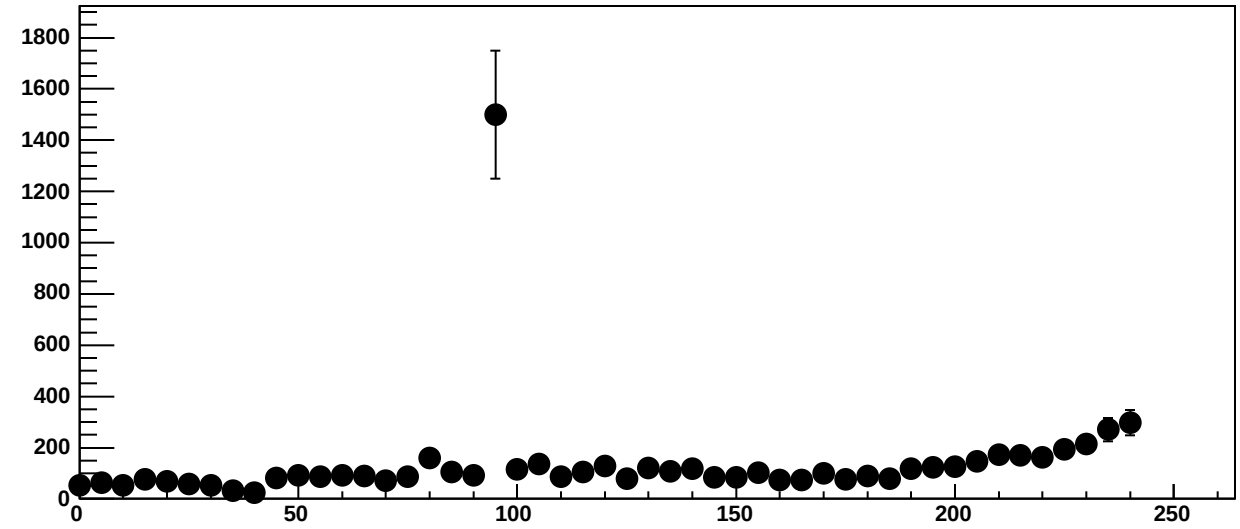

Chip 8, Channel 13, Enable 4!, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 8, Channel 13, Enable 4!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
0
50
100
150
200
250

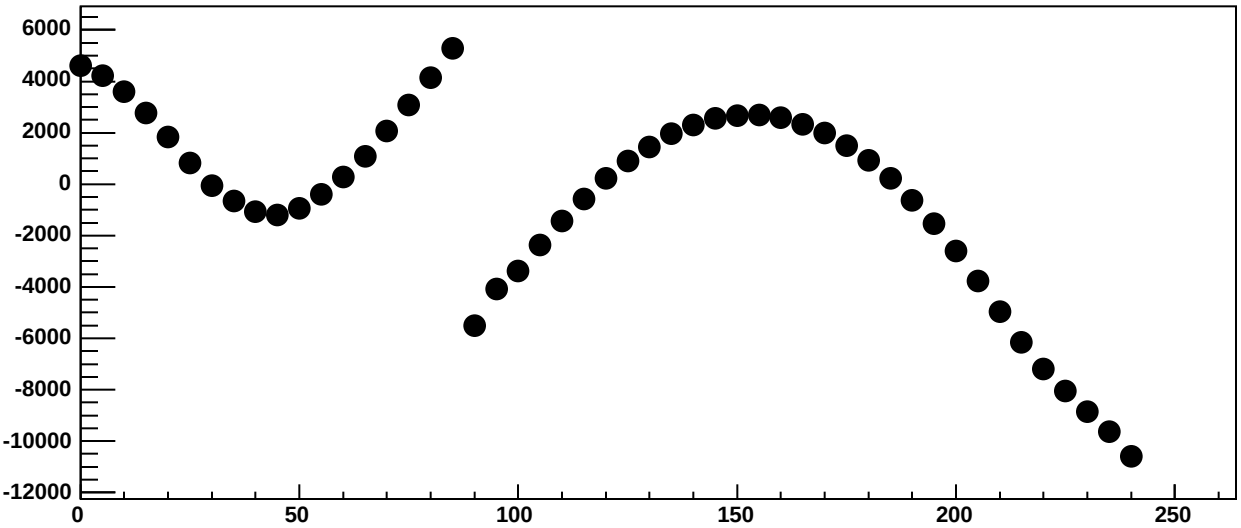
Chip 8, Channel 13, Enable 5, DAC=1600, ADC Mean vs Hold



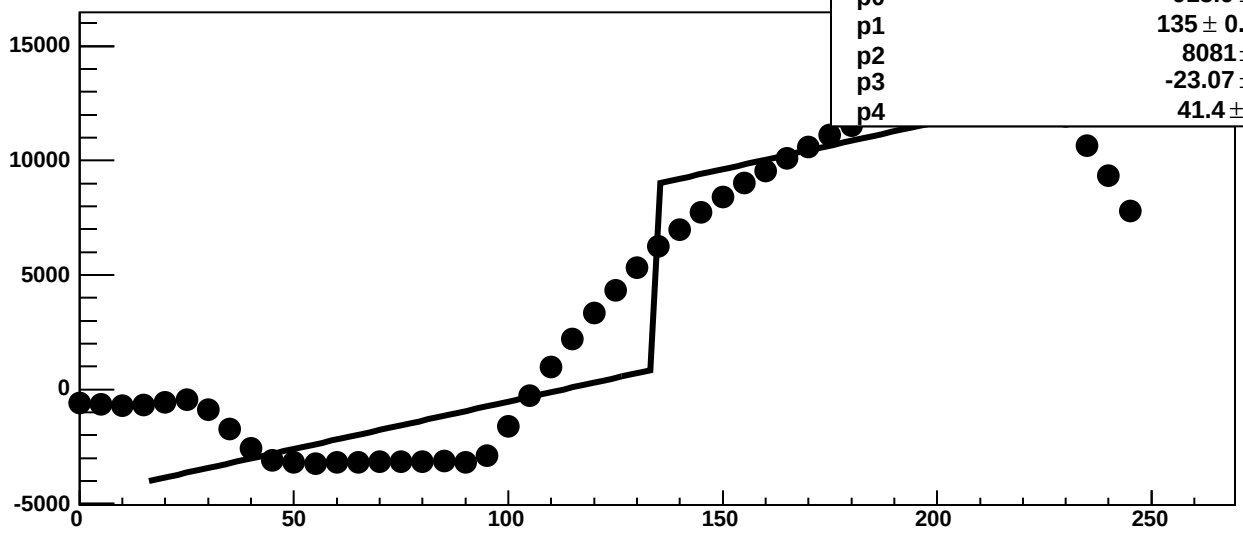
Chip 8, Channel 13, Enable 5, DAC=1600, ADC Noise vs Hold



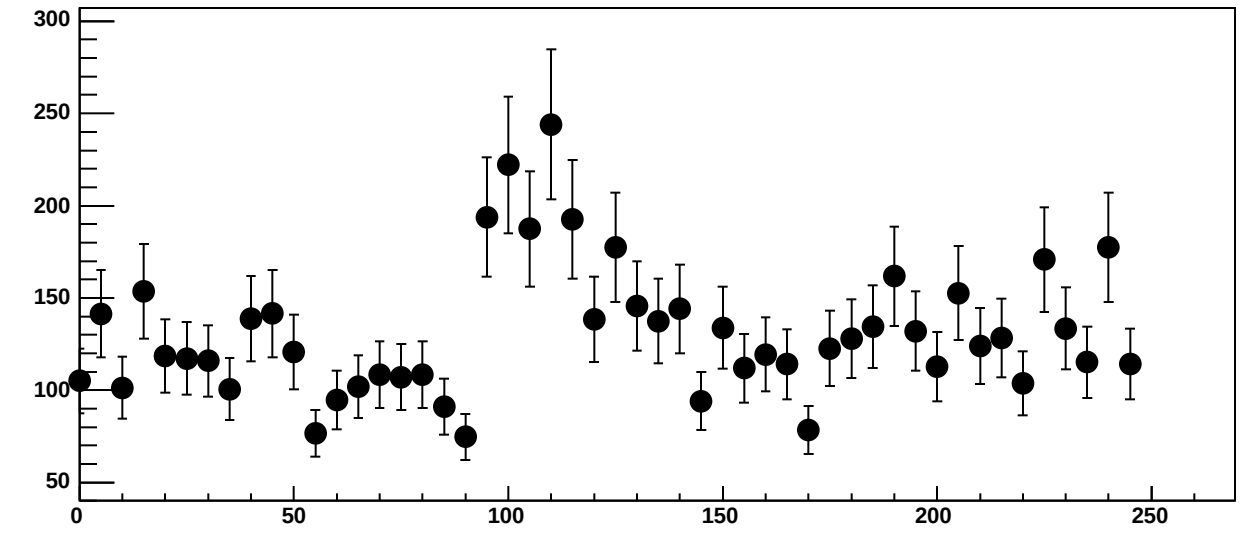
Chip 8, Channel 13, Enable 5, DAC=1600, ADC Residuals vs Hold



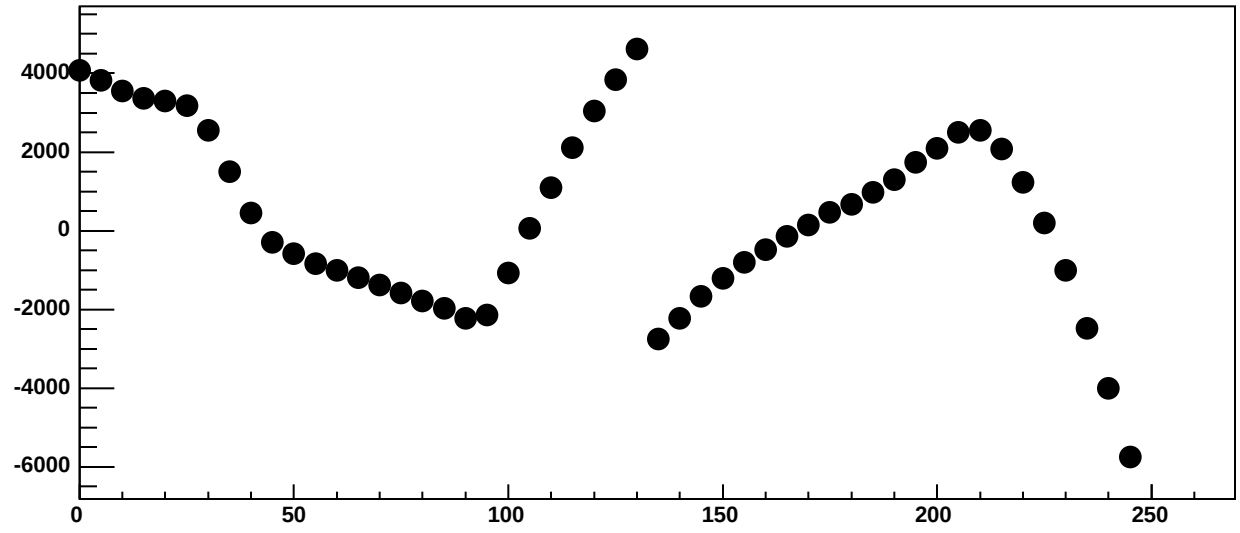
Chip 8, Channel 14, Enable 0, DAC=1600, ADC Mean vs Hold



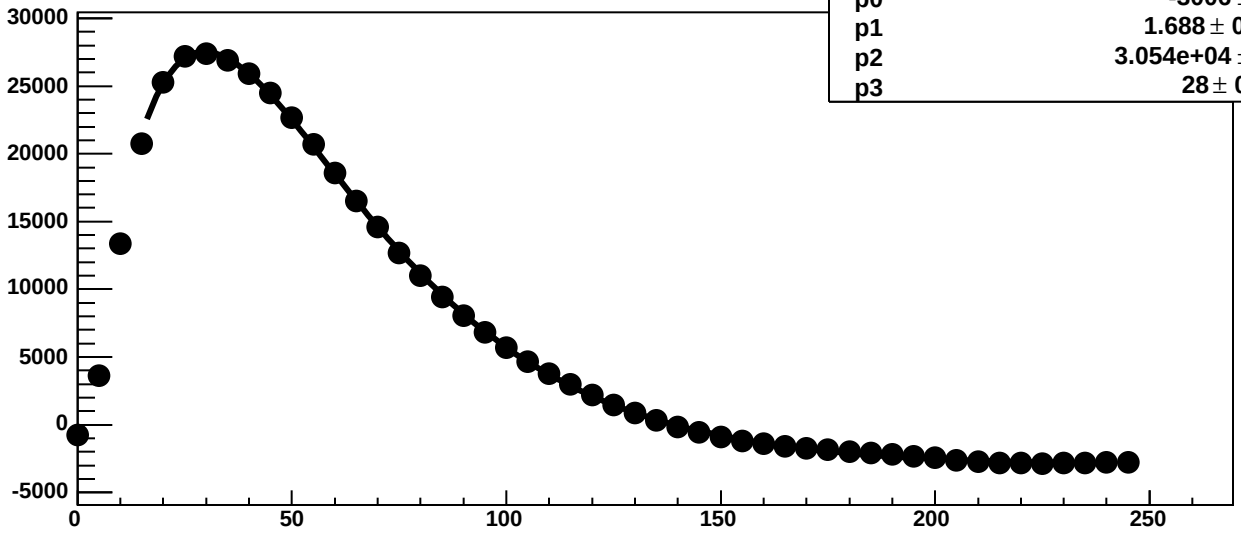
Chip 8, Channel 14, Enable 0, DAC=1600, ADC Noise vs Hold



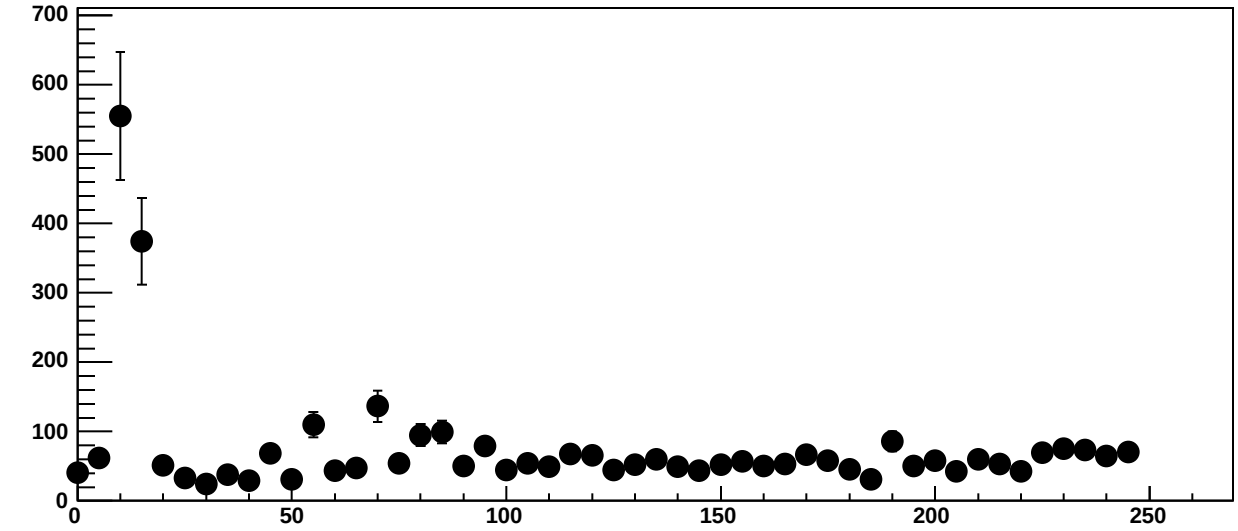
Chip 8, Channel 14, Enable 0, DAC=1600, ADC Residuals vs Hold



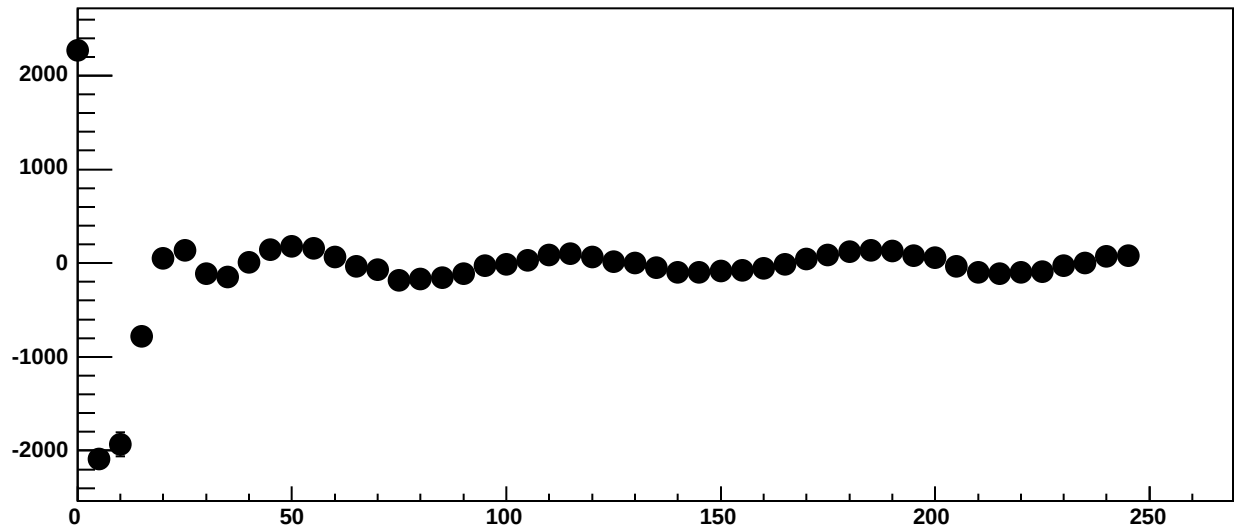
Chip 8, Channel 14, Enable 1!, DAC=1600, ADC Mean vs Hold



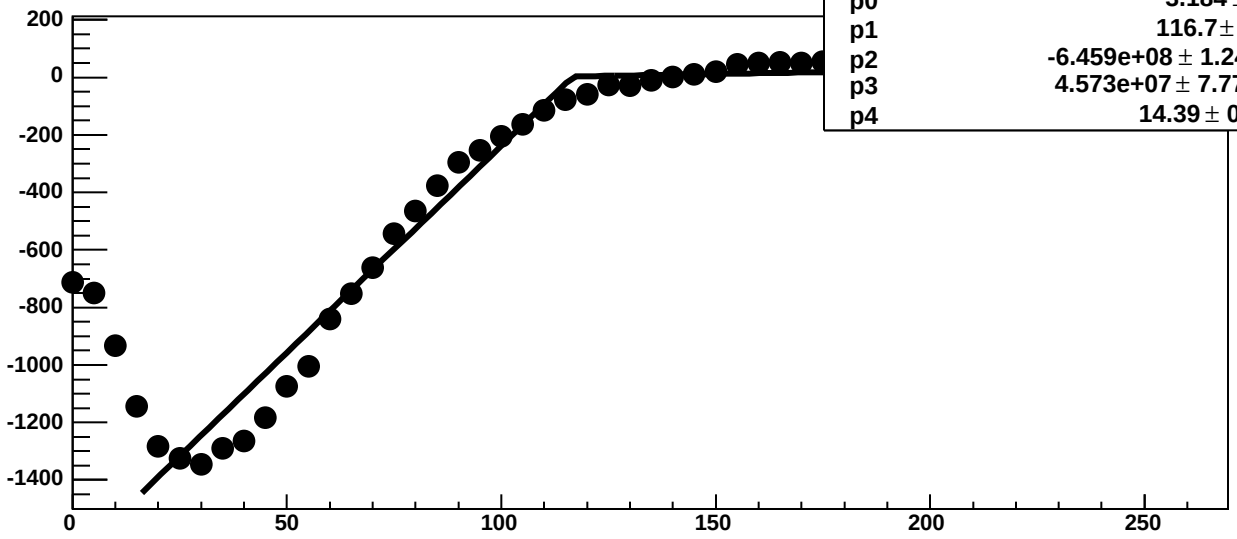
Chip 8, Channel 14, Enable 1!, DAC=1600, ADC Noise vs Hold



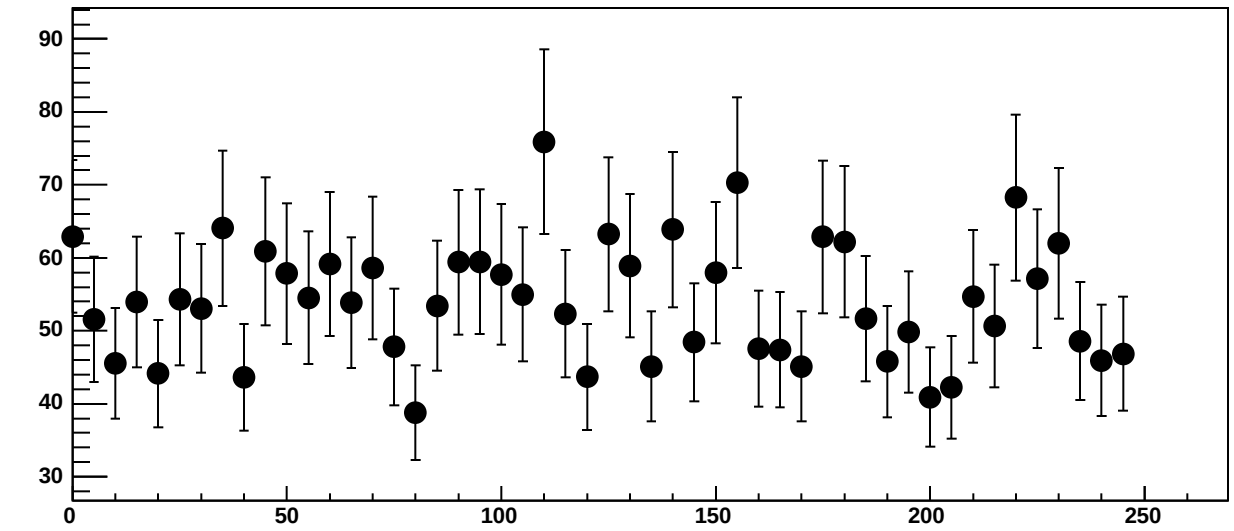
Chip 8, Channel 14, Enable 1!, DAC=1600, ADC Residuals vs Hold



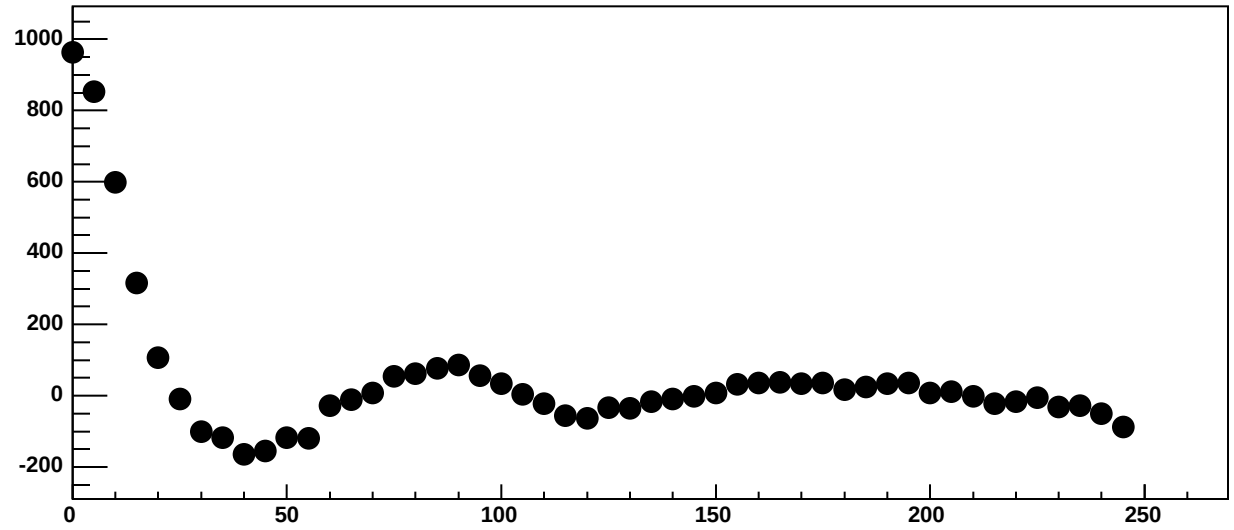
Chip 8, Channel 14, Enable 2, DAC=1600, ADC Mean vs Hold



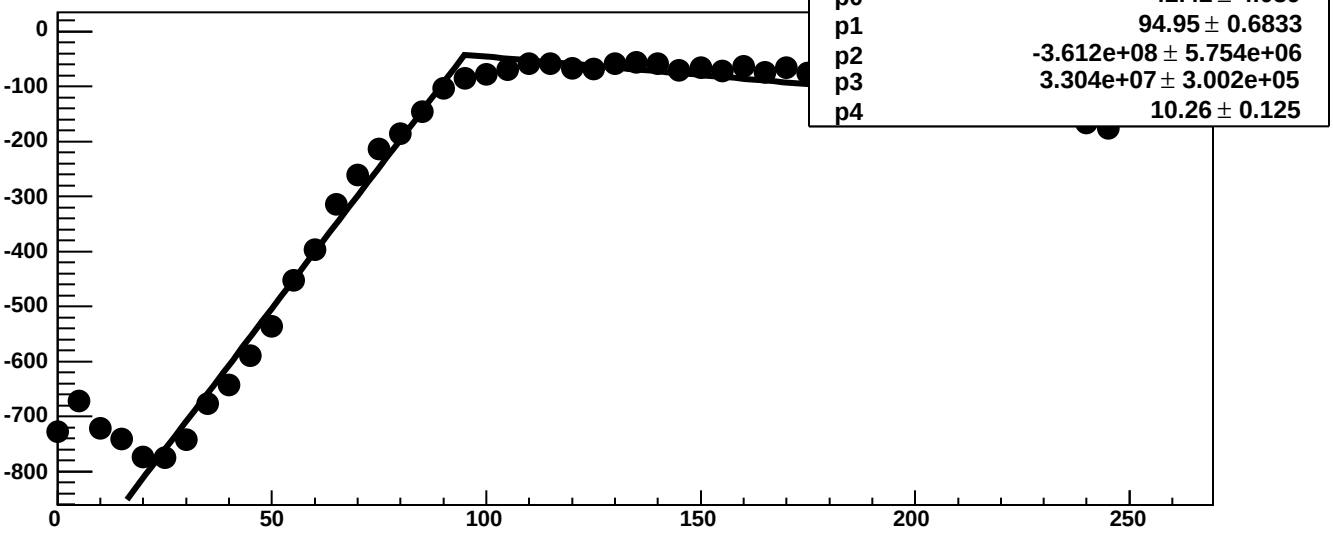
Chip 8, Channel 14, Enable 2, DAC=1600, ADC Noise vs Hold



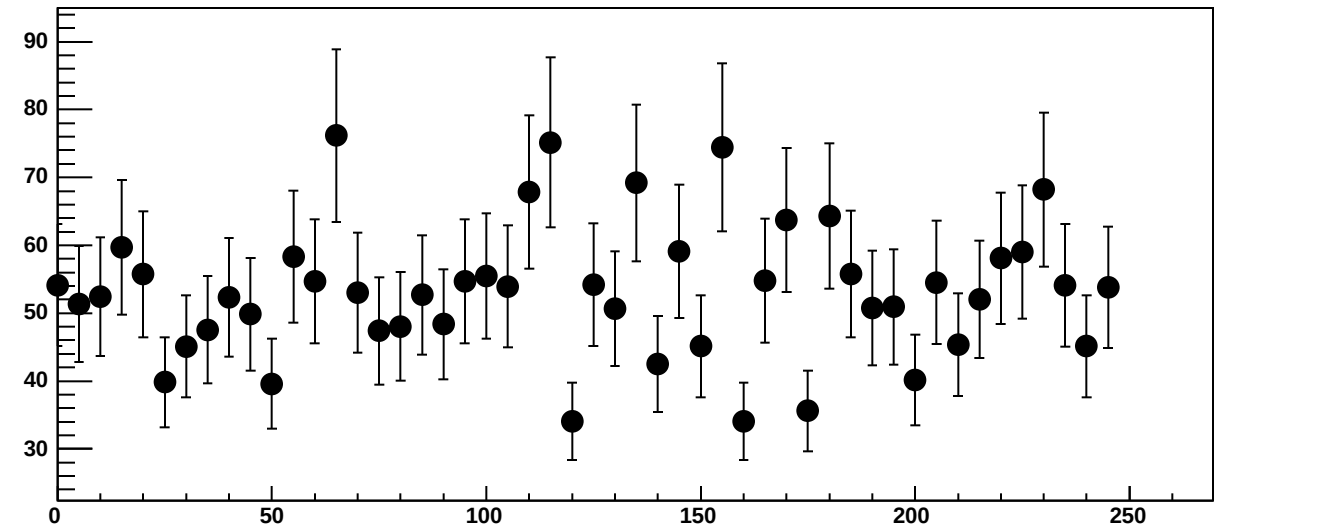
Chip 8, Channel 14, Enable 2, DAC=1600, ADC Residuals vs Hold



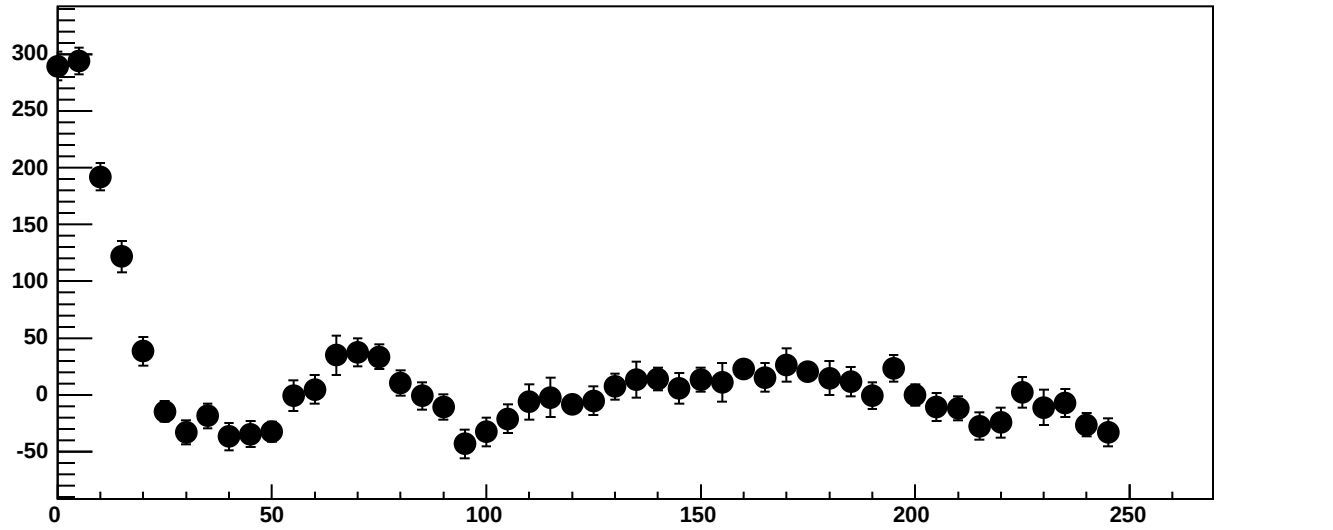
Chip 8, Channel 14, Enable 3, DAC=1600, ADC Mean vs Hold

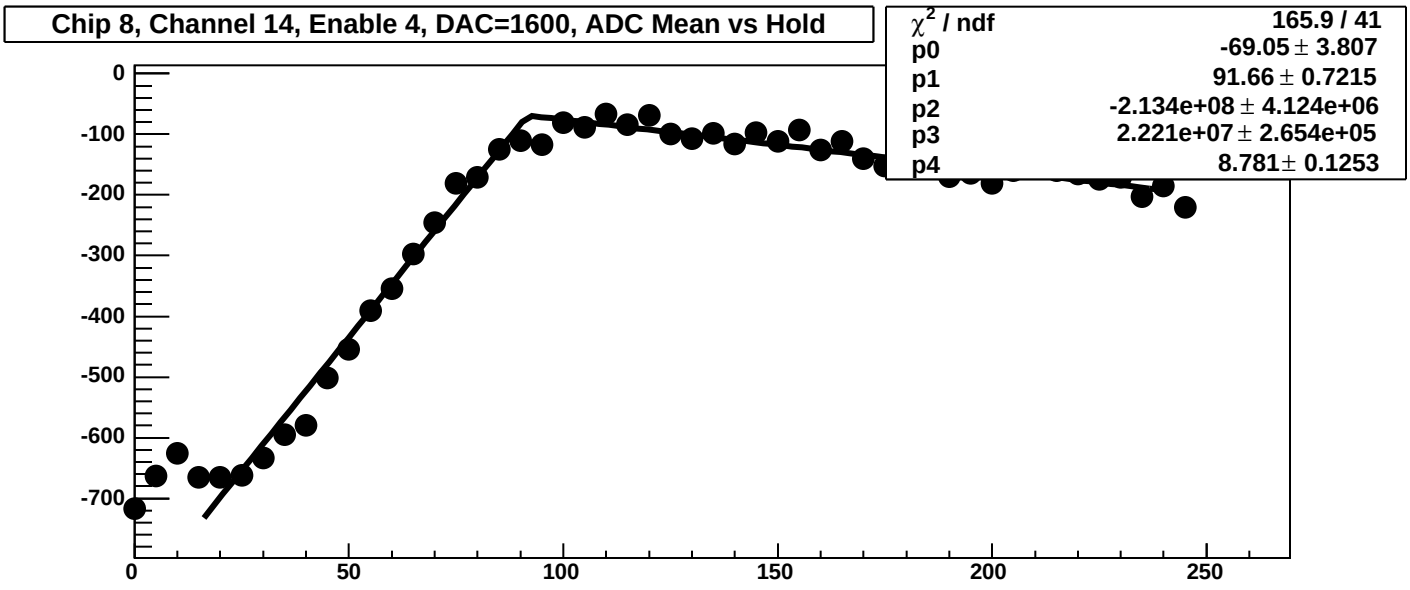


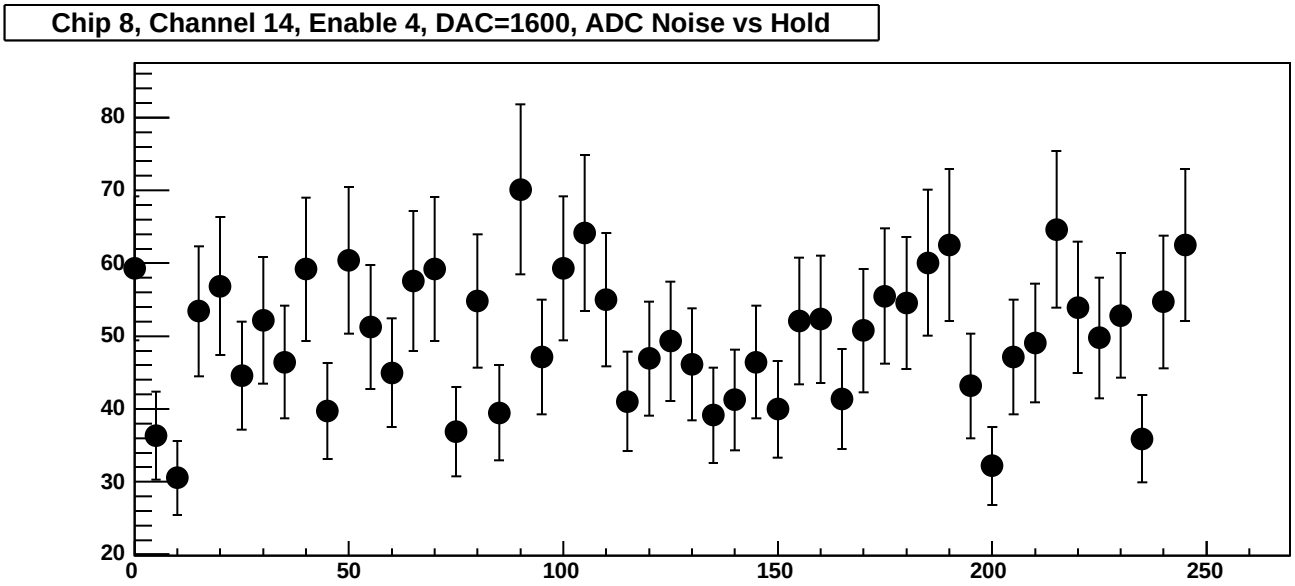
Chip 8, Channel 14, Enable 3, DAC=1600, ADC Noise vs Hold

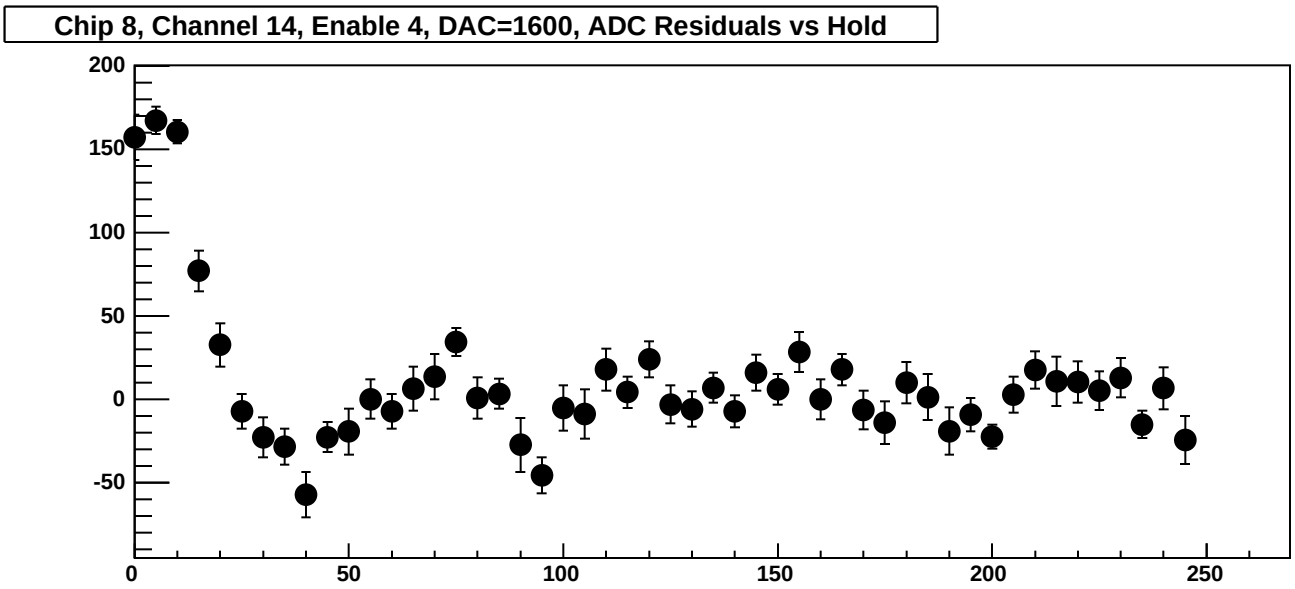


Chip 8, Channel 14, Enable 3, DAC=1600, ADC Residuals vs Hold

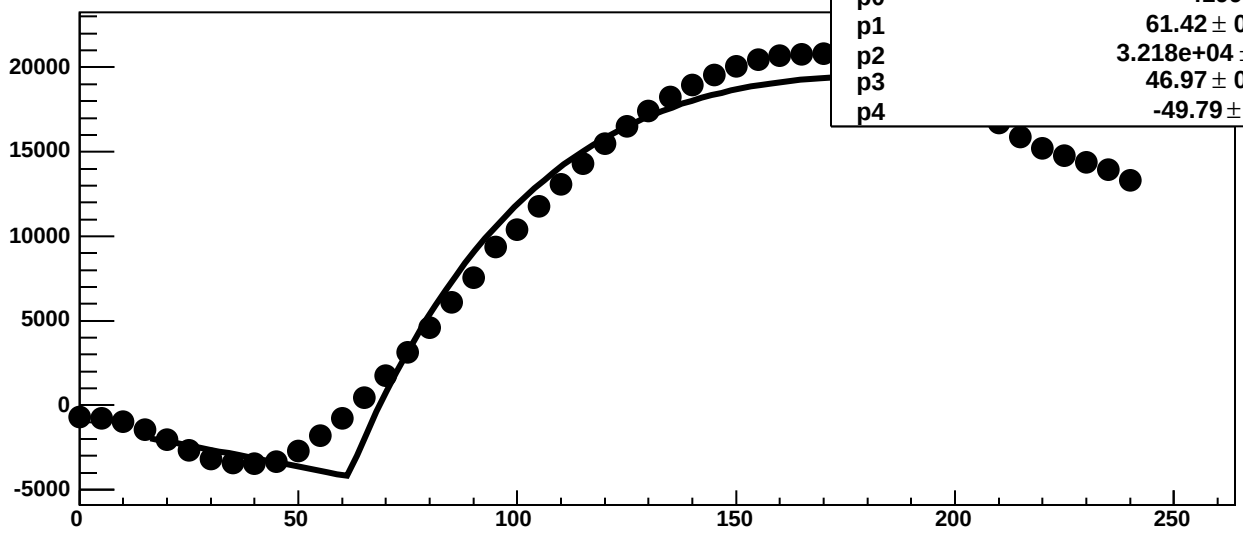



Chip 8, Channel 14, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 165.9 / 41
p0 -69.05 ± 3.807
p1 91.66 ± 0.7215
p2 -2.134e+08 ± 4.124e+06
p3 2.221e+07 ± 2.654e+05
p4 8.781 ± 0.1253
0
-100
-200
-300
-400
-500
-600
-700
0
50
100
150
200
250

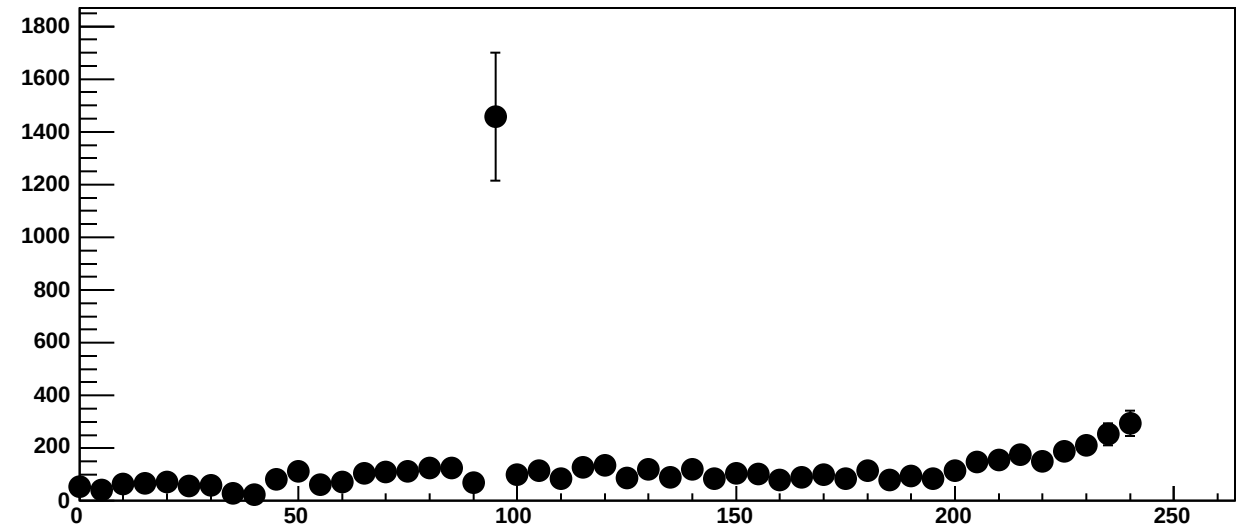

Chip 8, Channel 14, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 8, Channel 14, Enable 4, DAC=1600, ADC Residuals vs Hold
200
150
100
50
0
-50
0
50
100
150
200
250

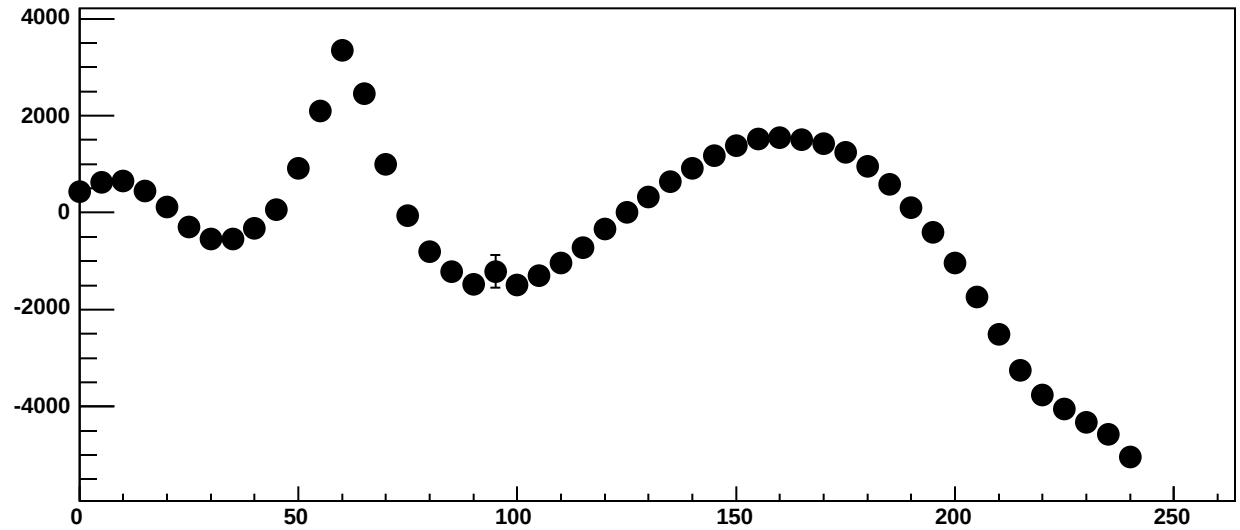
Chip 8, Channel 14, Enable 5, DAC=1600, ADC Mean vs Hold



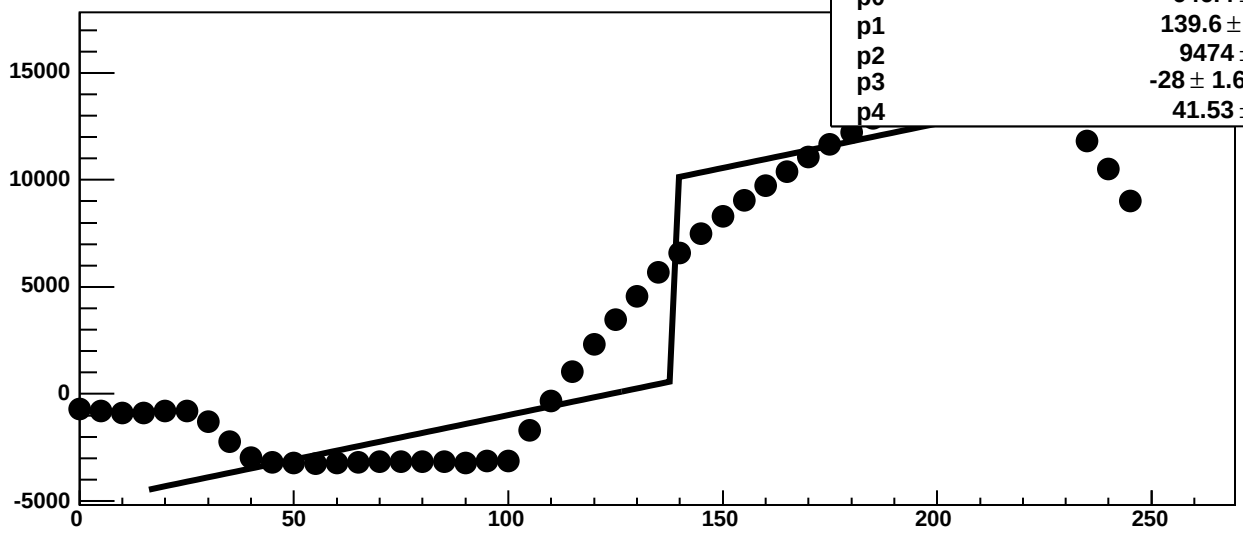
Chip 8, Channel 14, Enable 5, DAC=1600, ADC Noise vs Hold



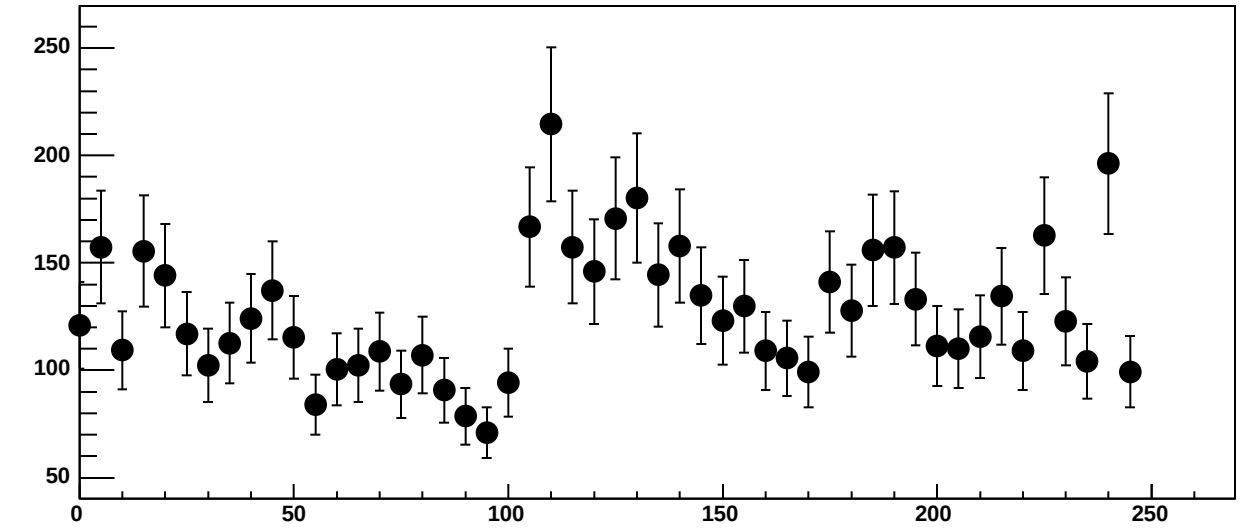
Chip 8, Channel 14, Enable 5, DAC=1600, ADC Residuals vs Hold



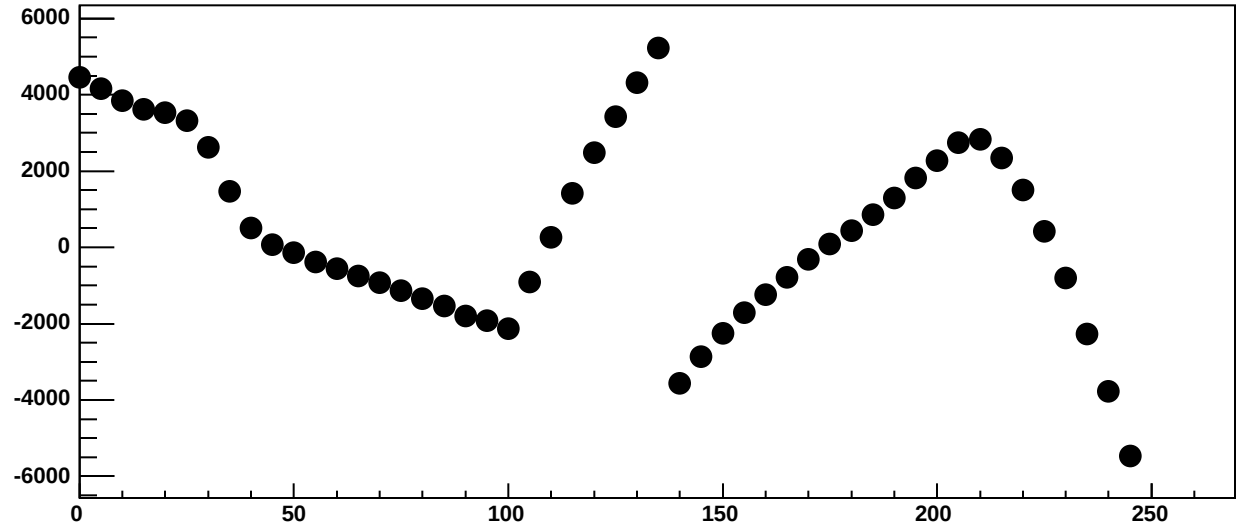
Chip 8, Channel 15, Enable 0, DAC=1600, ADC Mean vs Hold



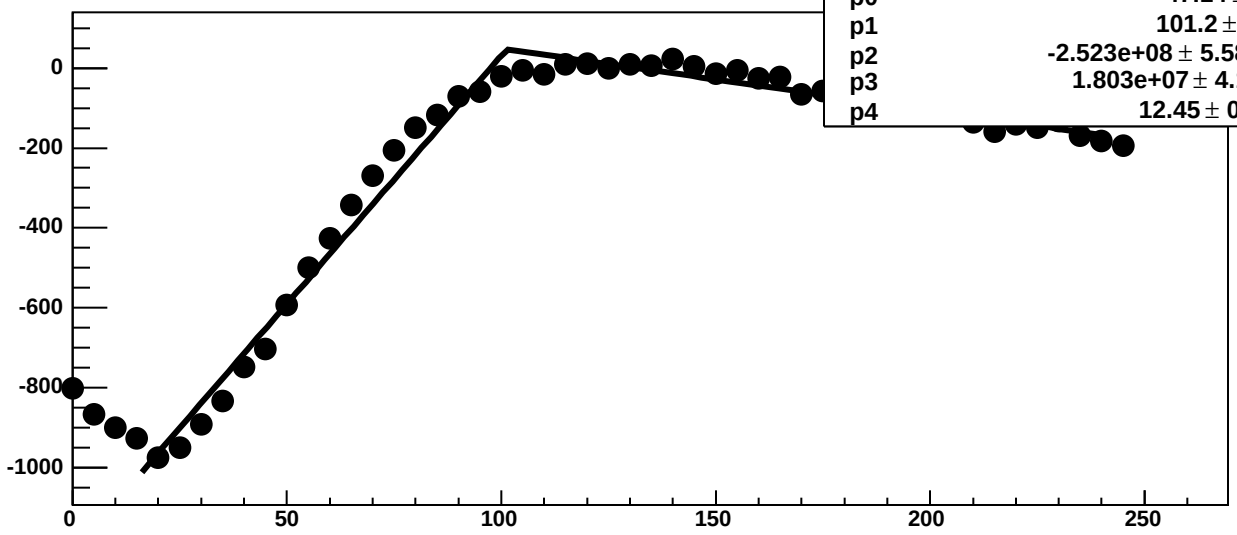
Chip 8, Channel 15, Enable 0, DAC=1600, ADC Noise vs Hold



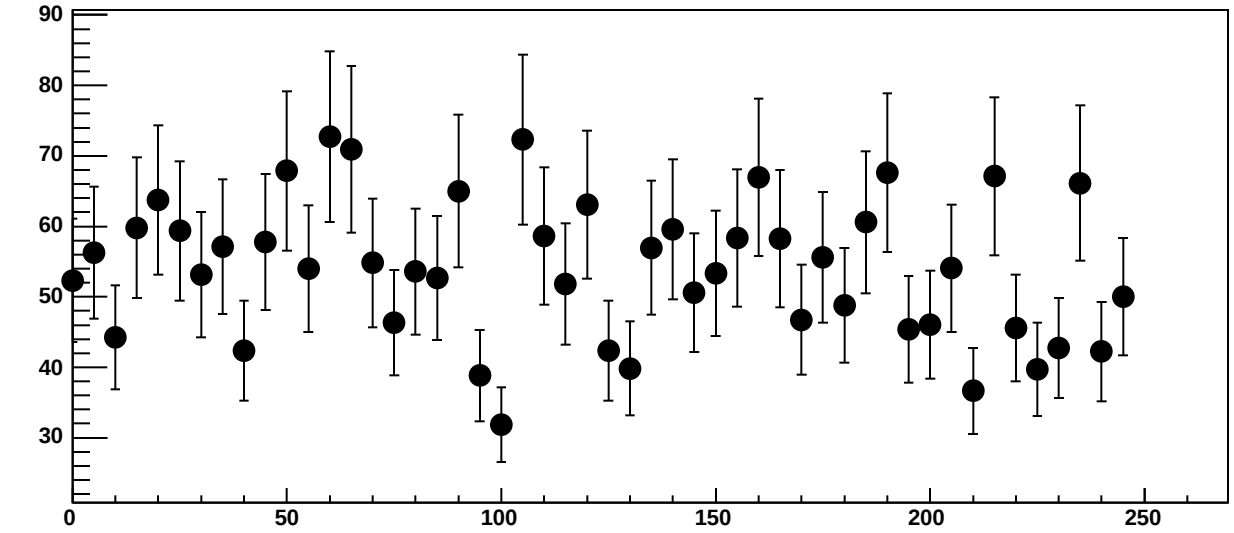
Chip 8, Channel 15, Enable 0, DAC=1600, ADC Residuals vs Hold



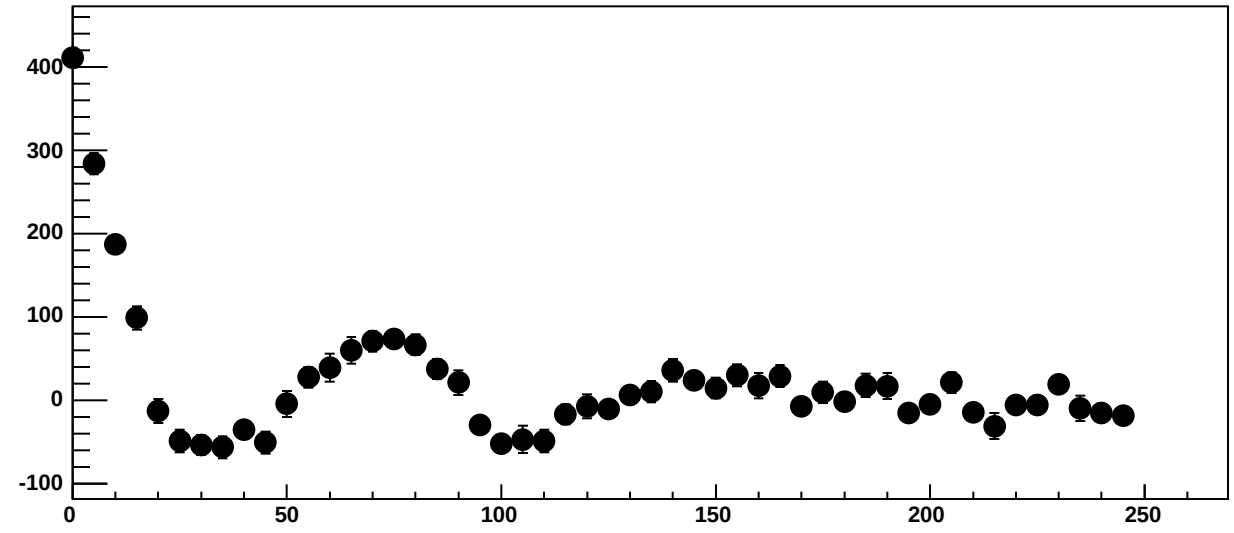
Chip 8, Channel 15, Enable 1, DAC=1600, ADC Mean vs Hold



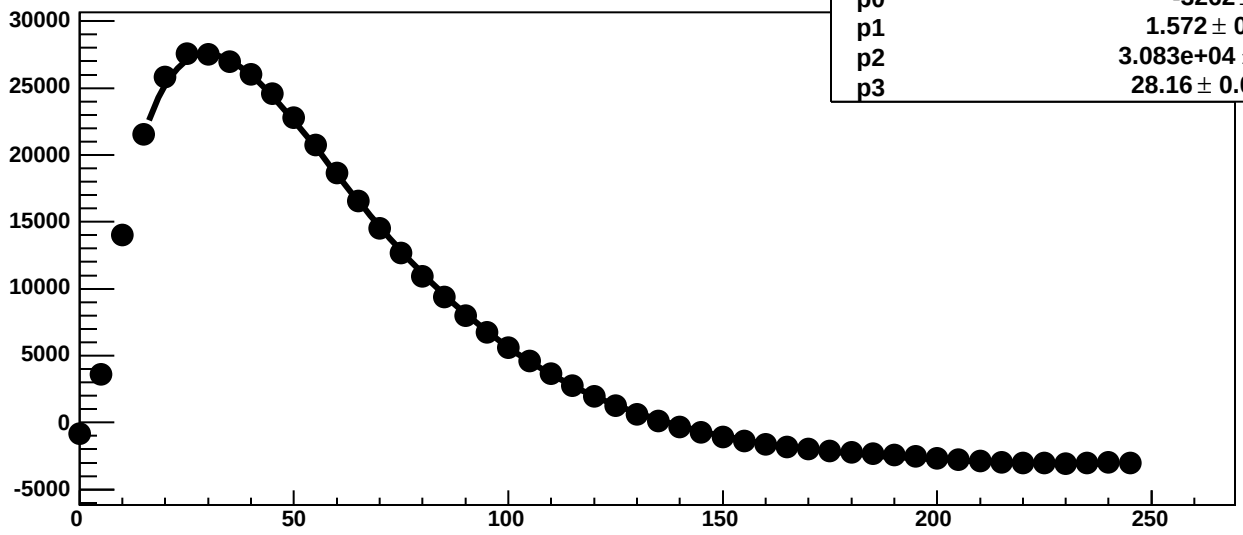
Chip 8, Channel 15, Enable 1, DAC=1600, ADC Noise vs Hold



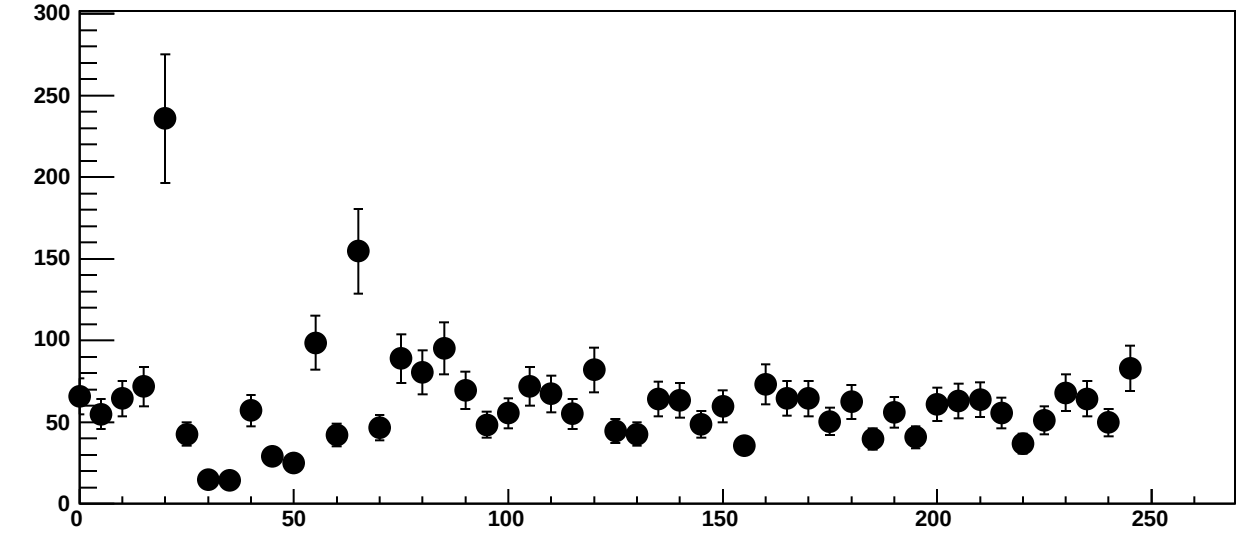
Chip 8, Channel 15, Enable 1, DAC=1600, ADC Residuals vs Hold



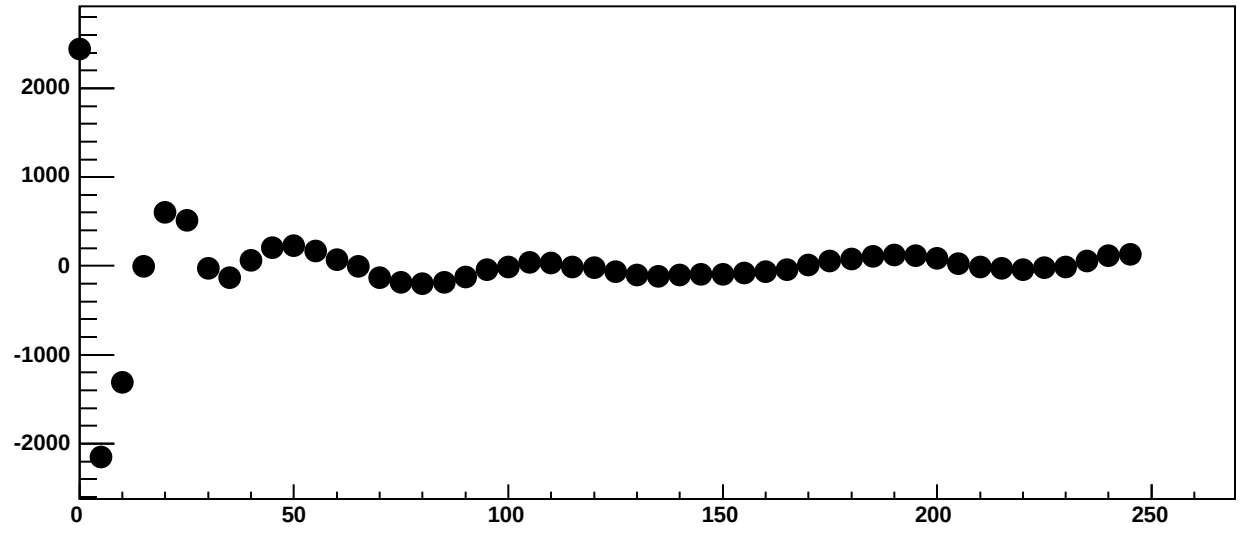
Chip 8, Channel 15, Enable 2!, DAC=1600, ADC Mean vs Hold

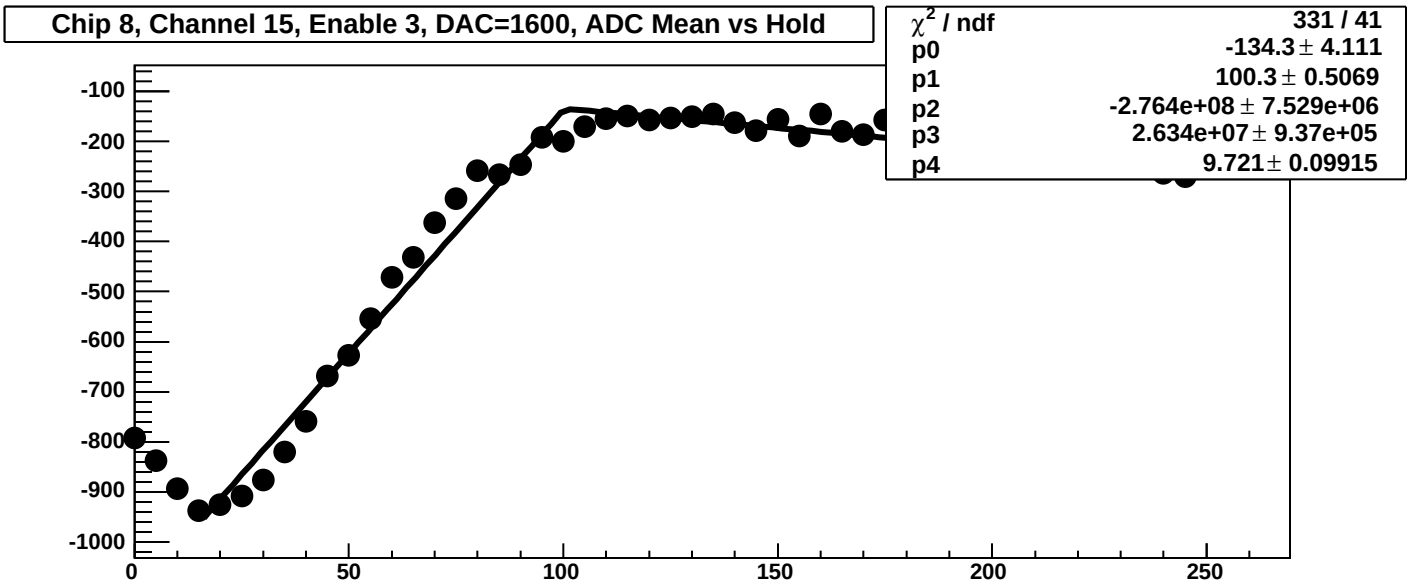


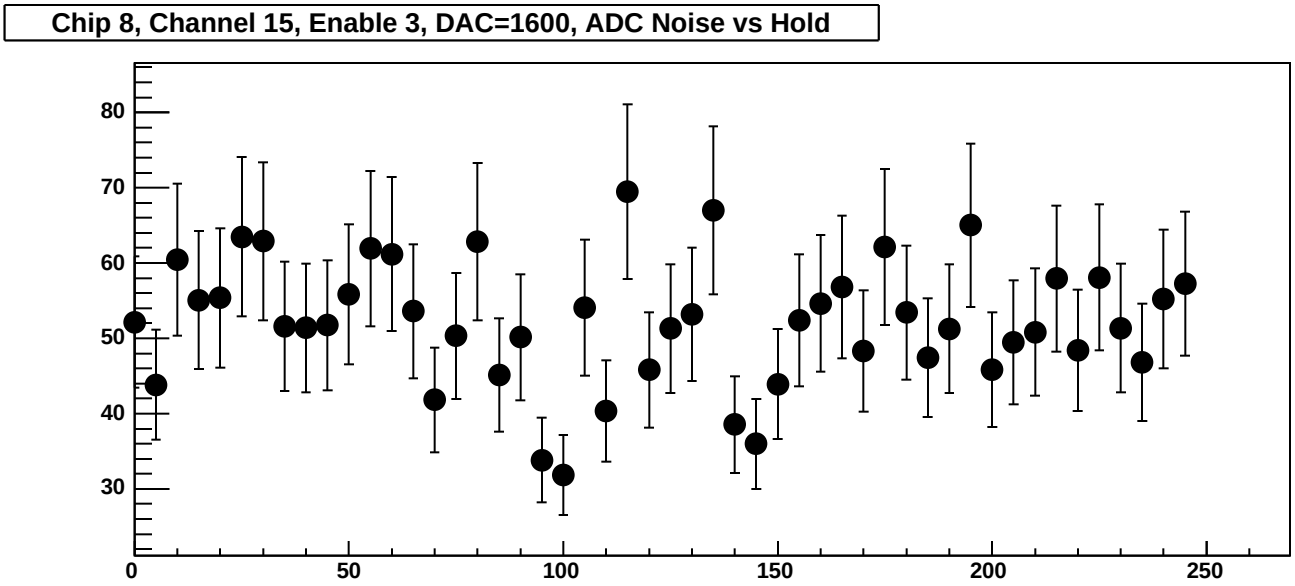
Chip 8, Channel 15, Enable 2!, DAC=1600, ADC Noise vs Hold

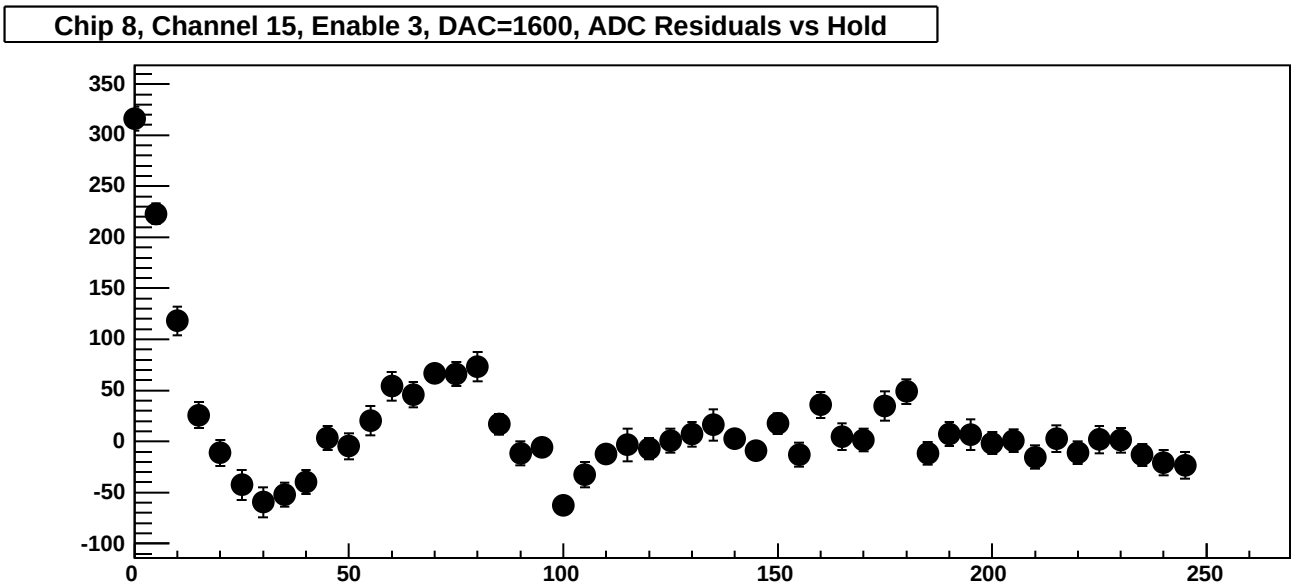


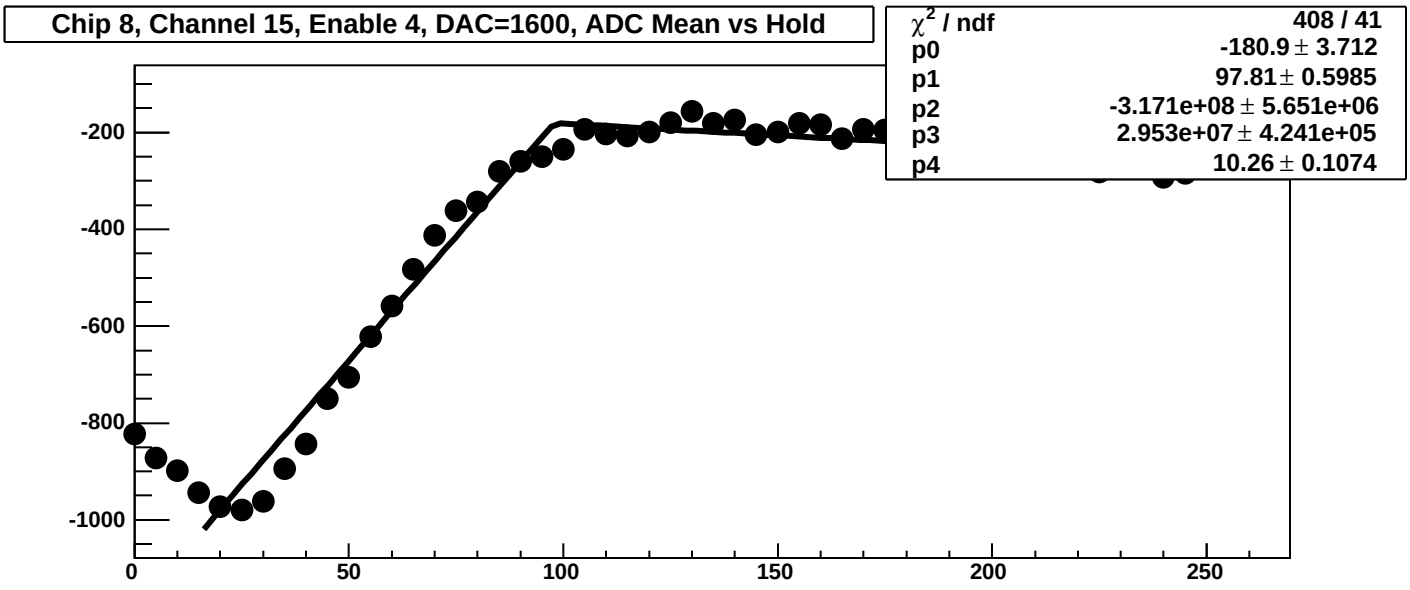
Chip 8, Channel 15, Enable 2!, DAC=1600, ADC Residuals vs Hold

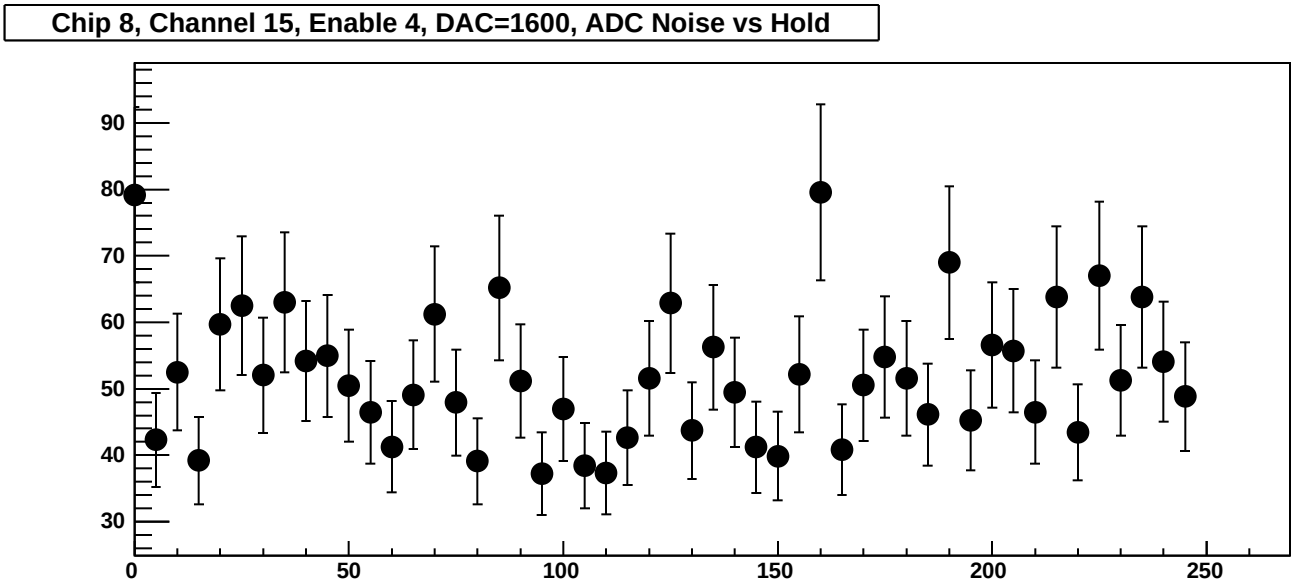


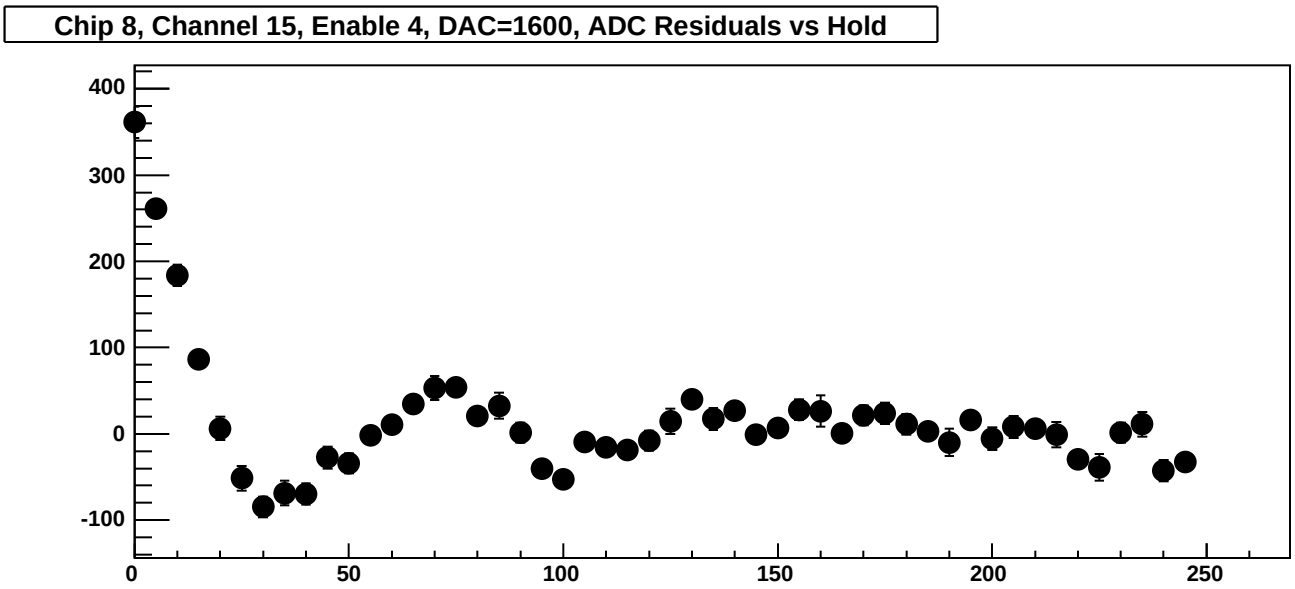

Chip 8, Channel 15, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 331 / 41
p0 -134.3 ± 4.111
p1 100.3 ± 0.5069
p2 -2.764e+08 ± 7.529e+06
p3 2.634e+07 ± 9.37e+05
p4 9.721 ± 0.09915
-100
-200
-300
-400
-500
-600
-700
-800
-900
-1000
0
50
100
150
200
250

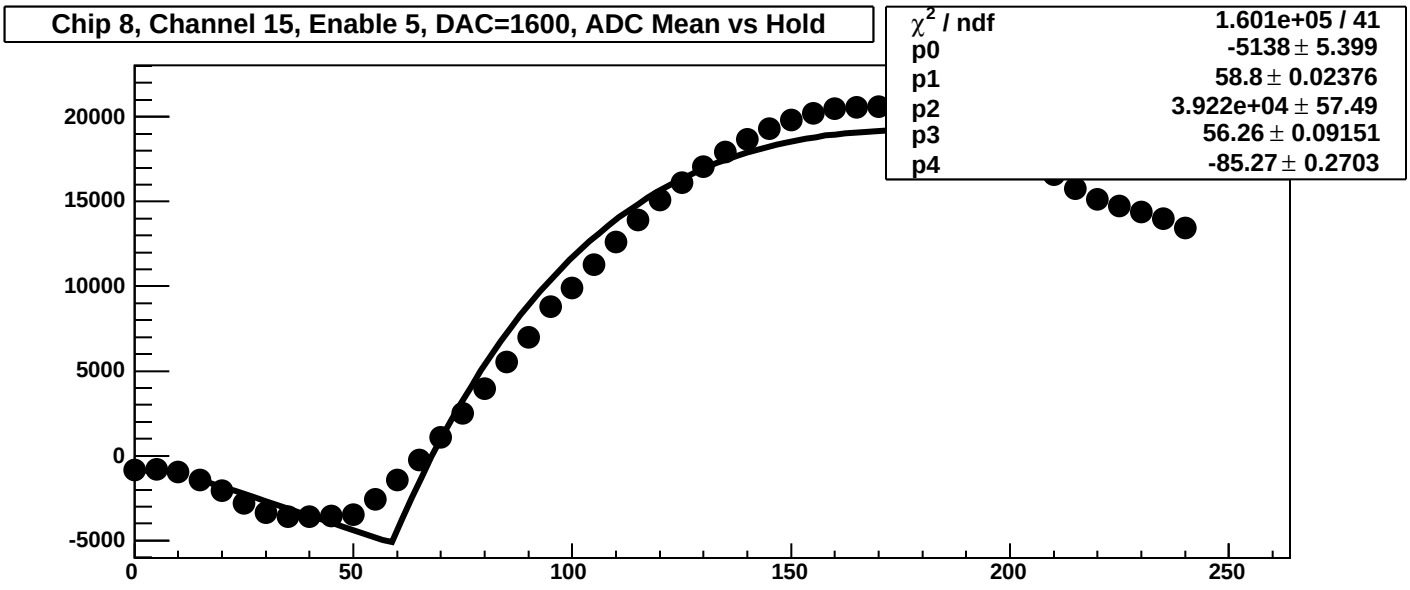

Chip 8, Channel 15, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

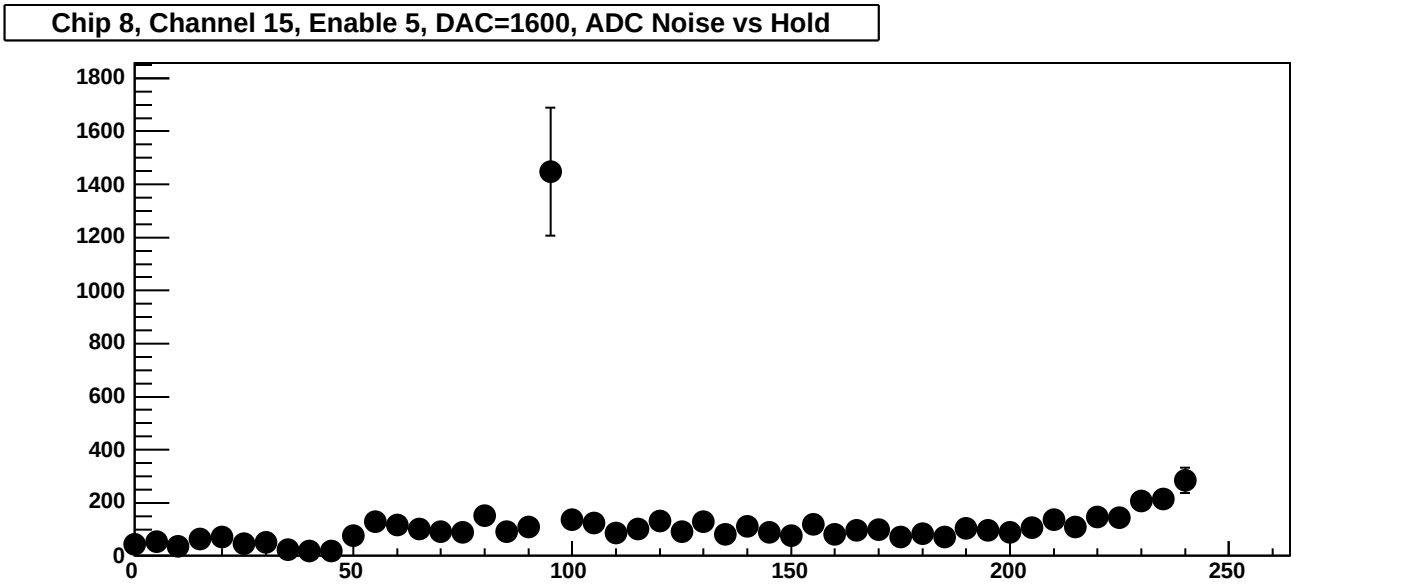

Chip 8, Channel 15, Enable 3, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

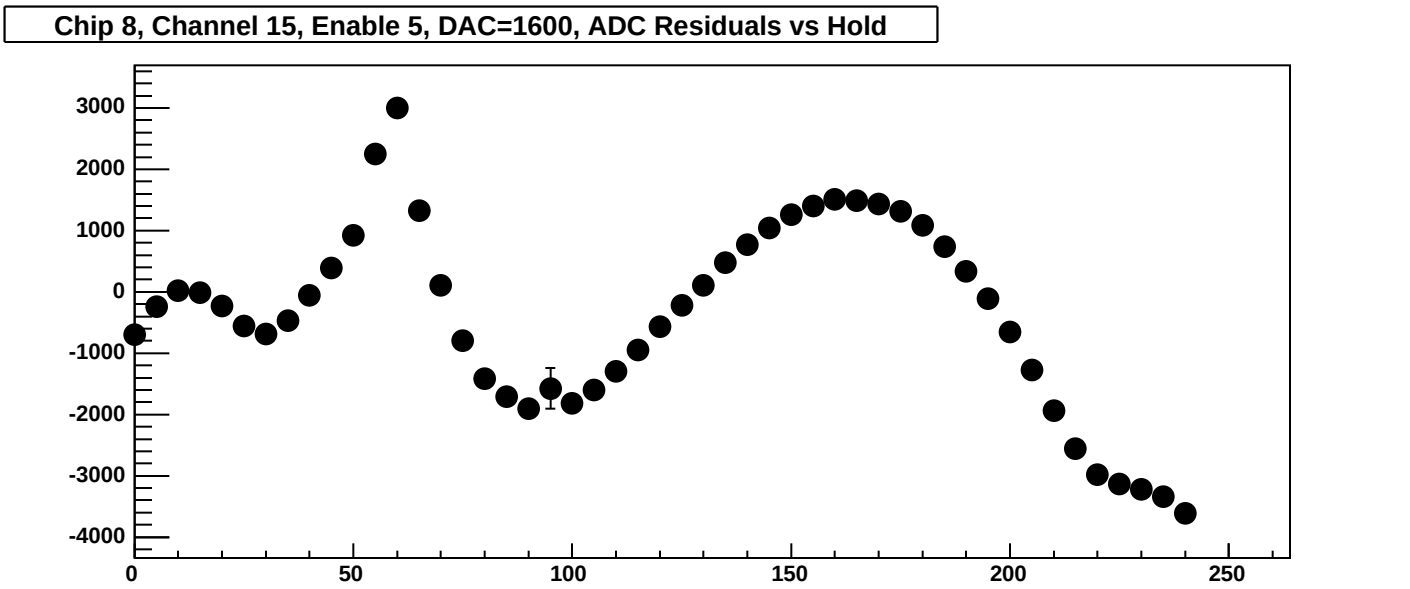

Chip 8, Channel 15, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 408 / 41
p0 -180.9 ± 3.712
p1 97.81 ± 0.5985
p2 -3.171e+08 ± 5.651e+06
p3 2.953e+07 ± 4.241e+05
p4 10.26 ± 0.1074
-200
-400
-600
-800
-1000
0
50
100
150
200
250


Chip 8, Channel 15, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

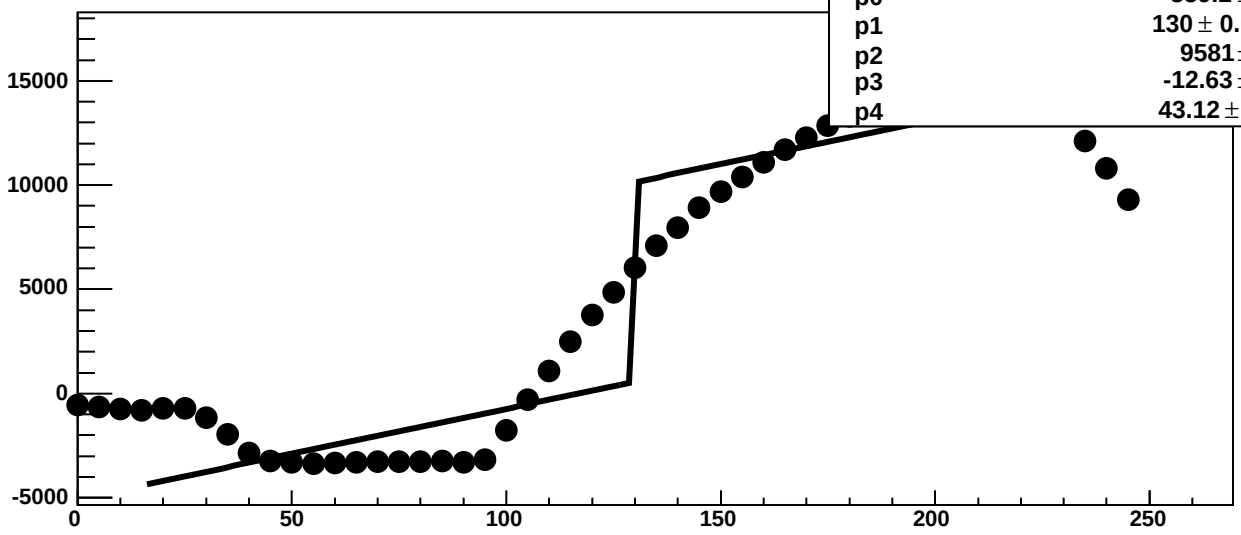

Chip 8, Channel 15, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 8, Channel 15, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.601e+05 / 41
p0 -5138 ± 5.399
p1 58.8 ± 0.02376
p2 3.922e+04 ± 57.49
p3 56.26 ± 0.09151
p4 -85.27 ± 0.2703
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

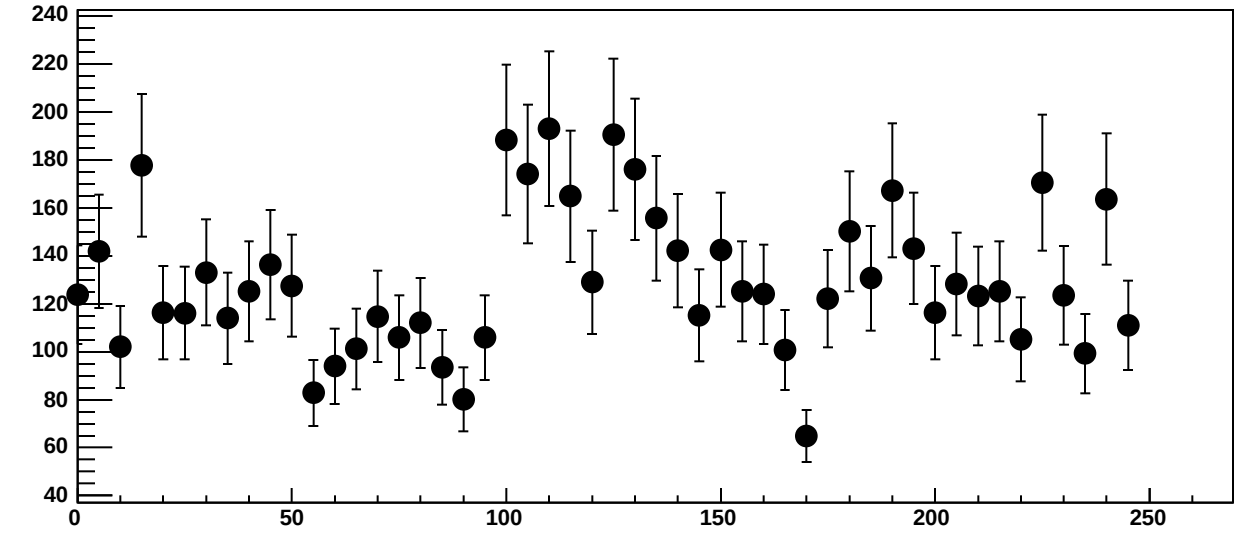

Chip 8, Channel 15, Enable 5, DAC=1600, ADC Noise vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 8, Channel 15, Enable 5, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

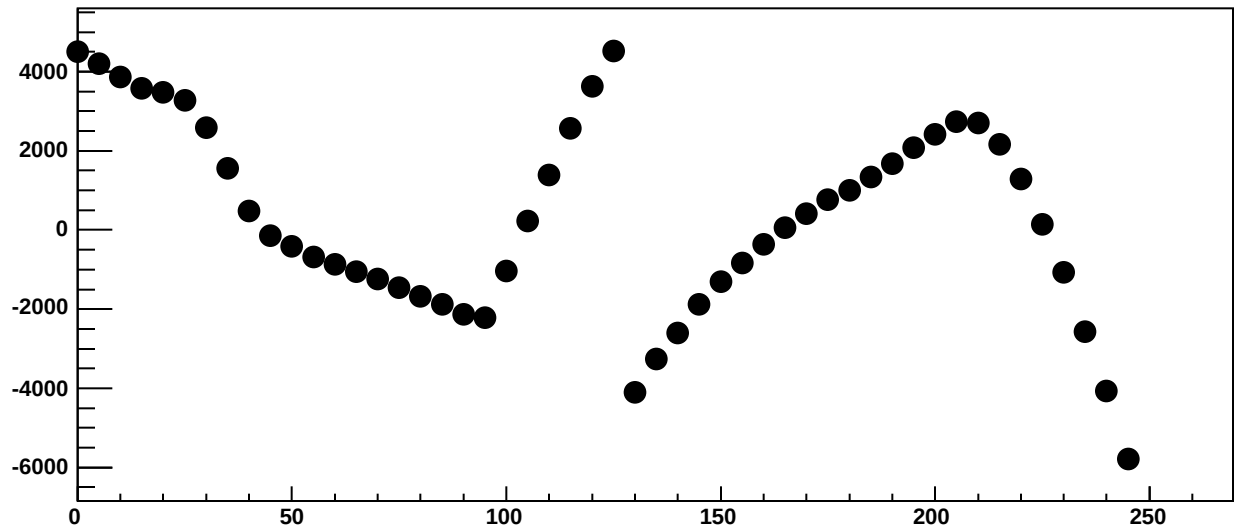
Chip 8, Channel 16, Enable 0, DAC=1600, ADC Mean vs Hold



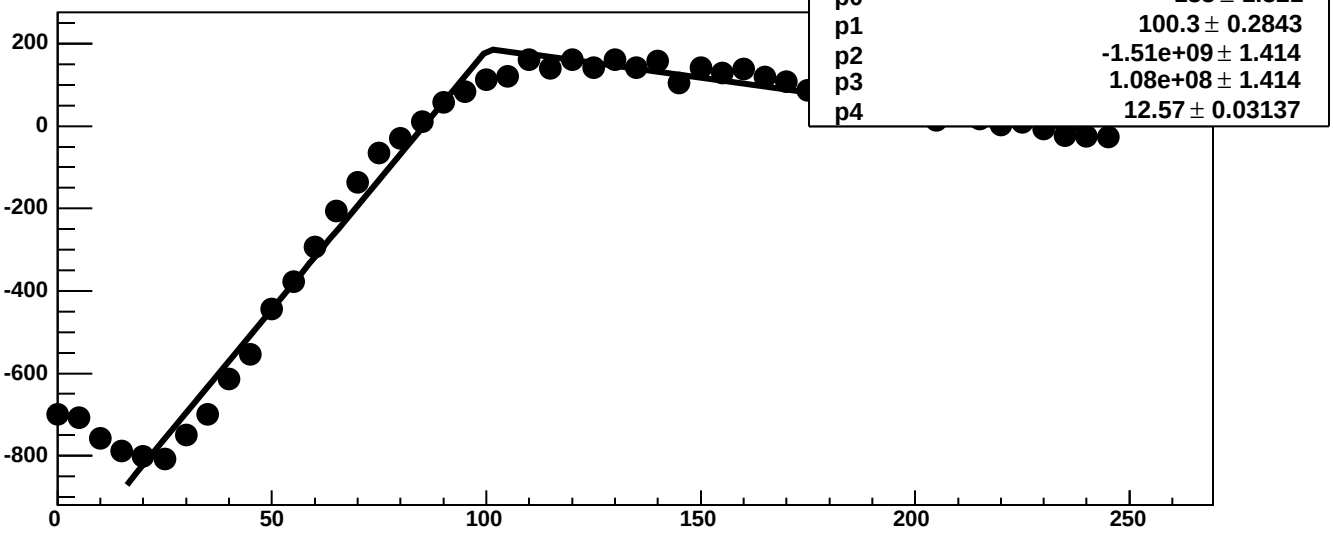
Chip 8, Channel 16, Enable 0, DAC=1600, ADC Noise vs Hold



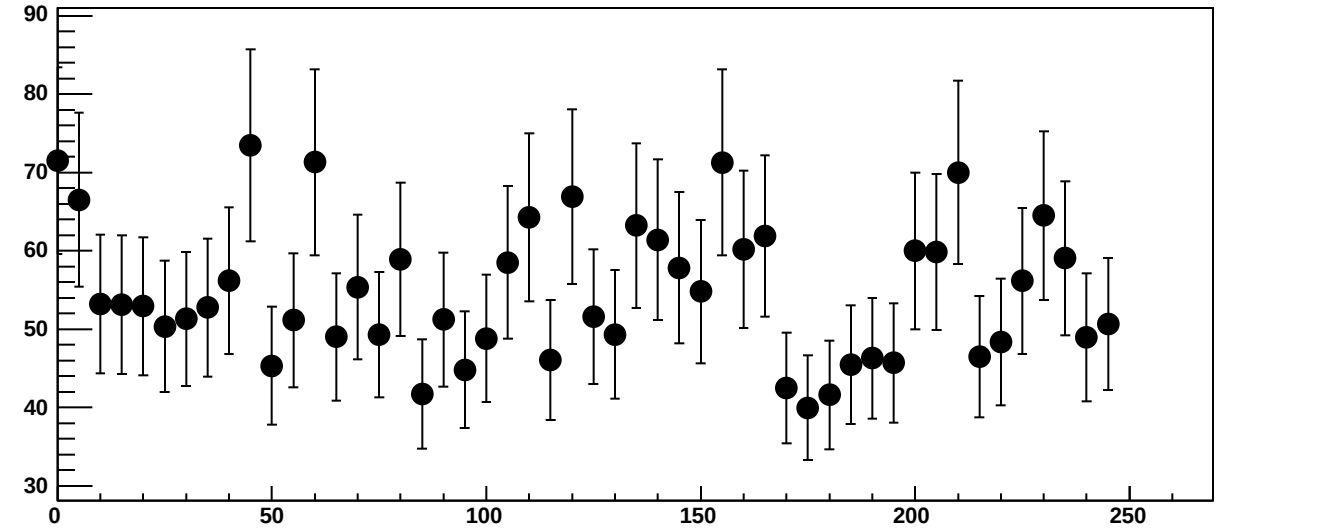
Chip 8, Channel 16, Enable 0, DAC=1600, ADC Residuals vs Hold



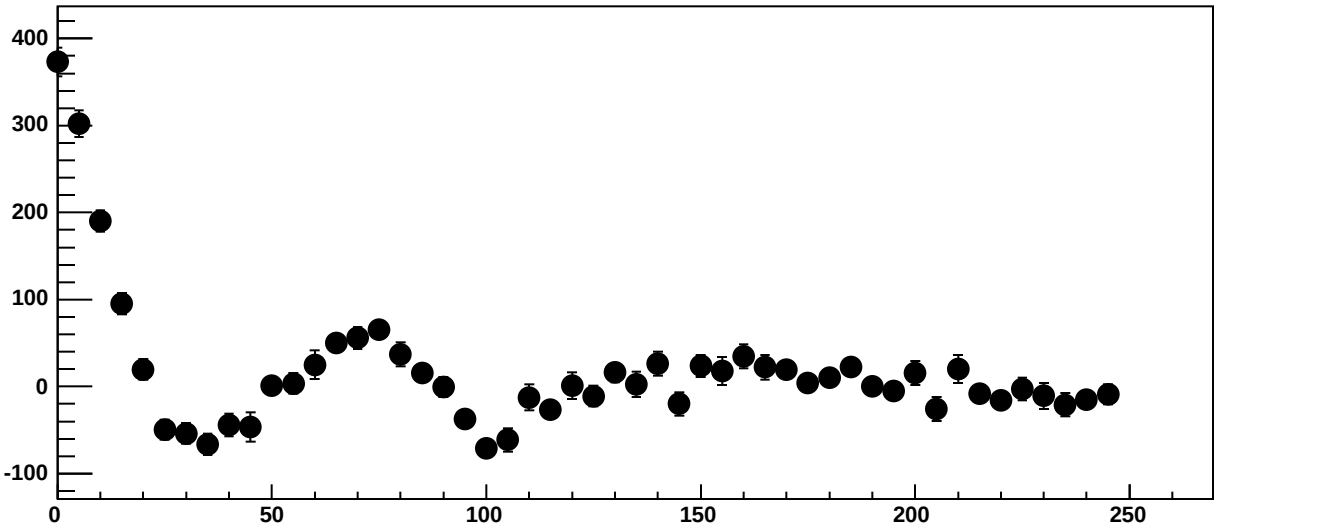
Chip 8, Channel 16, Enable 1, DAC=1600, ADC Mean vs Hold



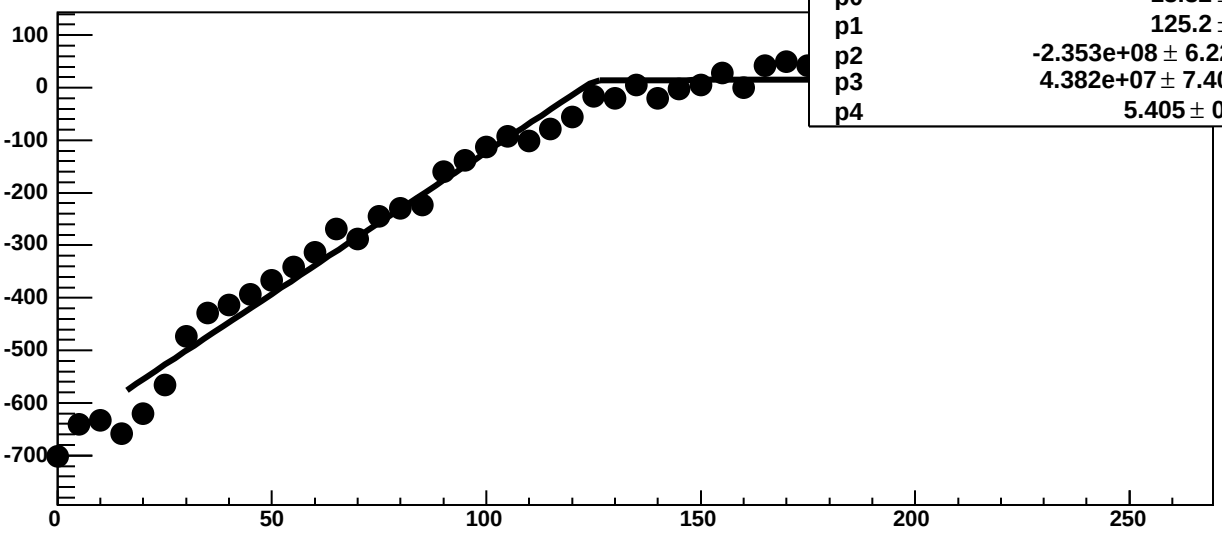
Chip 8, Channel 16, Enable 1, DAC=1600, ADC Noise vs Hold



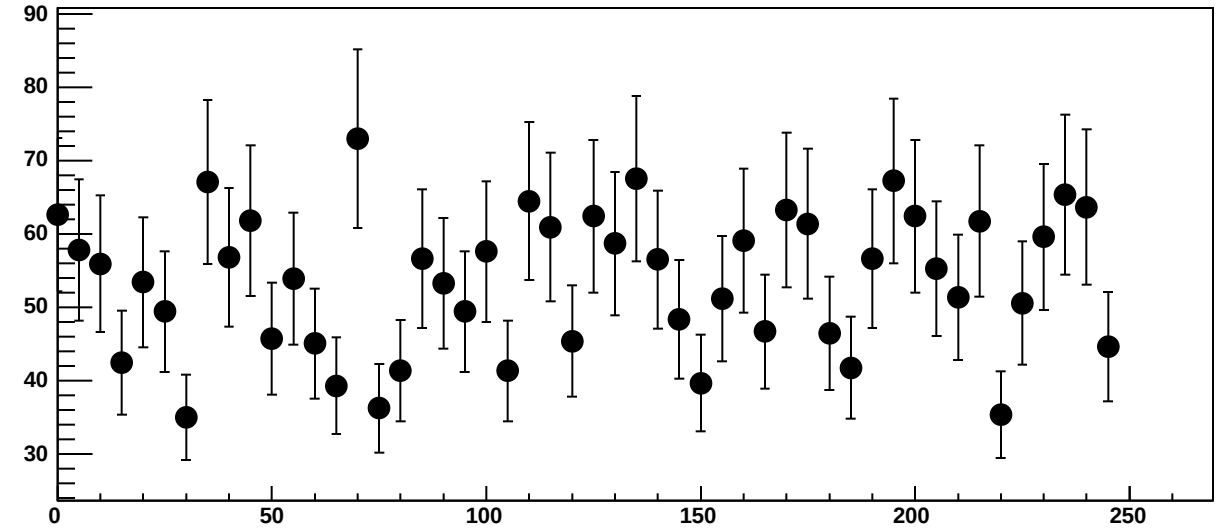
Chip 8, Channel 16, Enable 1, DAC=1600, ADC Residuals vs Hold



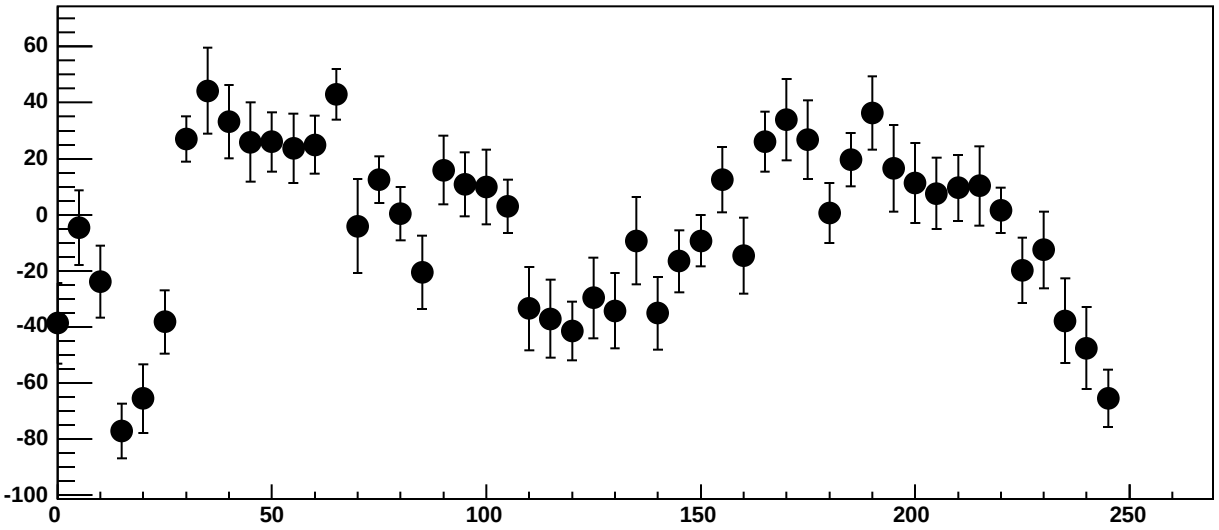
Chip 8, Channel 16, Enable 2, DAC=1600, ADC Mean vs Hold



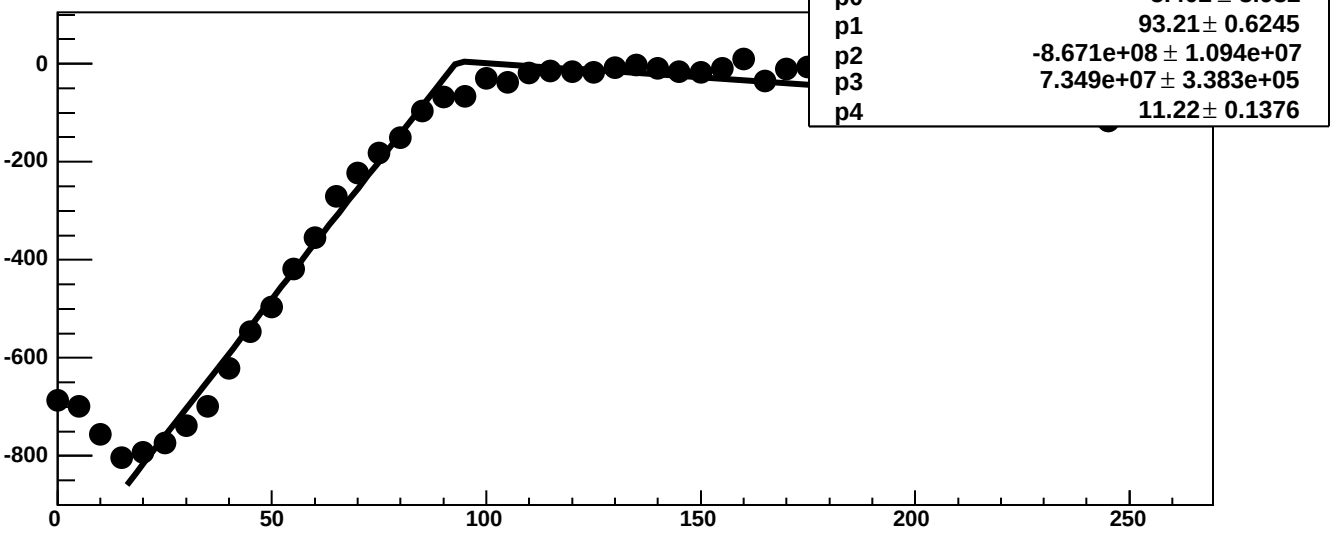
Chip 8, Channel 16, Enable 2, DAC=1600, ADC Noise vs Hold



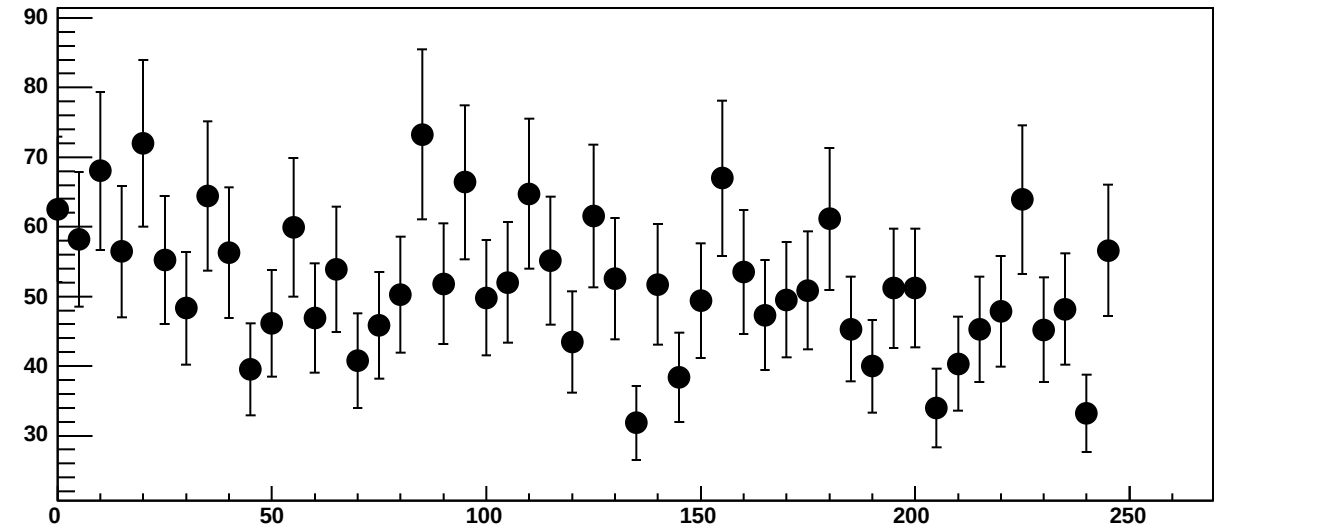
Chip 8, Channel 16, Enable 2, DAC=1600, ADC Residuals vs Hold



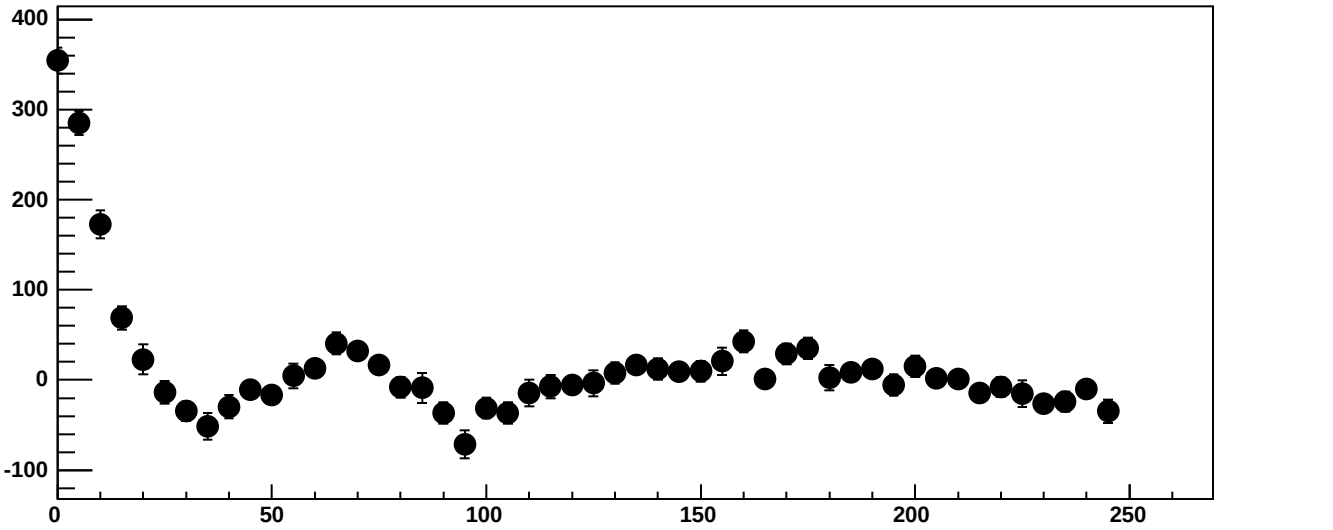
Chip 8, Channel 16, Enable 3, DAC=1600, ADC Mean vs Hold



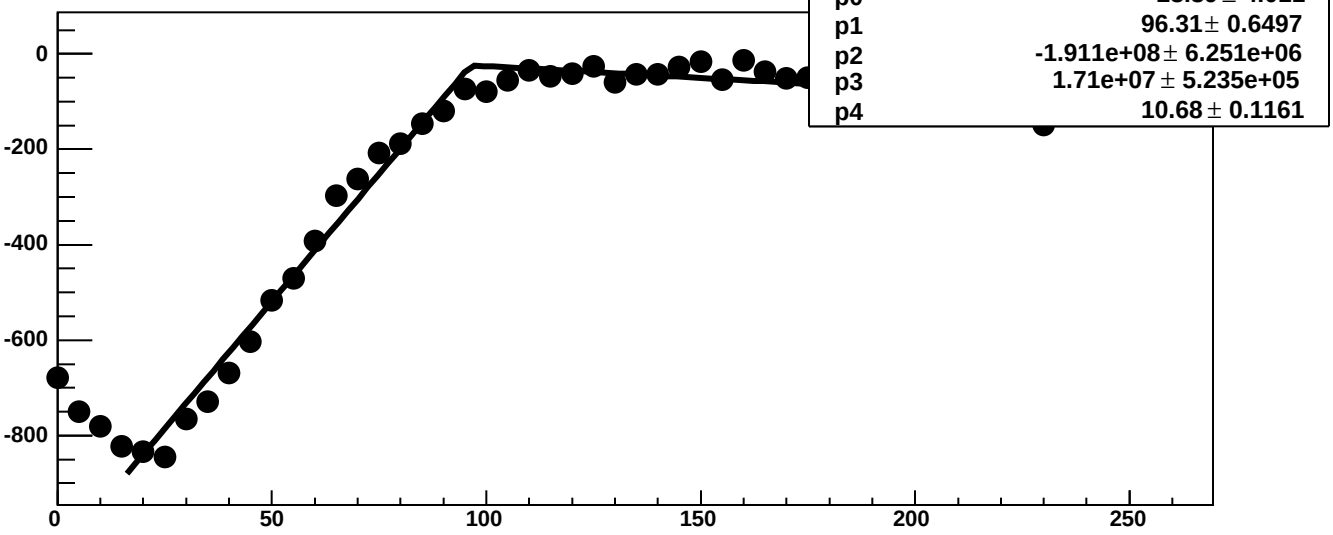
Chip 8, Channel 16, Enable 3, DAC=1600, ADC Noise vs Hold



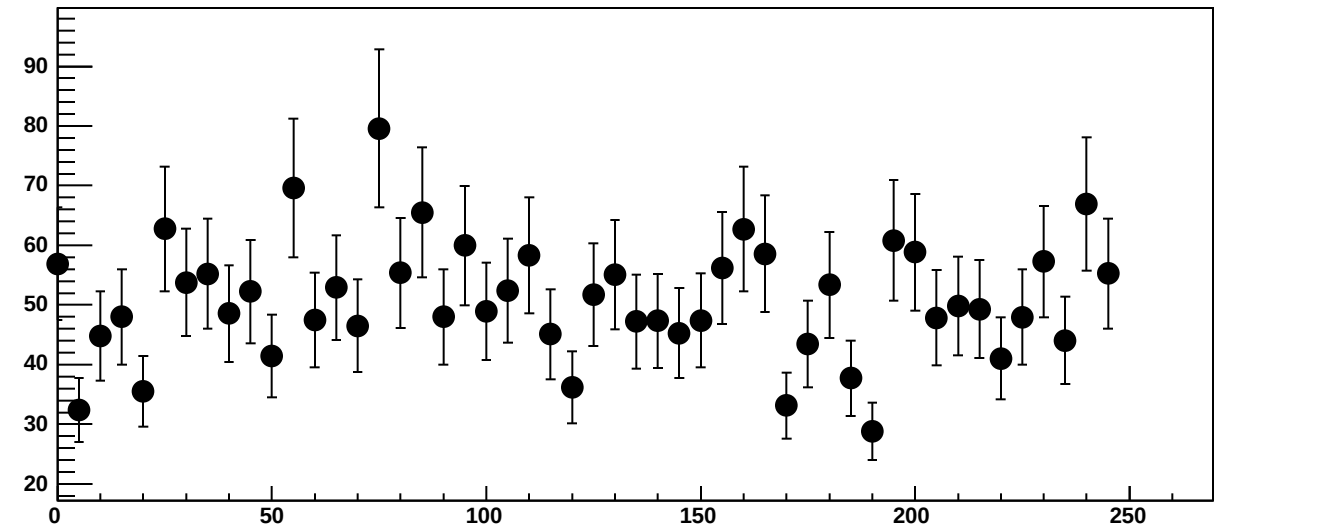
Chip 8, Channel 16, Enable 3, DAC=1600, ADC Residuals vs Hold



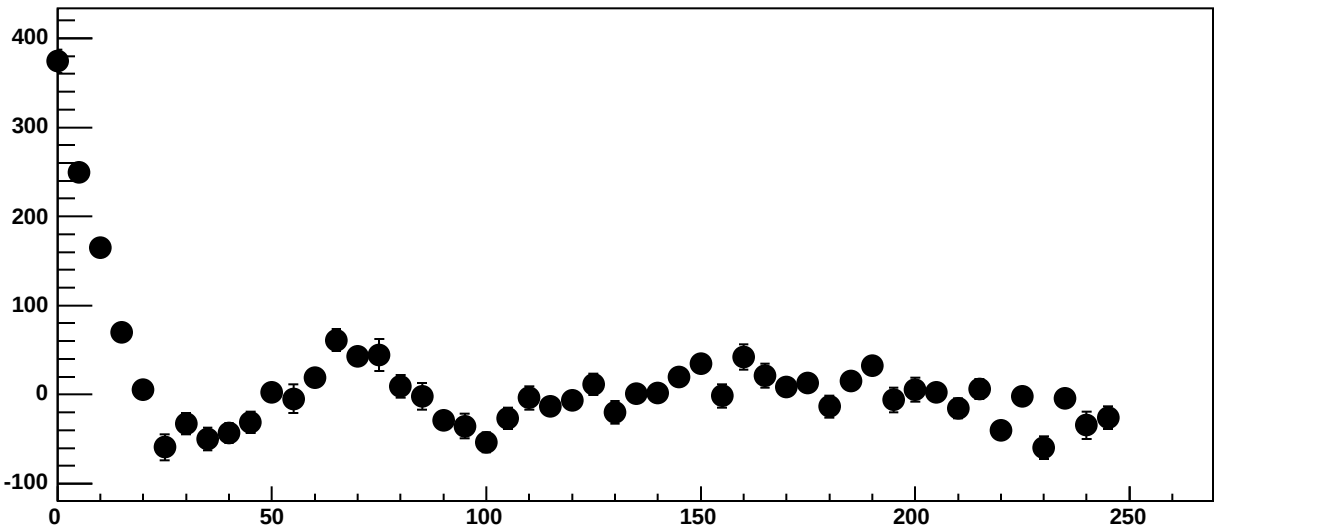
Chip 8, Channel 16, Enable 4, DAC=1600, ADC Mean vs Hold

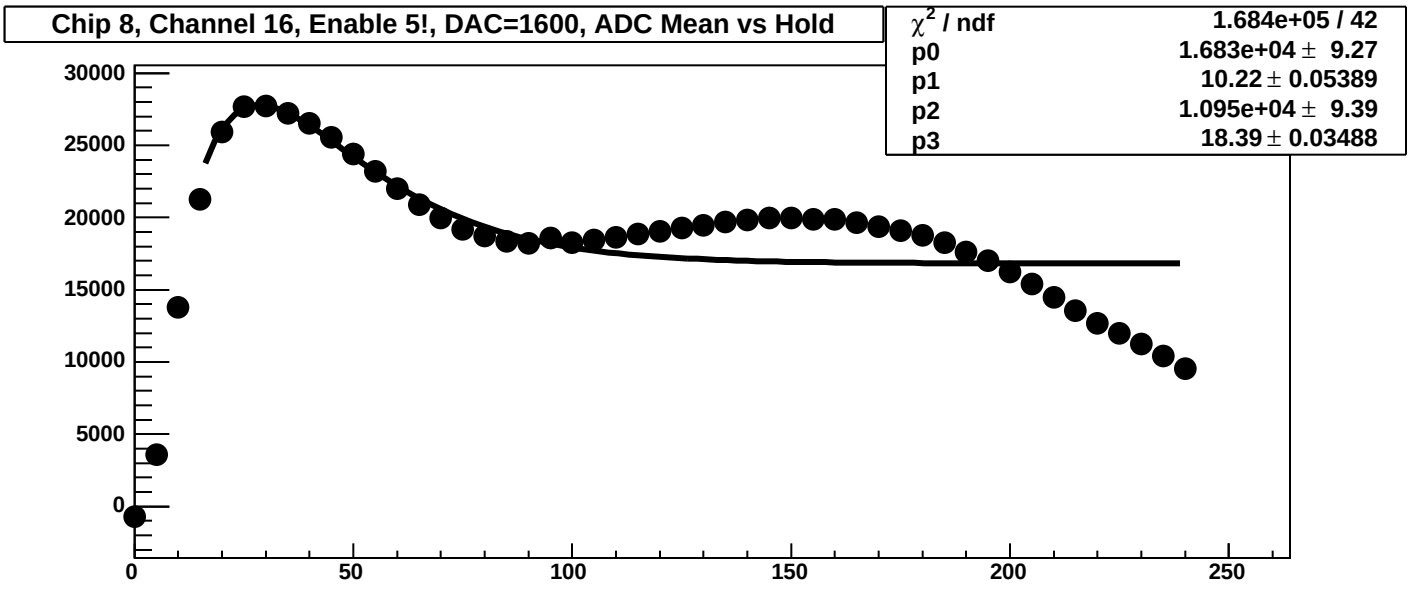


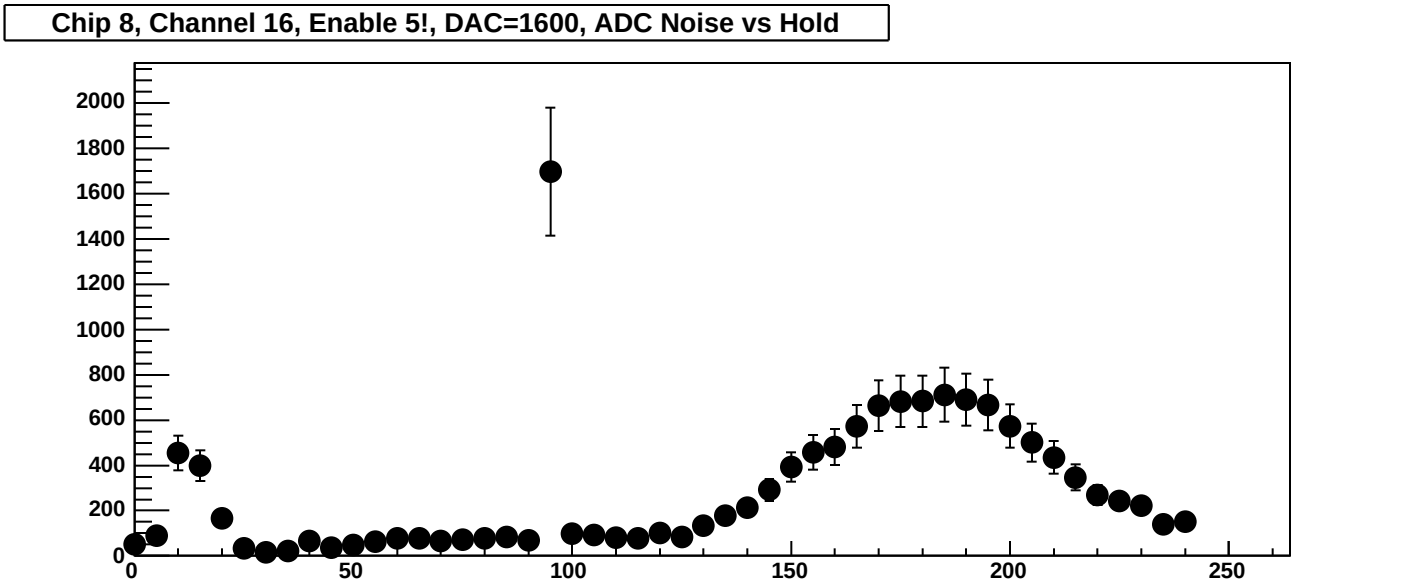
Chip 8, Channel 16, Enable 4, DAC=1600, ADC Noise vs Hold

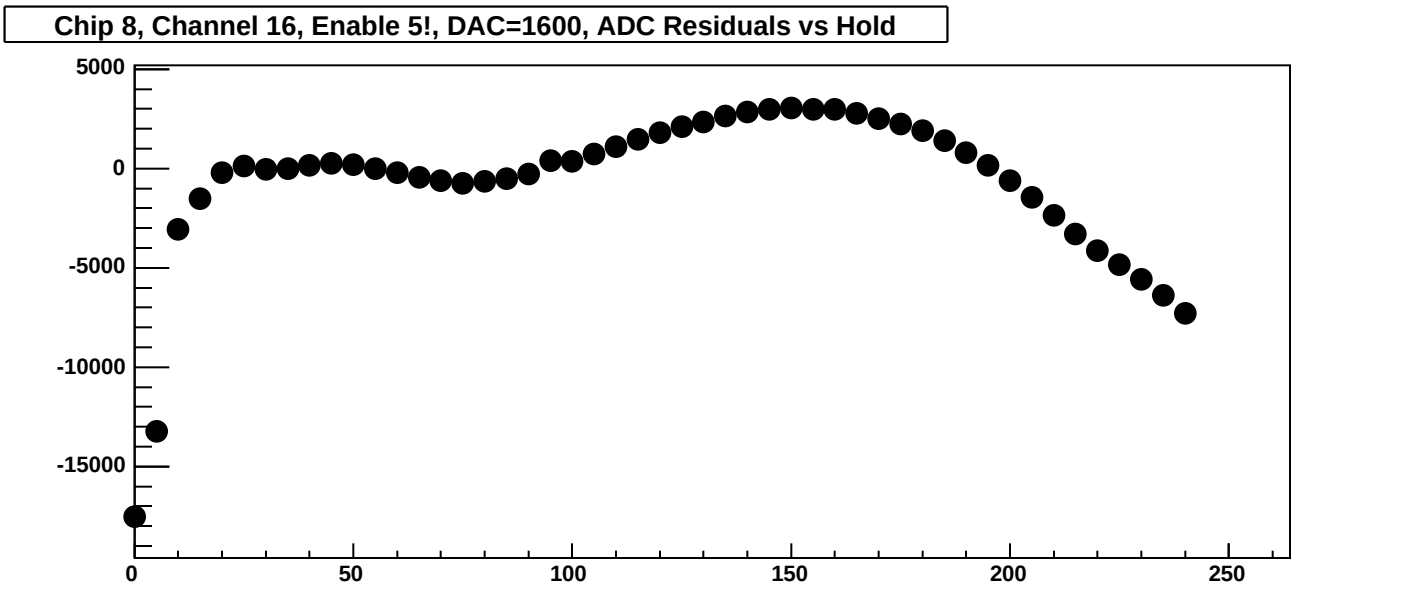


Chip 8, Channel 16, Enable 4, DAC=1600, ADC Residuals vs Hold

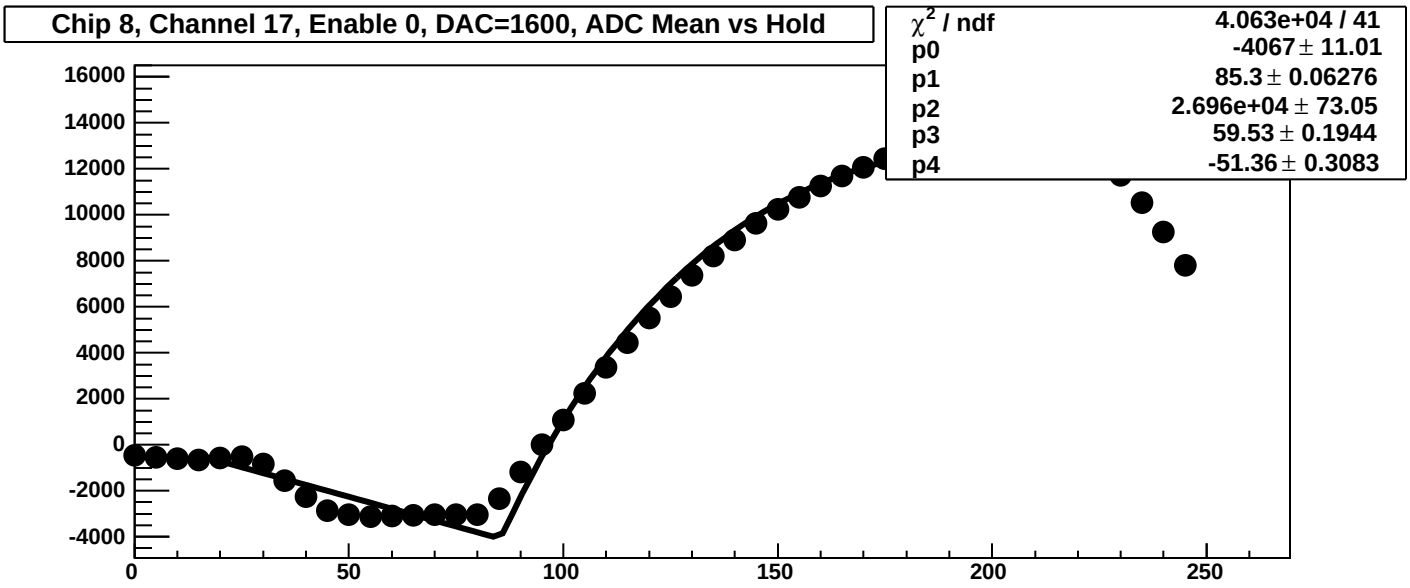



Chip 8, Channel 16, Enable 5!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.684e+05 / 42
p0 1.683e+04 ± 9.27
p1 10.22 ± 0.05389
p2 1.095e+04 ± 9.39
p3 18.39 ± 0.03488
30000
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

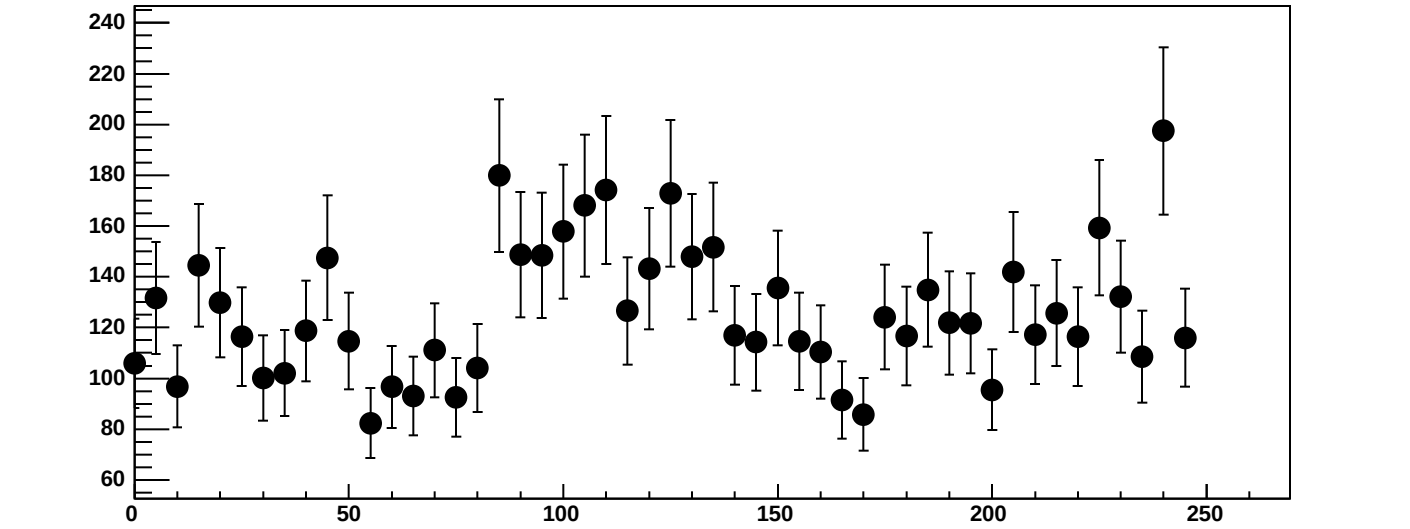

Chip 8, Channel 16, Enable 5!, DAC=1600, ADC Noise vs Hold
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 8, Channel 16, Enable 5!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
0
50
100
150
200
250

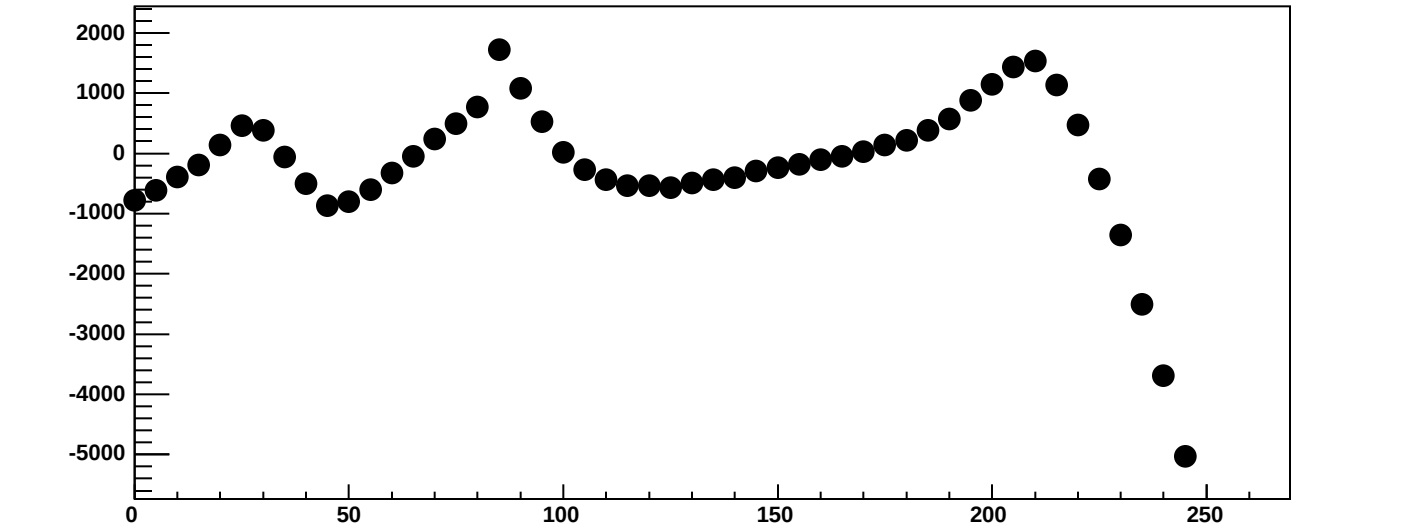
Chip 8, Channel 17, Enable 0, DAC=1600, ADC Mean vs Hold

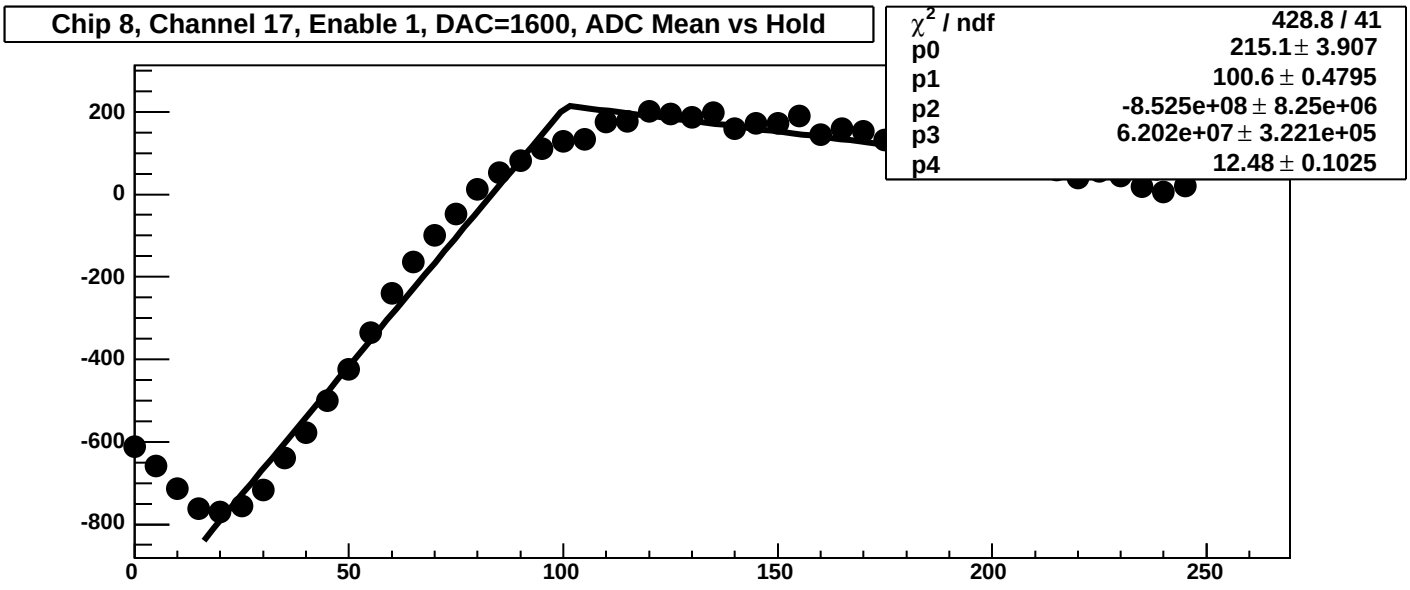


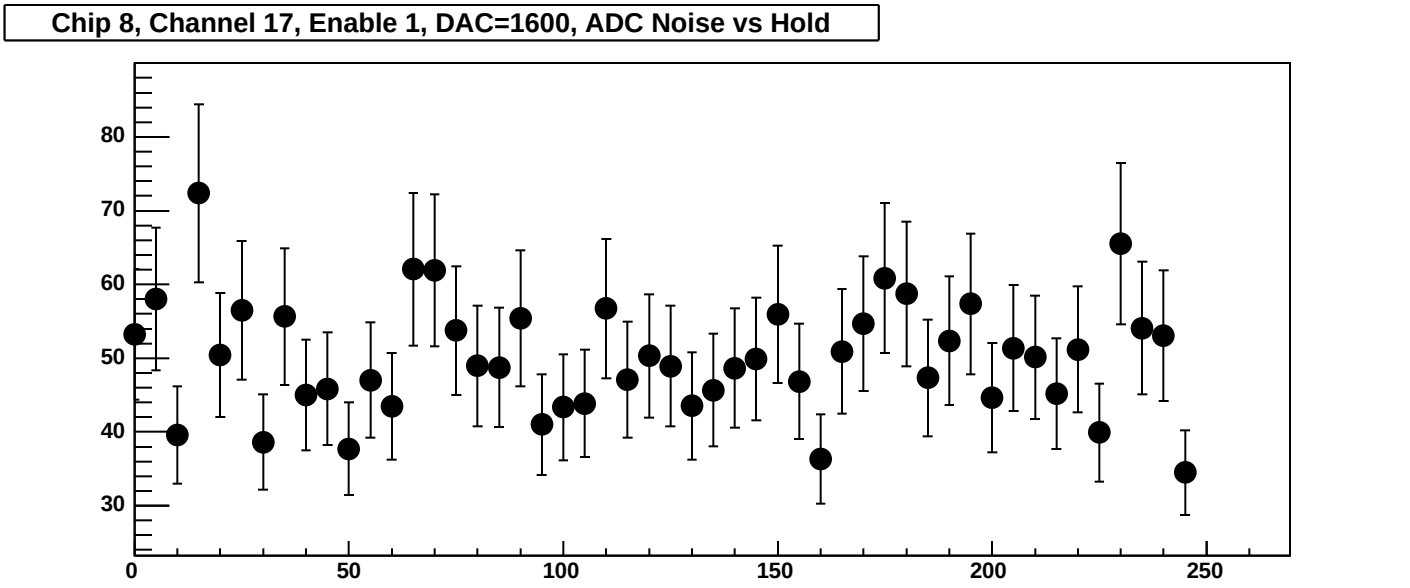
Chip 8, Channel 17, Enable 0, DAC=1600, ADC Noise vs Hold

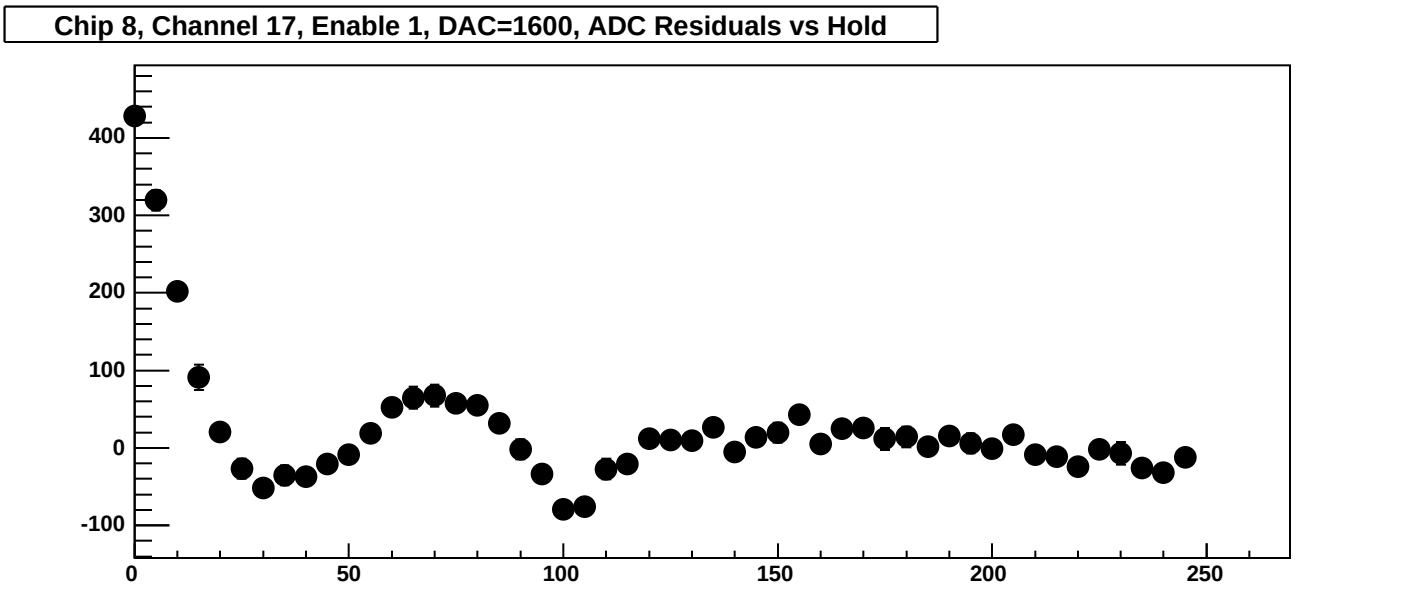


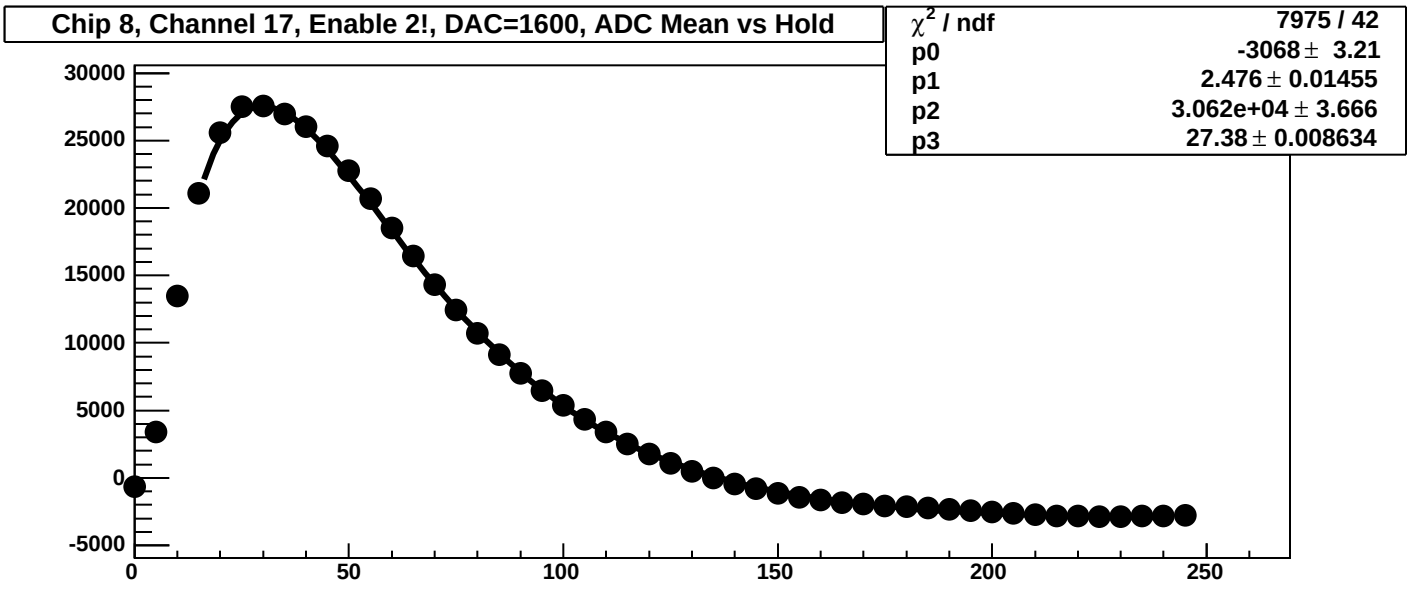
Chip 8, Channel 17, Enable 0, DAC=1600, ADC Residuals vs Hold

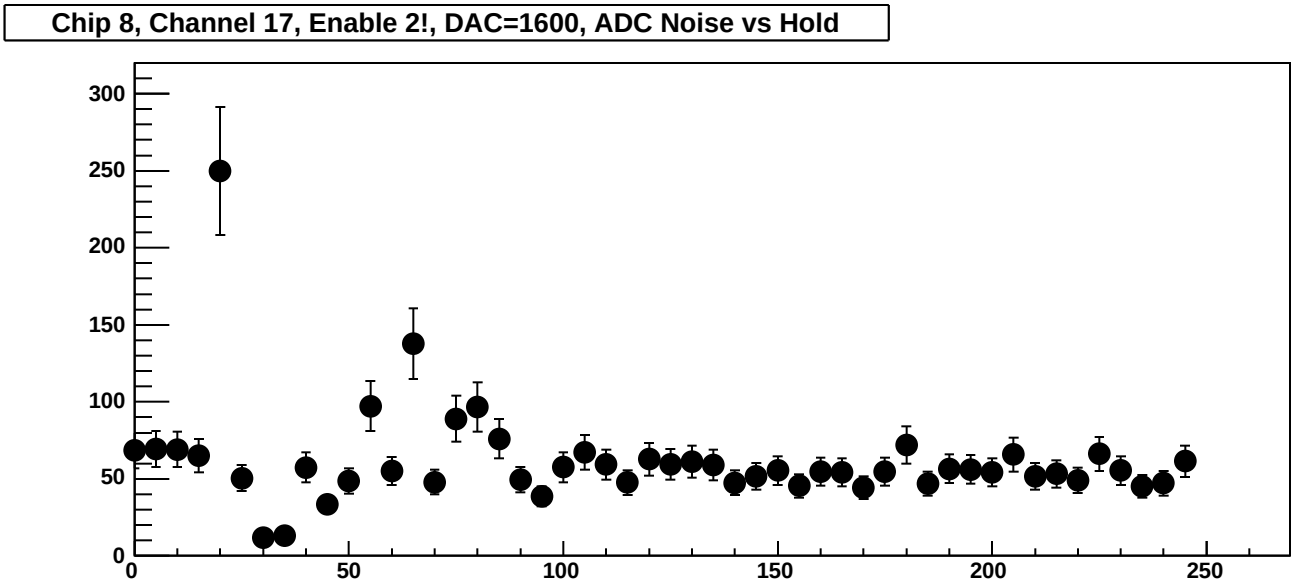


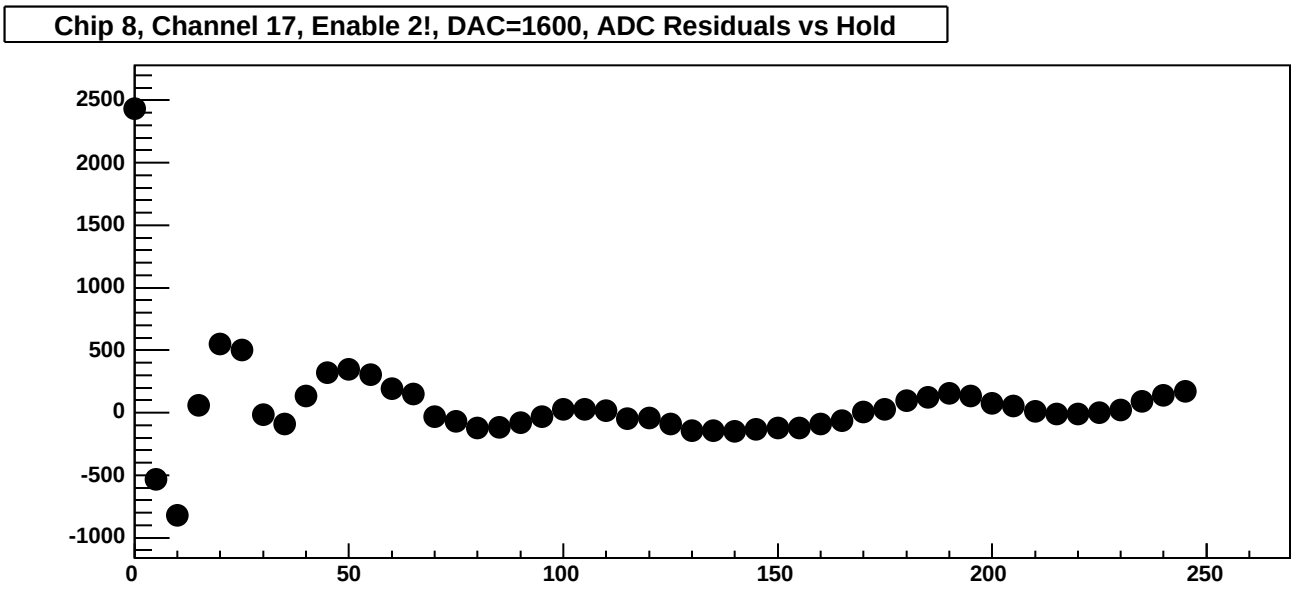

Chip 8, Channel 17, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 428.8 / 41
p0 215.1 ± 3.907
p1 100.6 ± 0.4795
p2 -8.525e+08 ± 8.25e+06
p3 6.202e+07 ± 3.221e+05
p4 12.48 ± 0.1025
200
0
-200
-400
-600
-800
0
50
100
150
200
250


Chip 8, Channel 17, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

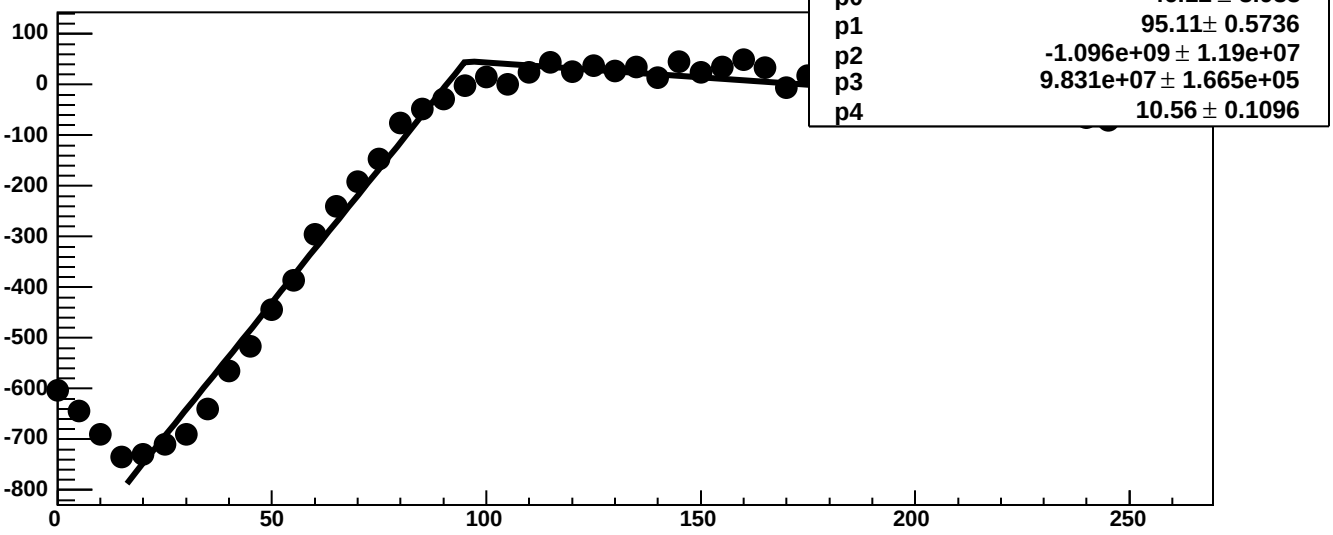

Chip 8, Channel 17, Enable 1, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 8, Channel 17, Enable 2!, DAC=1600, ADC Mean vs Hold
χ² / ndf 7975 / 42
p0 -3068 ± 3.21
p1 2.476 ± 0.01455
p2 3.062e+04 ± 3.666
p3 27.38 ± 0.008634
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

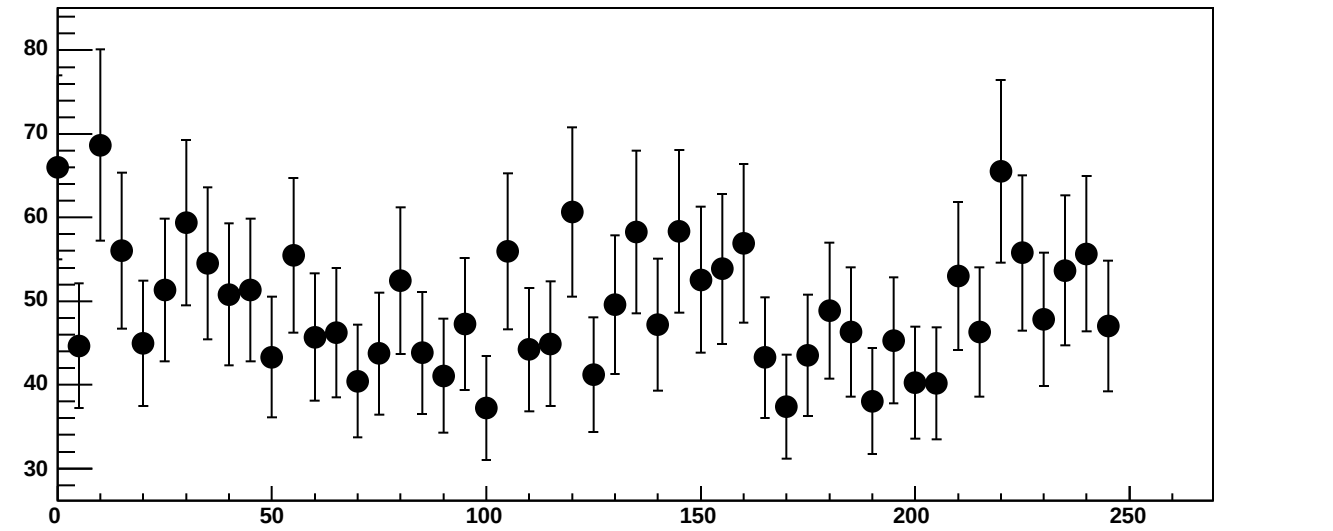

Chip 8, Channel 17, Enable 2!, DAC=1600, ADC Noise vs Hold
300
250
200
150
100
50
0
0
50
100
150
200
250


Chip 8, Channel 17, Enable 2!, DAC=1600, ADC Residuals vs Hold
2500
2000
1500
1000
500
0
-500
-1000
0
50
100
150
200
250

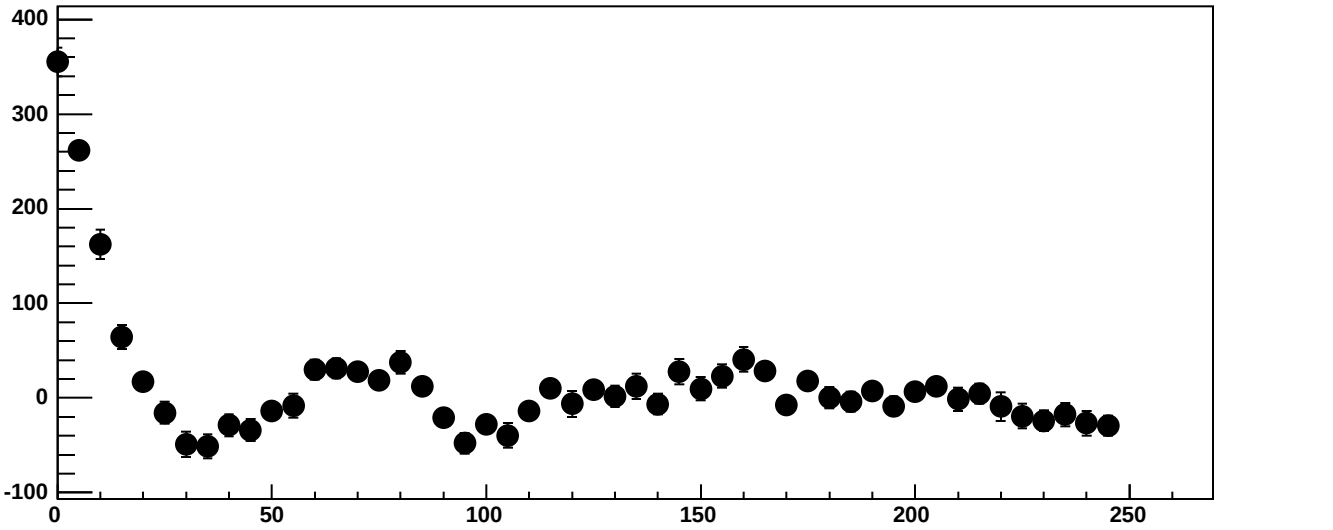
Chip 8, Channel 17, Enable 3, DAC=1600, ADC Mean vs Hold

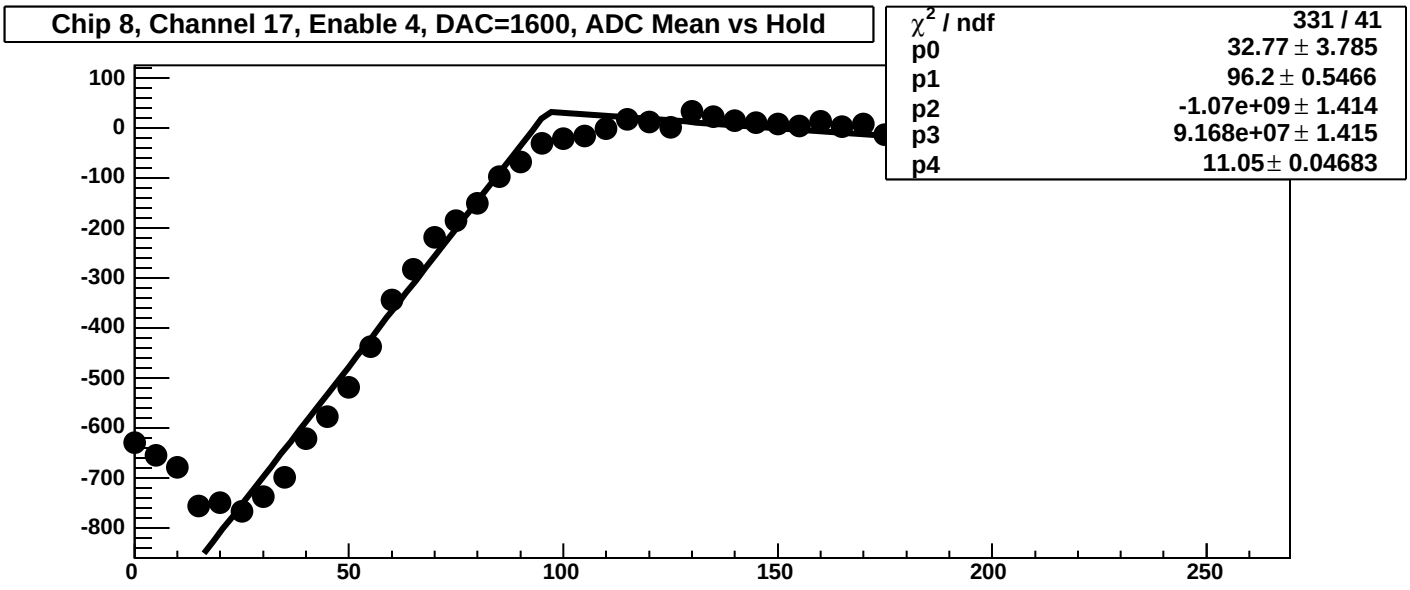


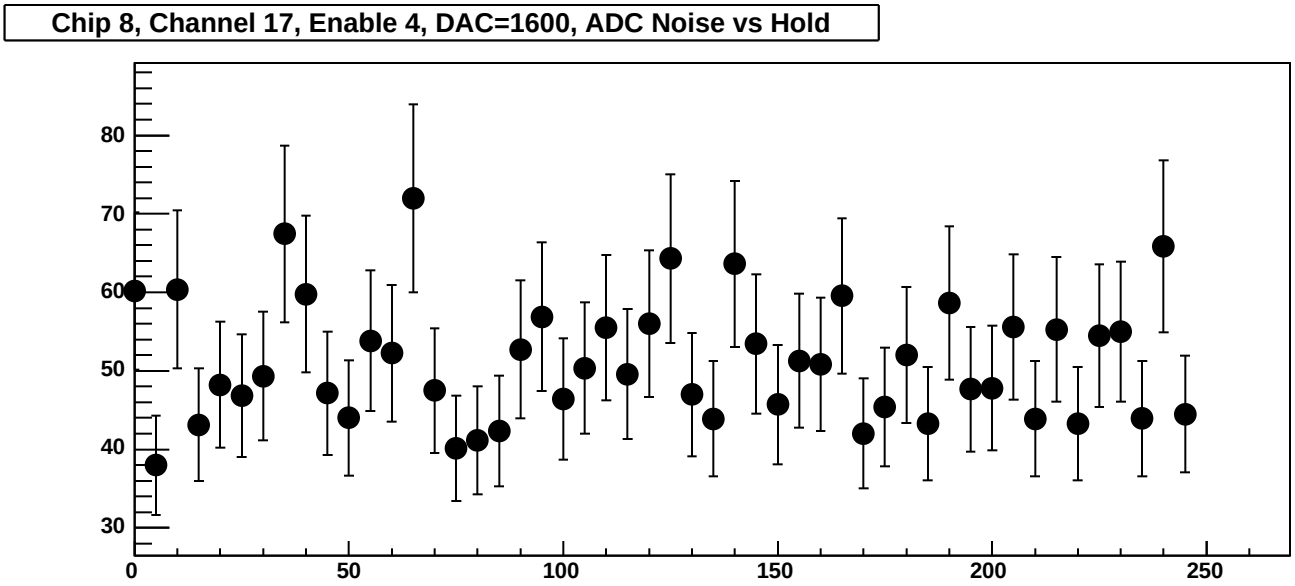
Chip 8, Channel 17, Enable 3, DAC=1600, ADC Noise vs Hold

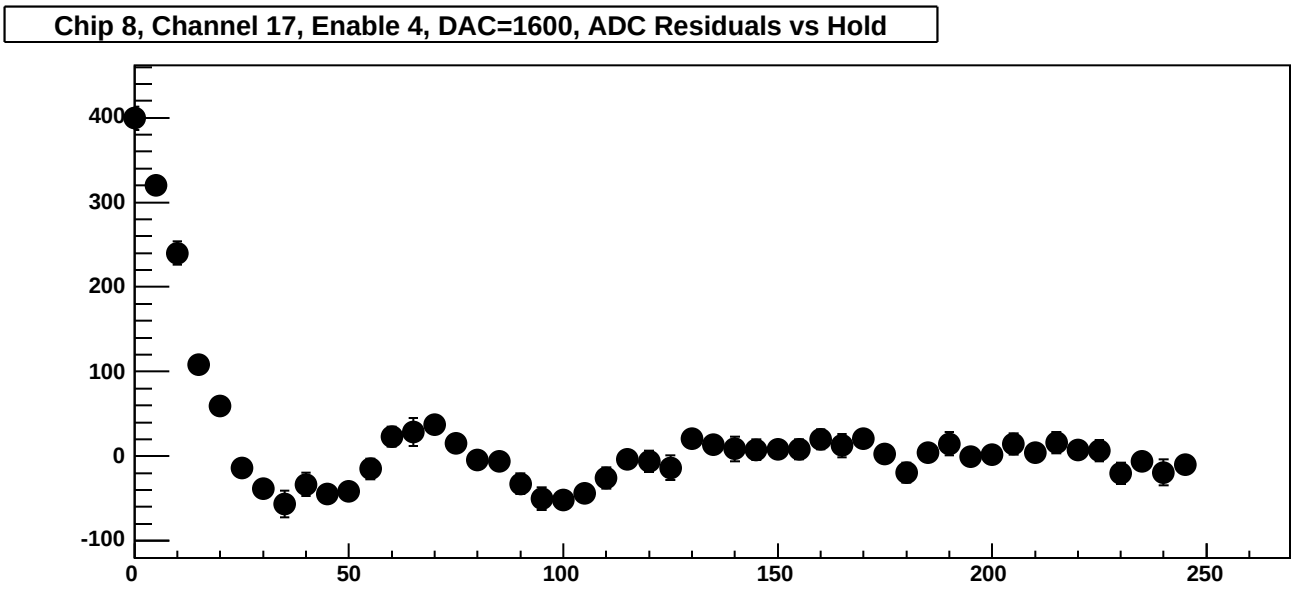


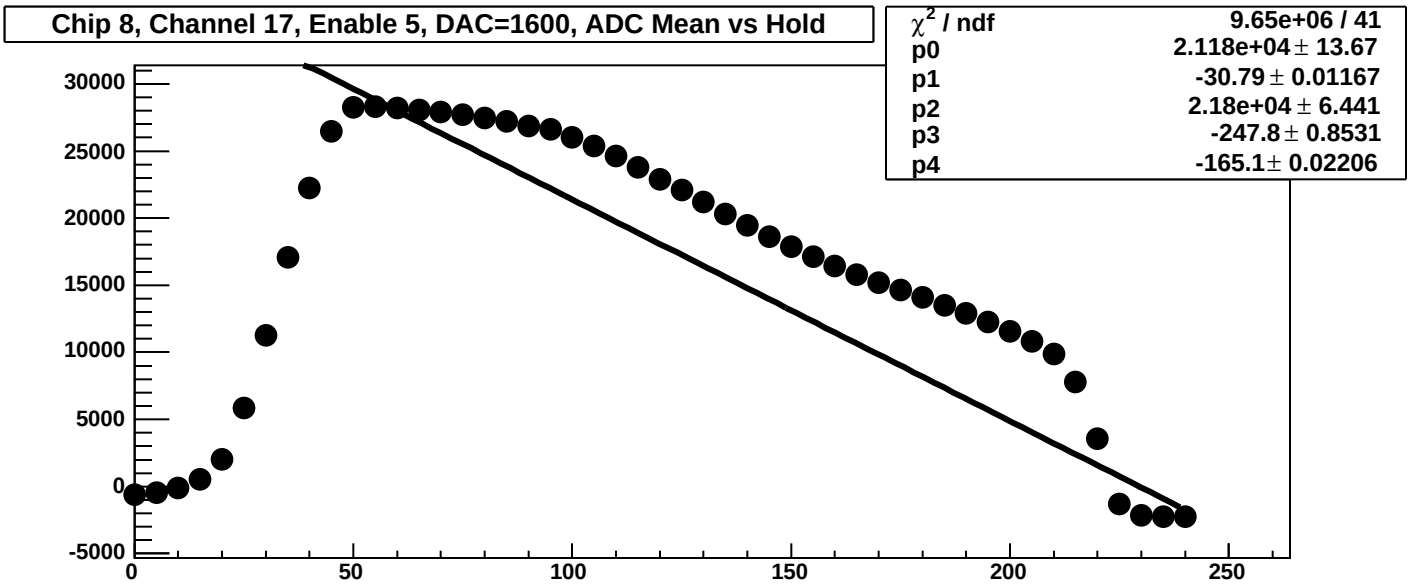
Chip 8, Channel 17, Enable 3, DAC=1600, ADC Residuals vs Hold

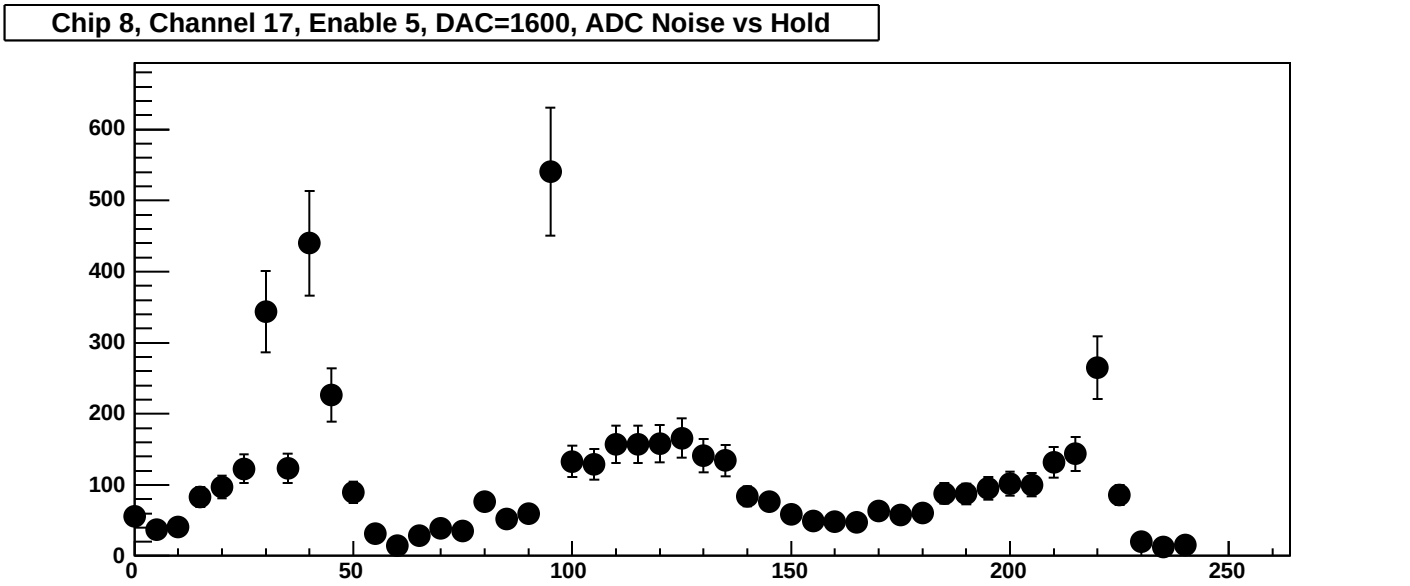


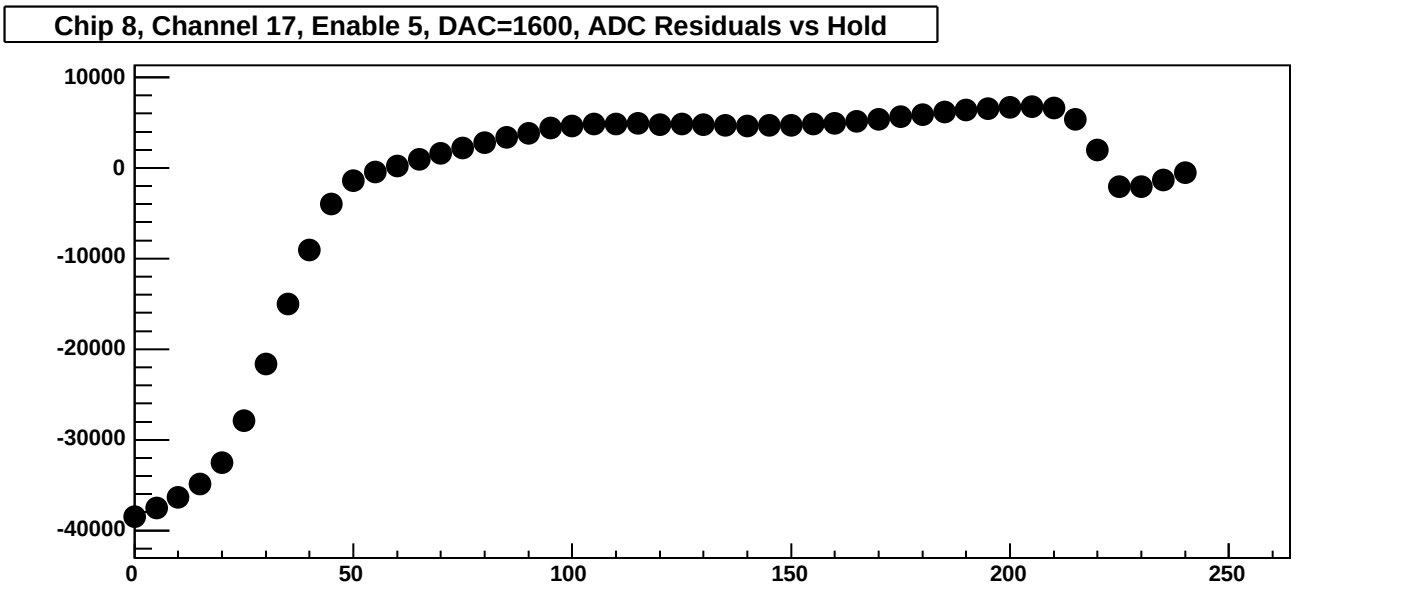

Chip 8, Channel 17, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 331 / 41
p0 32.77 ± 3.785
p1 96.2 ± 0.5466
p2 -1.07e+09 ± 1.414
p3 9.168e+07 ± 1.415
p4 11.05 ± 0.04683
100
0
-100
-200
-300
-400
-500
-600
-700
-800
0
50
100
150
200
250


Chip 8, Channel 17, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

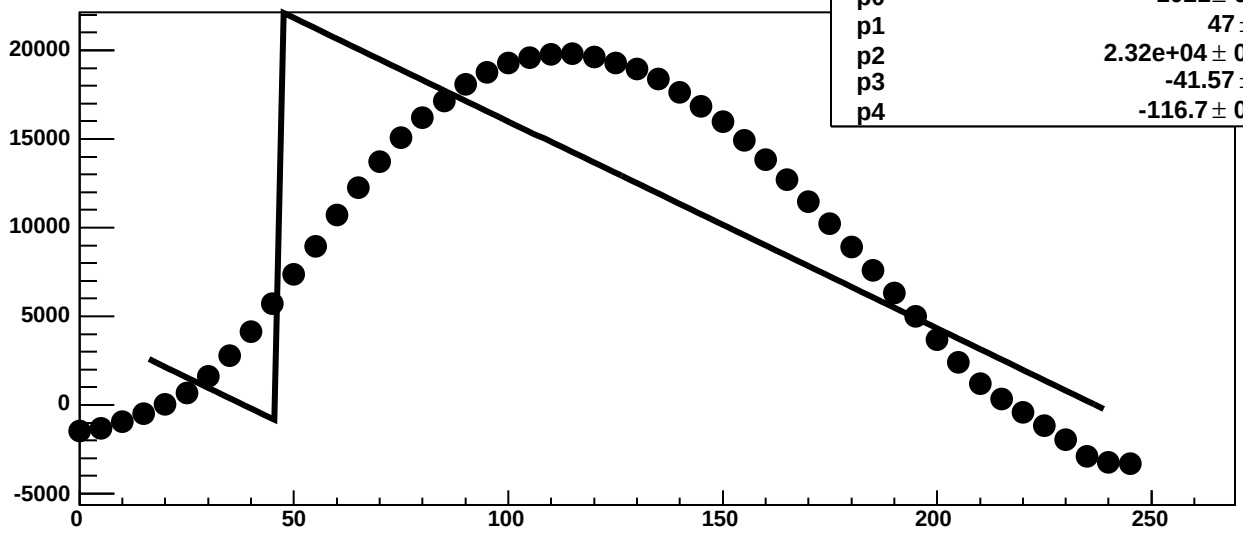

Chip 8, Channel 17, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 8, Channel 17, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 9.65e+06 / 41
p0 2.118e+04 ± 13.67
p1 -30.79 ± 0.01167
p2 2.18e+04 ± 6.441
p3 -247.8 ± 0.8531
p4 -165.1 ± 0.02206
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

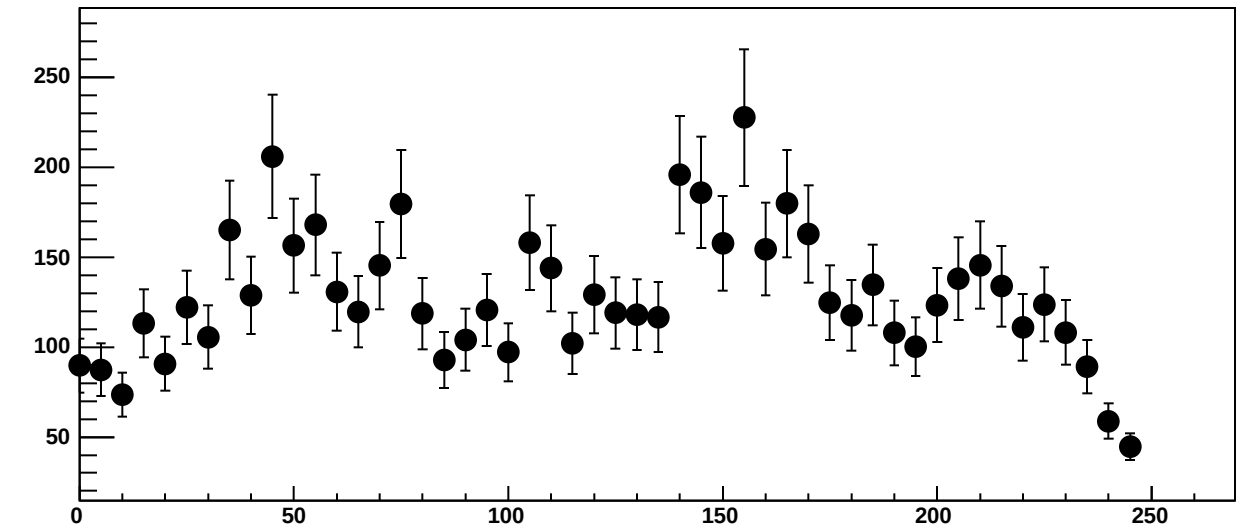

Chip 8, Channel 17, Enable 5, DAC=1600, ADC Noise vs Hold
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 8, Channel 17, Enable 5, DAC=1600, ADC Residuals vs Hold
10000
0
-10000
-20000
-30000
-40000
0
50
100
150
200
250

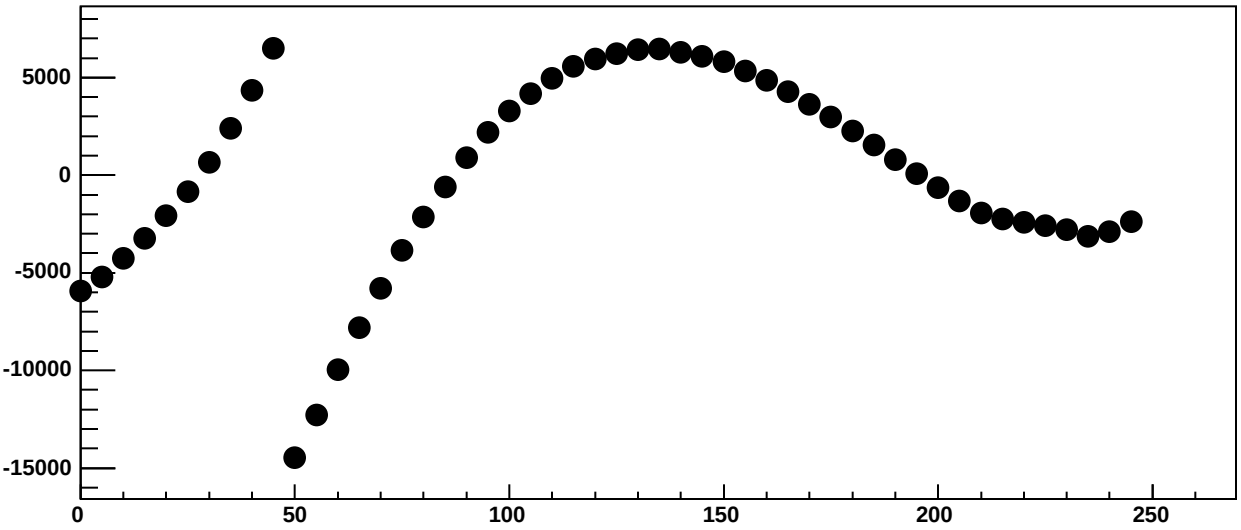
Chip 9, Channel 0, Enable 0, DAC=1600, ADC Mean vs Hold

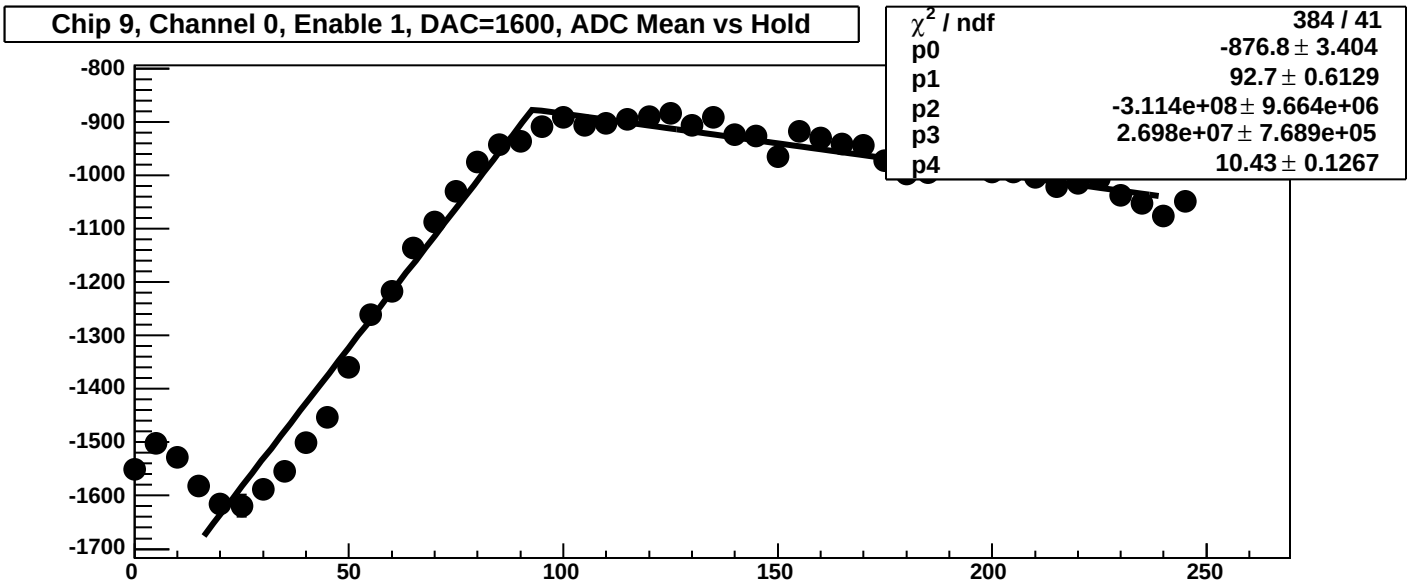


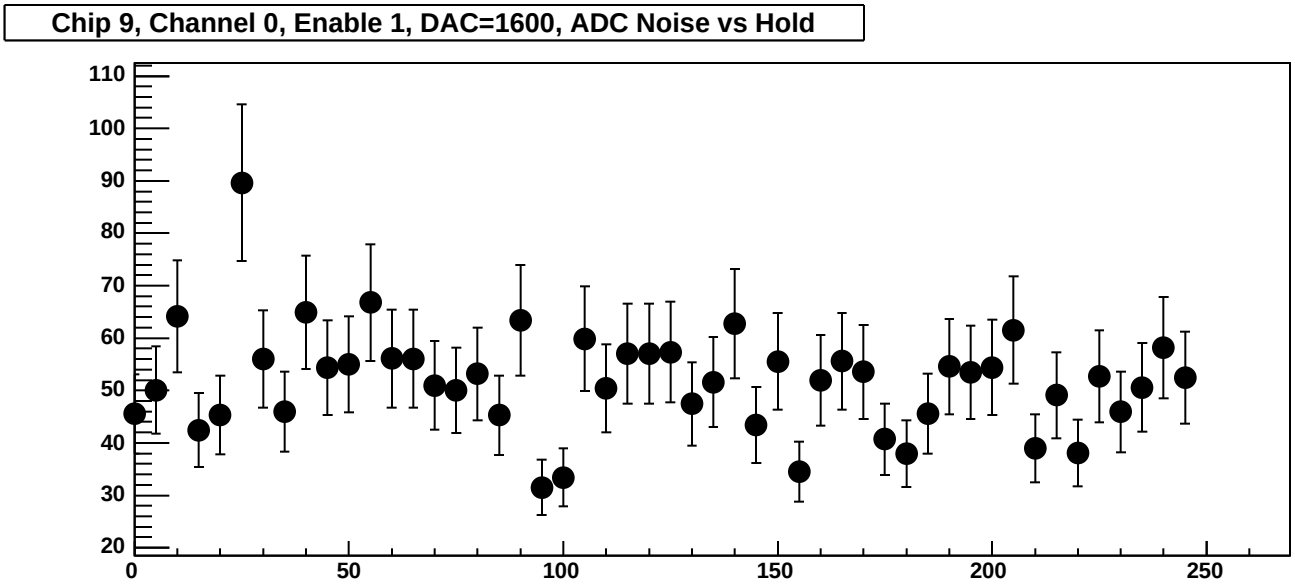
Chip 9, Channel 0, Enable 0, DAC=1600, ADC Noise vs Hold

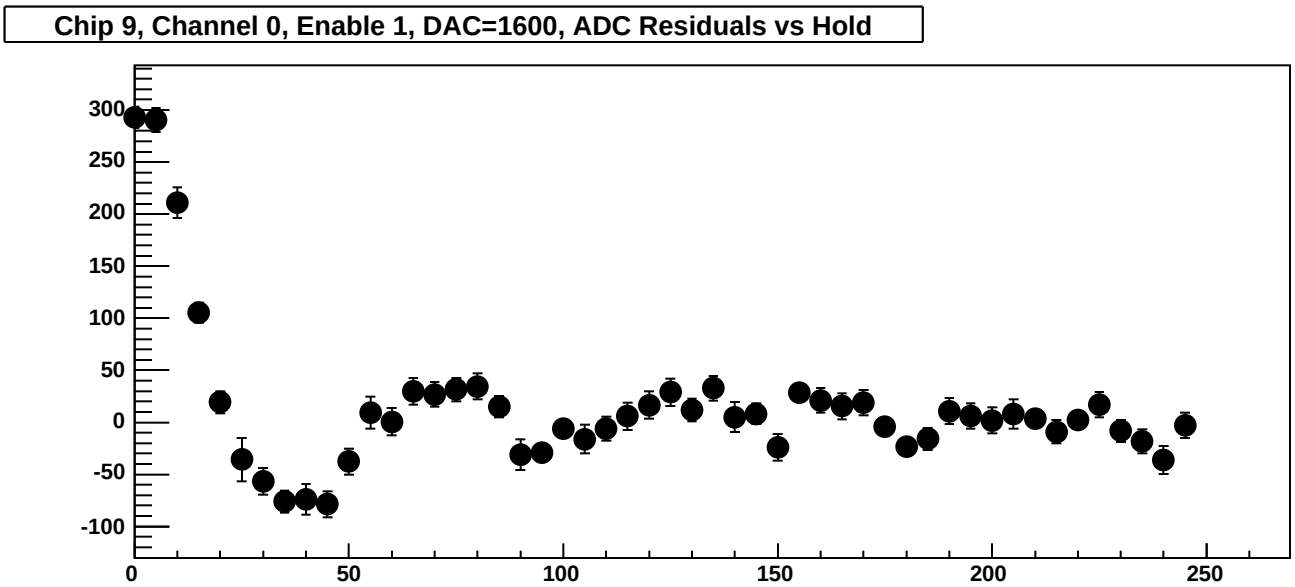


Chip 9, Channel 0, Enable 0, DAC=1600, ADC Residuals vs Hold

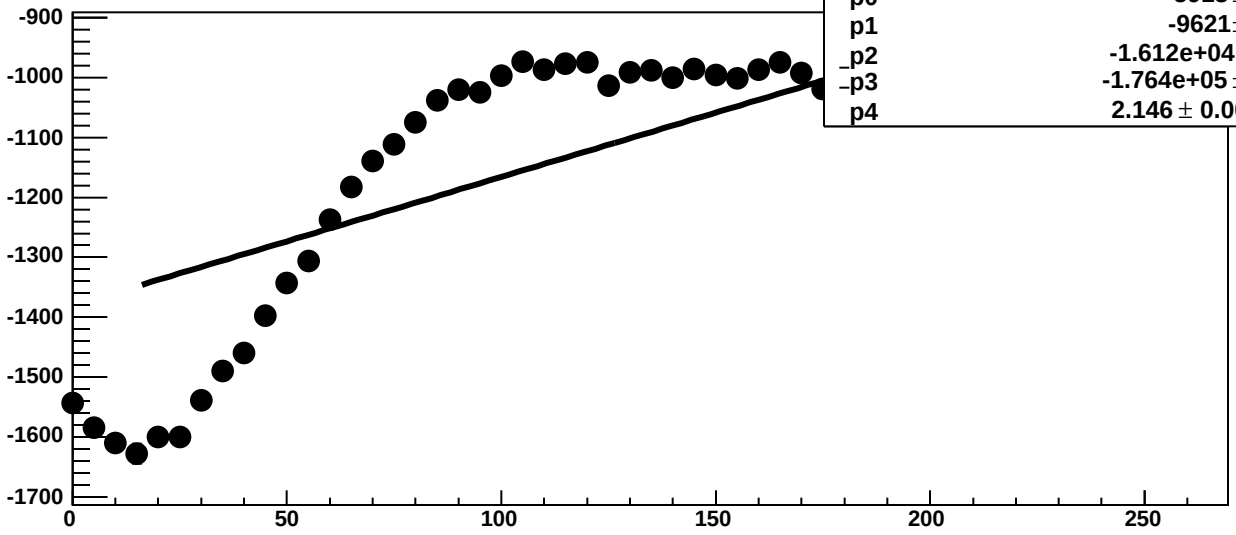



Chip 9, Channel 0, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 384 / 41
p0 -876.8 ± 3.404
p1 92.7 ± 0.6129
p2 -3.114e+08 ± 9.664e+06
p3 2.698e+07 ± 7.689e+05
p4 10.43 ± 0.1267
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
50
100
150
200
250

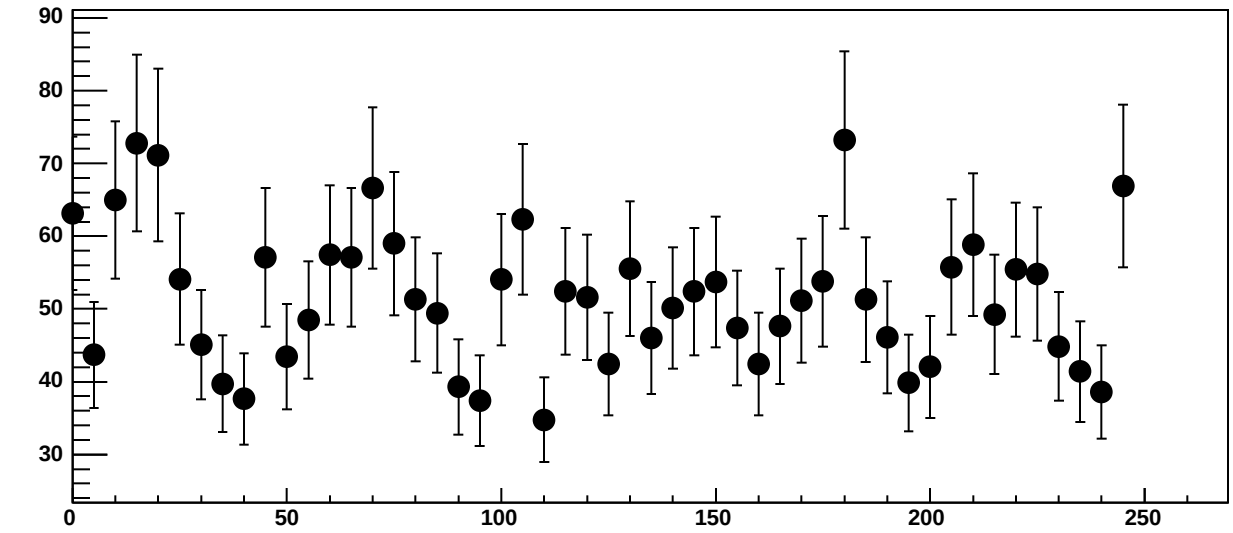

Chip 9, Channel 0, Enable 1, DAC=1600, ADC Noise vs Hold
110
100
90
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 9, Channel 0, Enable 1, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

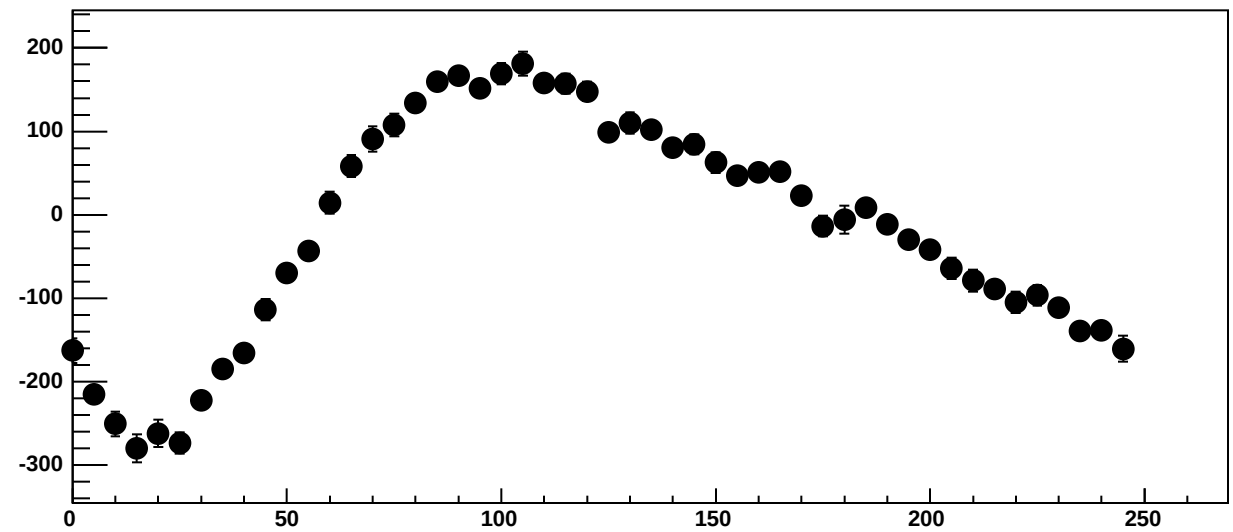
Chip 9, Channel 0, Enable 2, DAC=1600, ADC Mean vs Hold

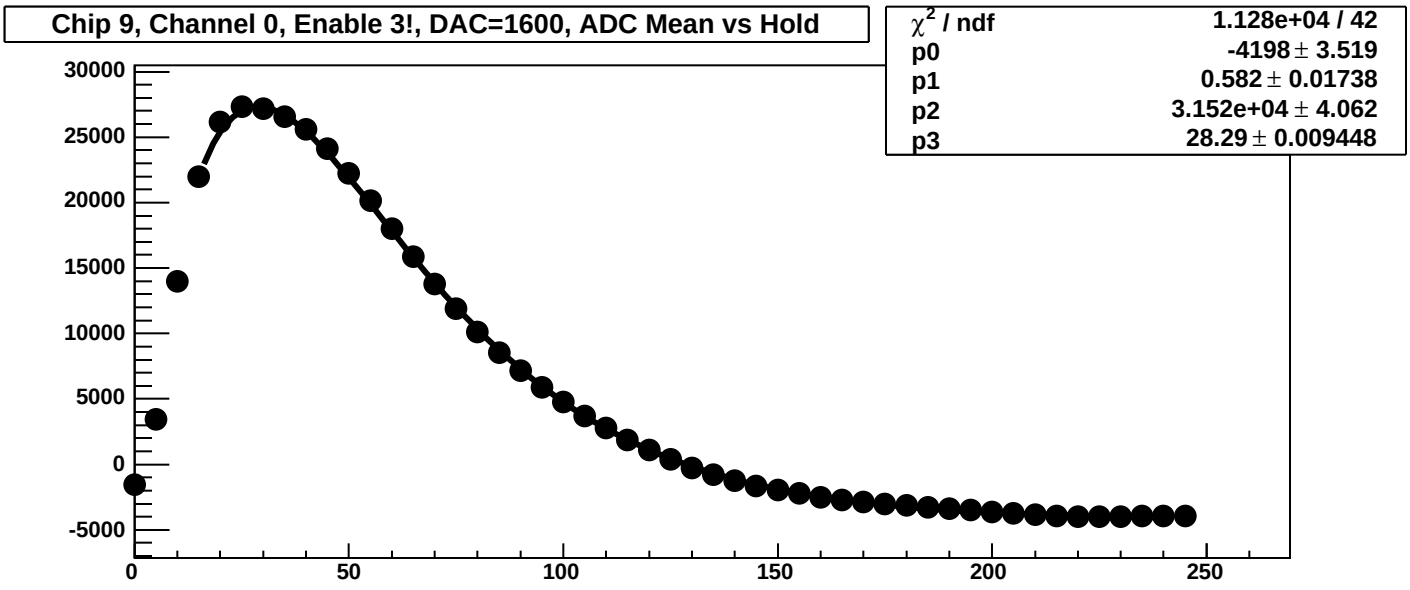


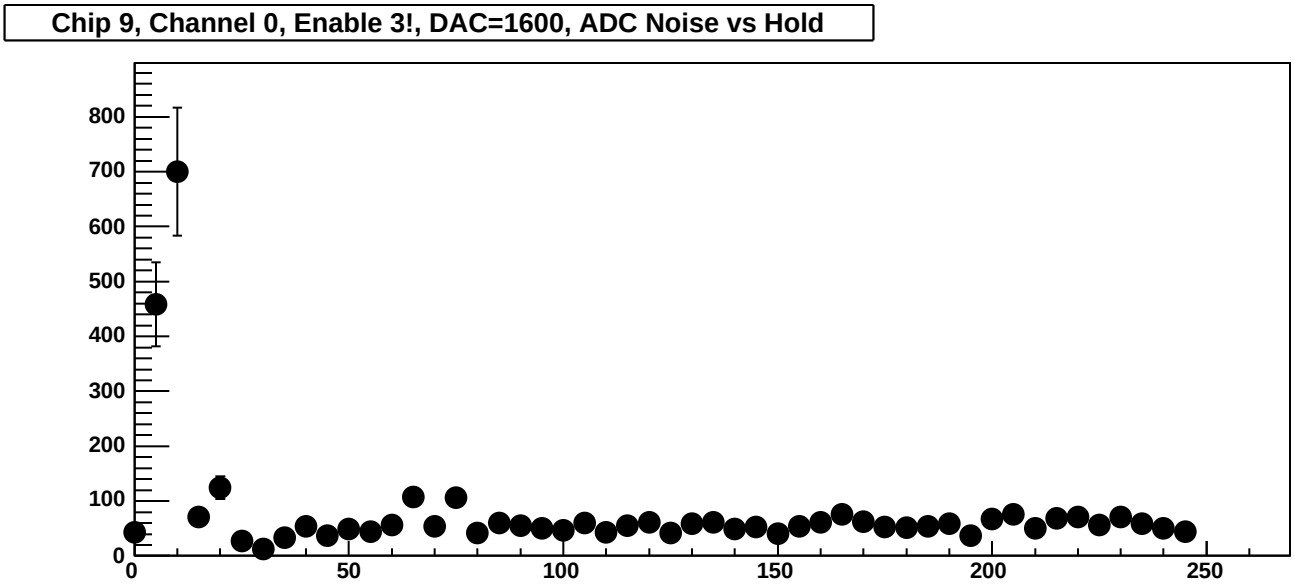
Chip 9, Channel 0, Enable 2, DAC=1600, ADC Noise vs Hold

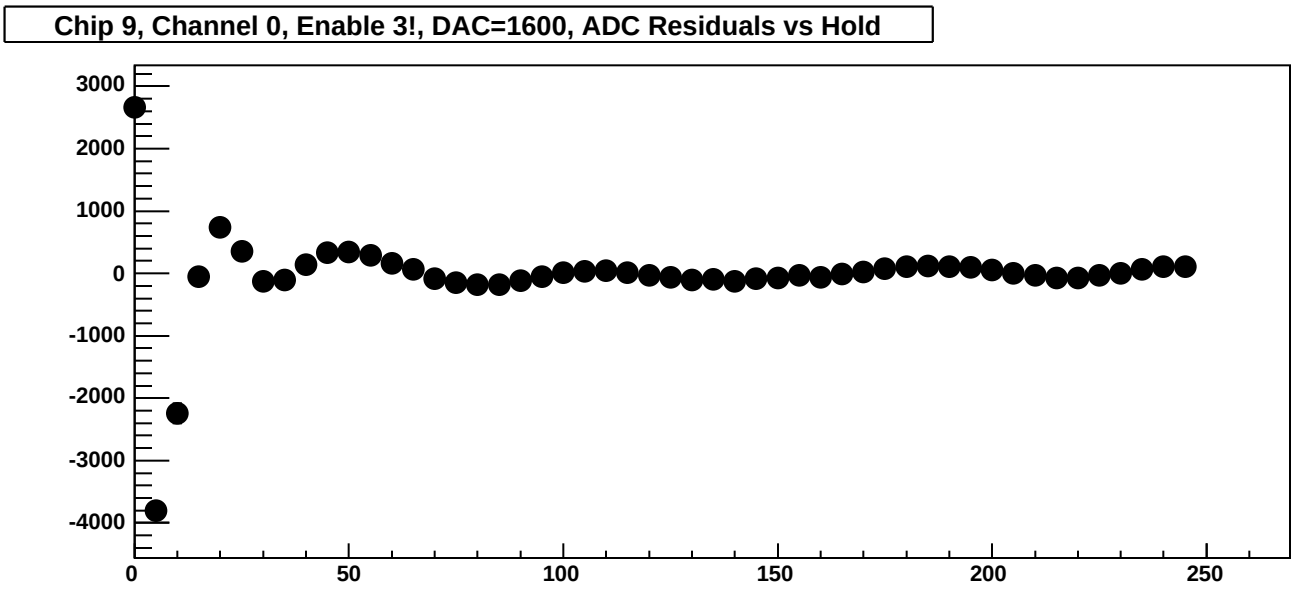


Chip 9, Channel 0, Enable 2, DAC=1600, ADC Residuals vs Hold

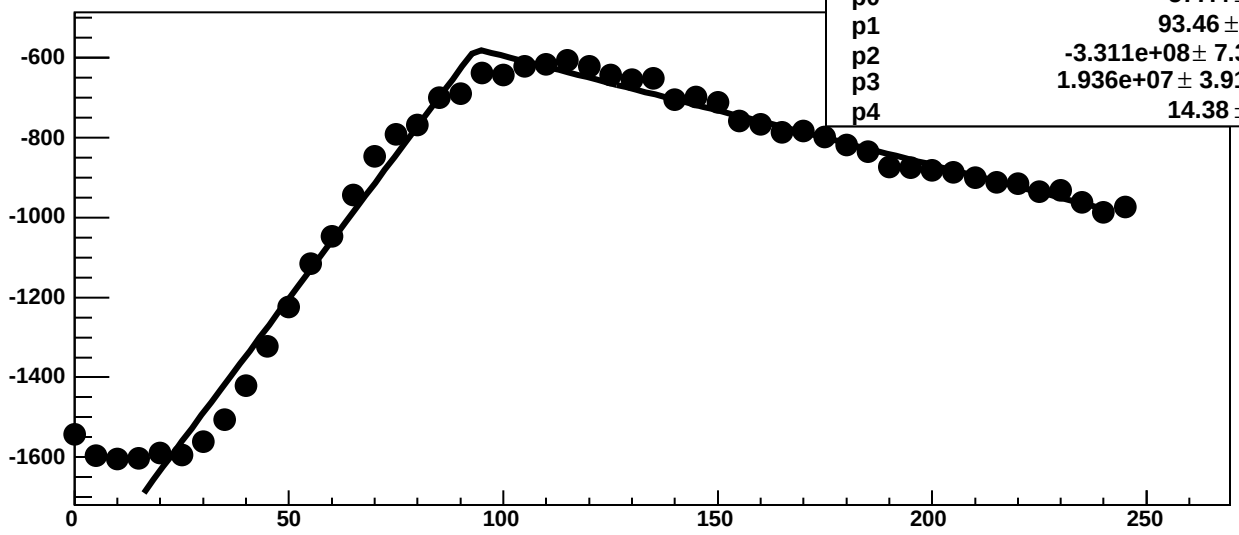



Chip 9, Channel 0, Enable 3!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.128e+04 / 42
p0 -4198 ± 3.519
p1 0.582 ± 0.01738
p2 3.152e+04 ± 4.062
p3 28.29 ± 0.009448
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

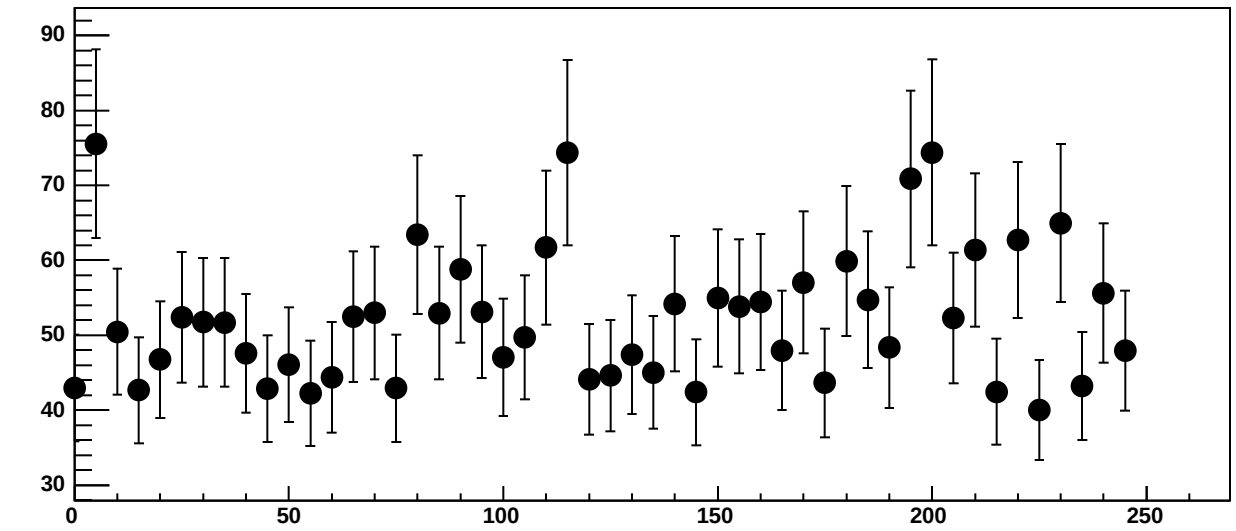

Chip 9, Channel 0, Enable 3!, DAC=1600, ADC Noise vs Hold
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 9, Channel 0, Enable 3!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

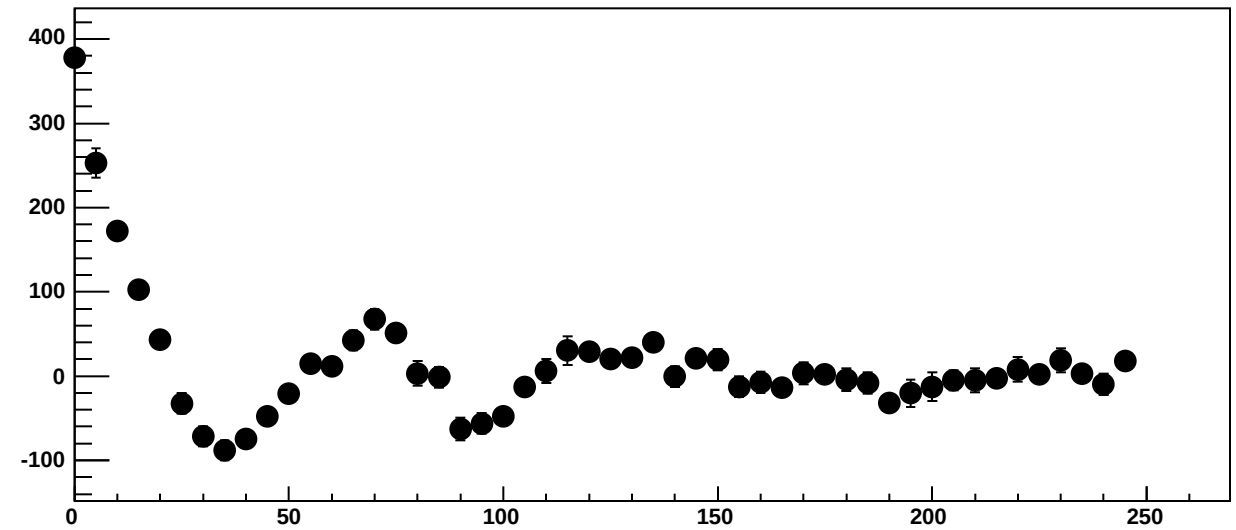
Chip 9, Channel 0, Enable 4, DAC=1600, ADC Mean vs Hold



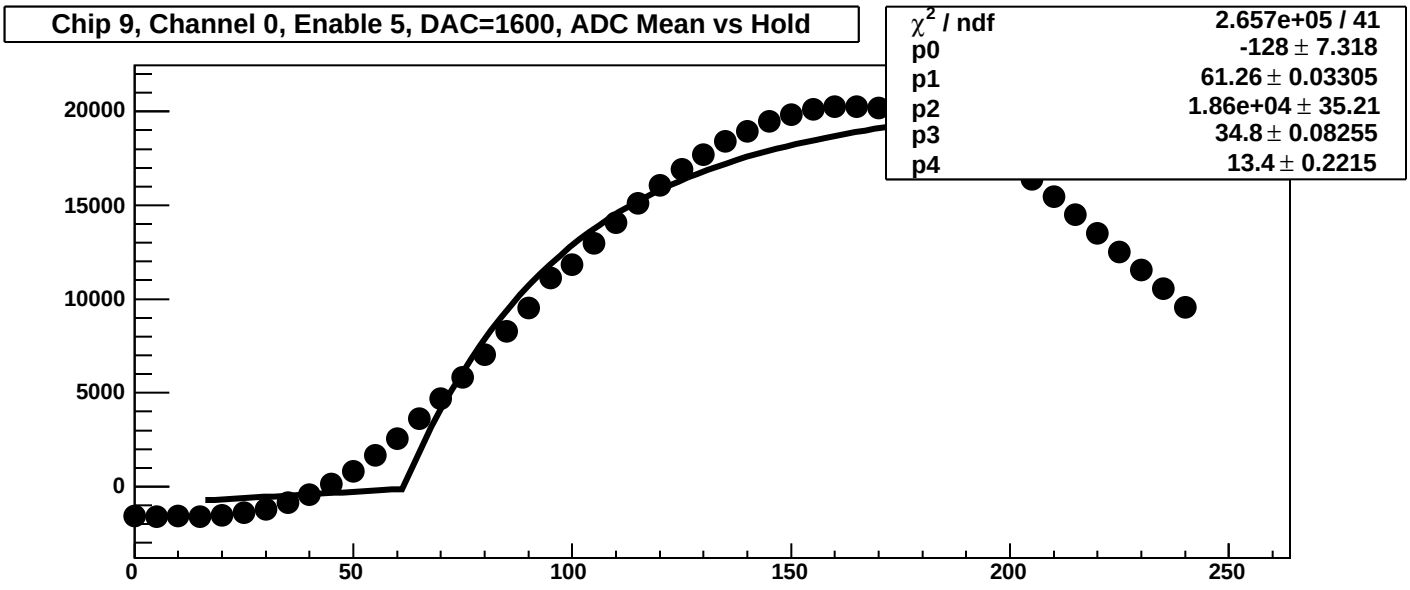
Chip 9, Channel 0, Enable 4, DAC=1600, ADC Noise vs Hold



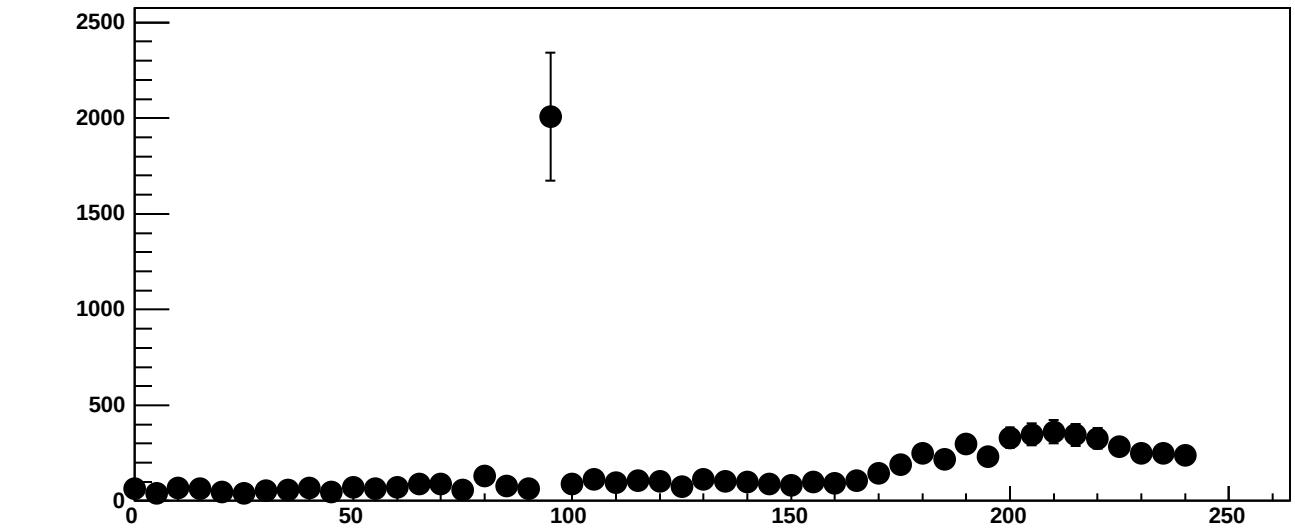
Chip 9, Channel 0, Enable 4, DAC=1600, ADC Residuals vs Hold



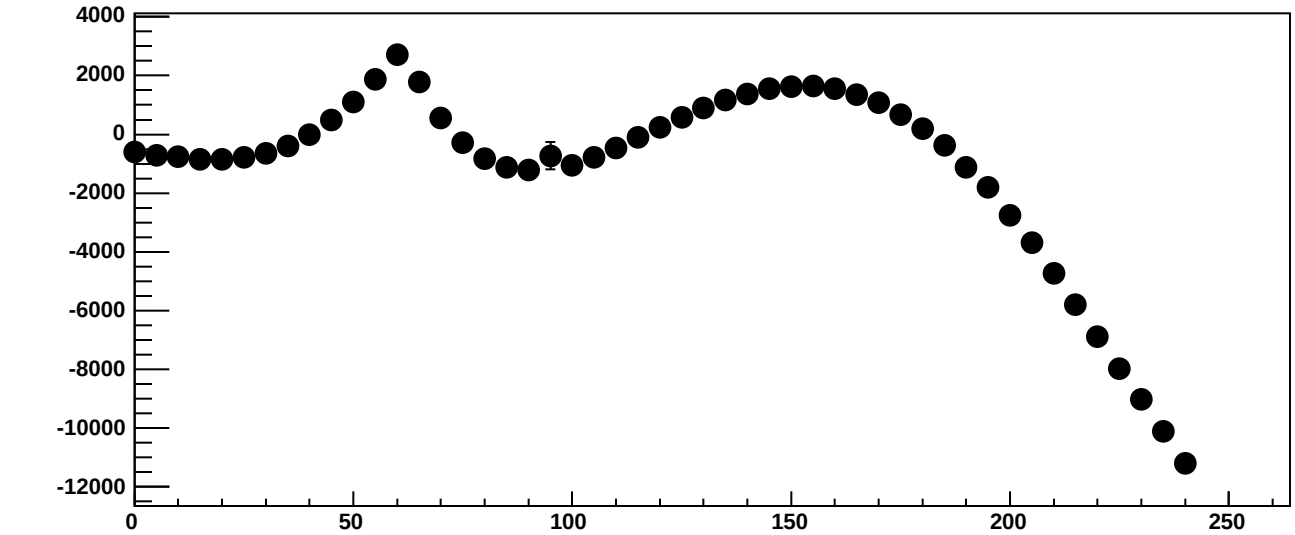
Chip 9, Channel 0, Enable 5, DAC=1600, ADC Mean vs Hold



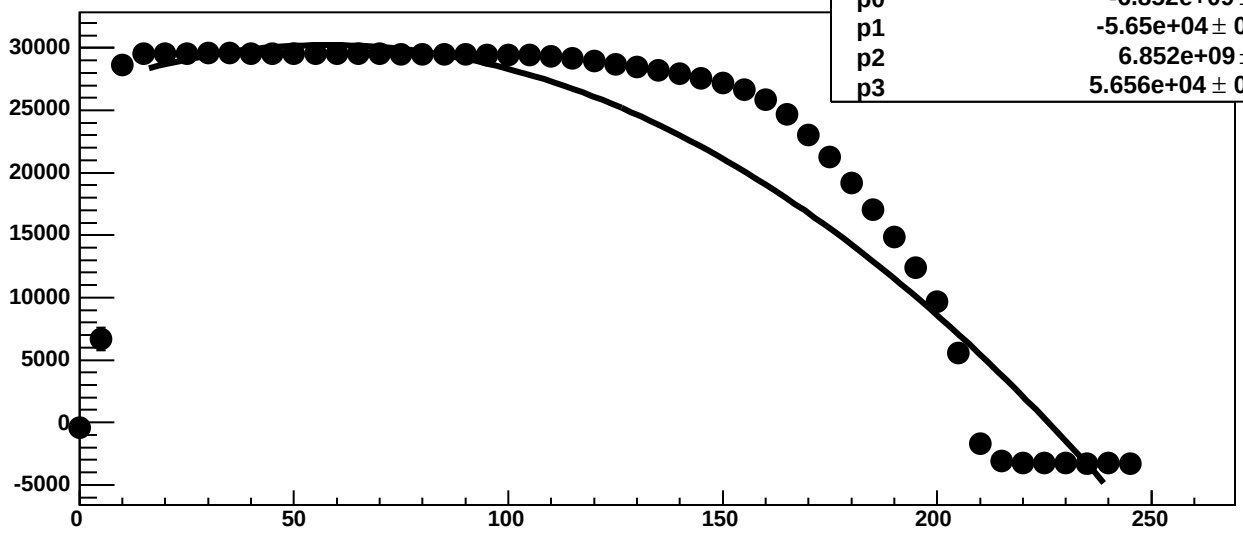
Chip 9, Channel 0, Enable 5, DAC=1600, ADC Noise vs Hold



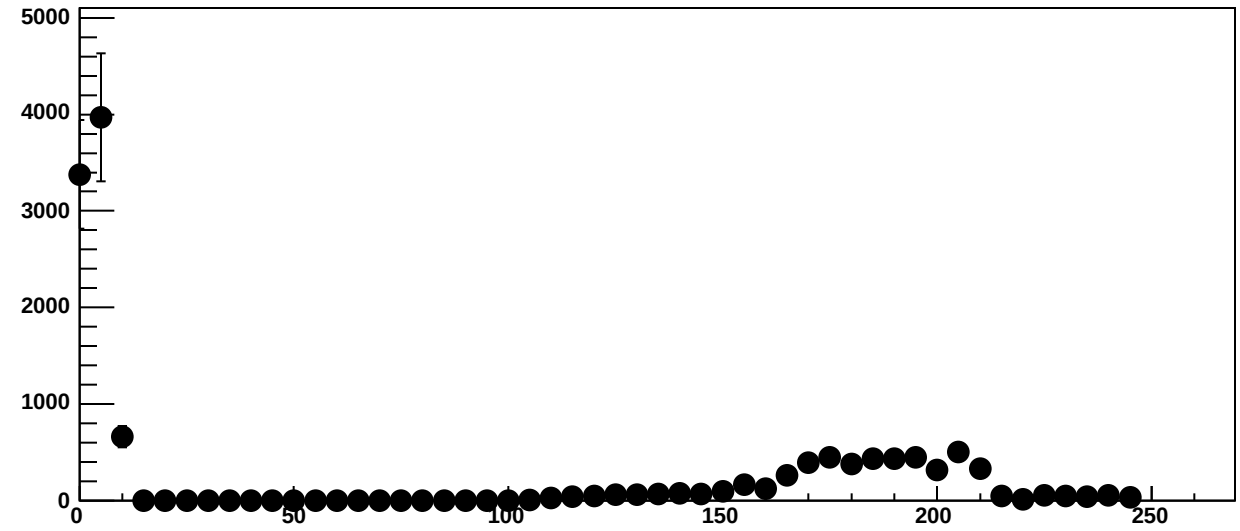
Chip 9, Channel 0, Enable 5, DAC=1600, ADC Residuals vs Hold



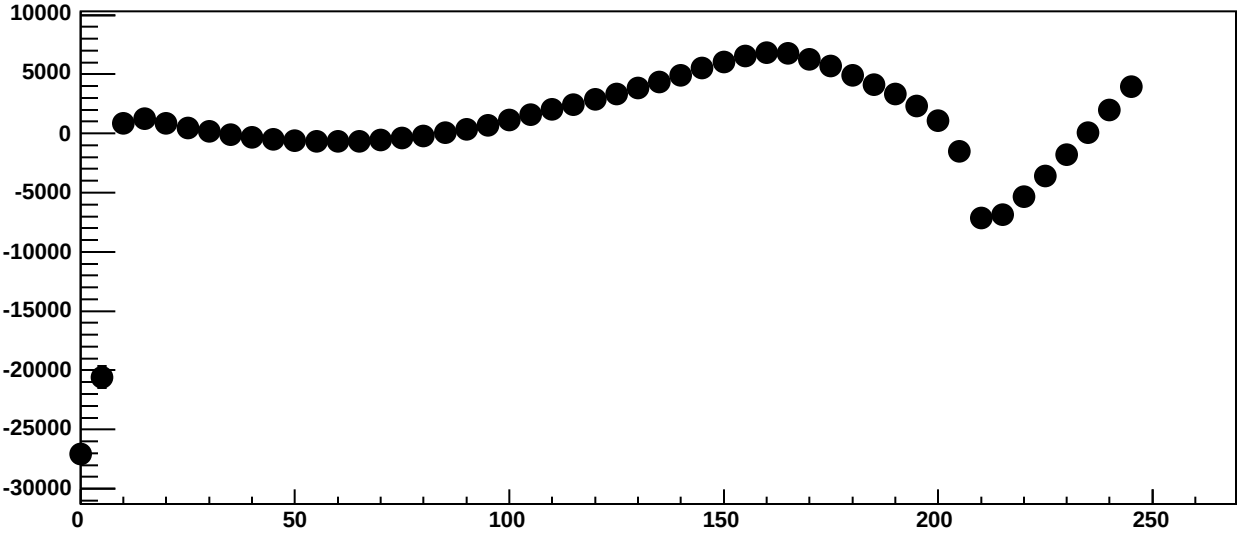
Chip 9, Channel 1, Enable 0!, DAC=1600, ADC Mean vs Hold



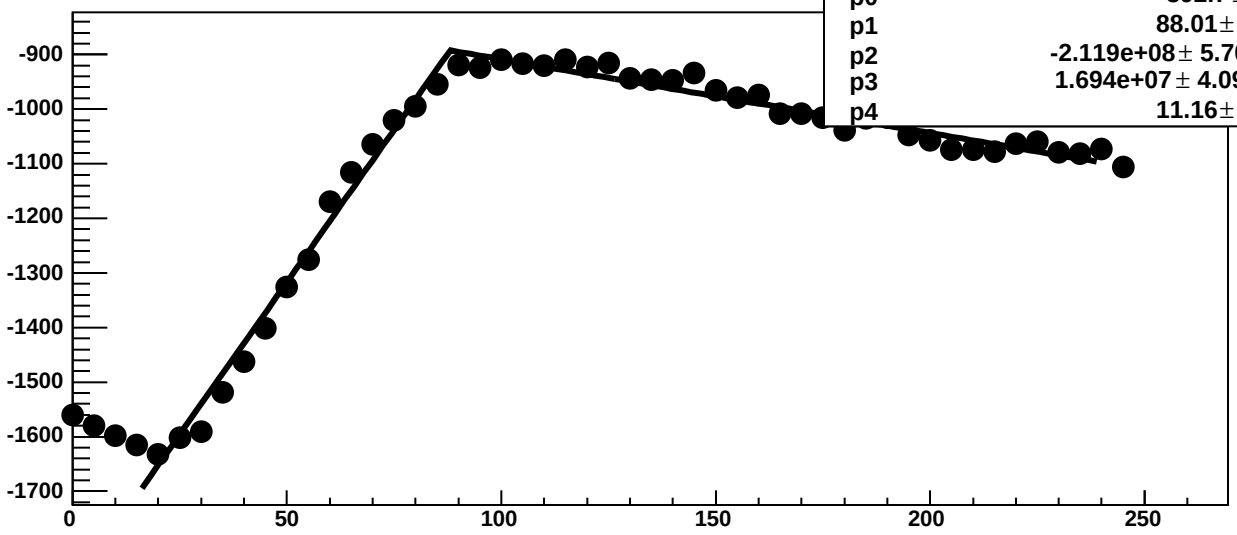
Chip 9, Channel 1, Enable 0!, DAC=1600, ADC Noise vs Hold



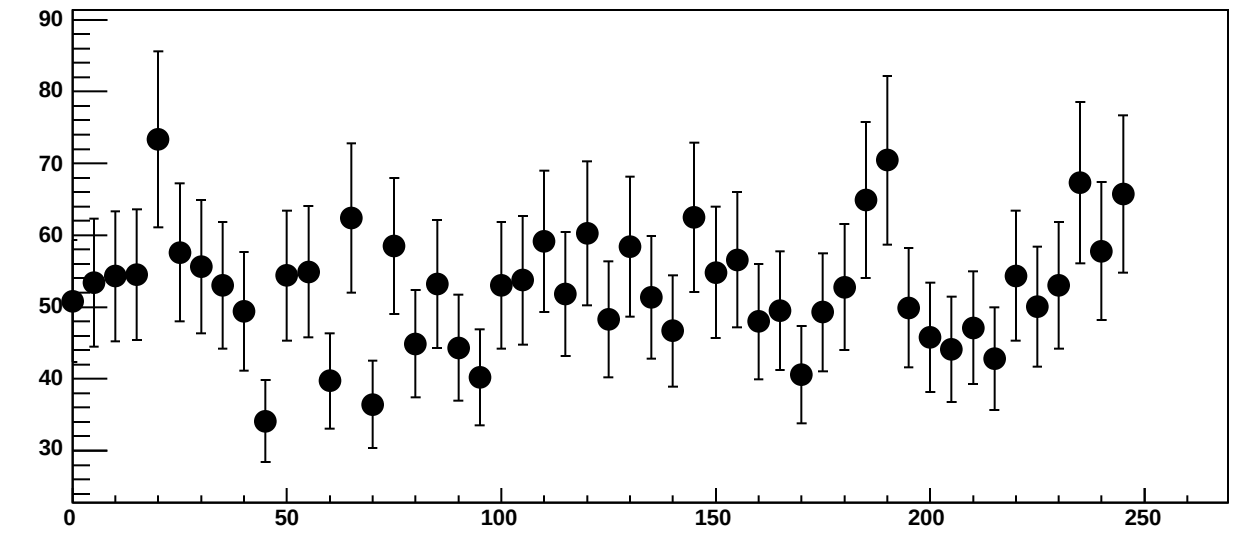
Chip 9, Channel 1, Enable 0!, DAC=1600, ADC Residuals vs Hold



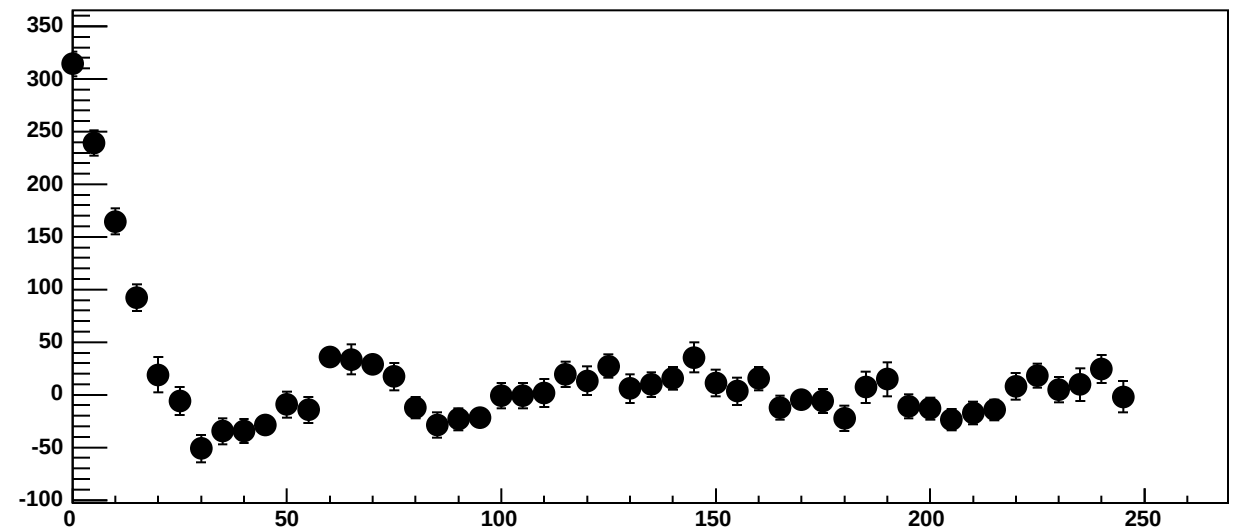
Chip 9, Channel 1, Enable 1, DAC=1600, ADC Mean vs Hold



Chip 9, Channel 1, Enable 1, DAC=1600, ADC Noise vs Hold

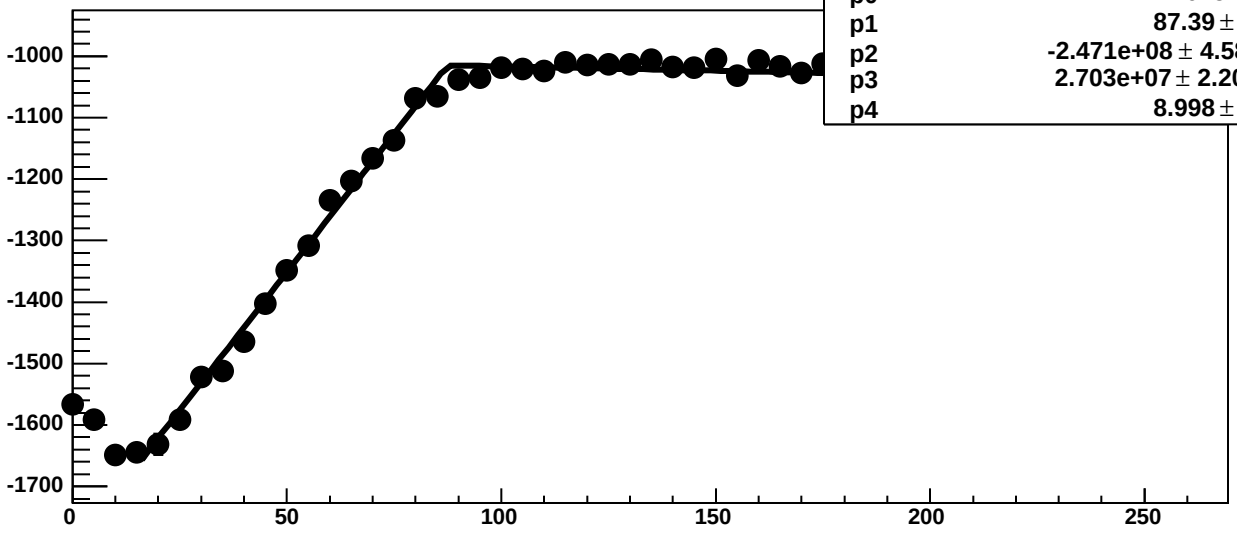


Chip 9, Channel 1, Enable 1, DAC=1600, ADC Residuals vs Hold

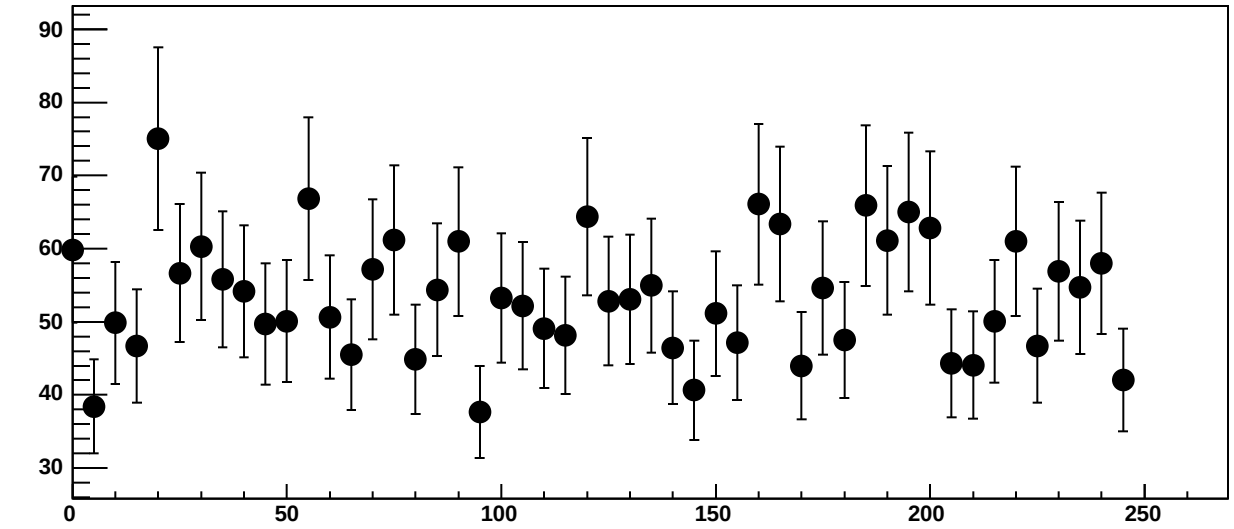


Chip 9, Channel 1, Enable 2, DAC=1600, ADC Mean vs Hold

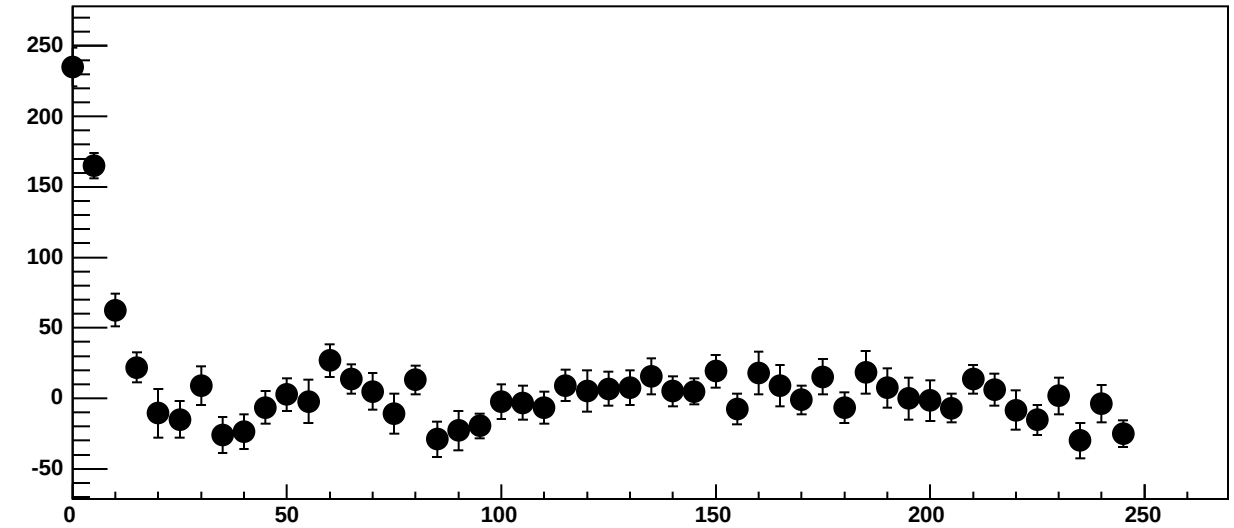
χ^2 / ndf	60.25 / 41
p0	-1015 ± 4.083
p1	87.39 ± 0.8123
p2	-2.471e+08 ± 4.582e+06
p3	2.703e+07 ± 2.209e+05
p4	8.998 ± 0.1467



Chip 9, Channel 1, Enable 2, DAC=1600, ADC Noise vs Hold

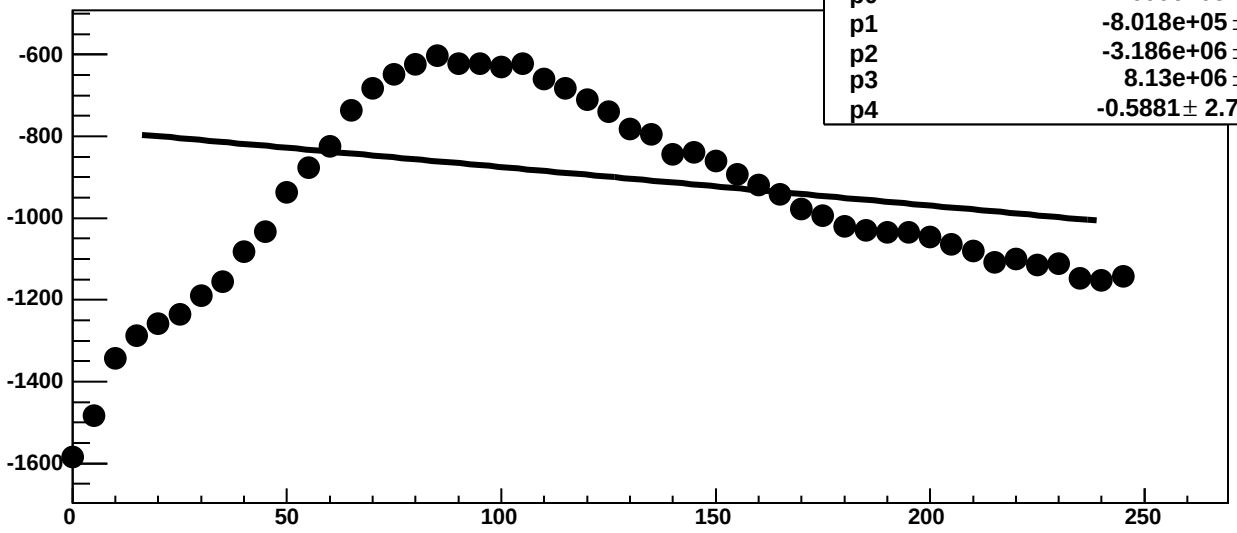


Chip 9, Channel 1, Enable 2, DAC=1600, ADC Residuals vs Hold

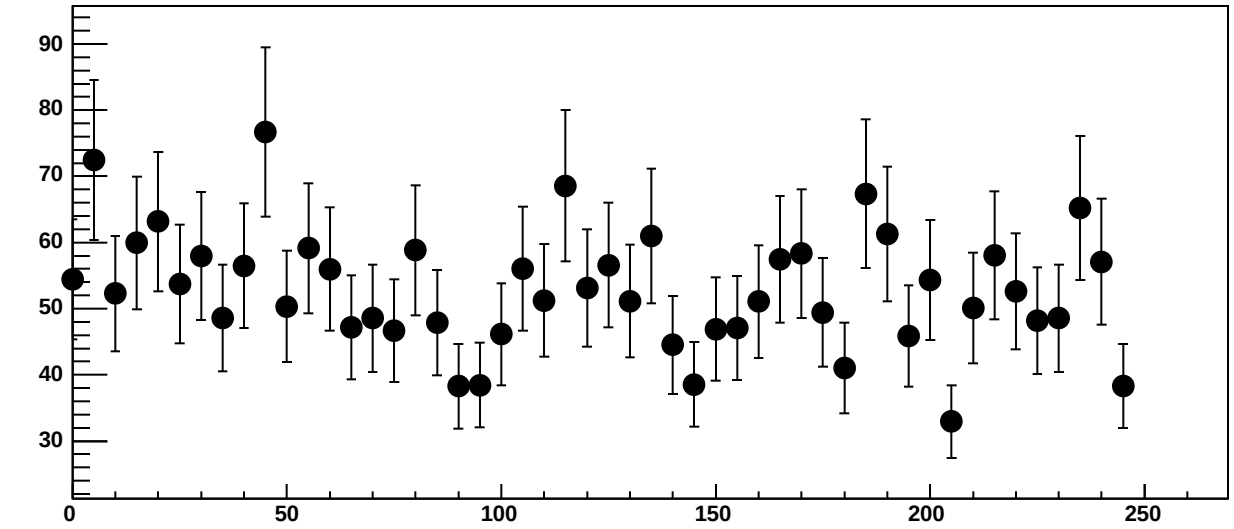


Chip 9, Channel 1, Enable 3, DAC=1600, ADC Mean vs Hold

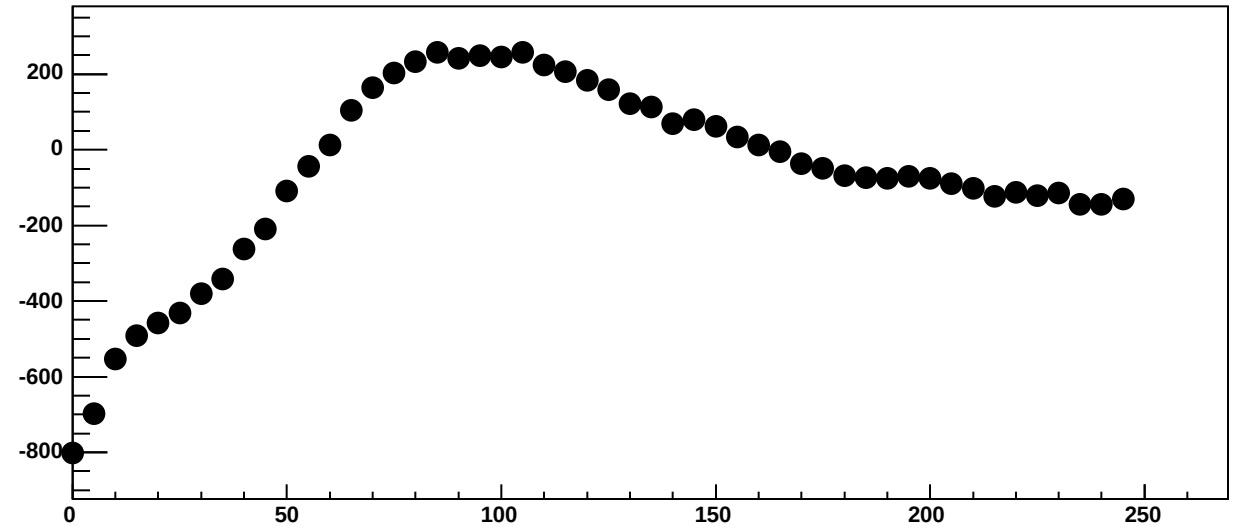
χ^2 / ndf	1.223e+04 / 41
p0	7.699e+05 ± 21.67
p1	-8.018e+05 ± 22.97
p2	-3.186e+06 ± 230.7
p3	8.13e+06 ± 618.6
p4	-0.5881 ± 2.702e-05



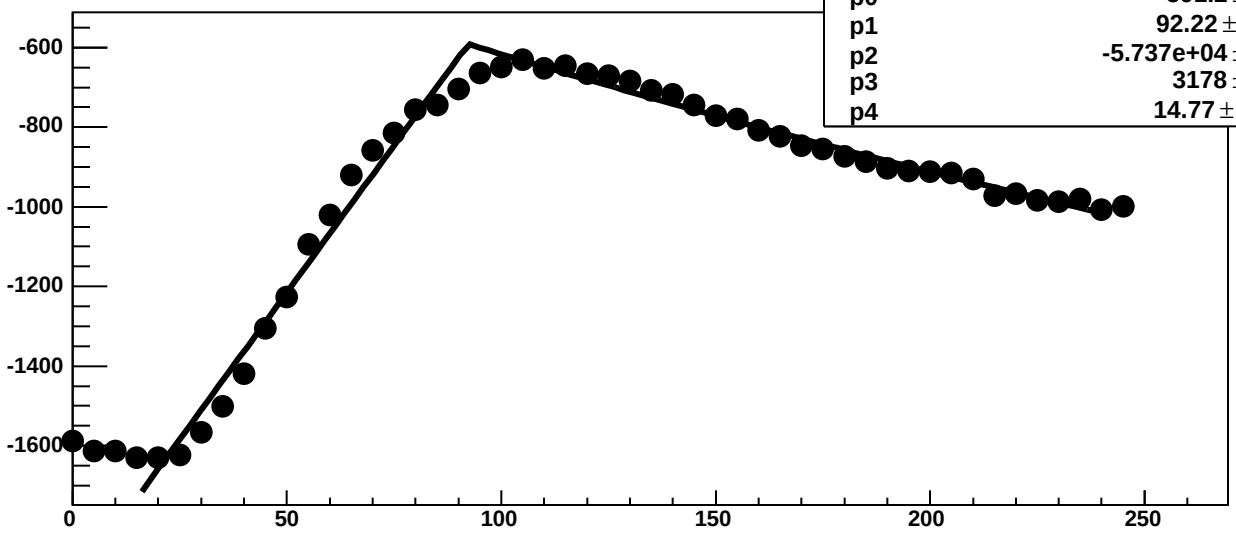
Chip 9, Channel 1, Enable 3, DAC=1600, ADC Noise vs Hold



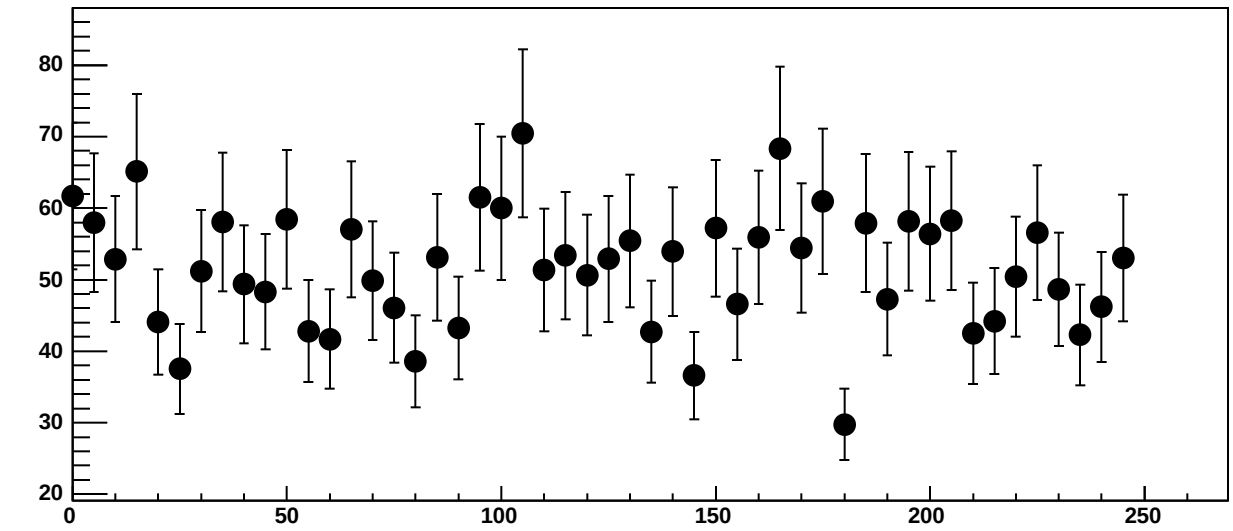
Chip 9, Channel 1, Enable 3, DAC=1600, ADC Residuals vs Hold



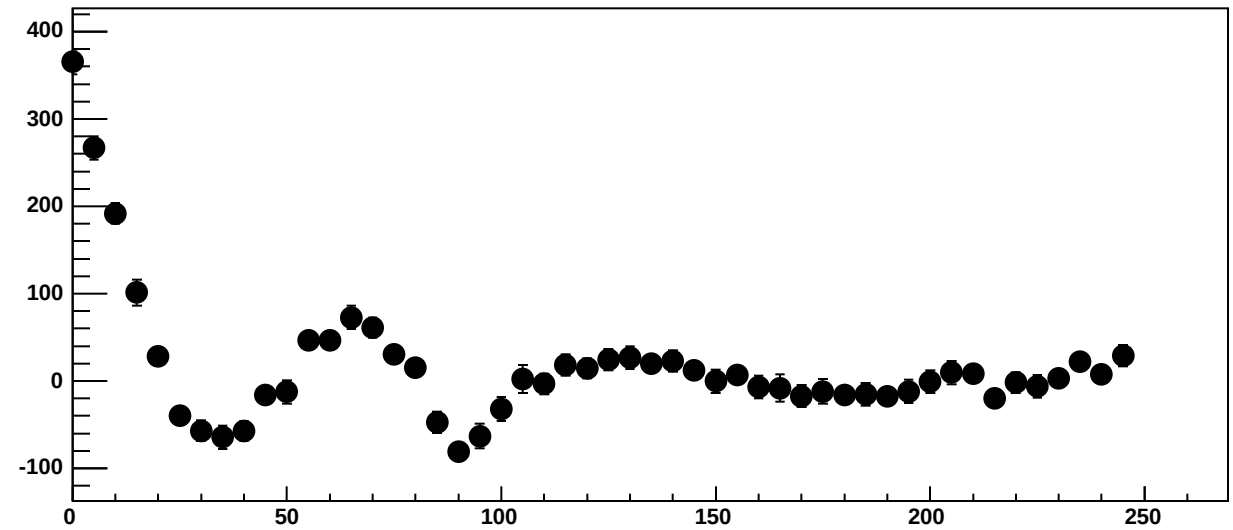
Chip 9, Channel 1, Enable 4, DAC=1600, ADC Mean vs Hold



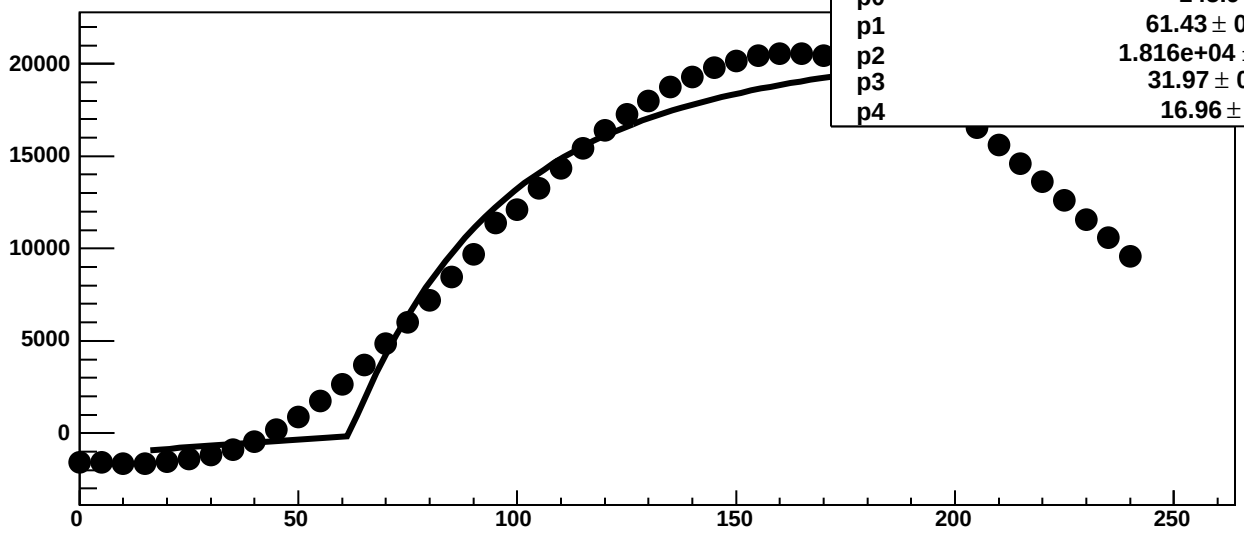
Chip 9, Channel 1, Enable 4, DAC=1600, ADC Noise vs Hold



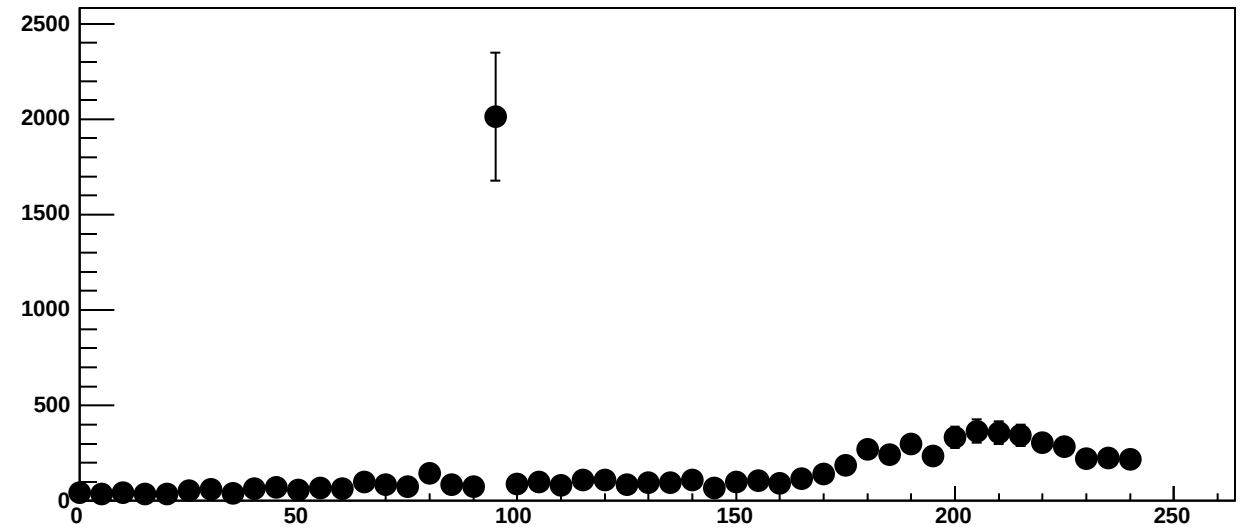
Chip 9, Channel 1, Enable 4, DAC=1600, ADC Residuals vs Hold



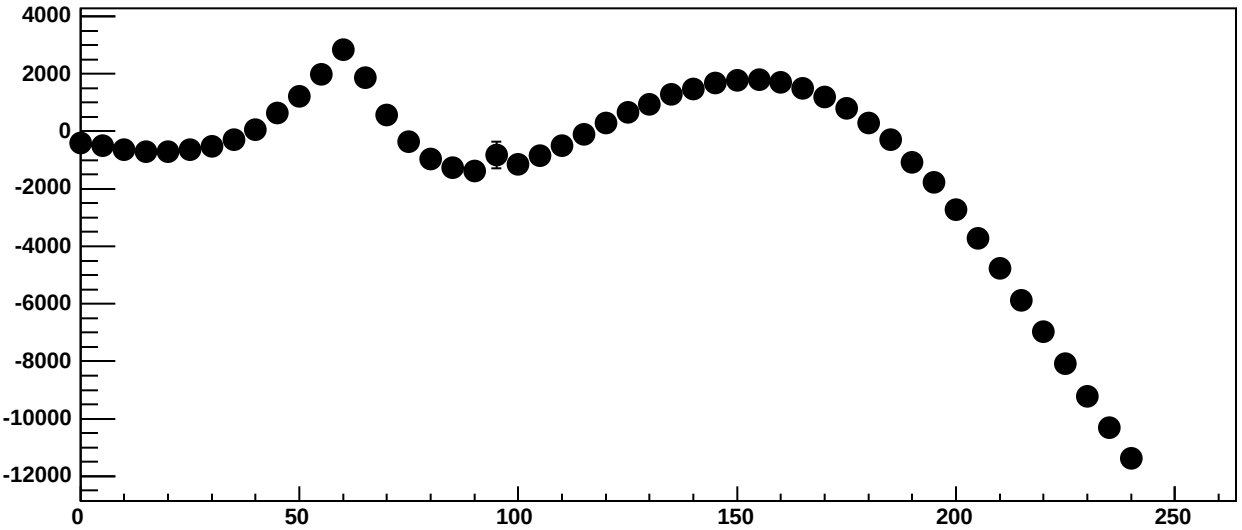
Chip 9, Channel 1, Enable 5, DAC=1600, ADC Mean vs Hold



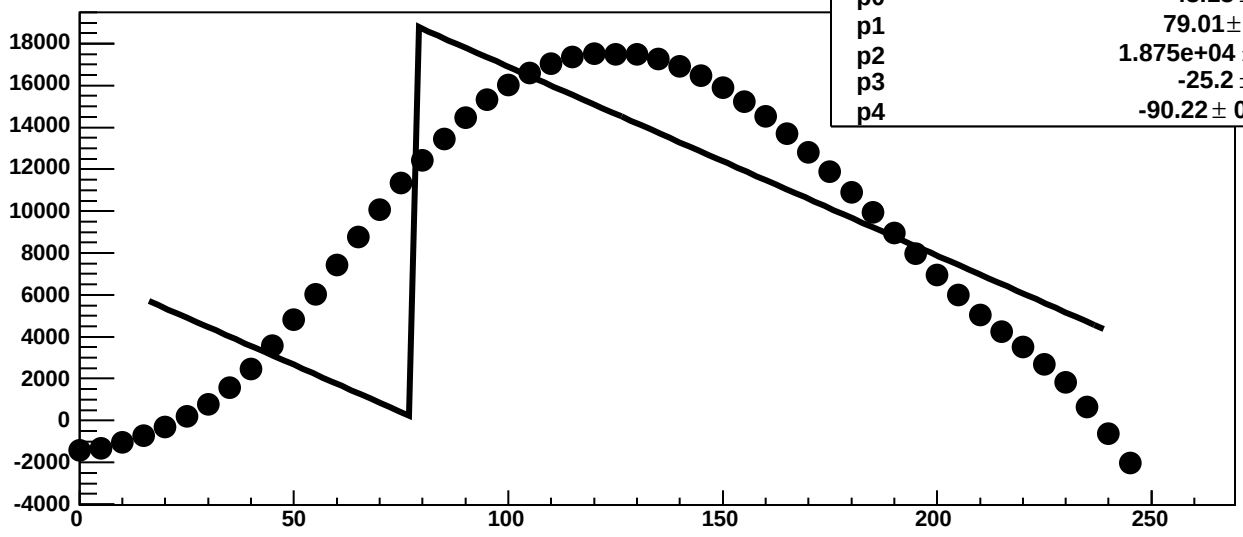
Chip 9, Channel 1, Enable 5, DAC=1600, ADC Noise vs Hold



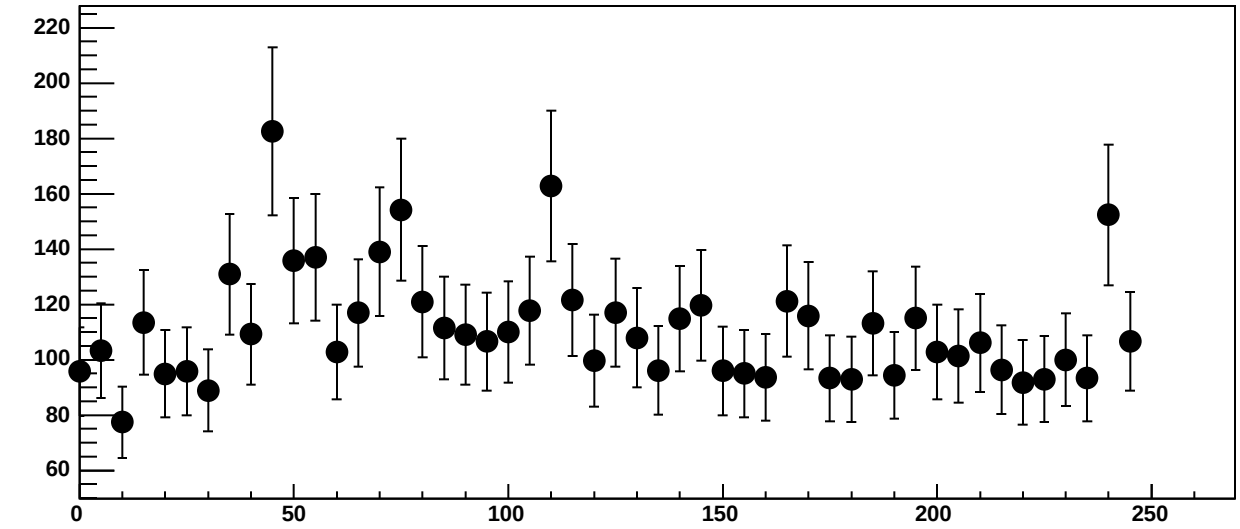
Chip 9, Channel 1, Enable 5, DAC=1600, ADC Residuals vs Hold



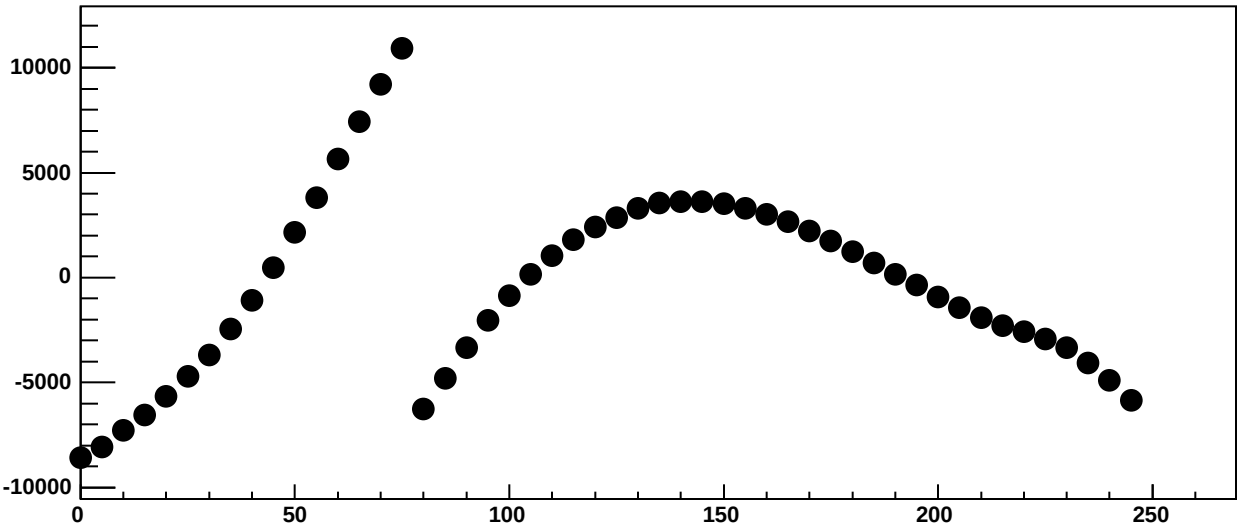
Chip 9, Channel 2, Enable 0, DAC=1600, ADC Mean vs Hold



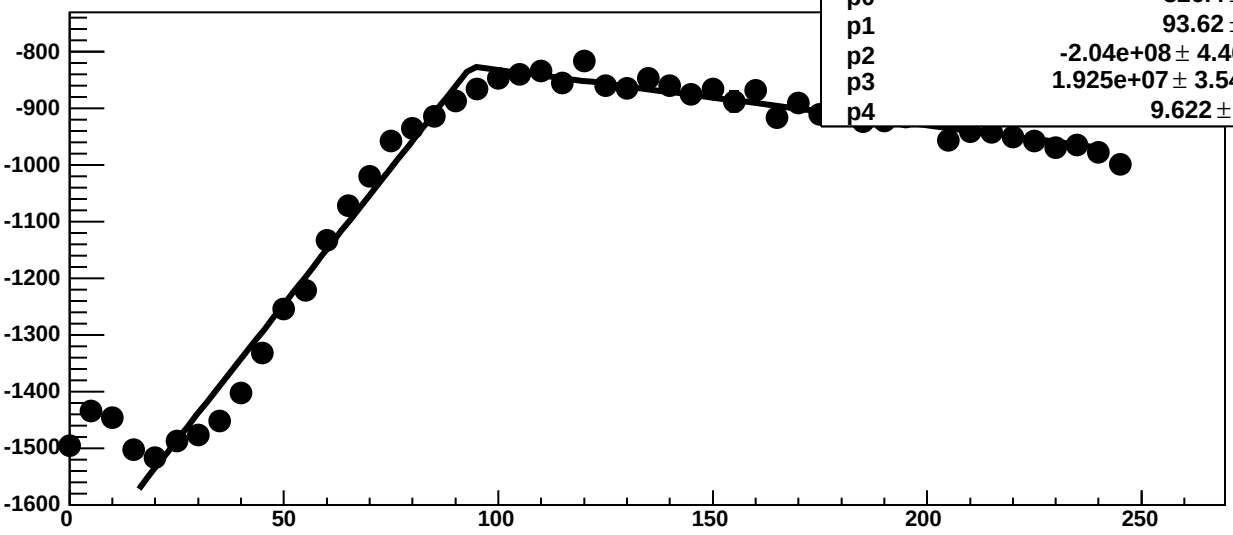
Chip 9, Channel 2, Enable 0, DAC=1600, ADC Noise vs Hold



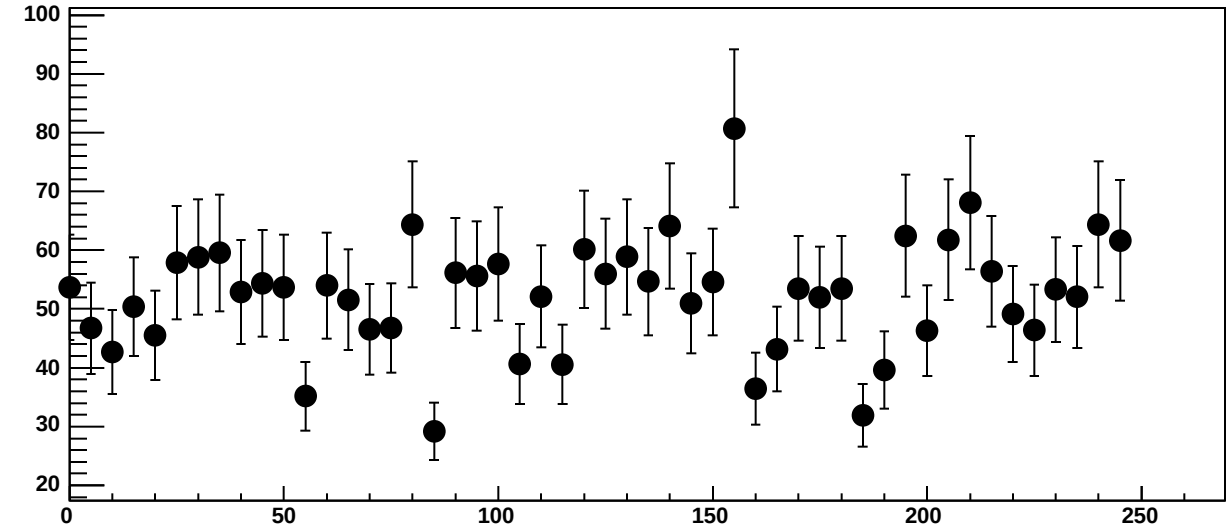
Chip 9, Channel 2, Enable 0, DAC=1600, ADC Residuals vs Hold



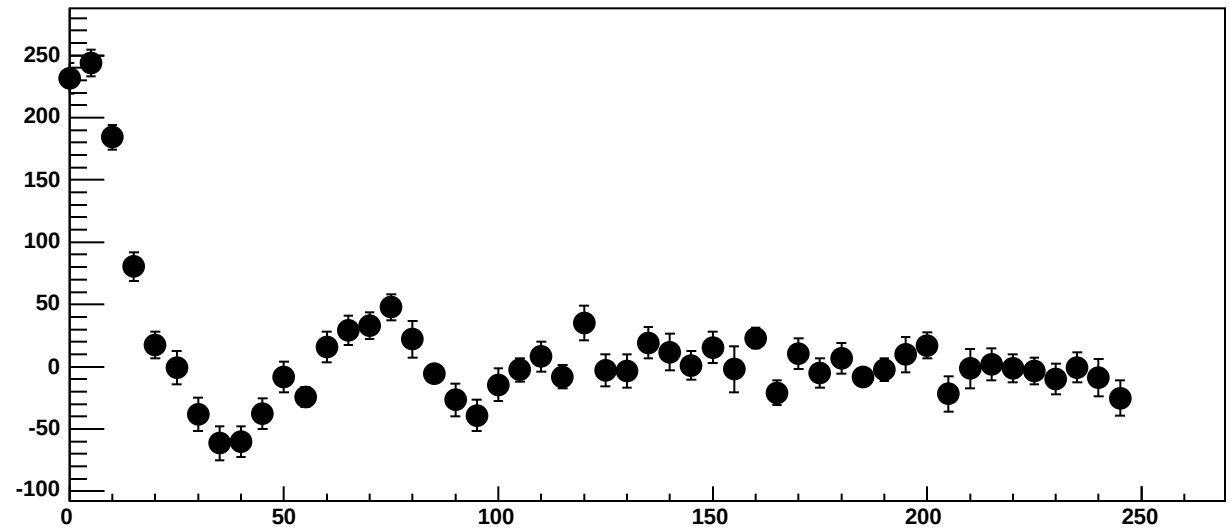
Chip 9, Channel 2, Enable 1, DAC=1600, ADC Mean vs Hold

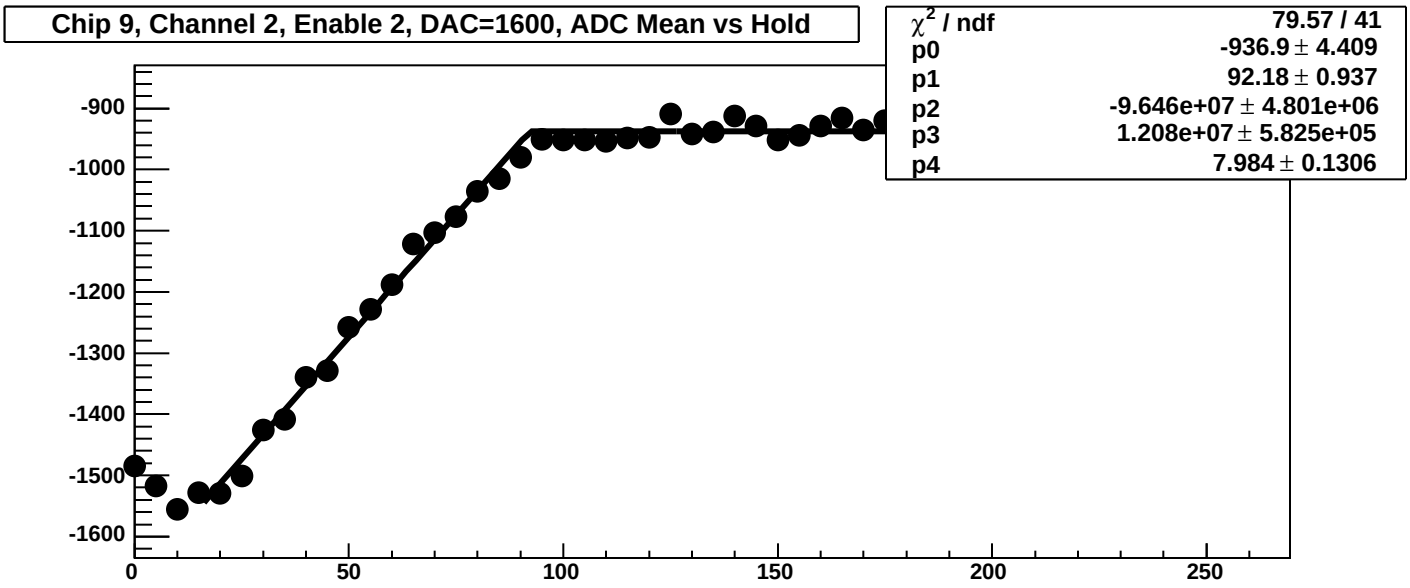


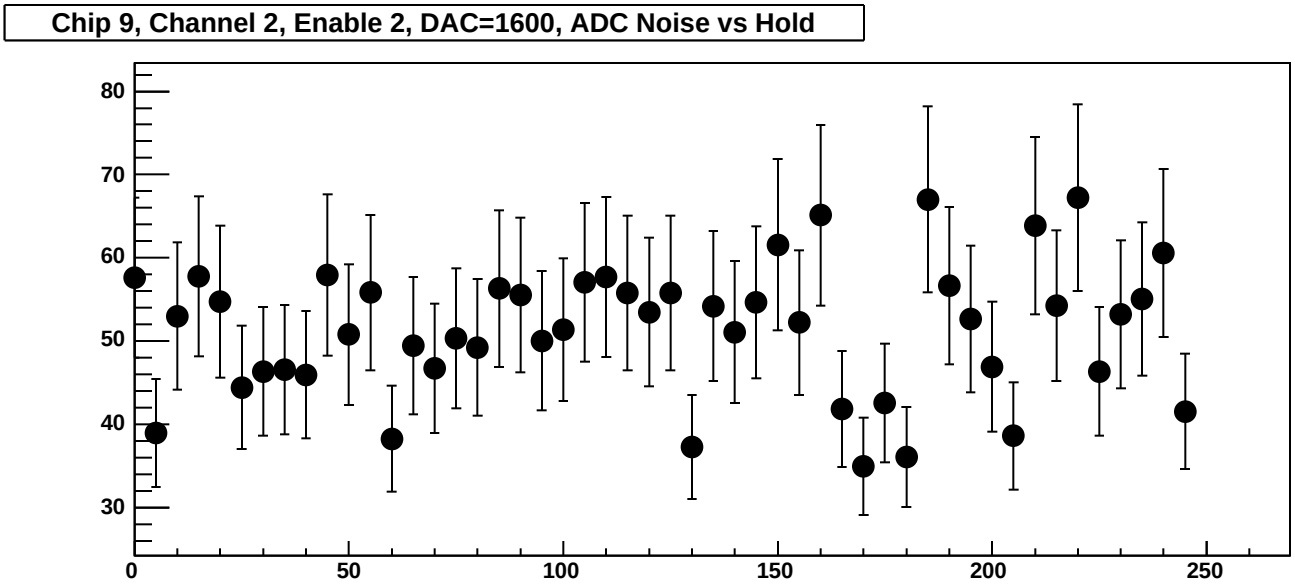
Chip 9, Channel 2, Enable 1, DAC=1600, ADC Noise vs Hold

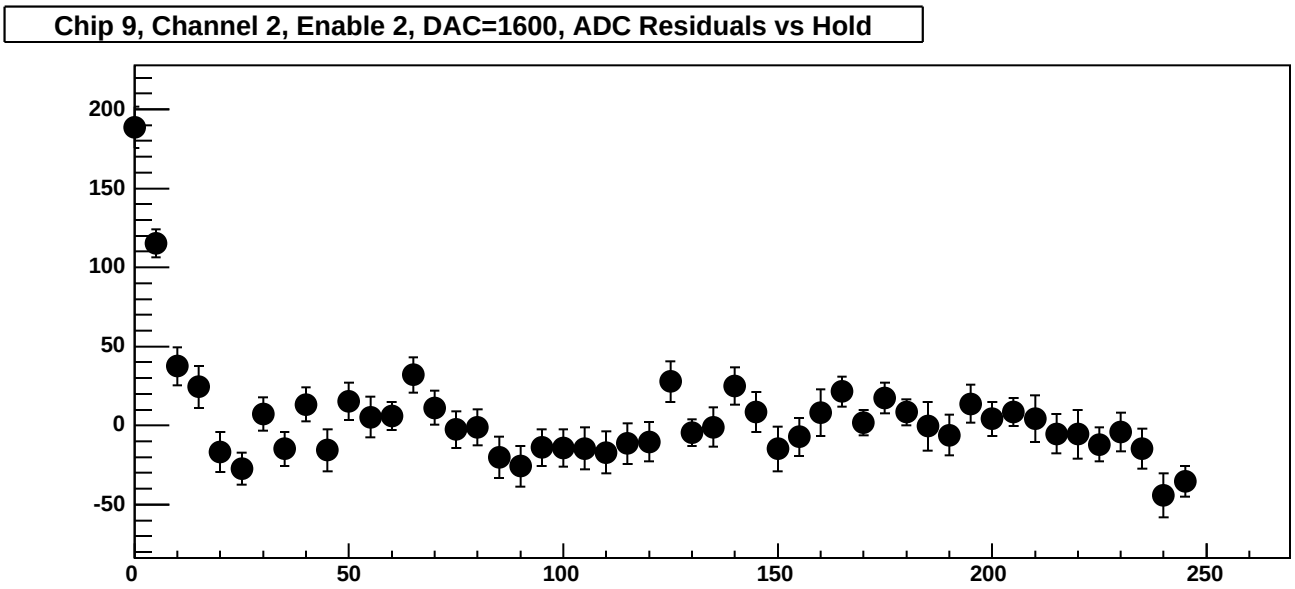


Chip 9, Channel 2, Enable 1, DAC=1600, ADC Residuals vs Hold



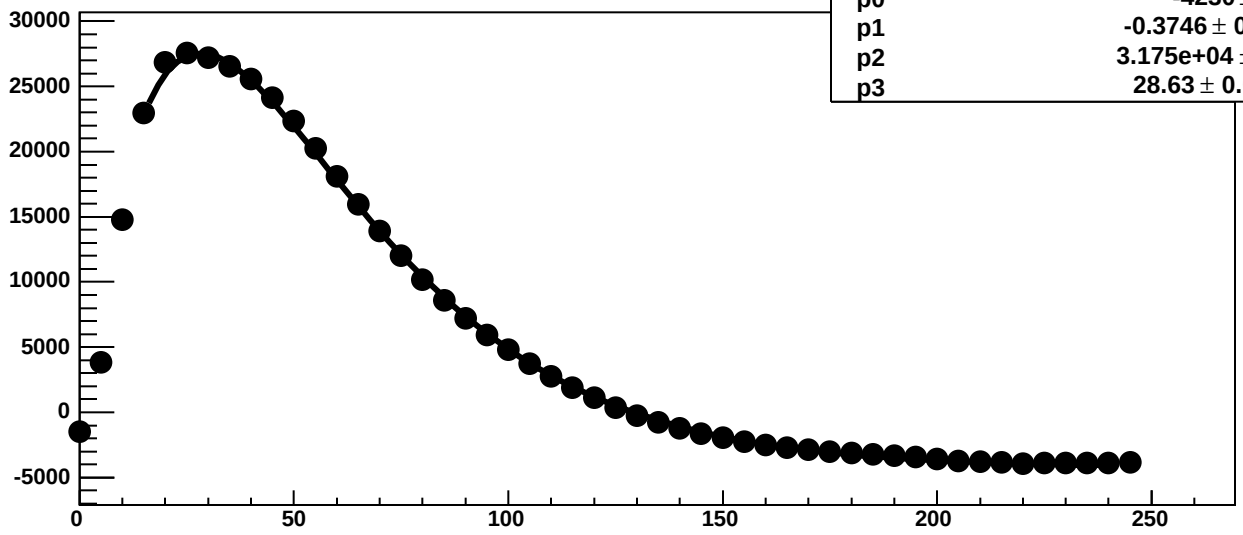

Chip 9, Channel 2, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 79.57 / 41
p0 -936.9 ± 4.409
p1 92.18 ± 0.937
p2 -9.646e+07 ± 4.801e+06
p3 1.208e+07 ± 5.825e+05
p4 7.984 ± 0.1306
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
50
100
150
200
250


Chip 9, Channel 2, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

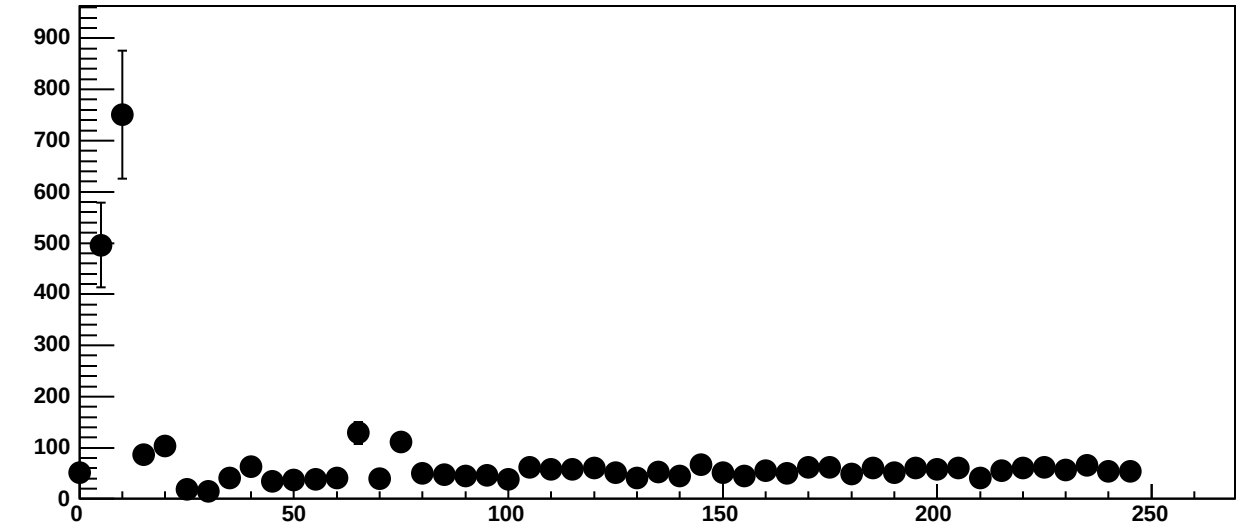

Chip 9, Channel 2, Enable 2, DAC=1600, ADC Residuals vs Hold
200
150
100
50
0
-50
0
50
100
150
200
250

Chip 9, Channel 2, Enable 3!, DAC=1600, ADC Mean vs Hold

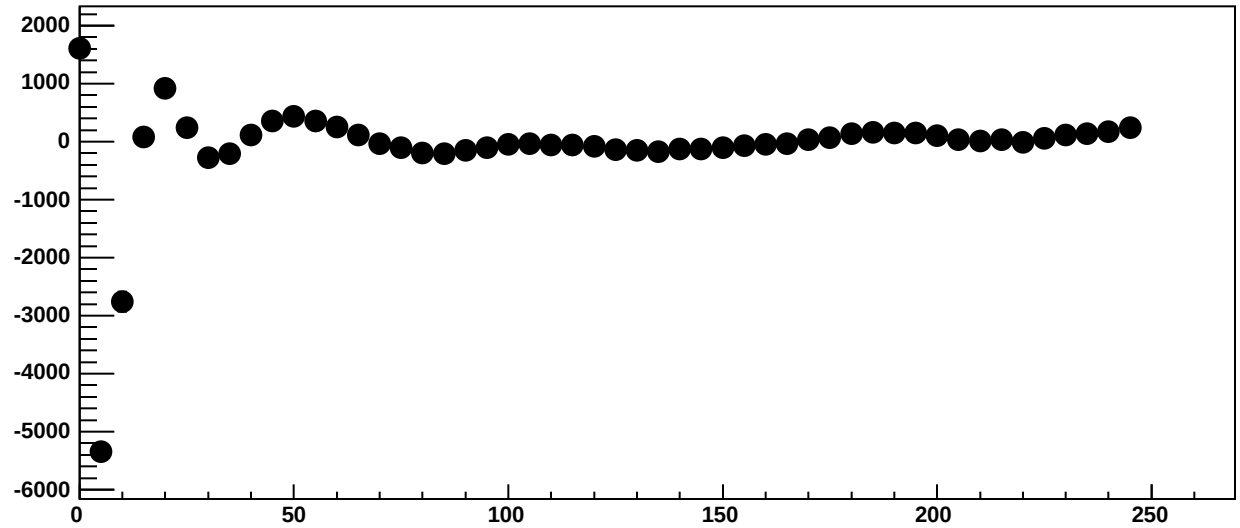
χ^2 / ndf	2.219e+04 / 42
p0	-4230 ± 3.483
p1	-0.3746 ± 0.01878
p2	3.175e+04 ± 3.995
p3	28.63 ± 0.009812



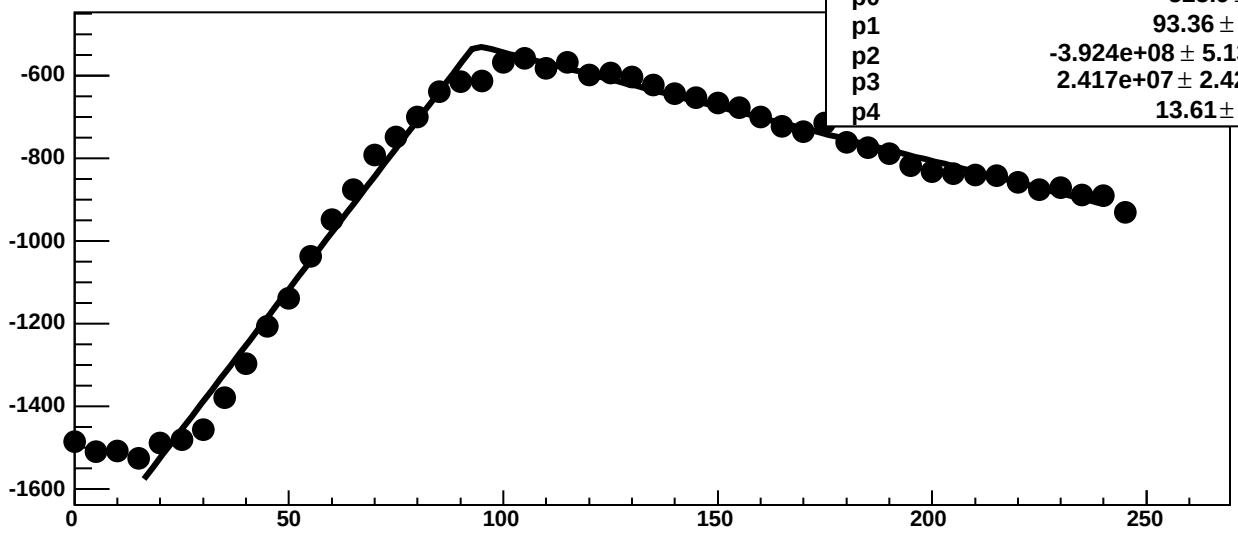
Chip 9, Channel 2, Enable 3!, DAC=1600, ADC Noise vs Hold



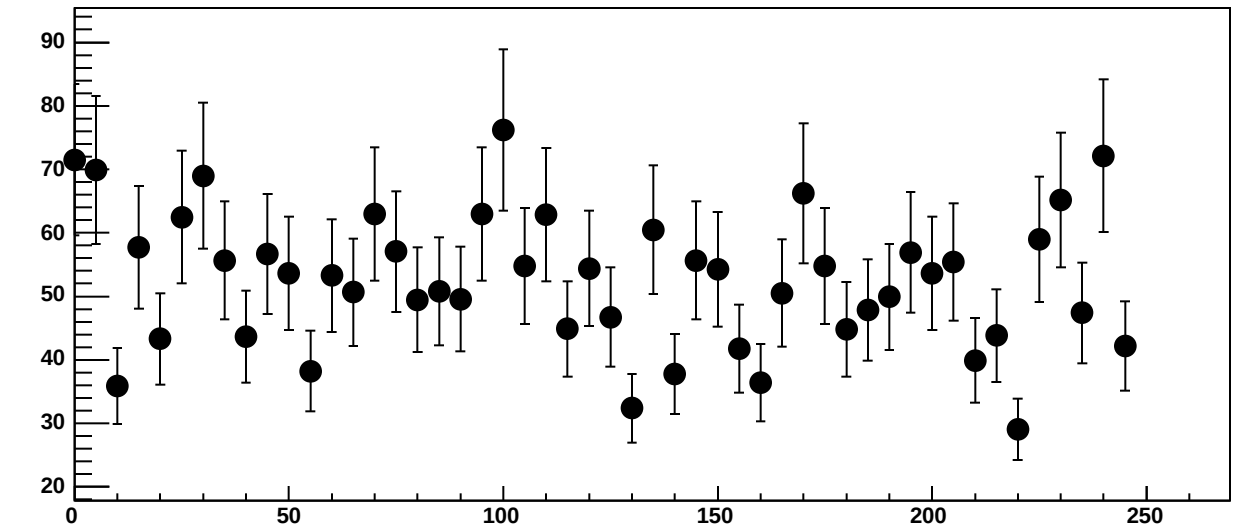
Chip 9, Channel 2, Enable 3!, DAC=1600, ADC Residuals vs Hold



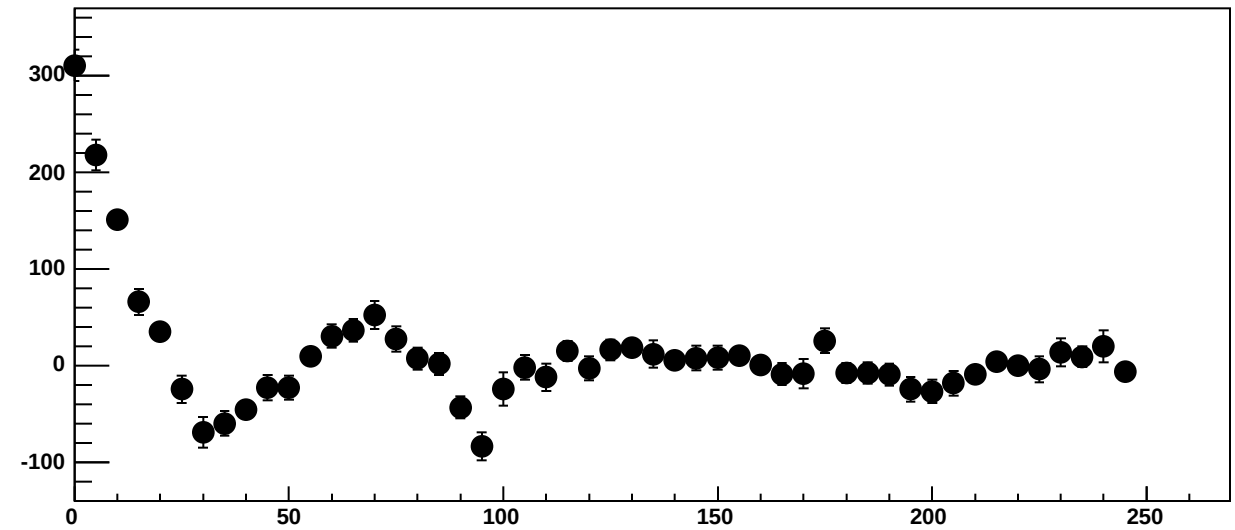
Chip 9, Channel 2, Enable 4, DAC=1600, ADC Mean vs Hold

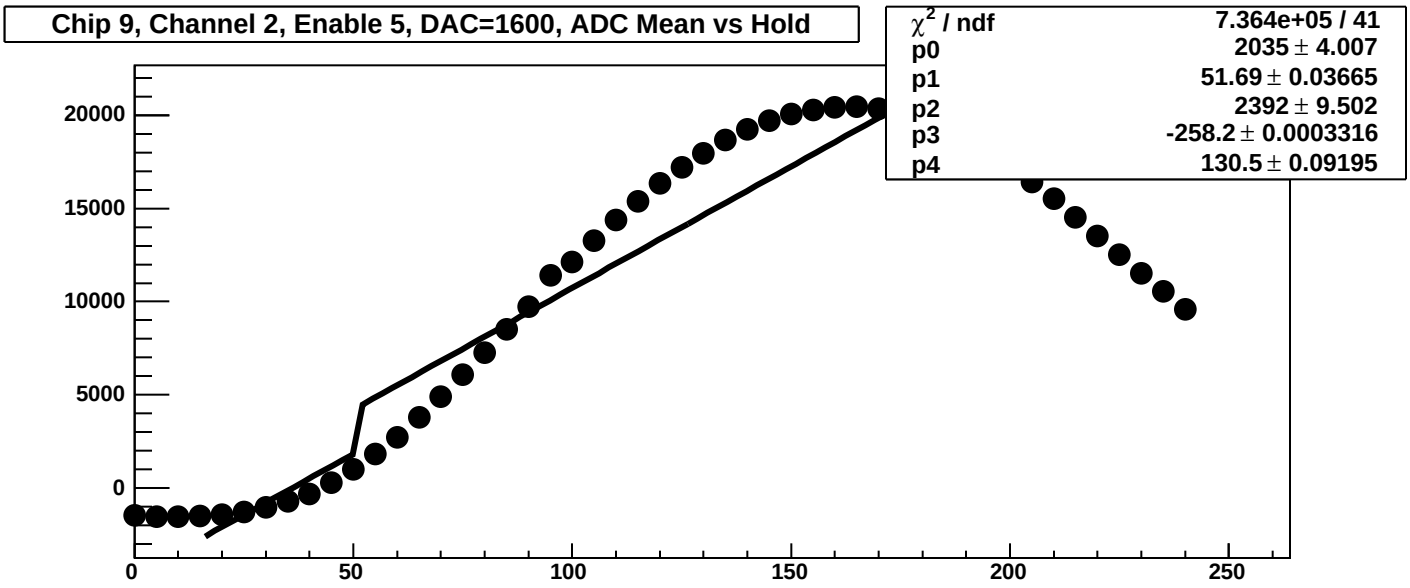


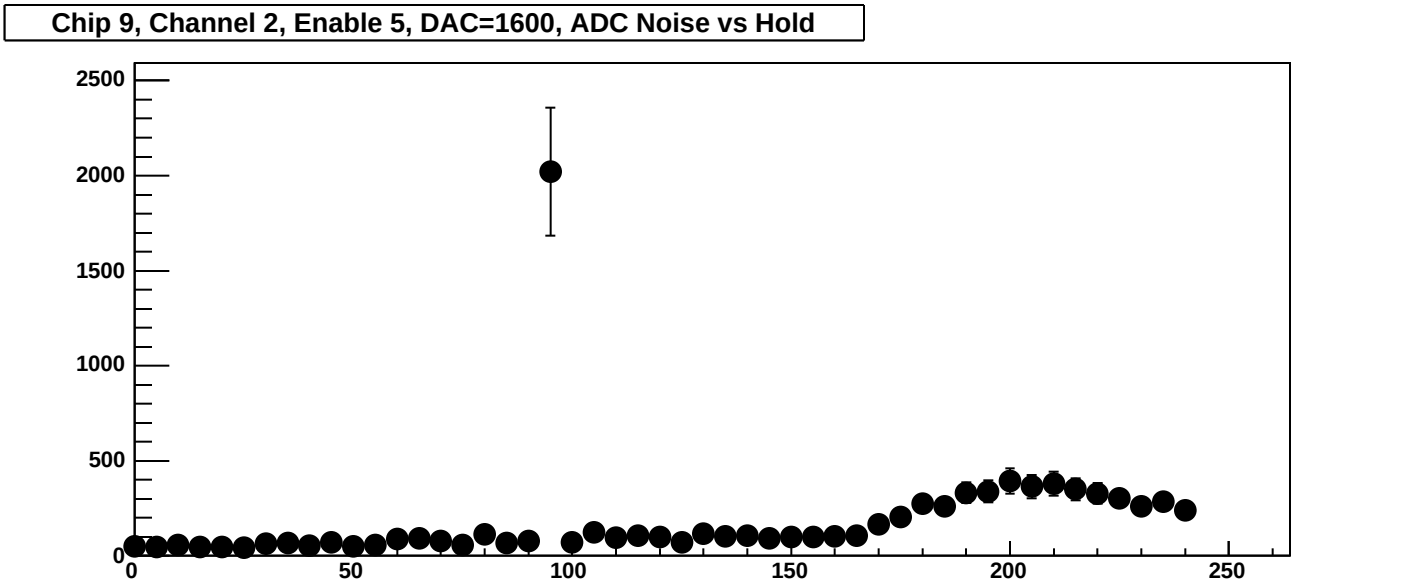
Chip 9, Channel 2, Enable 4, DAC=1600, ADC Noise vs Hold

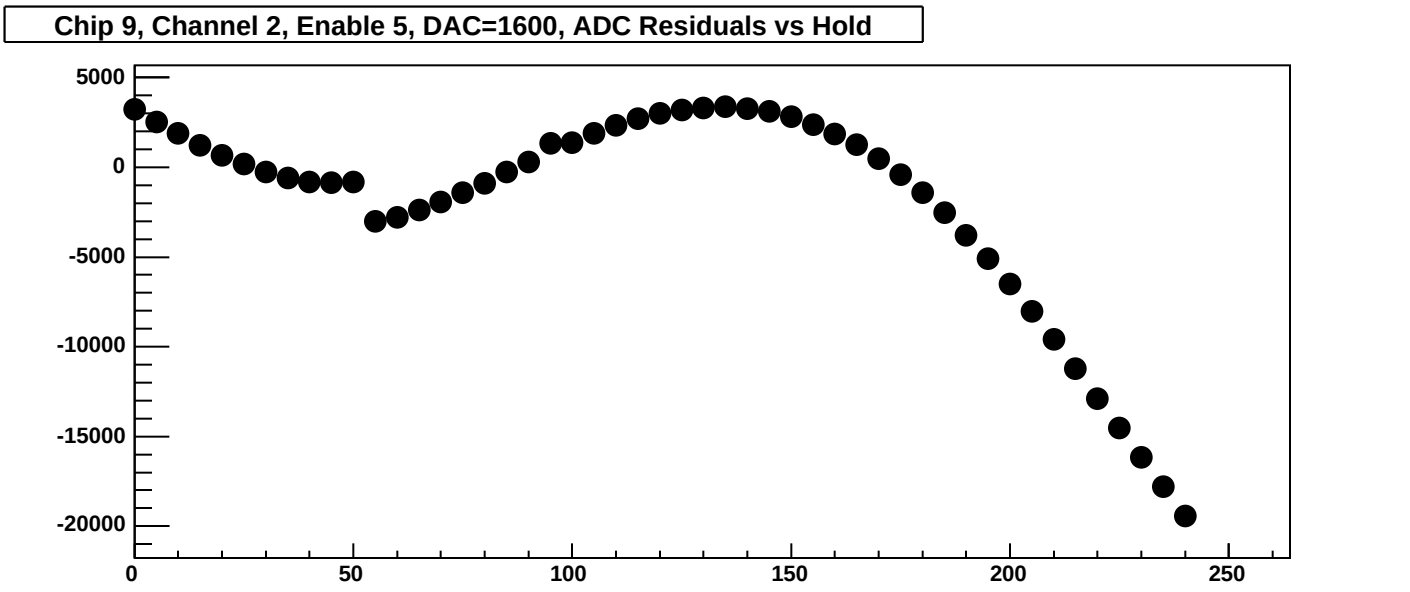


Chip 9, Channel 2, Enable 4, DAC=1600, ADC Residuals vs Hold

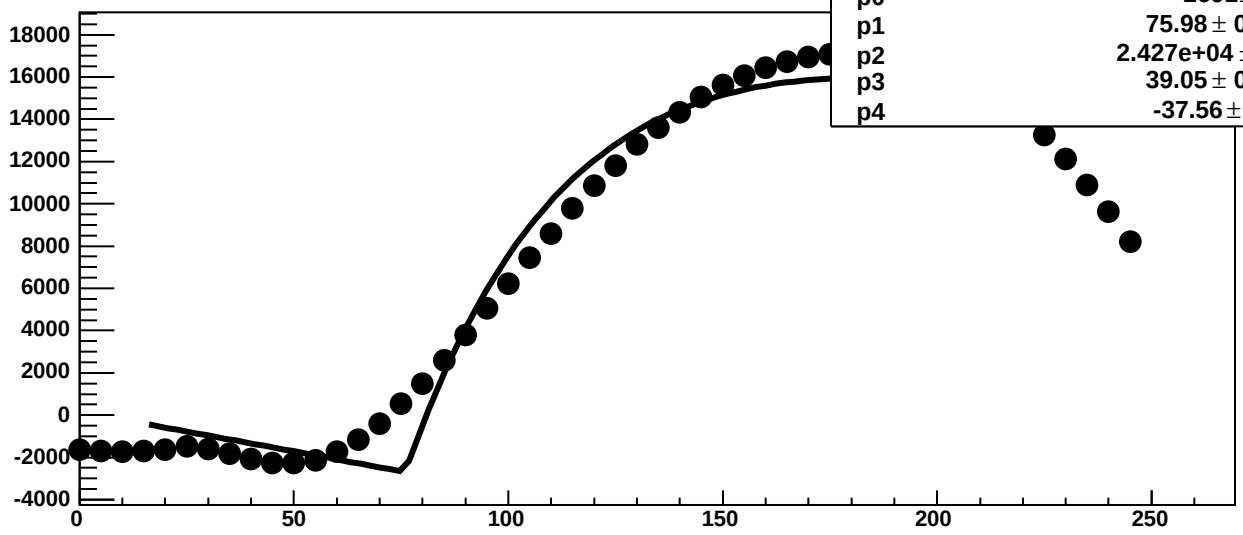



Chip 9, Channel 2, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.364e+05 / 41
p0 2035 ± 4.007
p1 51.69 ± 0.03665
p2 2392 ± 9.502
p3 -258.2 ± 0.0003316
p4 130.5 ± 0.09195
20000
15000
10000
5000
0
0
50
100
150
200
250

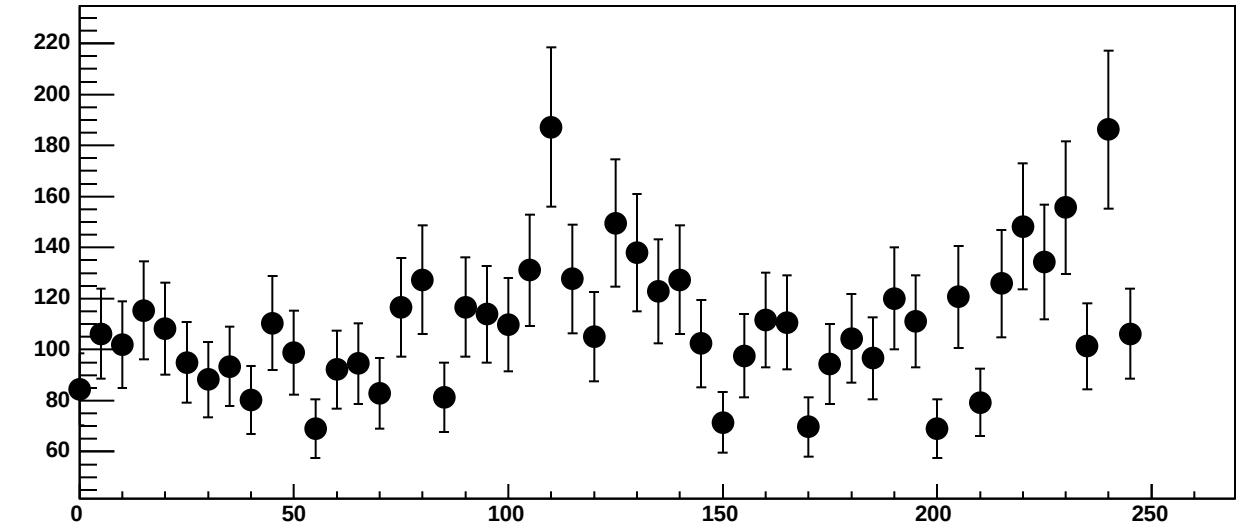

Chip 9, Channel 2, Enable 5, DAC=1600, ADC Noise vs Hold
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 9, Channel 2, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

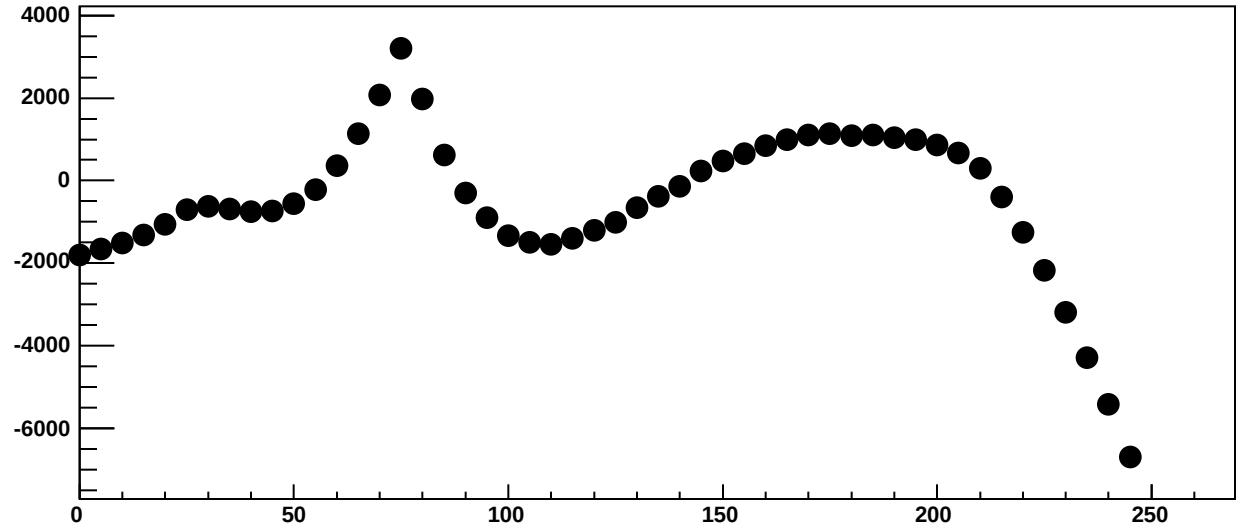
Chip 9, Channel 3, Enable 0, DAC=1600, ADC Mean vs Hold



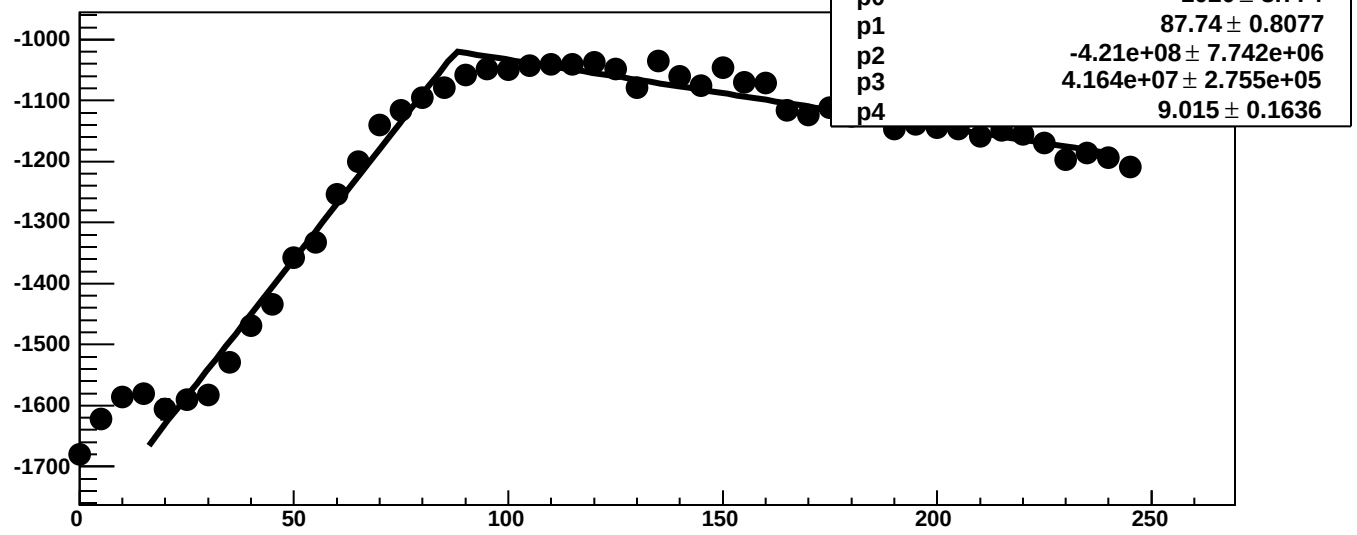
Chip 9, Channel 3, Enable 0, DAC=1600, ADC Noise vs Hold



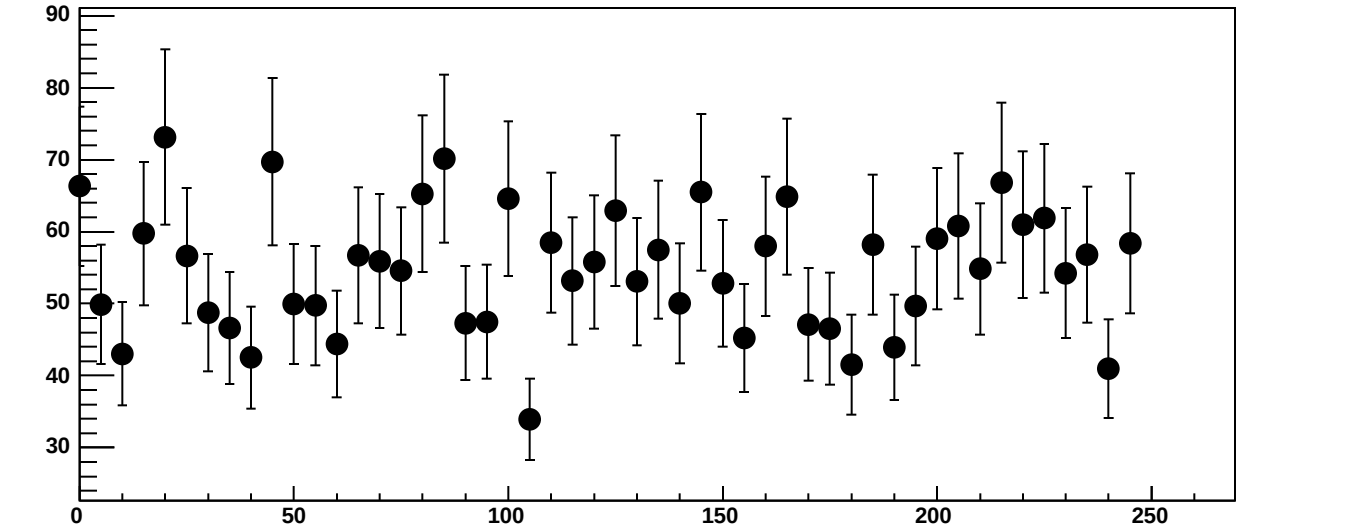
Chip 9, Channel 3, Enable 0, DAC=1600, ADC Residuals vs Hold



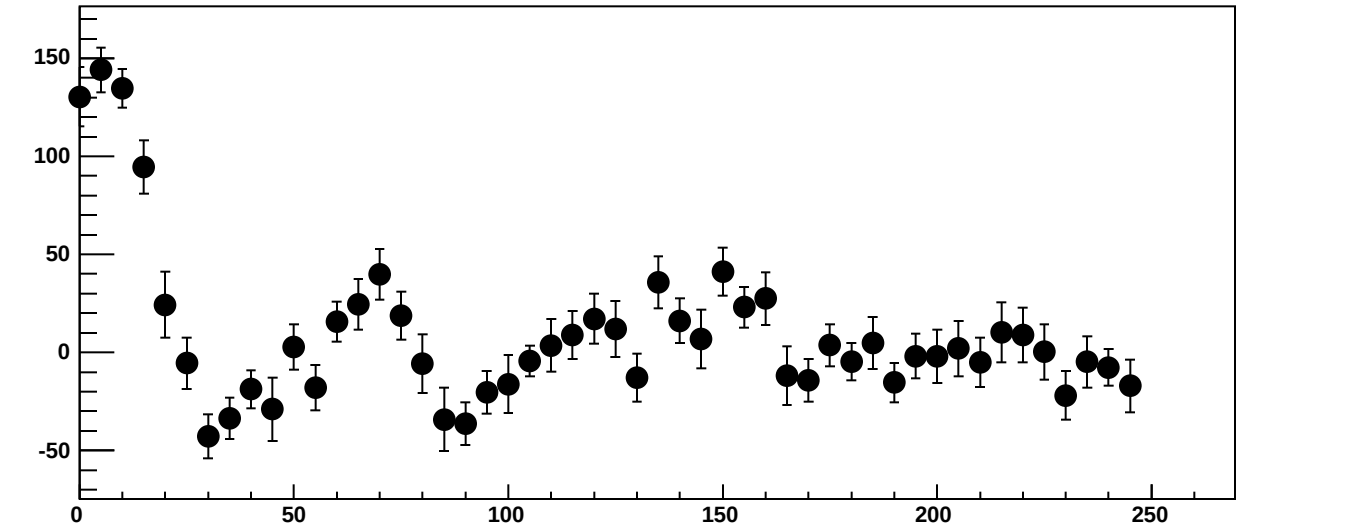
Chip 9, Channel 3, Enable 1, DAC=1600, ADC Mean vs Hold



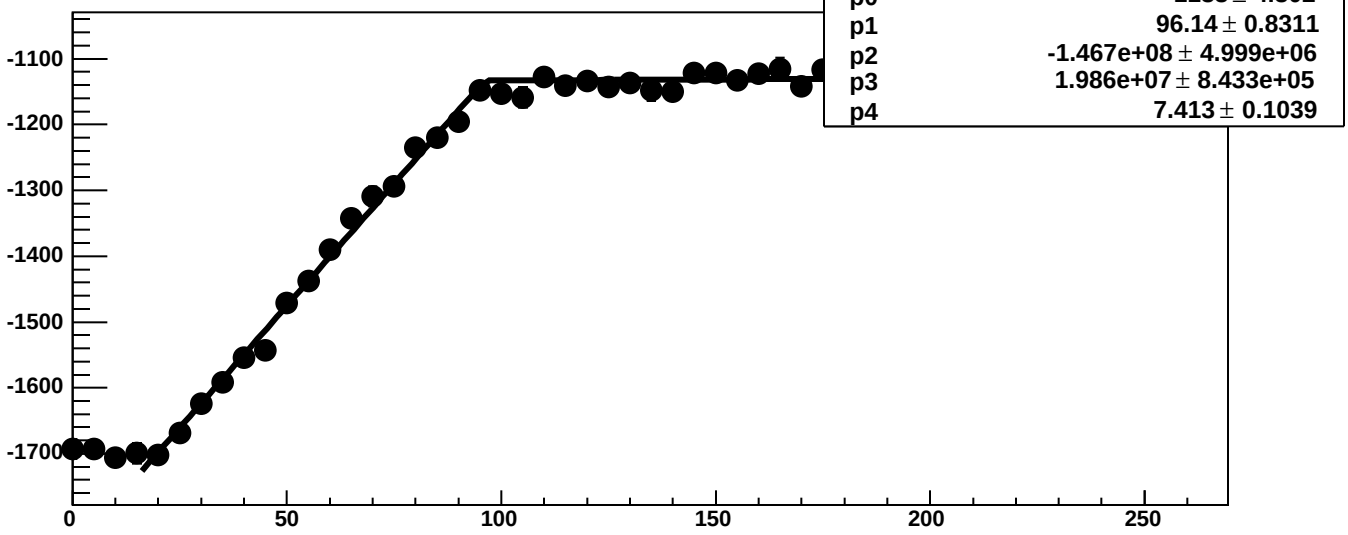
Chip 9, Channel 3, Enable 1, DAC=1600, ADC Noise vs Hold



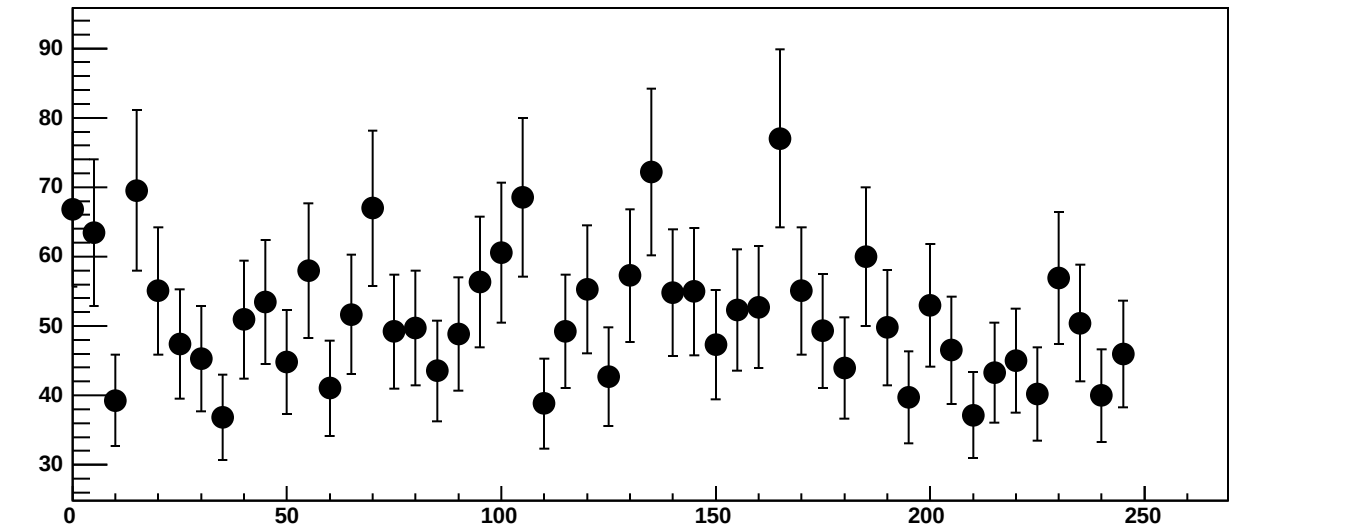
Chip 9, Channel 3, Enable 1, DAC=1600, ADC Residuals vs Hold



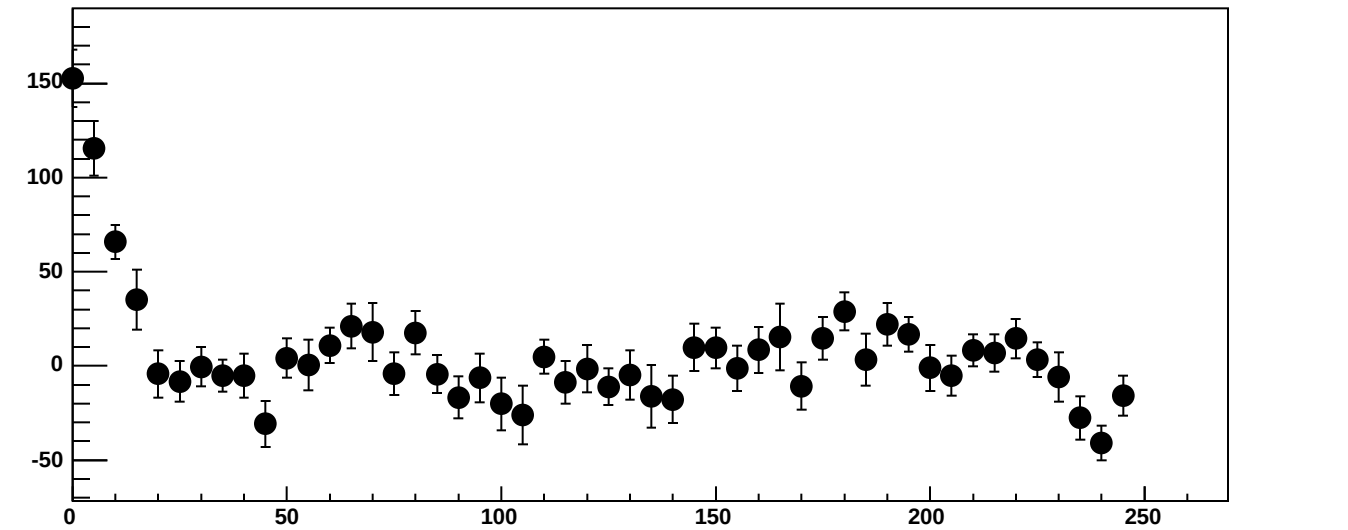
Chip 9, Channel 3, Enable 2, DAC=1600, ADC Mean vs Hold



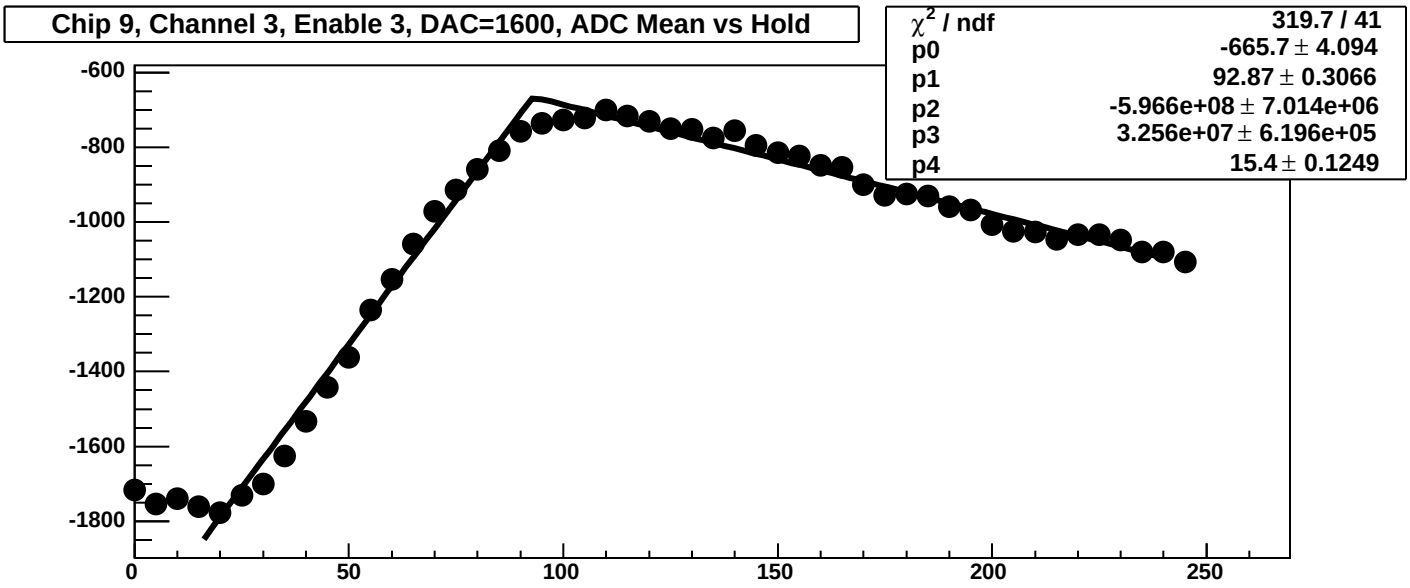
Chip 9, Channel 3, Enable 2, DAC=1600, ADC Noise vs Hold



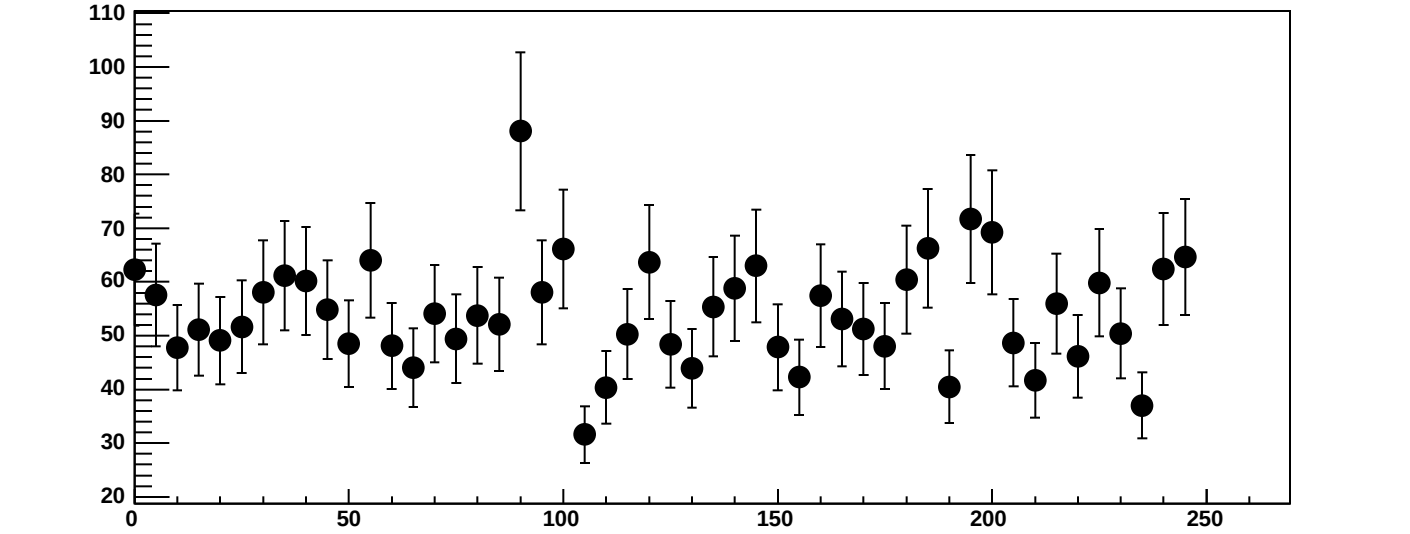
Chip 9, Channel 3, Enable 2, DAC=1600, ADC Residuals vs Hold



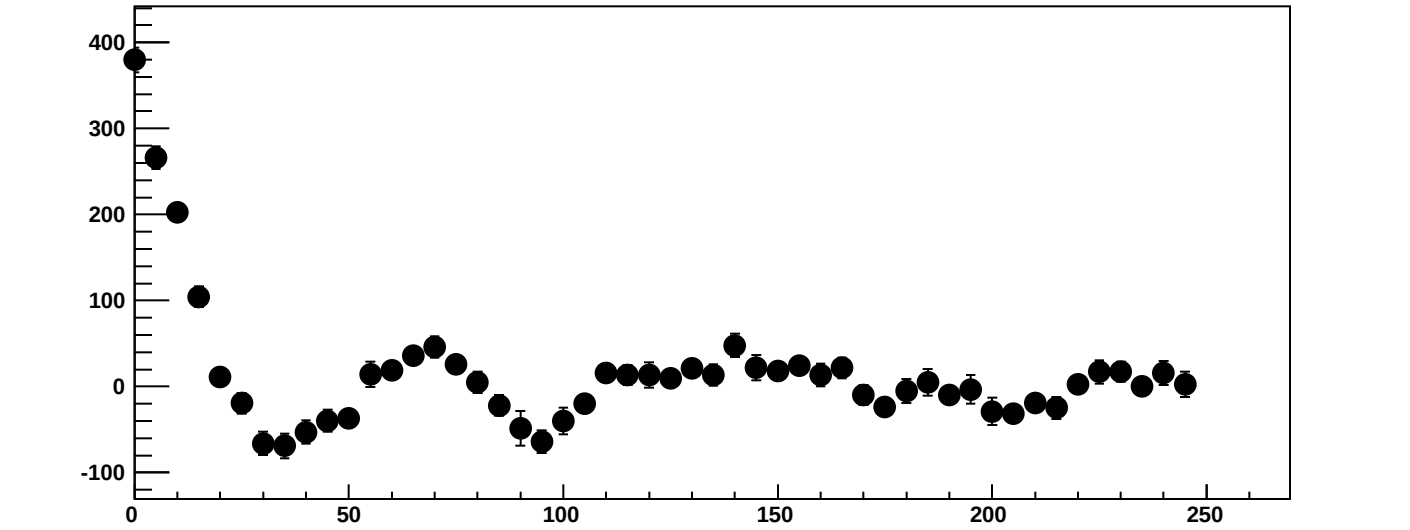
Chip 9, Channel 3, Enable 3, DAC=1600, ADC Mean vs Hold

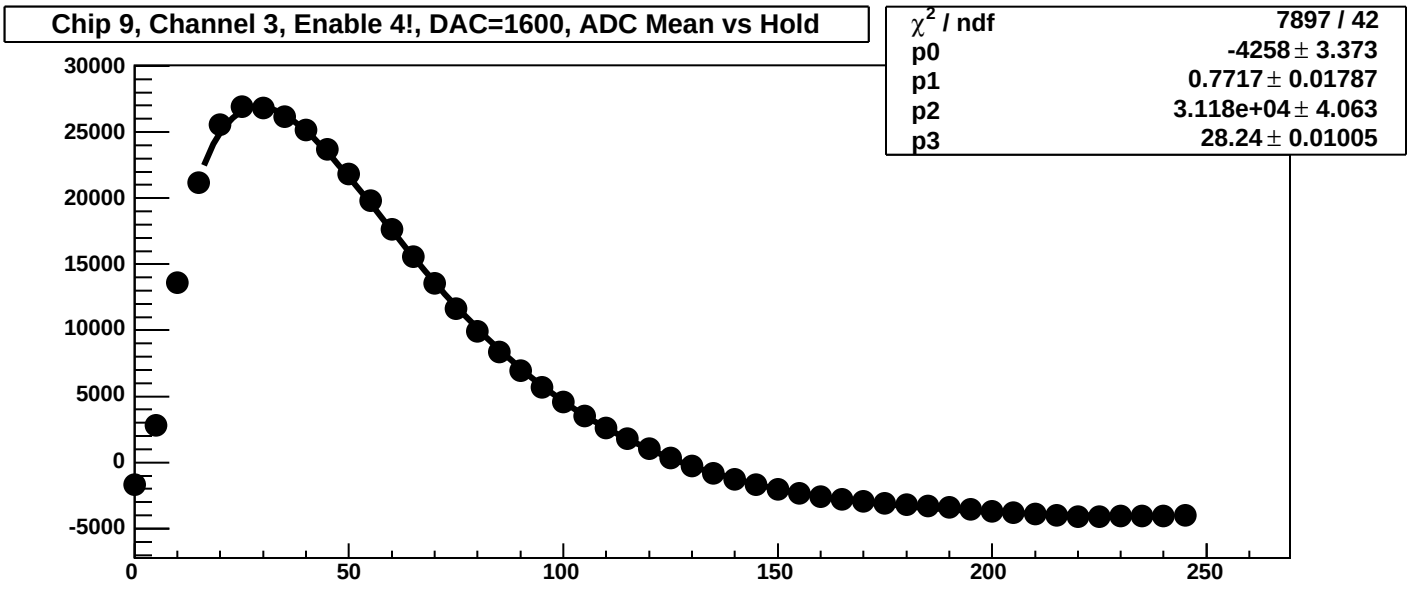


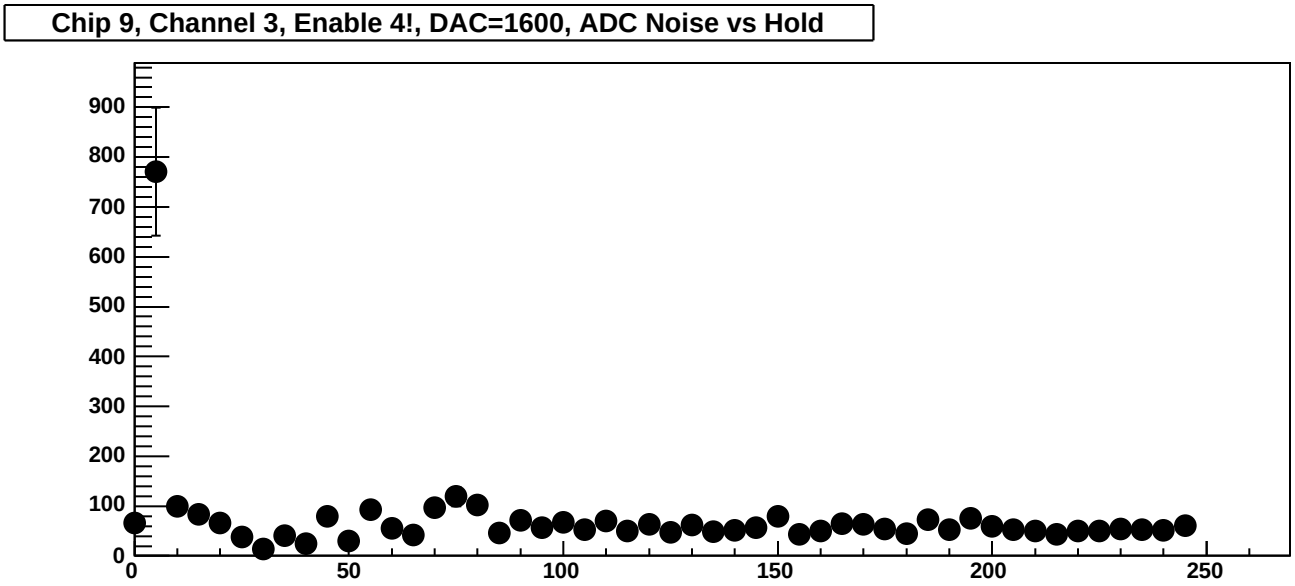
Chip 9, Channel 3, Enable 3, DAC=1600, ADC Noise vs Hold

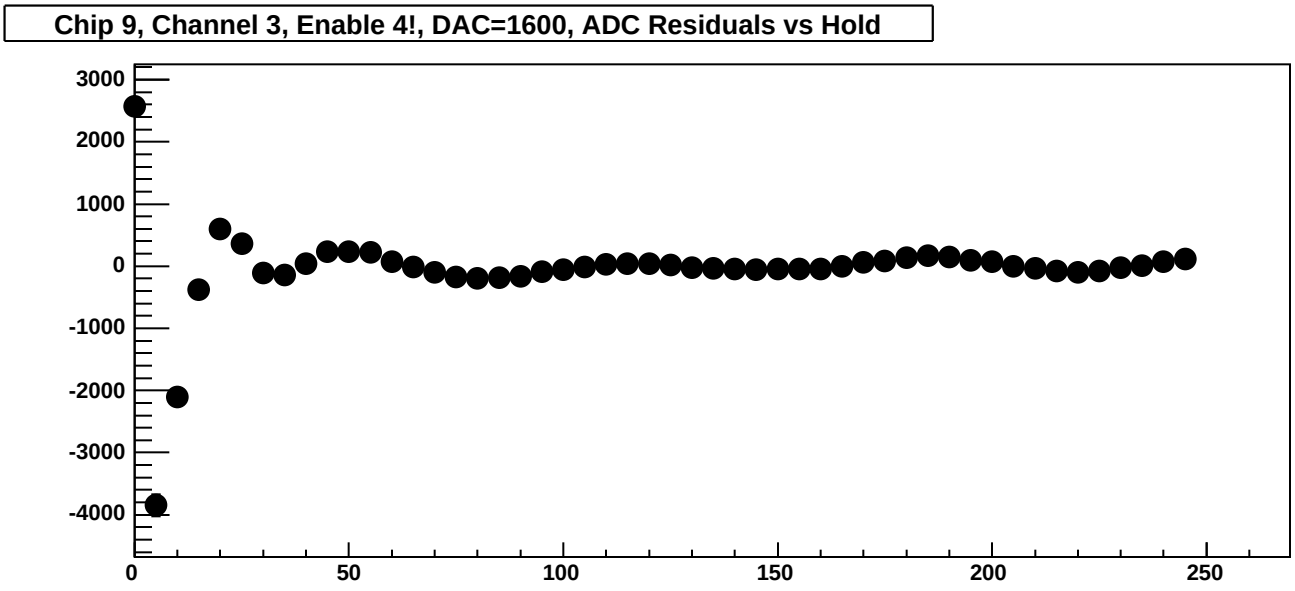


Chip 9, Channel 3, Enable 3, DAC=1600, ADC Residuals vs Hold

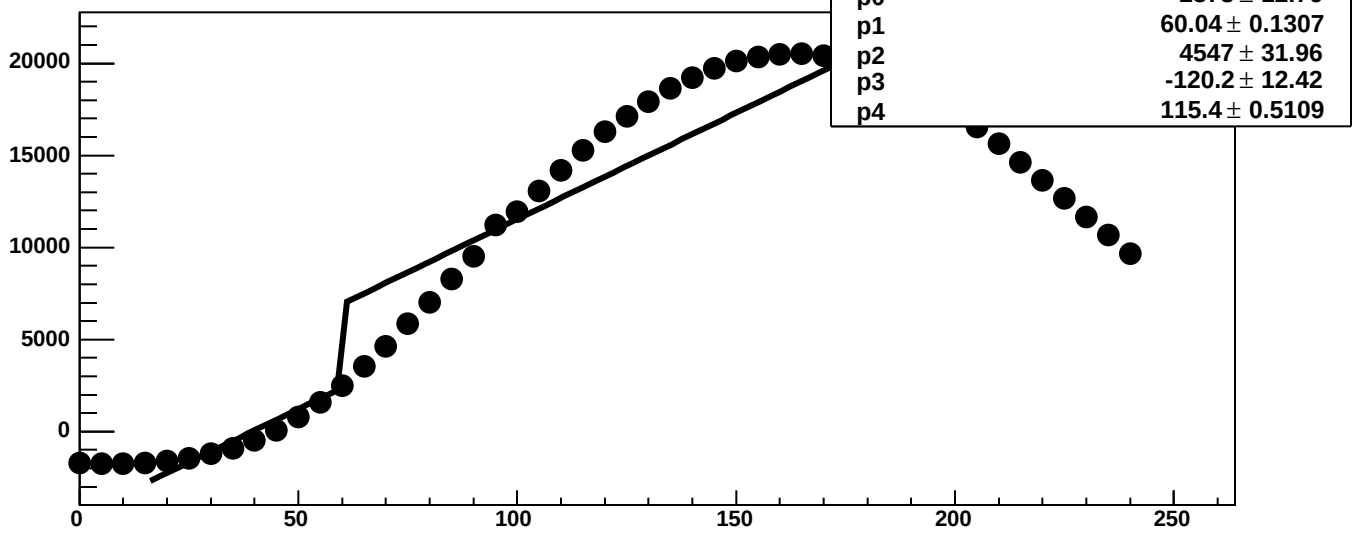



Chip 9, Channel 3, Enable 4!, DAC=1600, ADC Mean vs Hold
χ² / ndf 7897 / 42
p0 -4258 ± 3.373
p1 0.7717 ± 0.01787
p2 3.118e+04 ± 4.063
p3 28.24 ± 0.01005
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

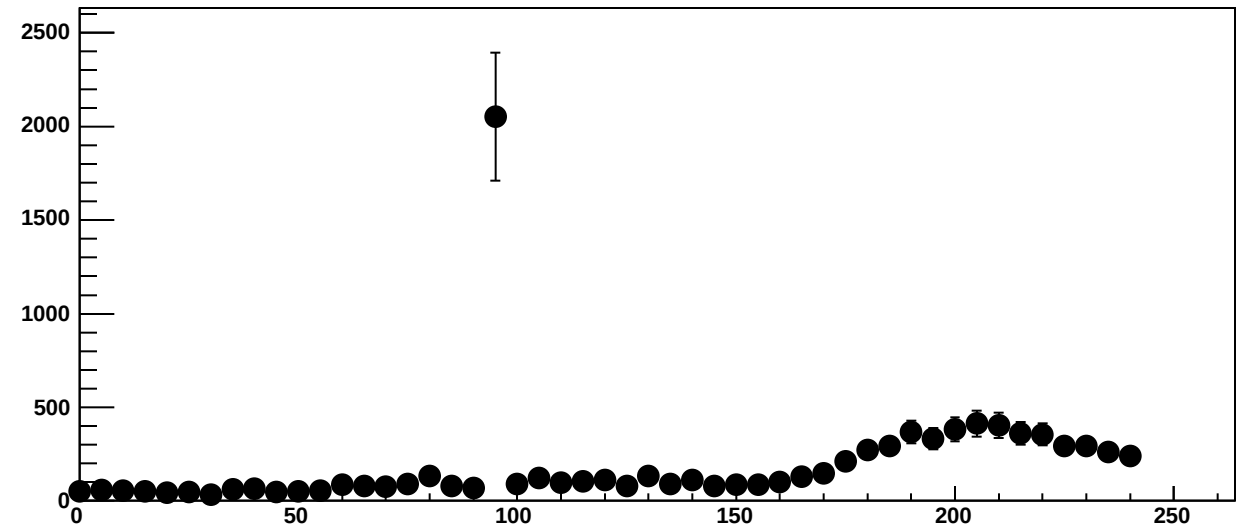

Chip 9, Channel 3, Enable 4!, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 9, Channel 3, Enable 4!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

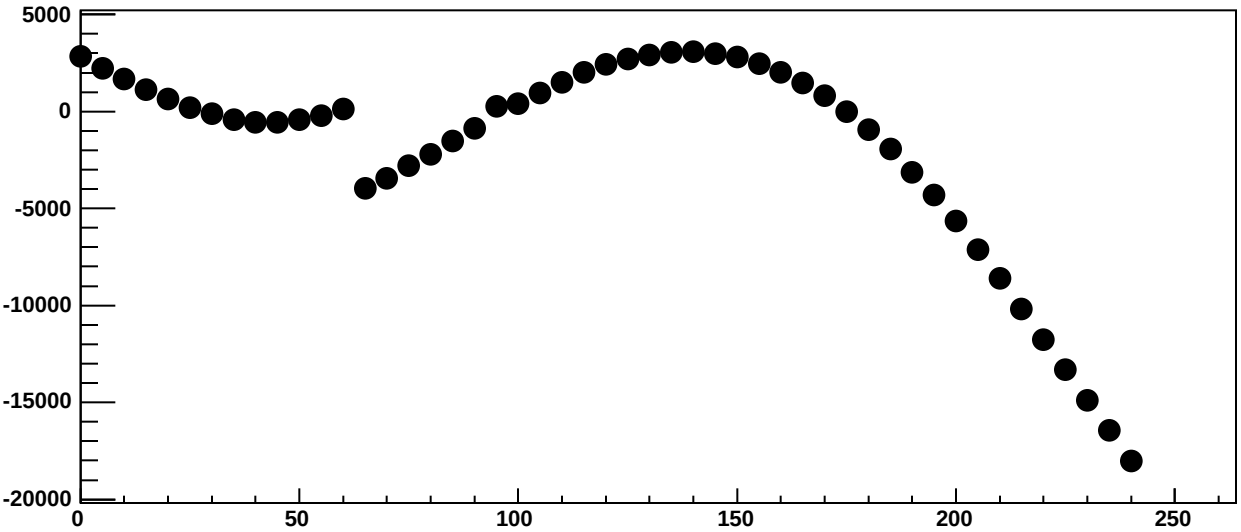
Chip 9, Channel 3, Enable 5, DAC=1600, ADC Mean vs Hold



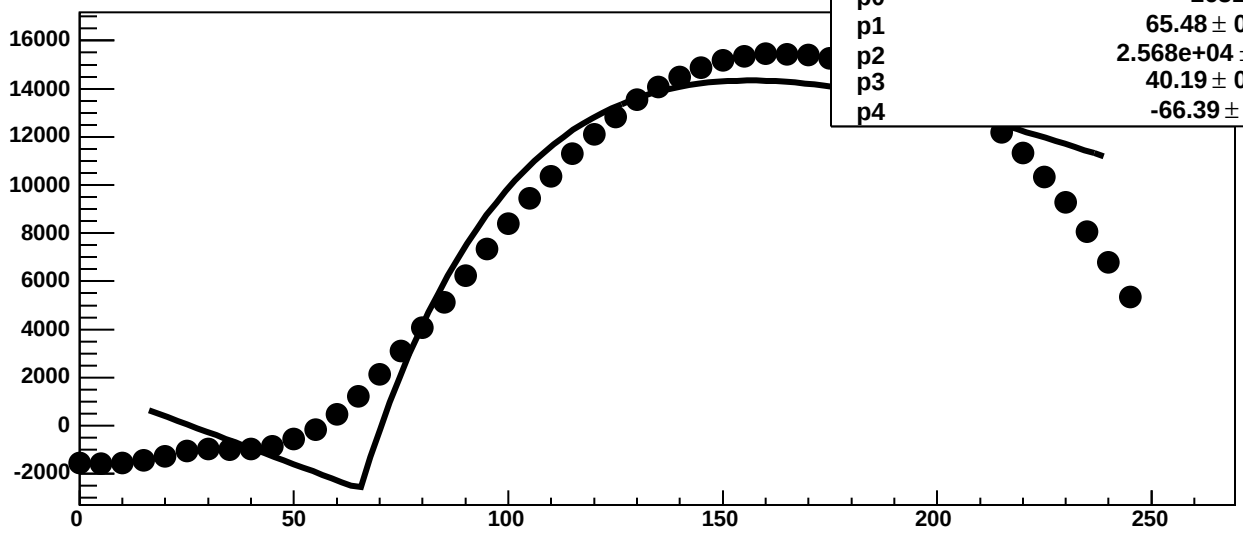
Chip 9, Channel 3, Enable 5, DAC=1600, ADC Noise vs Hold



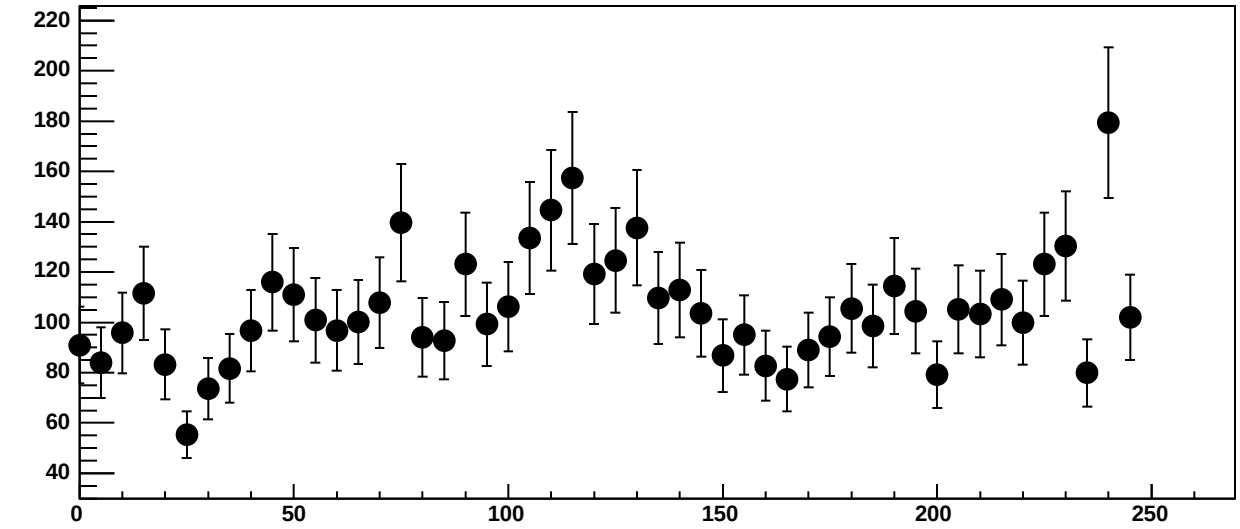
Chip 9, Channel 3, Enable 5, DAC=1600, ADC Residuals vs Hold



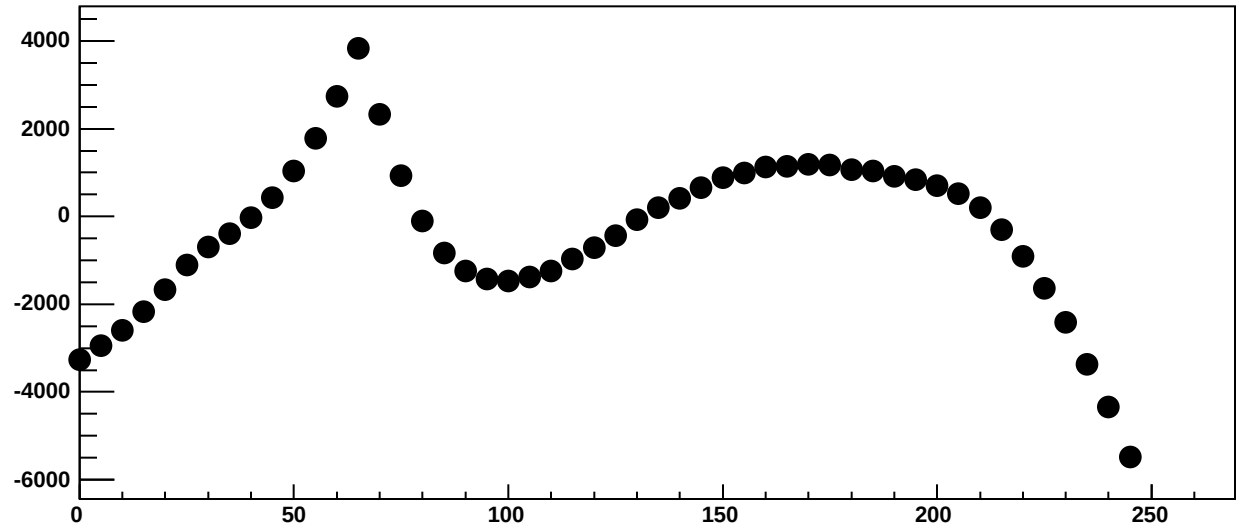
Chip 9, Channel 4, Enable 0, DAC=1600, ADC Mean vs Hold



Chip 9, Channel 4, Enable 0, DAC=1600, ADC Noise vs Hold

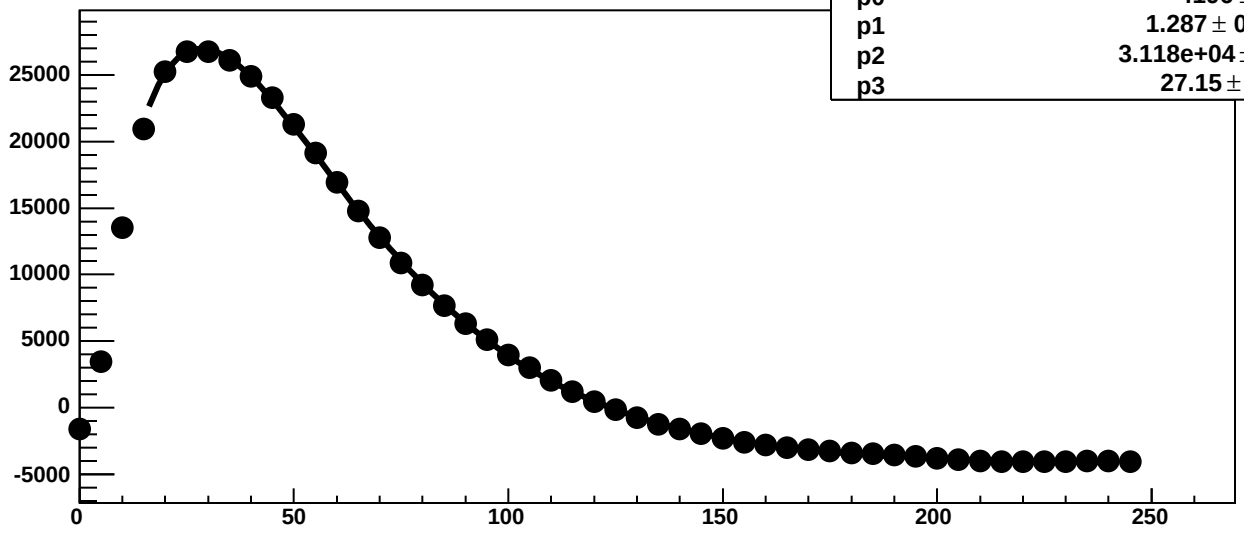


Chip 9, Channel 4, Enable 0, DAC=1600, ADC Residuals vs Hold

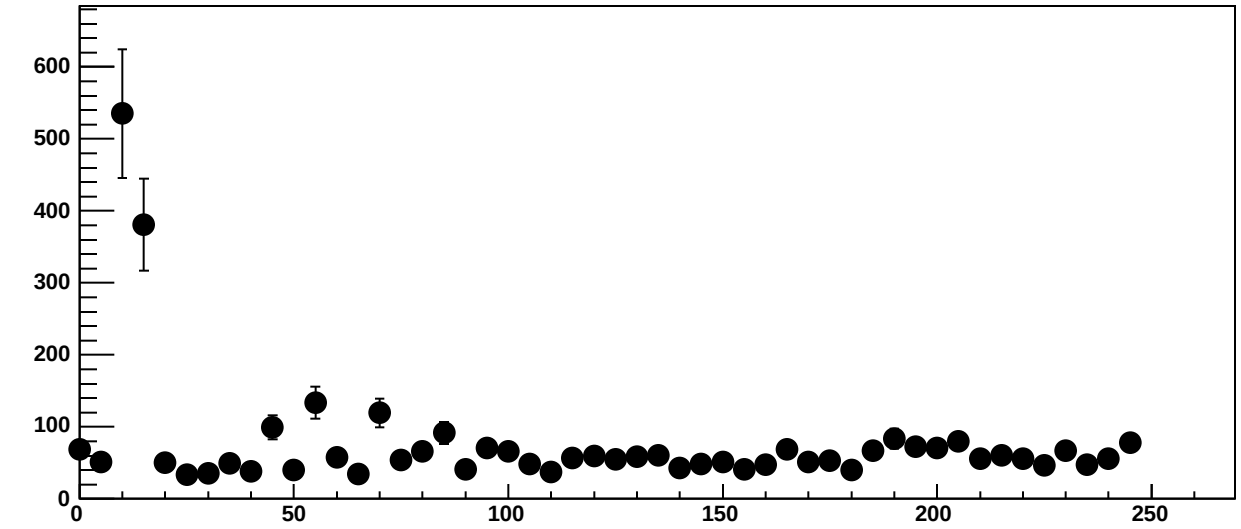


Chip 9, Channel 4, Enable 1!, DAC=1600, ADC Mean vs Hold

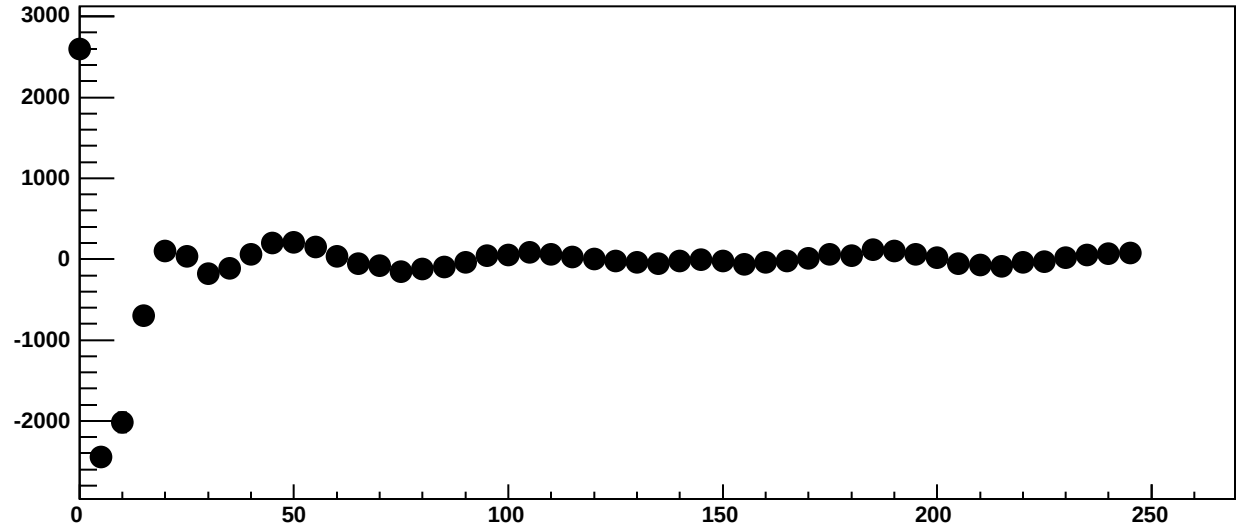
χ^2 / ndf	2252 / 42
p0	-4196 ± 3.508
p1	1.287 ± 0.02212
p2	3.118e+04 ± 4.943
p3	27.15 ± 0.0113

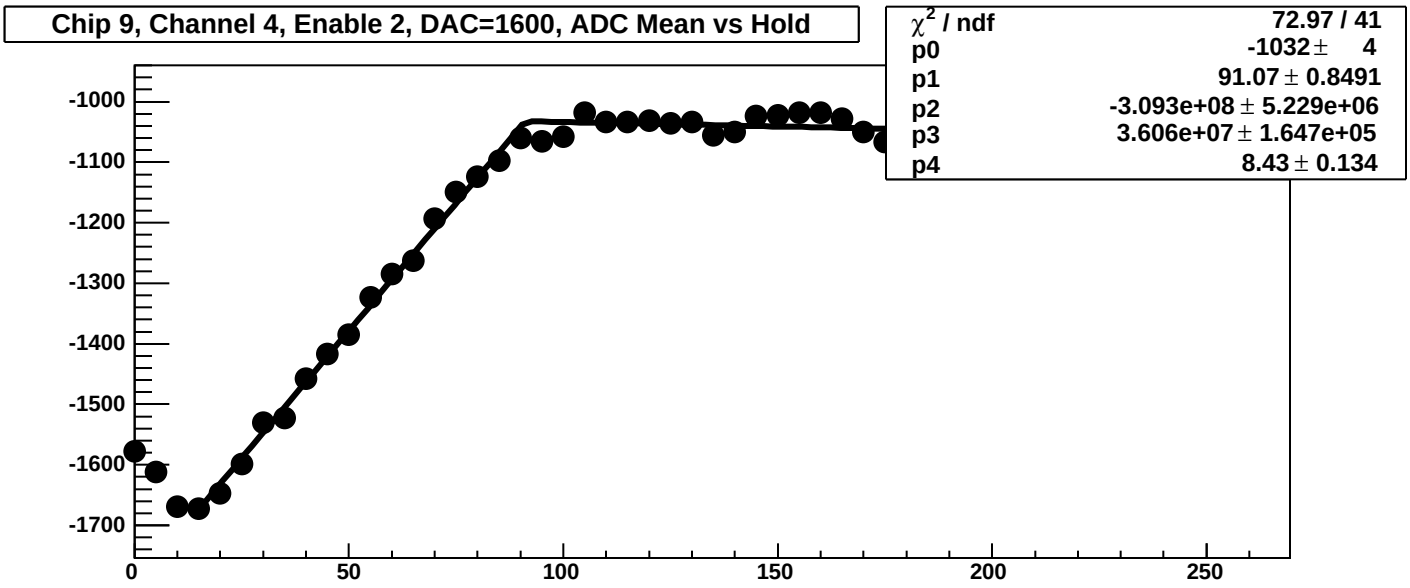


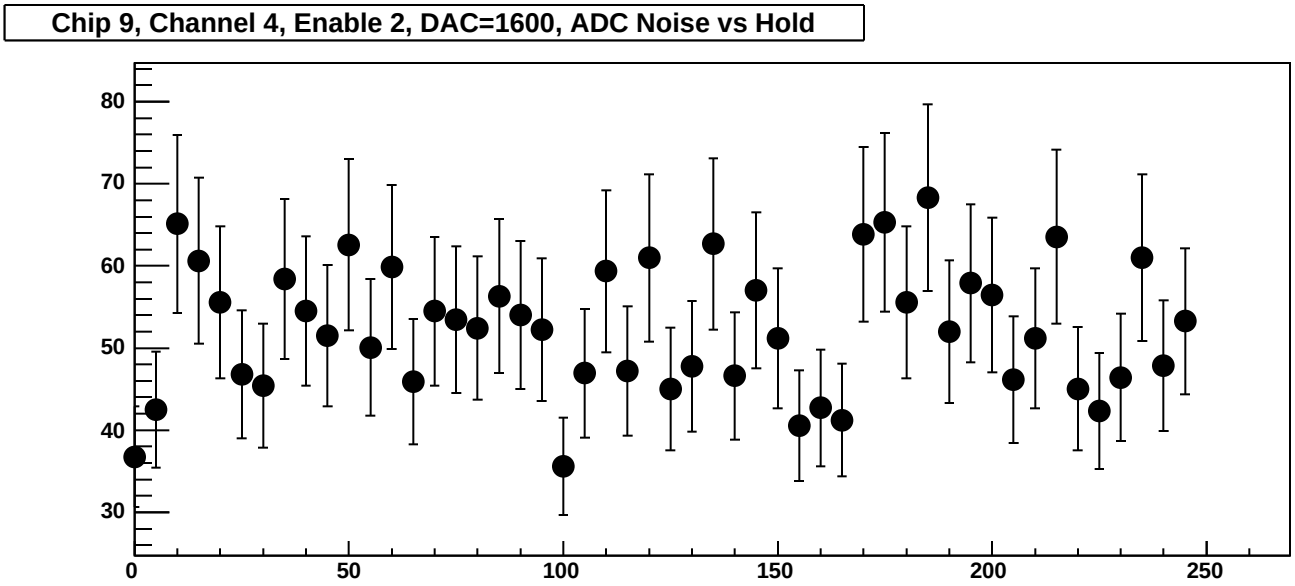
Chip 9, Channel 4, Enable 1!, DAC=1600, ADC Noise vs Hold

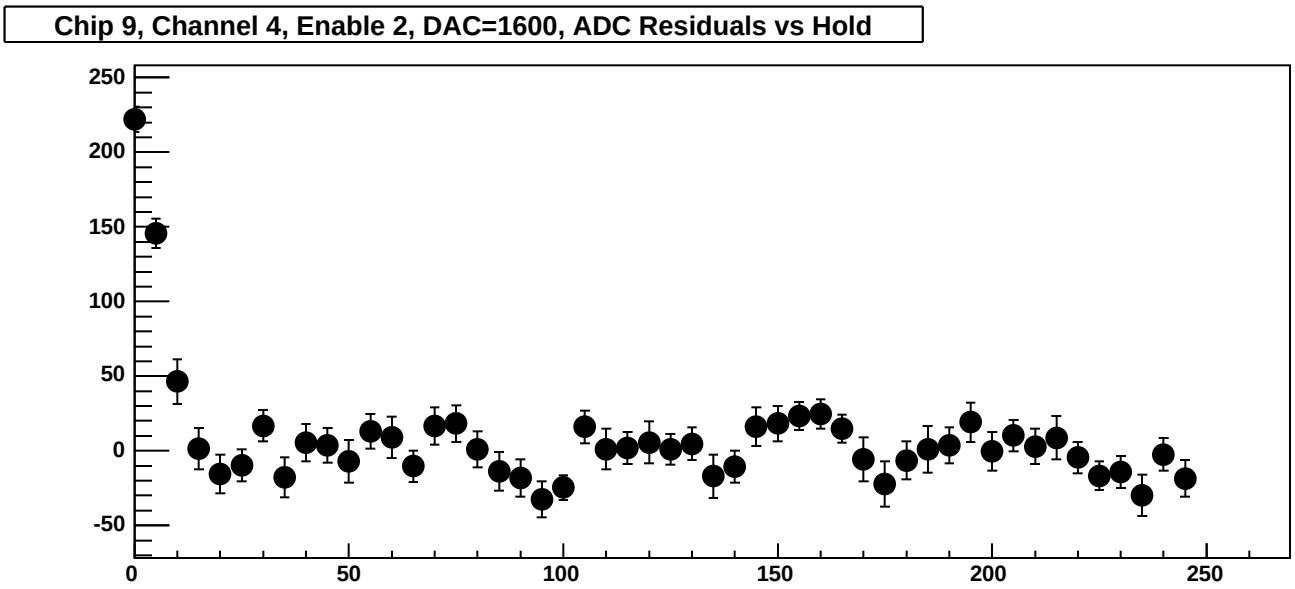


Chip 9, Channel 4, Enable 1!, DAC=1600, ADC Residuals vs Hold

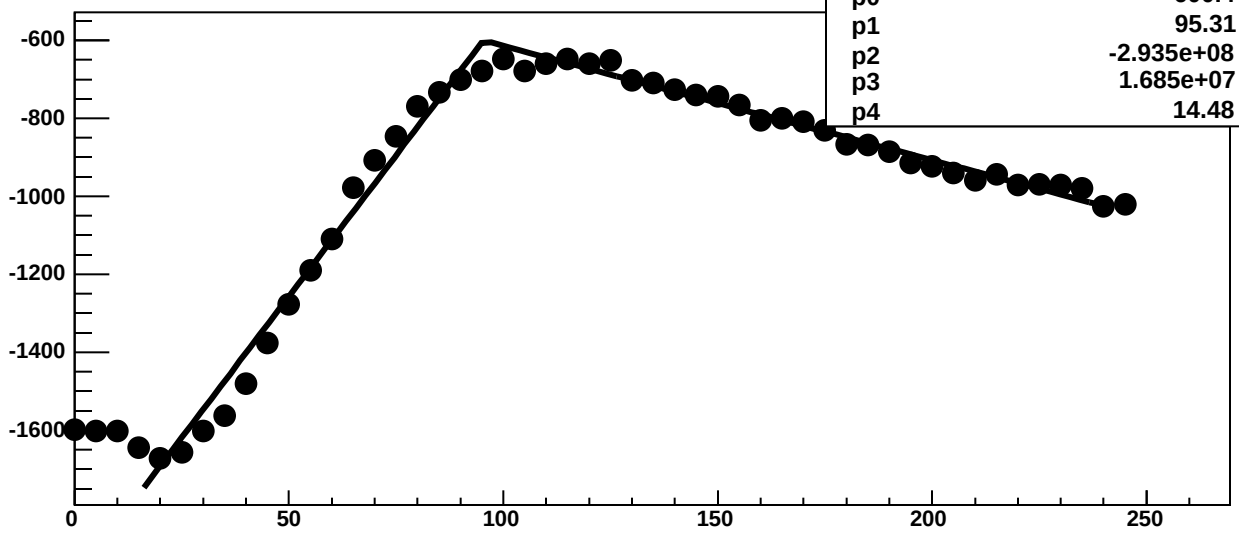



Chip 9, Channel 4, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 72.97 / 41
p0 -1032 ± 4
p1 91.07 ± 0.8491
p2 -3.093e+08 ± 5.229e+06
p3 3.606e+07 ± 1.647e+05
p4 8.43 ± 0.134
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
50
100
150
200
250

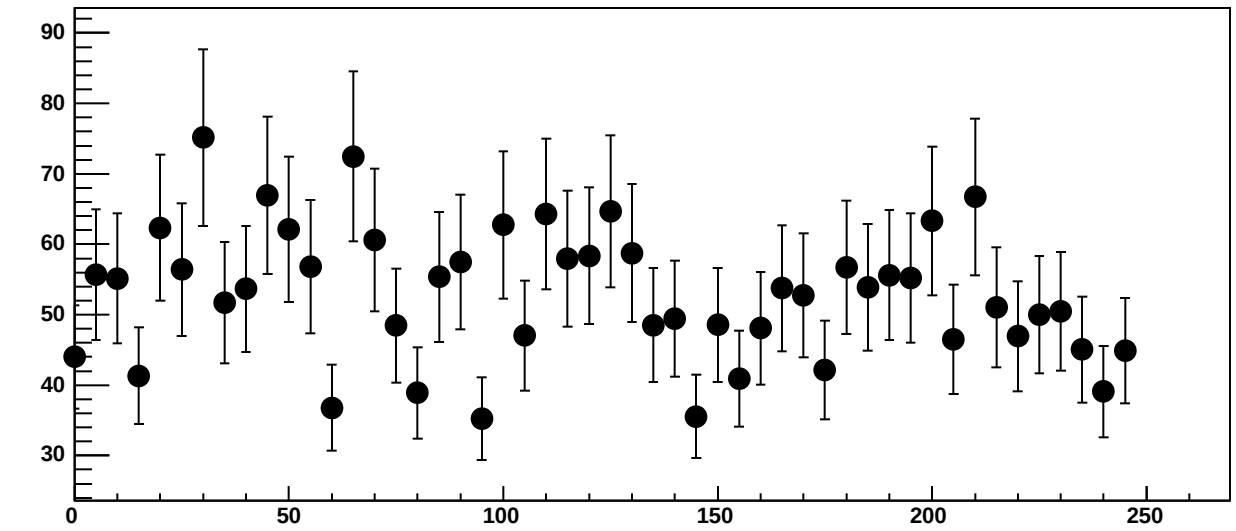

Chip 9, Channel 4, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 9, Channel 4, Enable 2, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
0
50
100
150
200
250

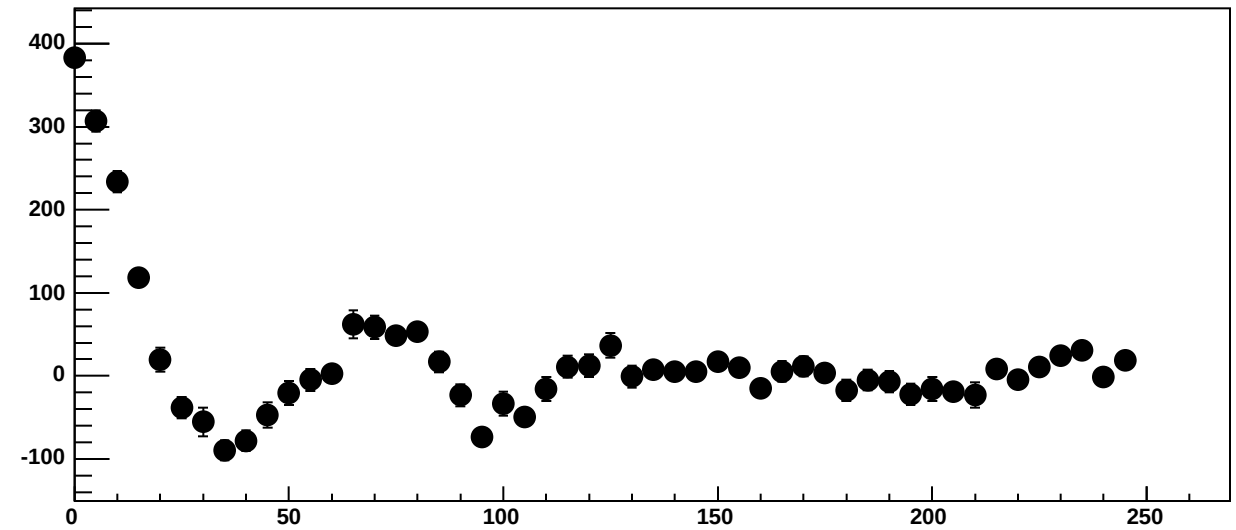
Chip 9, Channel 4, Enable 3, DAC=1600, ADC Mean vs Hold



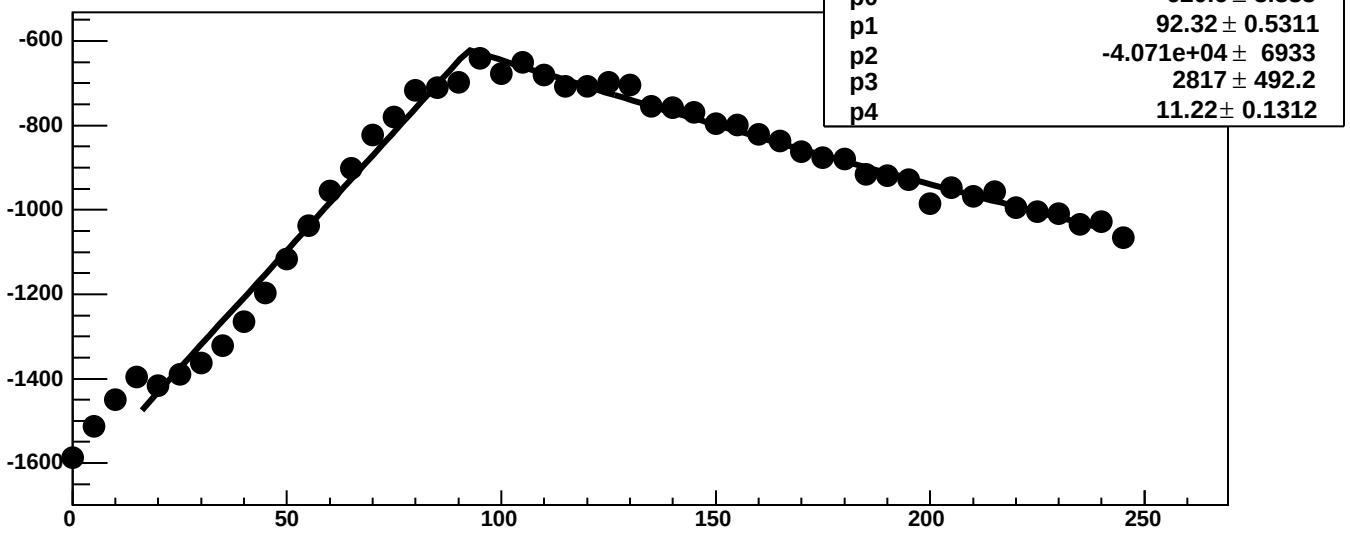
Chip 9, Channel 4, Enable 3, DAC=1600, ADC Noise vs Hold



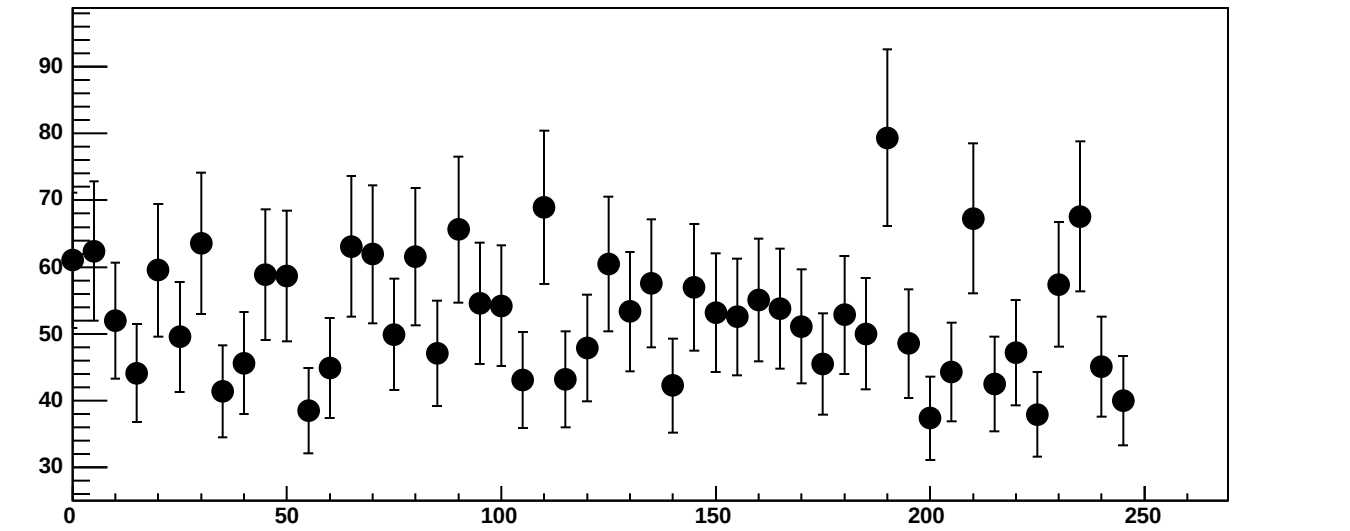
Chip 9, Channel 4, Enable 3, DAC=1600, ADC Residuals vs Hold



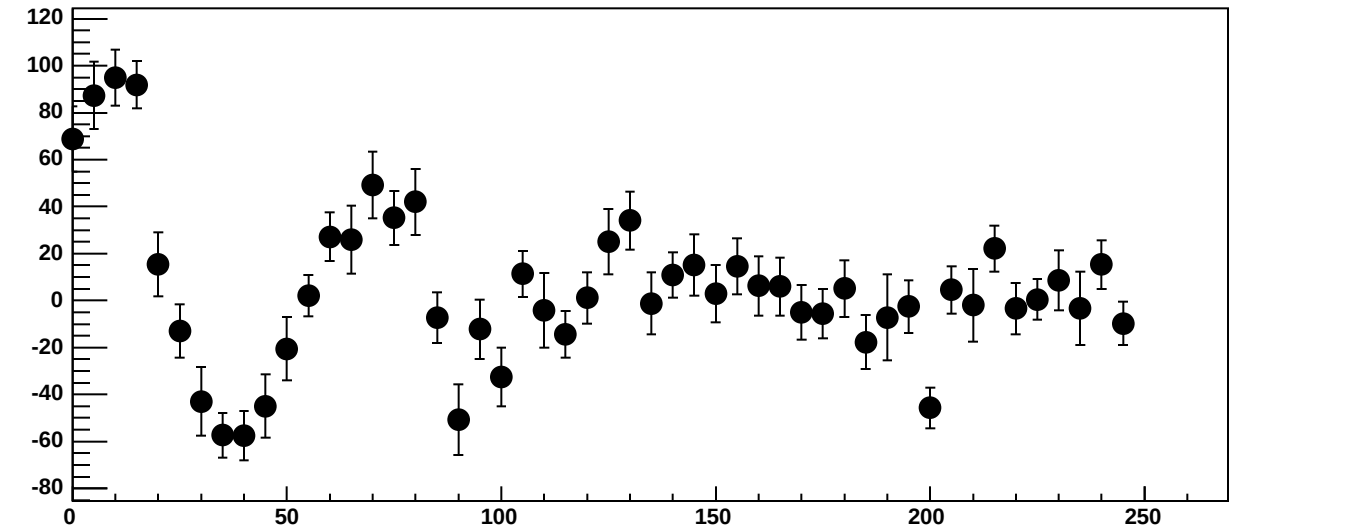
Chip 9, Channel 4, Enable 4, DAC=1600, ADC Mean vs Hold



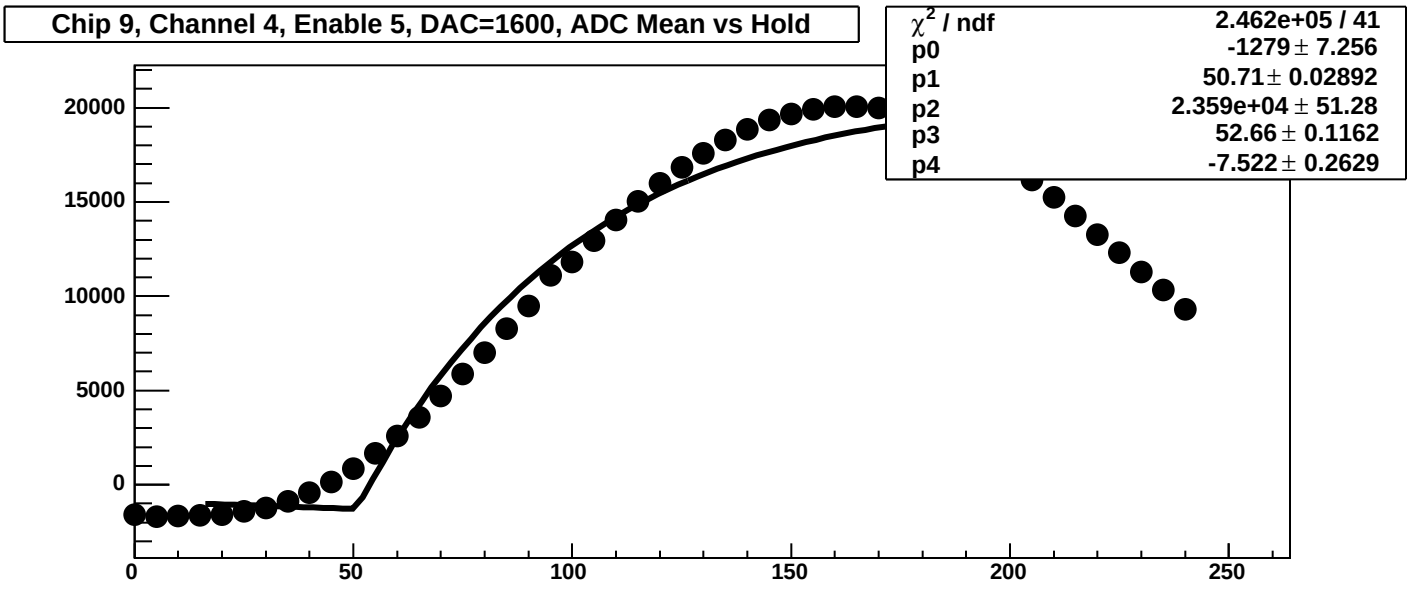
Chip 9, Channel 4, Enable 4, DAC=1600, ADC Noise vs Hold



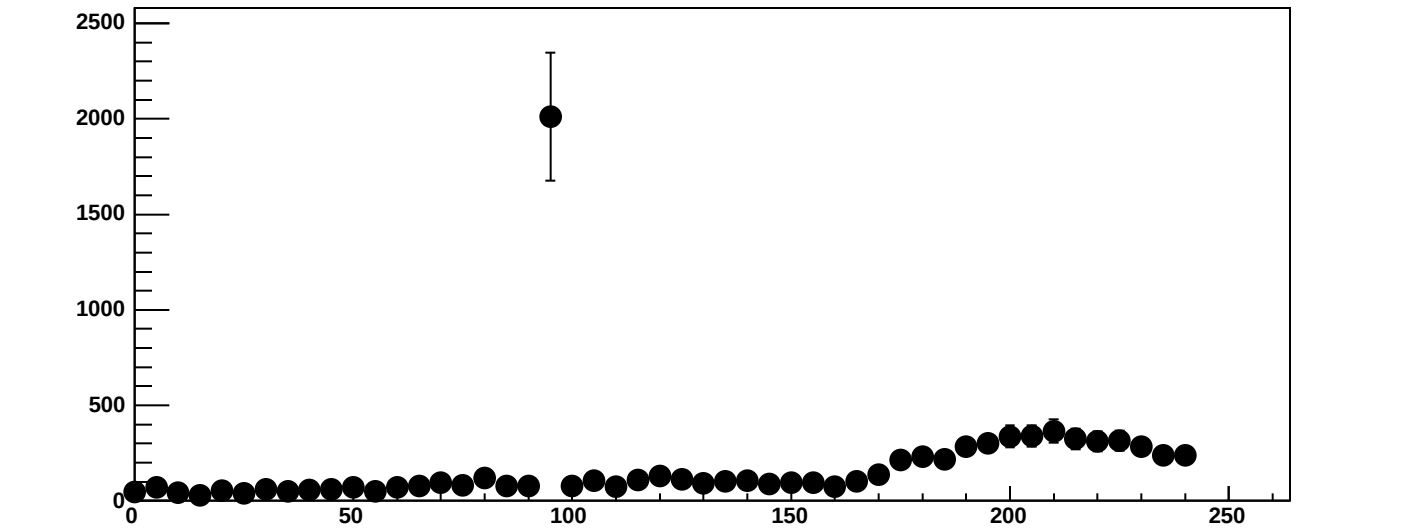
Chip 9, Channel 4, Enable 4, DAC=1600, ADC Residuals vs Hold



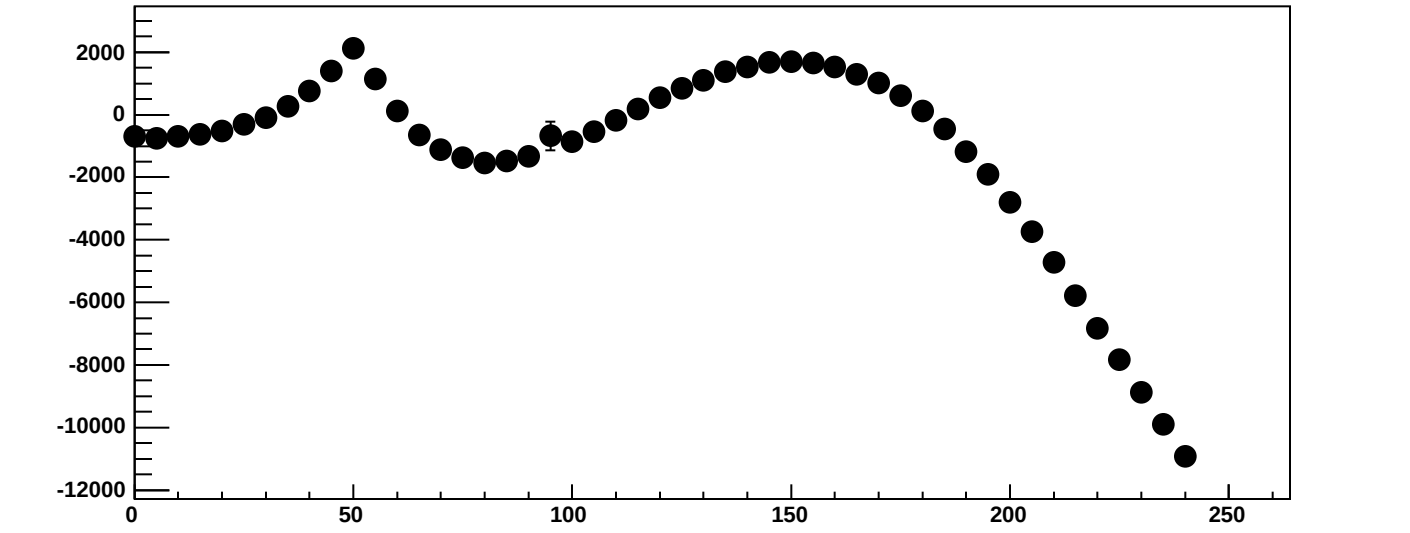
Chip 9, Channel 4, Enable 5, DAC=1600, ADC Mean vs Hold

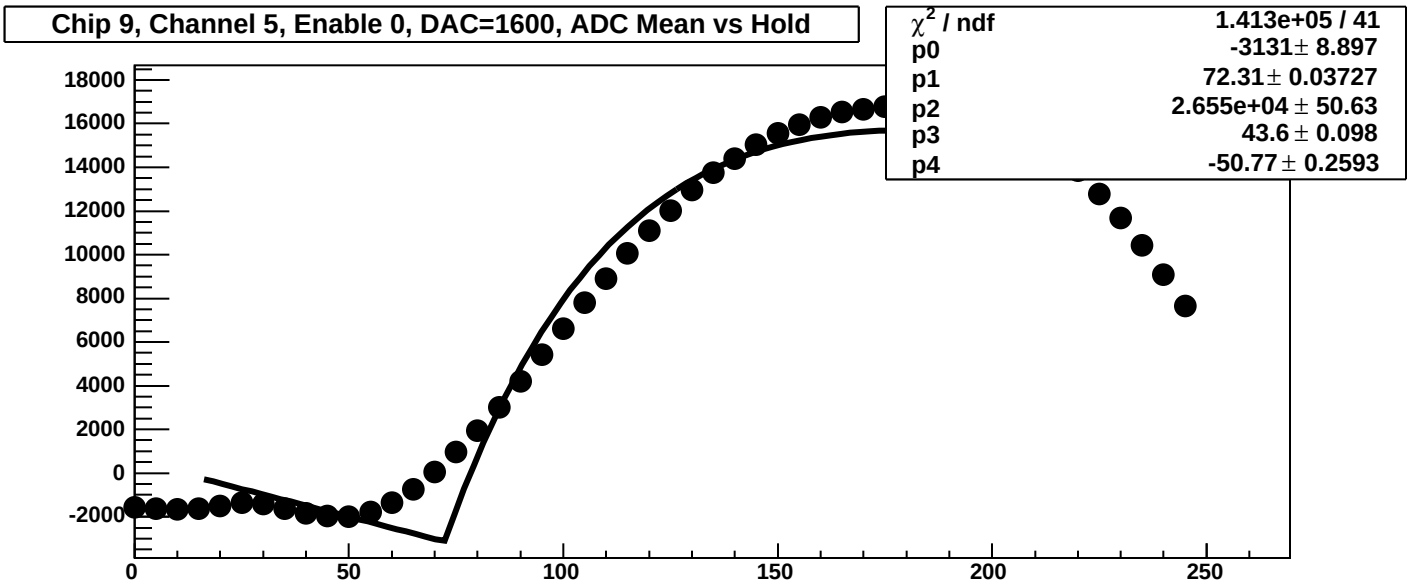


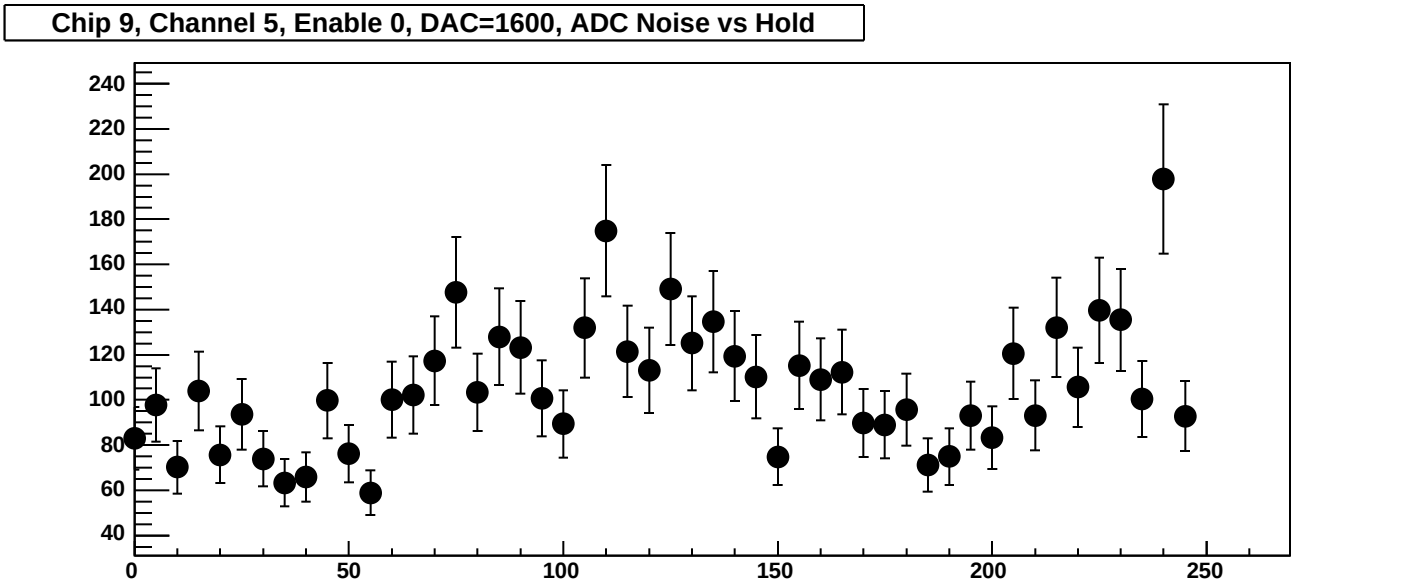
Chip 9, Channel 4, Enable 5, DAC=1600, ADC Noise vs Hold

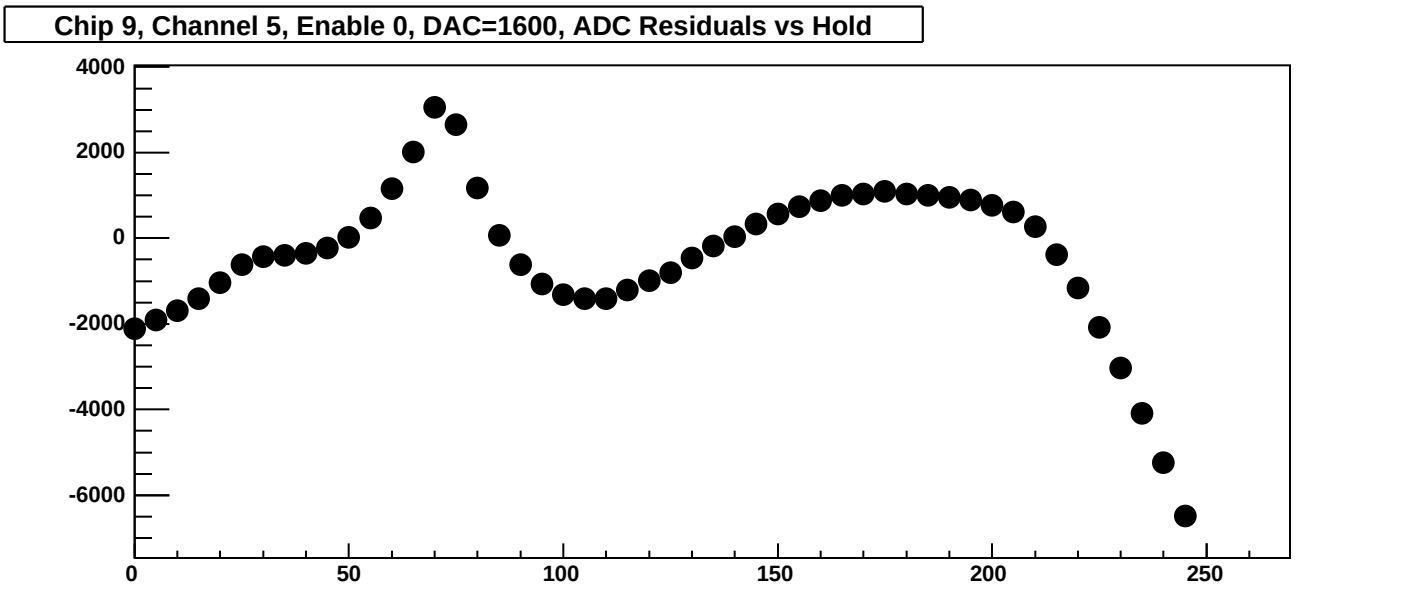


Chip 9, Channel 4, Enable 5, DAC=1600, ADC Residuals vs Hold

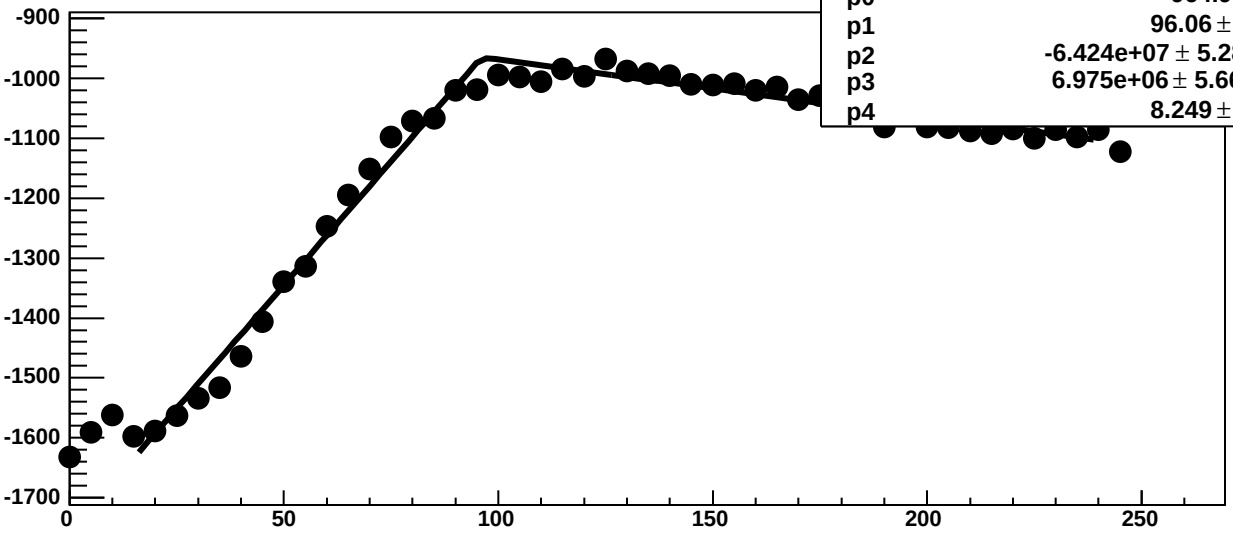



Chip 9, Channel 5, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.413e+05 / 41
p0 -3131 ± 8.897
p1 72.31 ± 0.03727
p2 2.655e+04 ± 50.63
p3 43.6 ± 0.098
p4 -50.77 ± 0.2593
18000
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

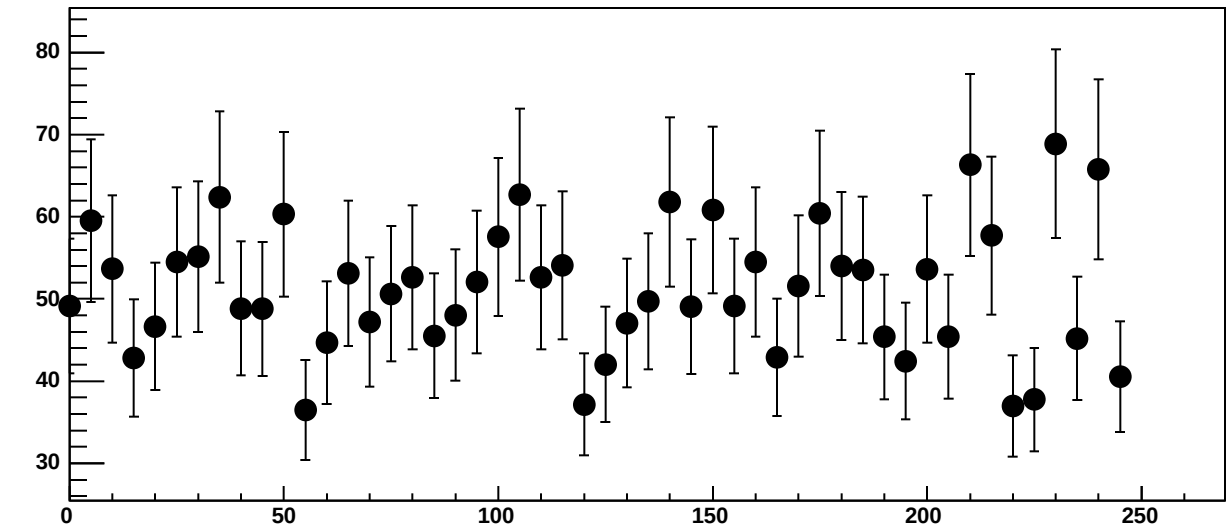

Chip 9, Channel 5, Enable 0, DAC=1600, ADC Noise vs Hold
240
220
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250


Chip 9, Channel 5, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

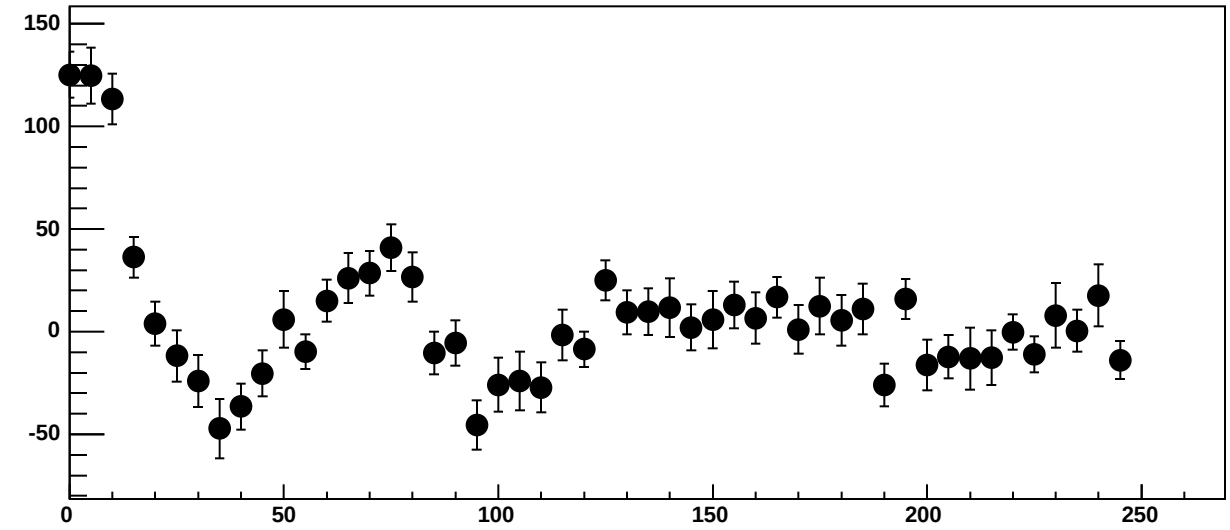
Chip 9, Channel 5, Enable 1, DAC=1600, ADC Mean vs Hold



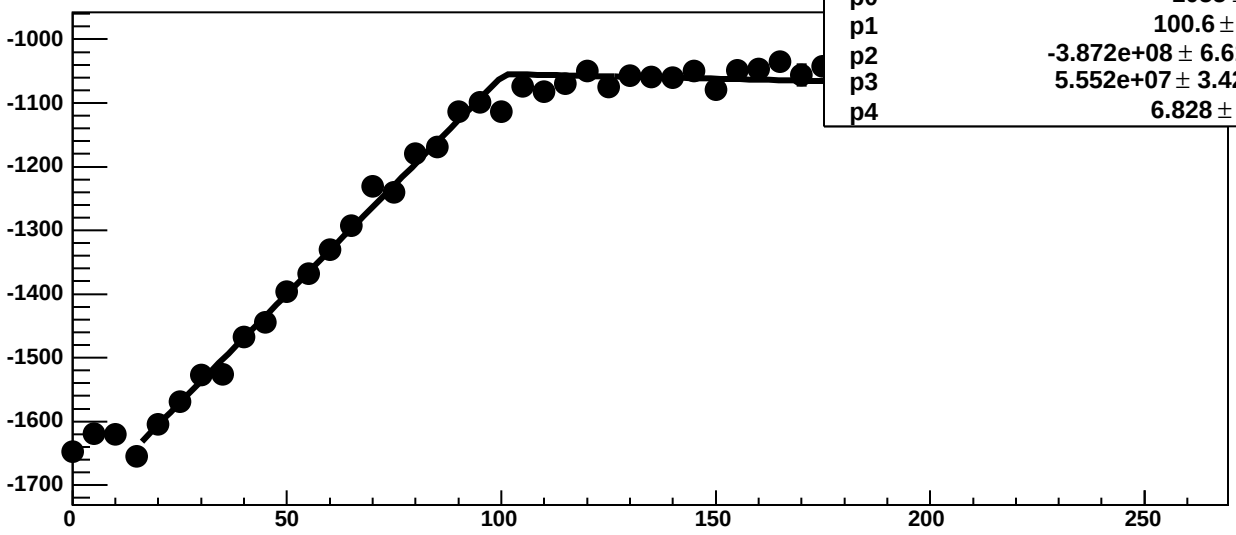
Chip 9, Channel 5, Enable 1, DAC=1600, ADC Noise vs Hold



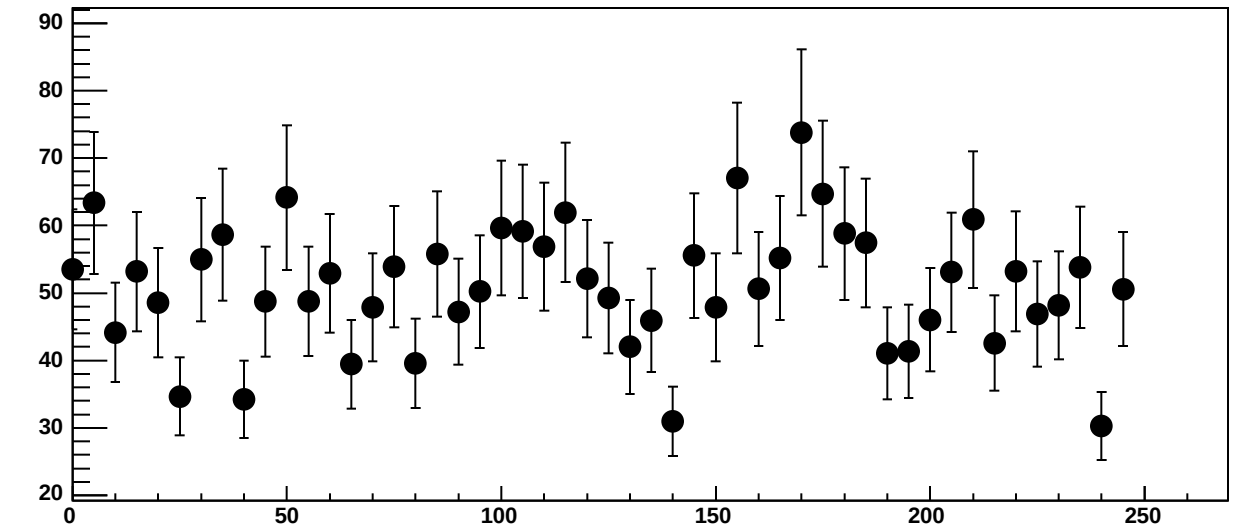
Chip 9, Channel 5, Enable 1, DAC=1600, ADC Residuals vs Hold



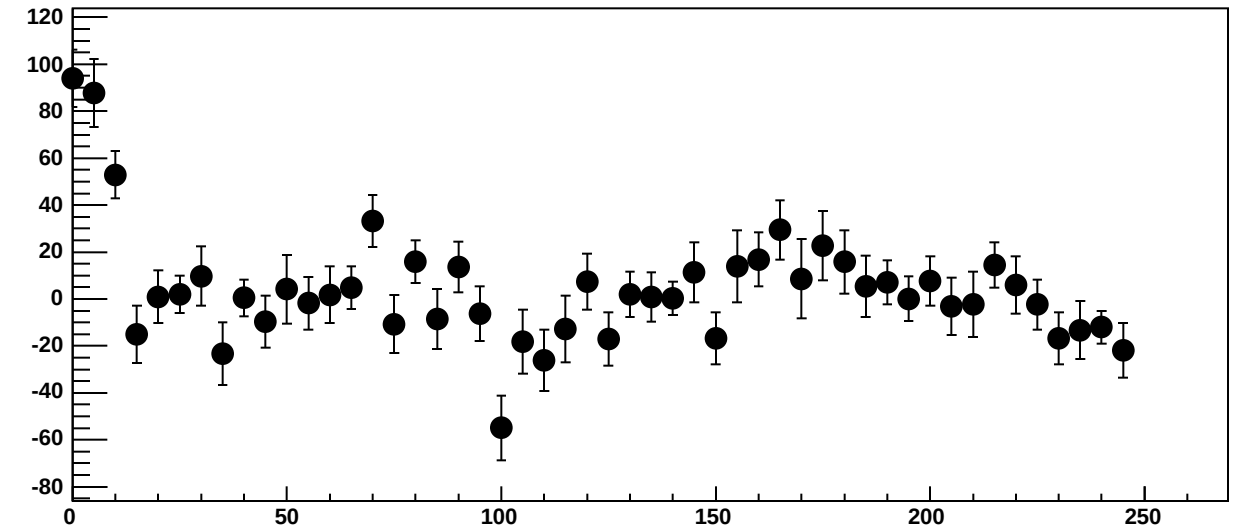
Chip 9, Channel 5, Enable 2, DAC=1600, ADC Mean vs Hold



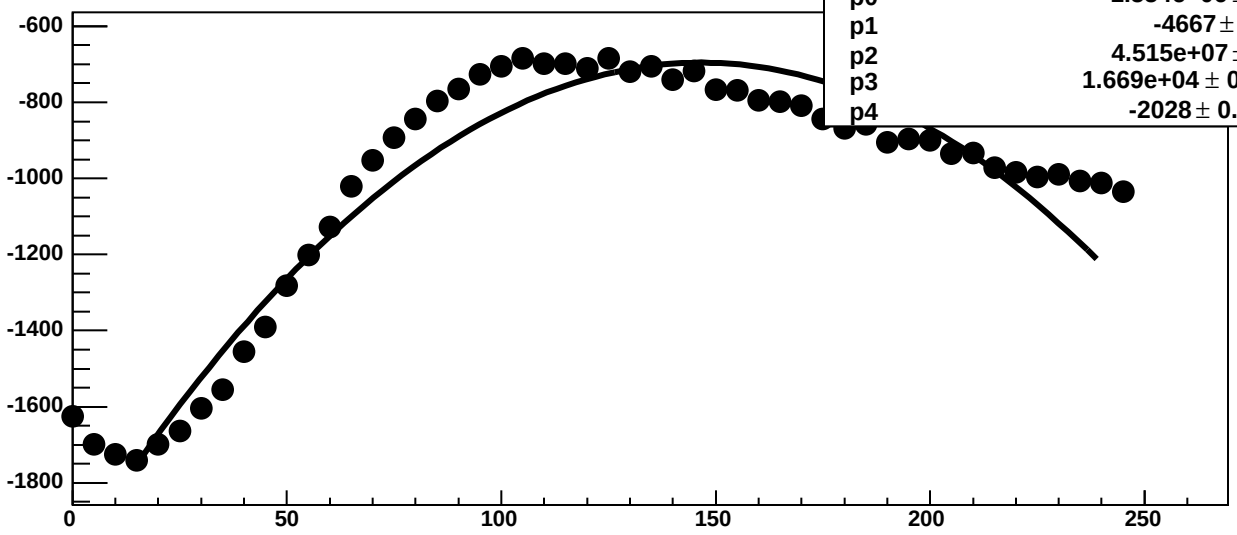
Chip 9, Channel 5, Enable 2, DAC=1600, ADC Noise vs Hold



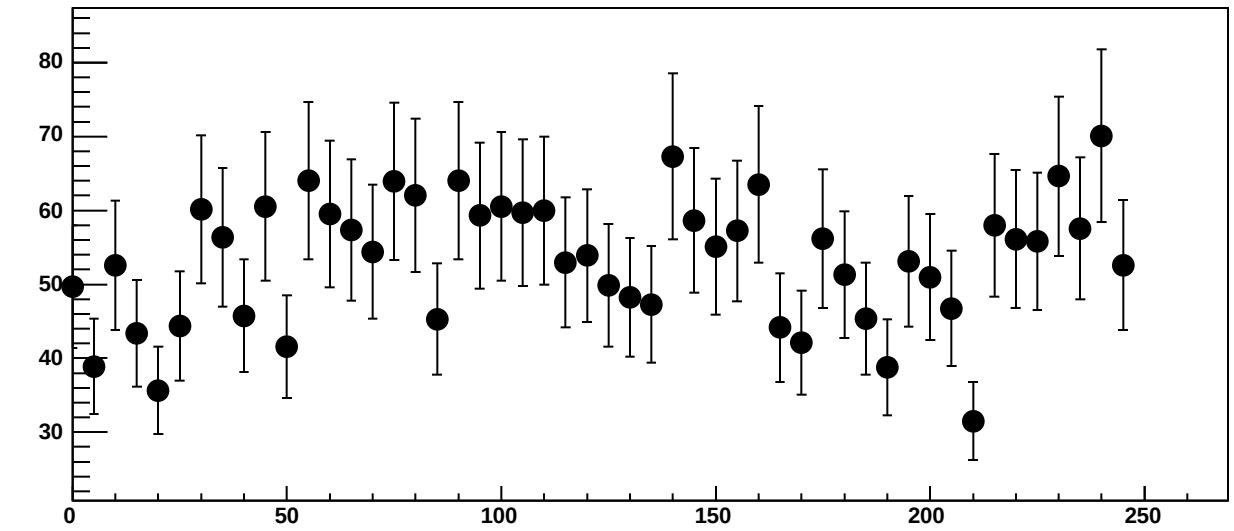
Chip 9, Channel 5, Enable 2, DAC=1600, ADC Residuals vs Hold



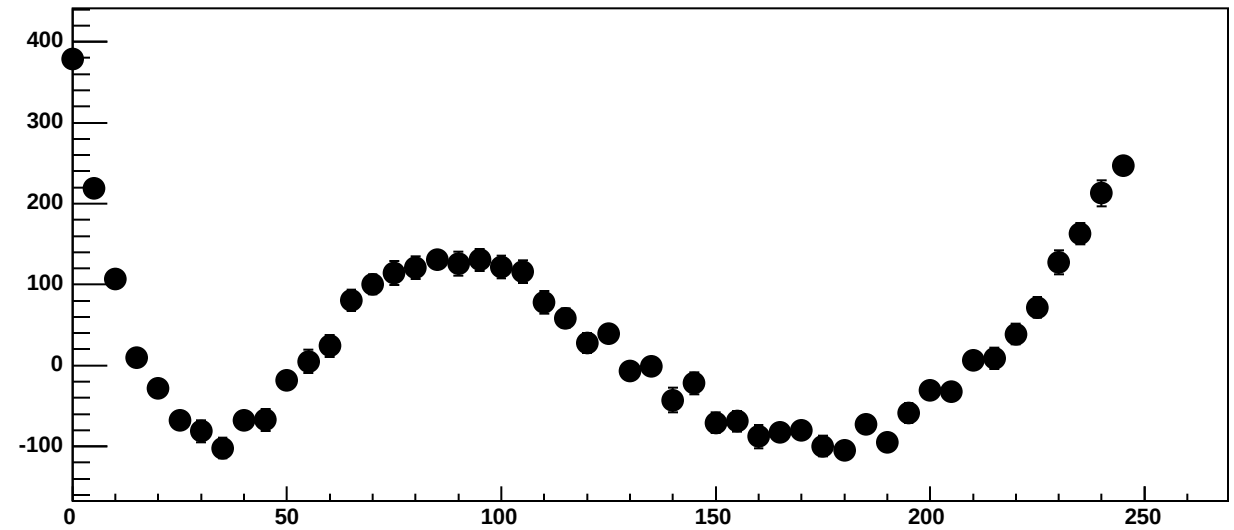
Chip 9, Channel 5, Enable 3, DAC=1600, ADC Mean vs Hold

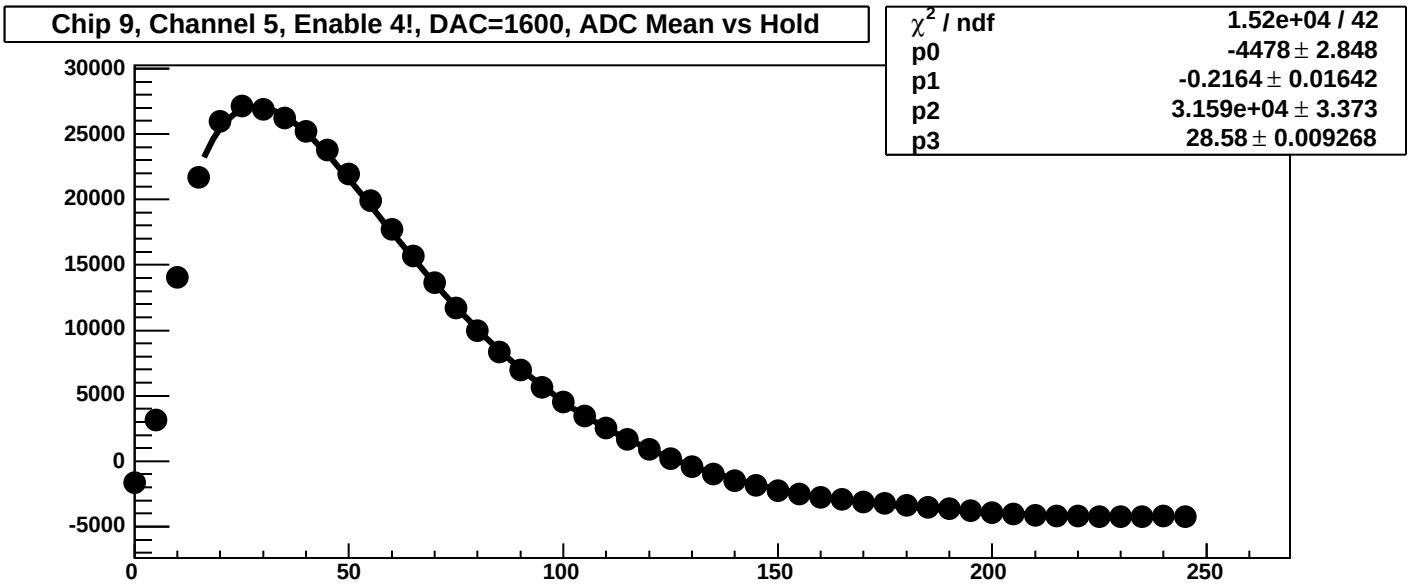


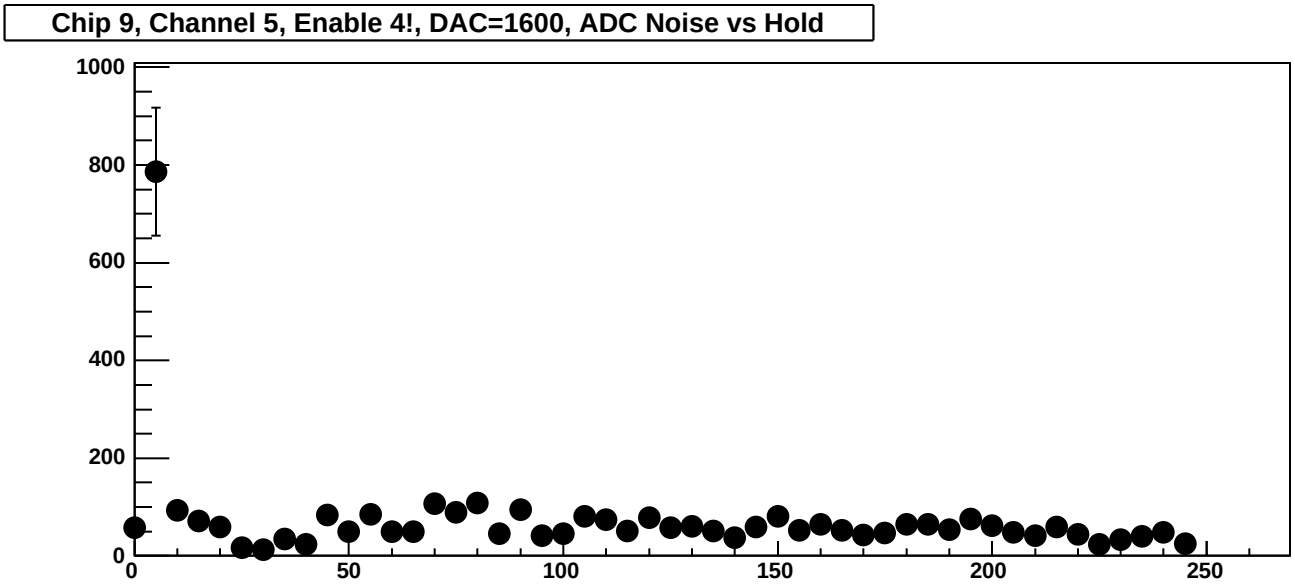
Chip 9, Channel 5, Enable 3, DAC=1600, ADC Noise vs Hold

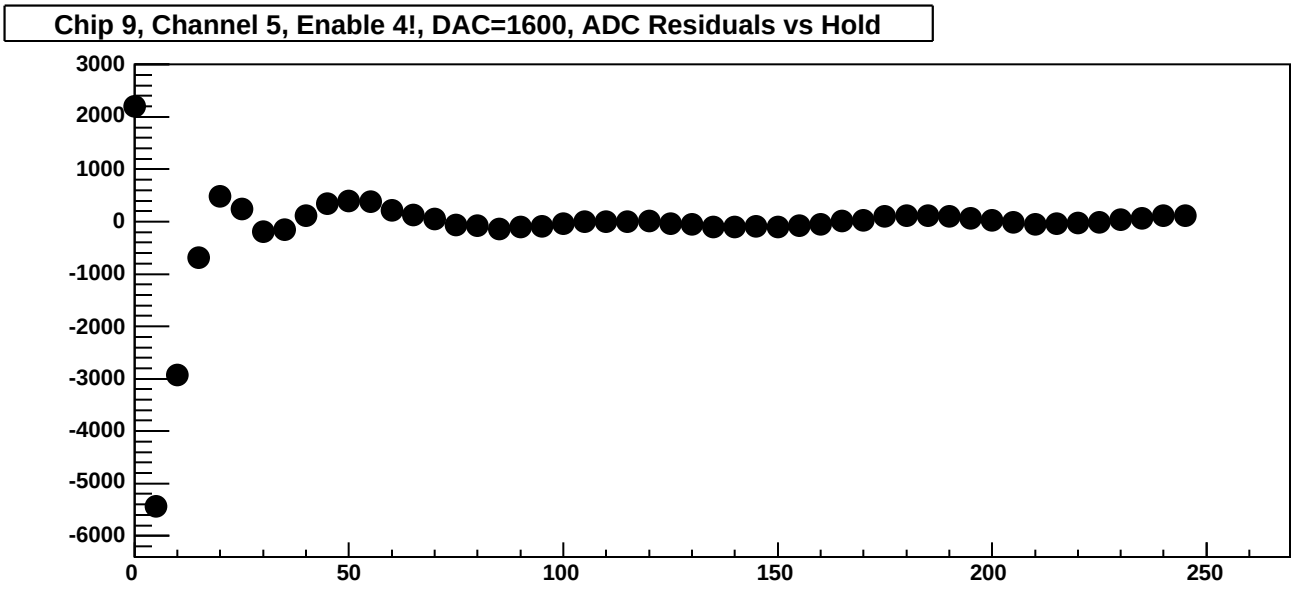


Chip 9, Channel 5, Enable 3, DAC=1600, ADC Residuals vs Hold

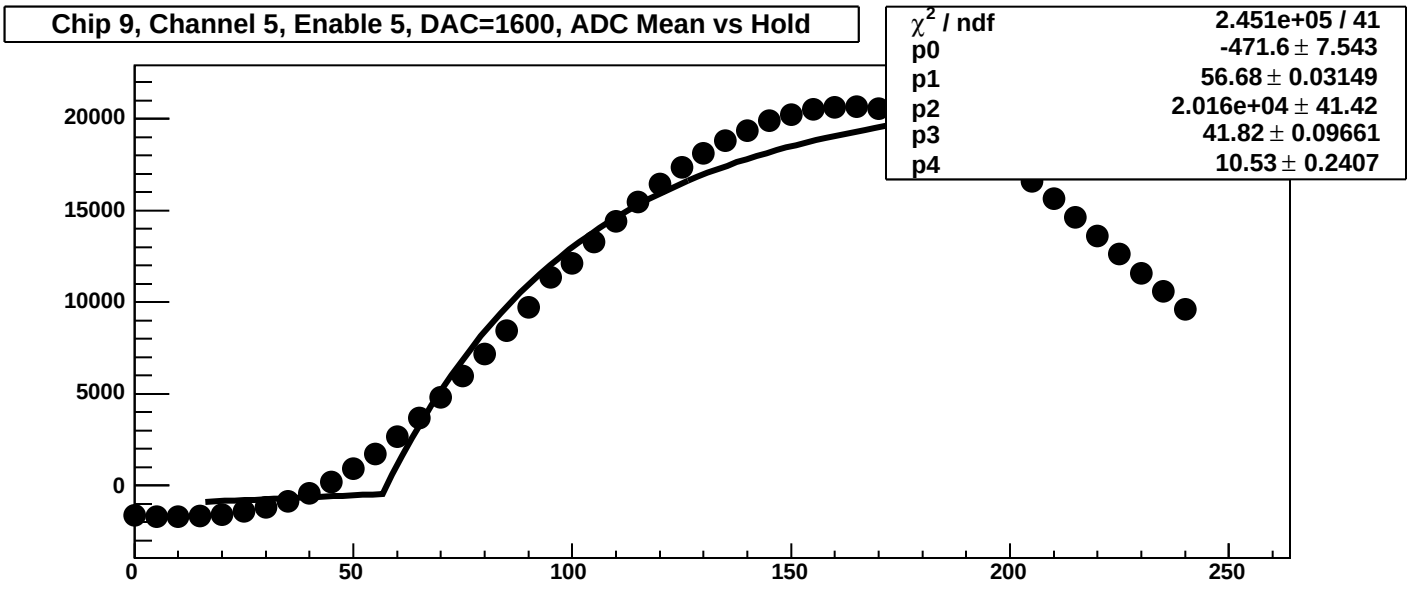



Chip 9, Channel 5, Enable 4!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.52e+04 / 42
p0 -4478 ± 2.848
p1 -0.2164 ± 0.01642
p2 3.159e+04 ± 3.373
p3 28.58 ± 0.009268
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

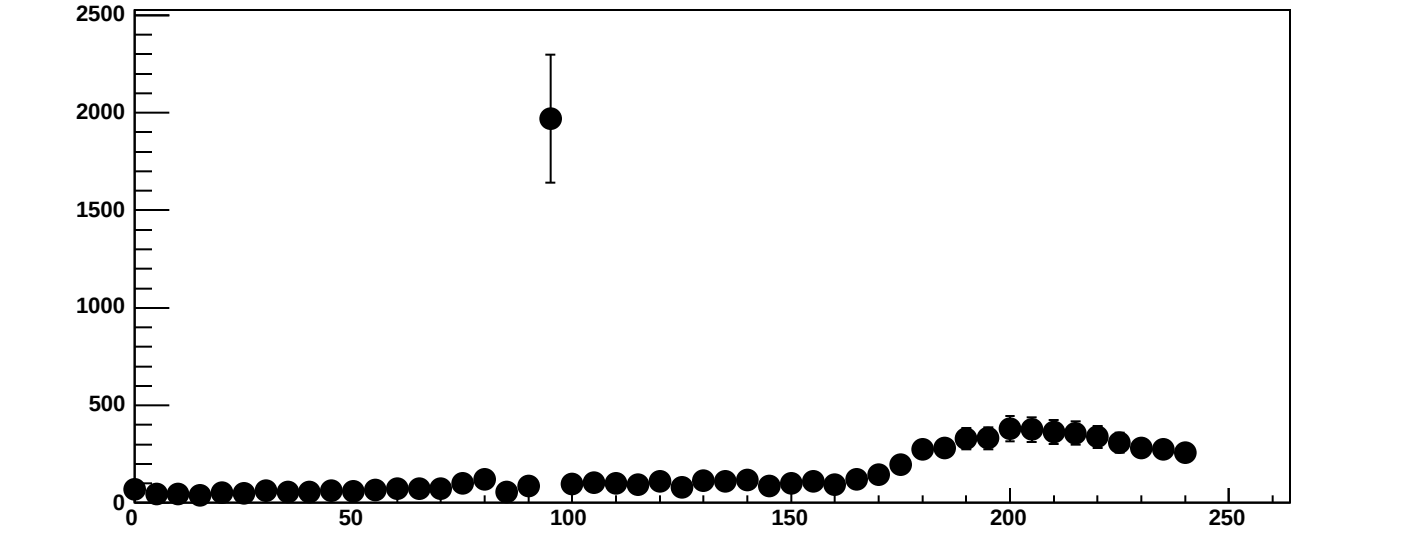

Chip 9, Channel 5, Enable 4!, DAC=1600, ADC Noise vs Hold
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 9, Channel 5, Enable 4!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
50
100
150
200
250

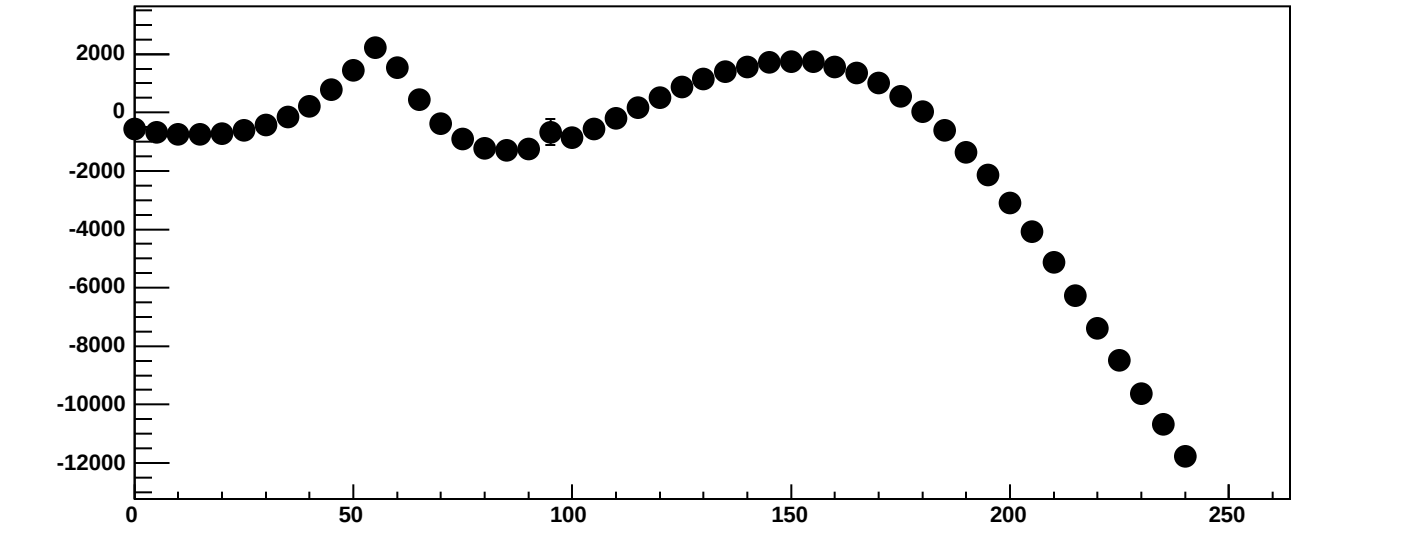
Chip 9, Channel 5, Enable 5, DAC=1600, ADC Mean vs Hold

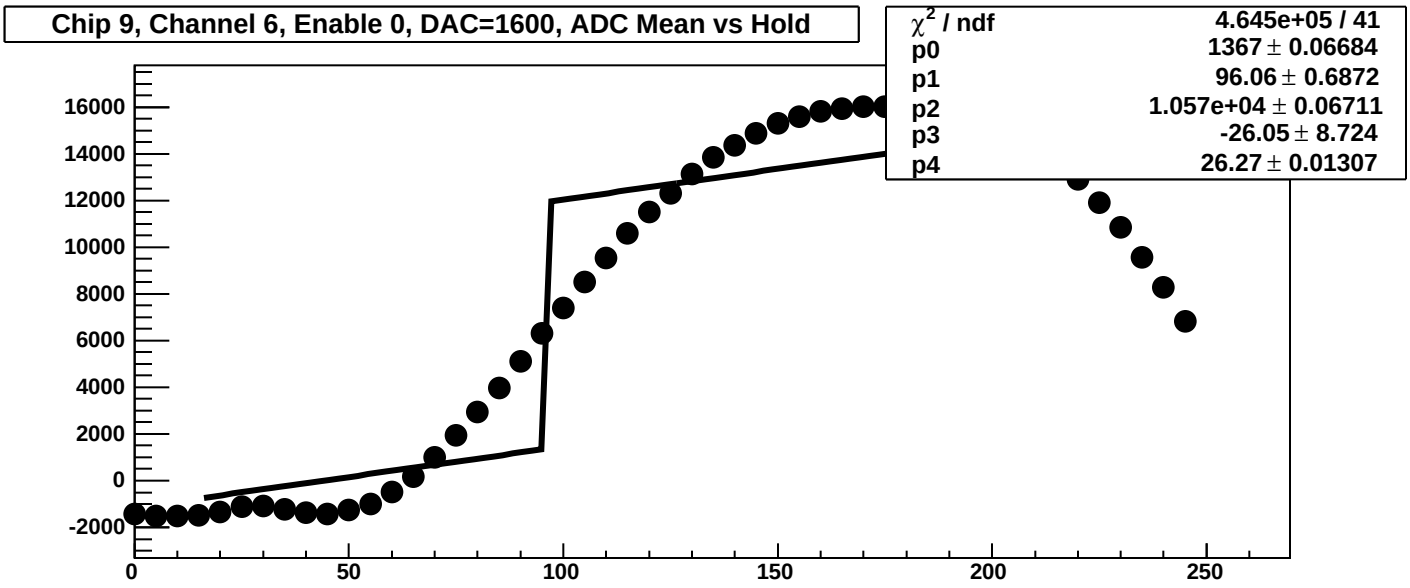


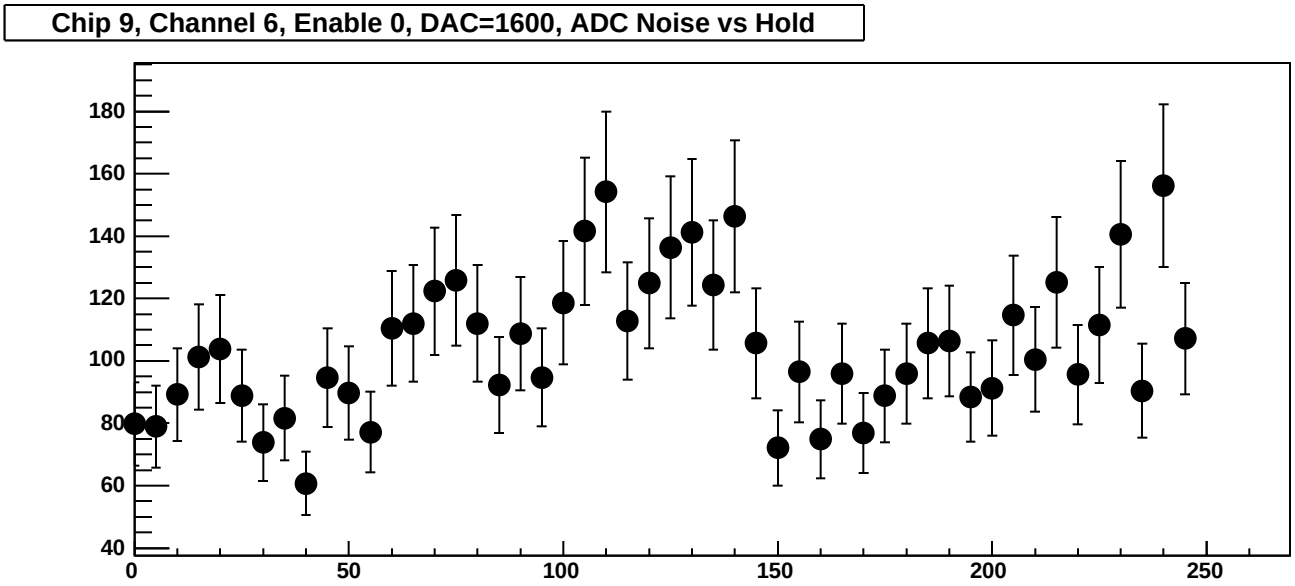
Chip 9, Channel 5, Enable 5, DAC=1600, ADC Noise vs Hold

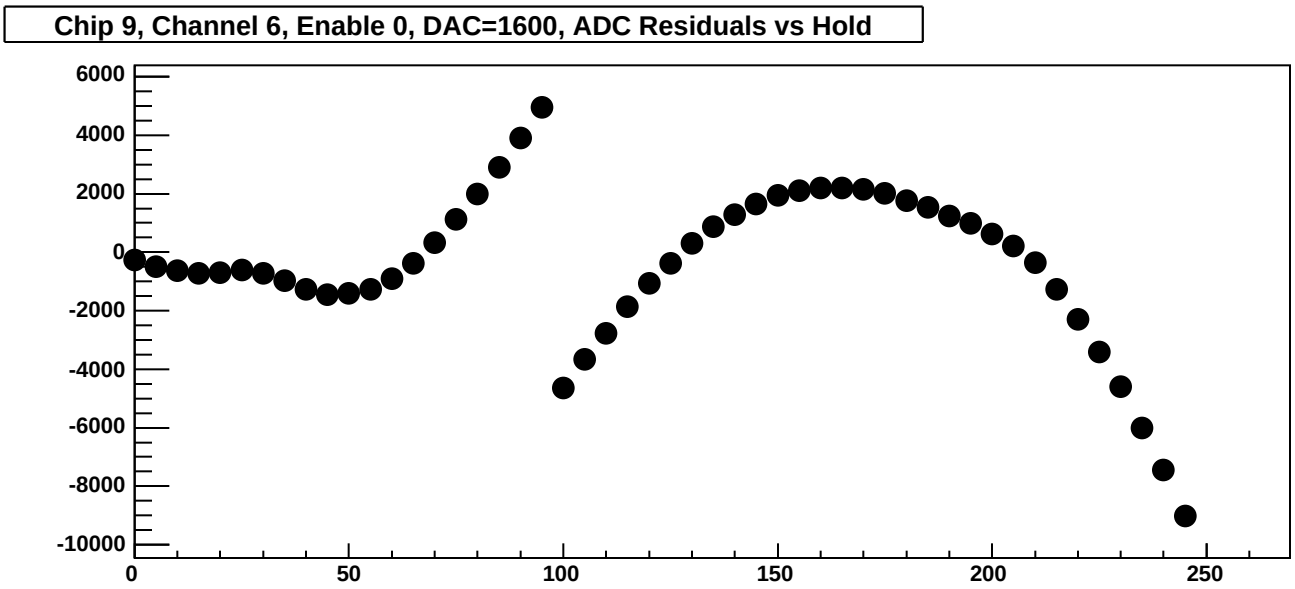


Chip 9, Channel 5, Enable 5, DAC=1600, ADC Residuals vs Hold

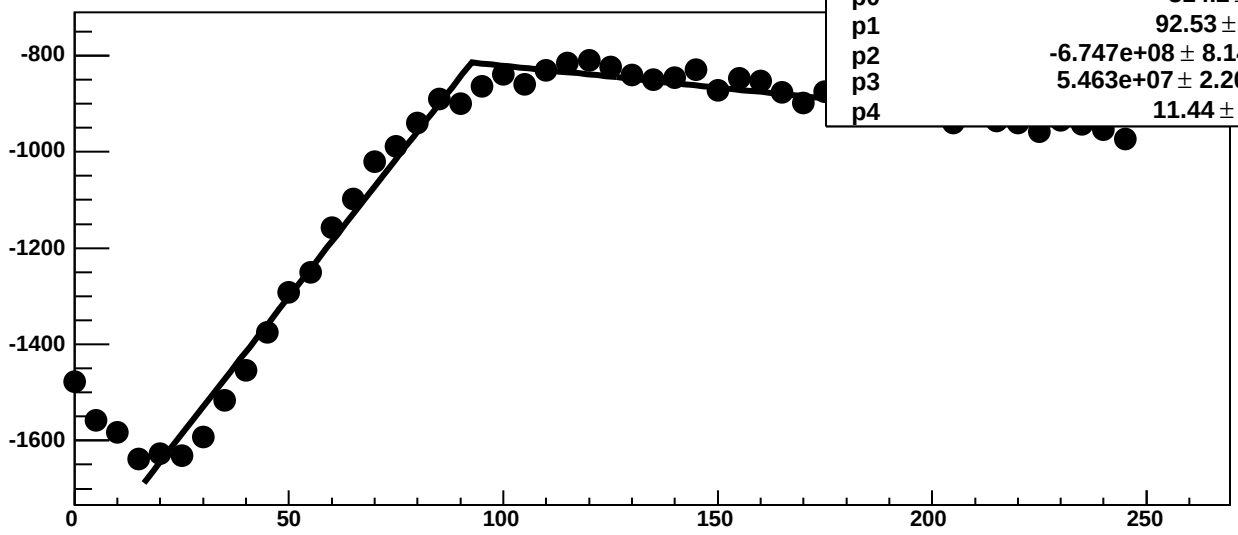



Chip 9, Channel 6, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.645e+05 / 41
p0 1367 ± 0.06684
p1 96.06 ± 0.6872
p2 1.057e+04 ± 0.06711
p3 -26.05 ± 8.724
p4 26.27 ± 0.01307
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

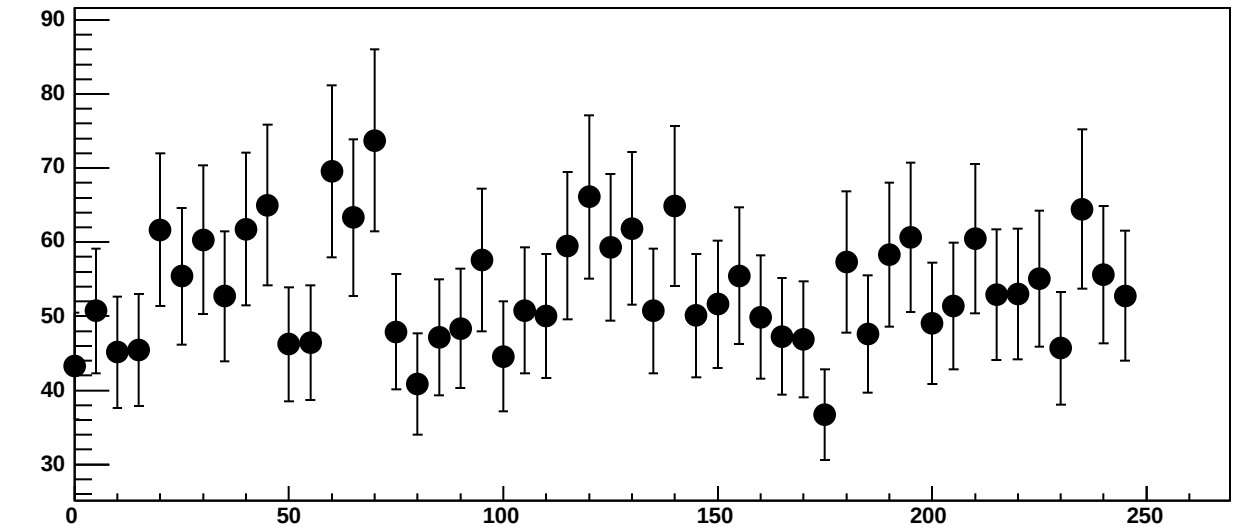

Chip 9, Channel 6, Enable 0, DAC=1600, ADC Noise vs Hold
180
160
140
120
100
80
60
40
0
50
100
150
200
250


Chip 9, Channel 6, Enable 0, DAC=1600, ADC Residuals vs Hold
6000
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

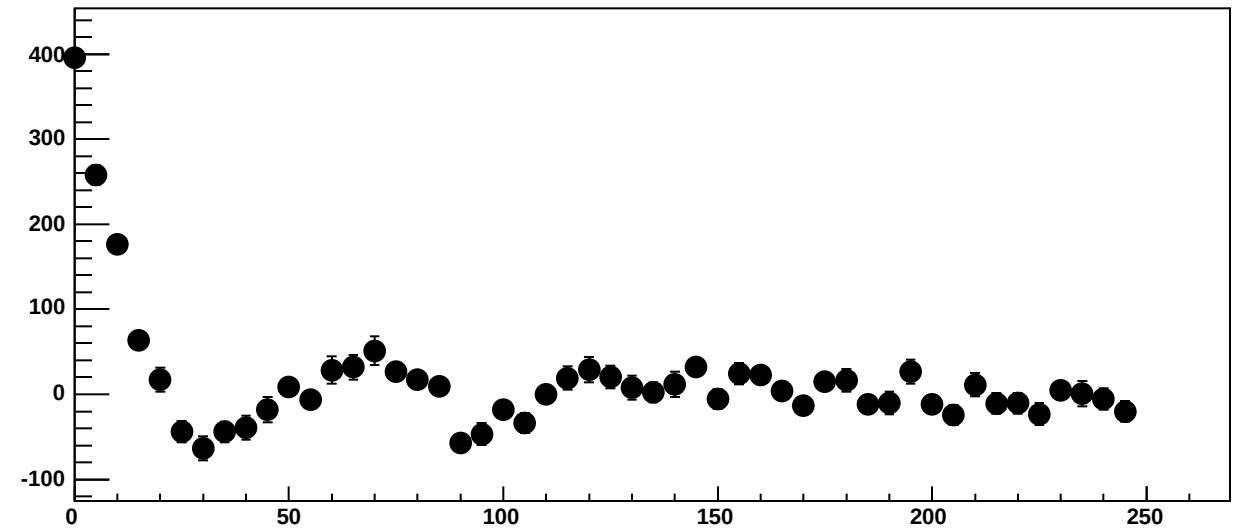
Chip 9, Channel 6, Enable 1, DAC=1600, ADC Mean vs Hold



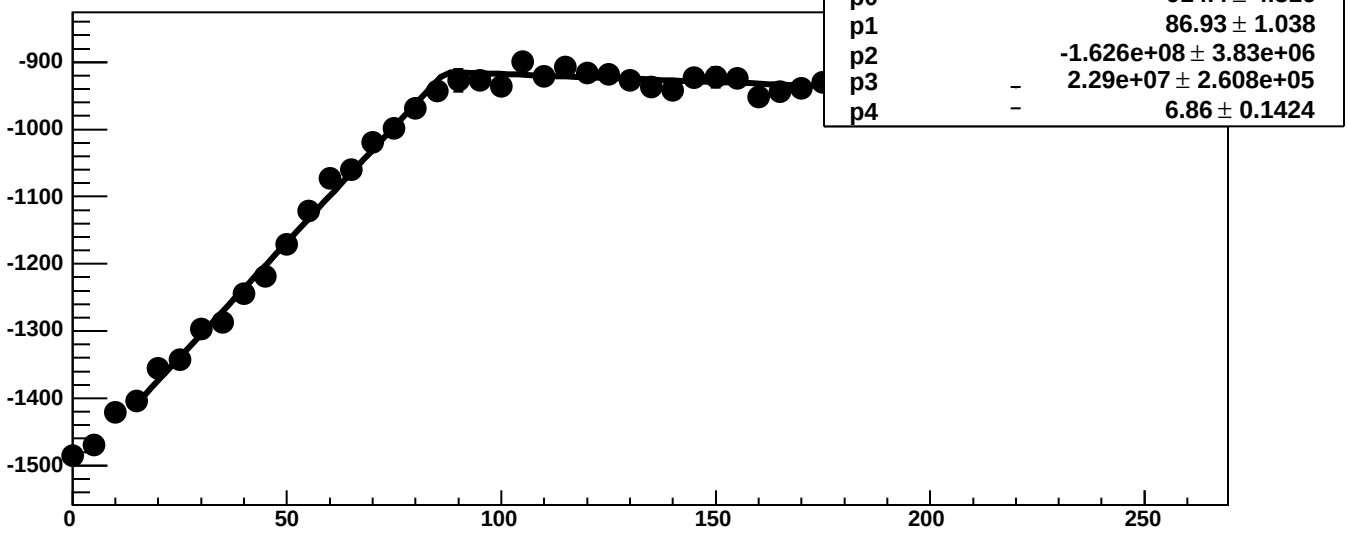
Chip 9, Channel 6, Enable 1, DAC=1600, ADC Noise vs Hold



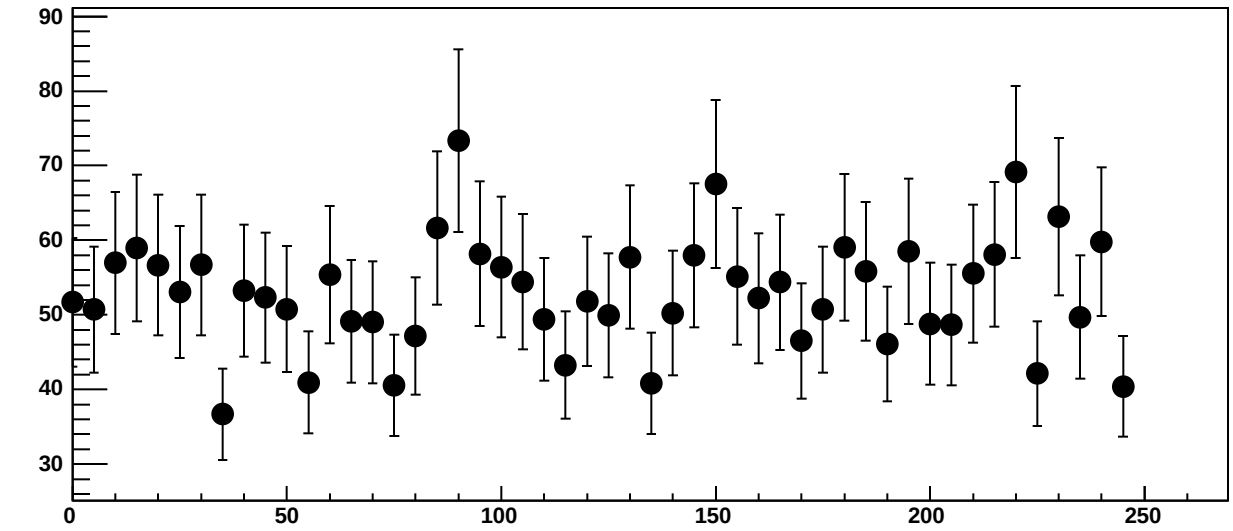
Chip 9, Channel 6, Enable 1, DAC=1600, ADC Residuals vs Hold



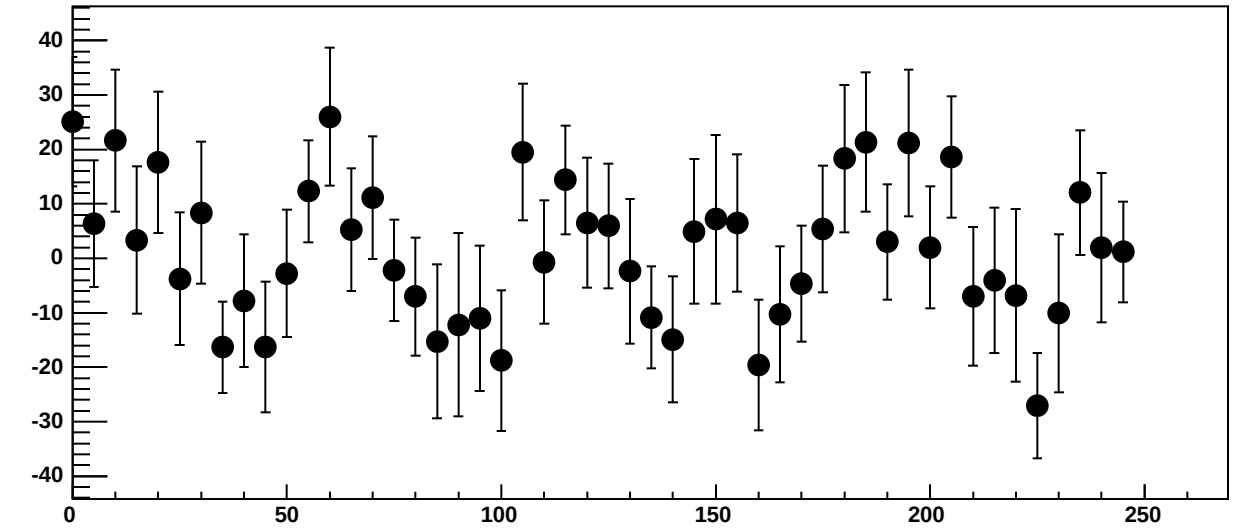
Chip 9, Channel 6, Enable 2, DAC=1600, ADC Mean vs Hold

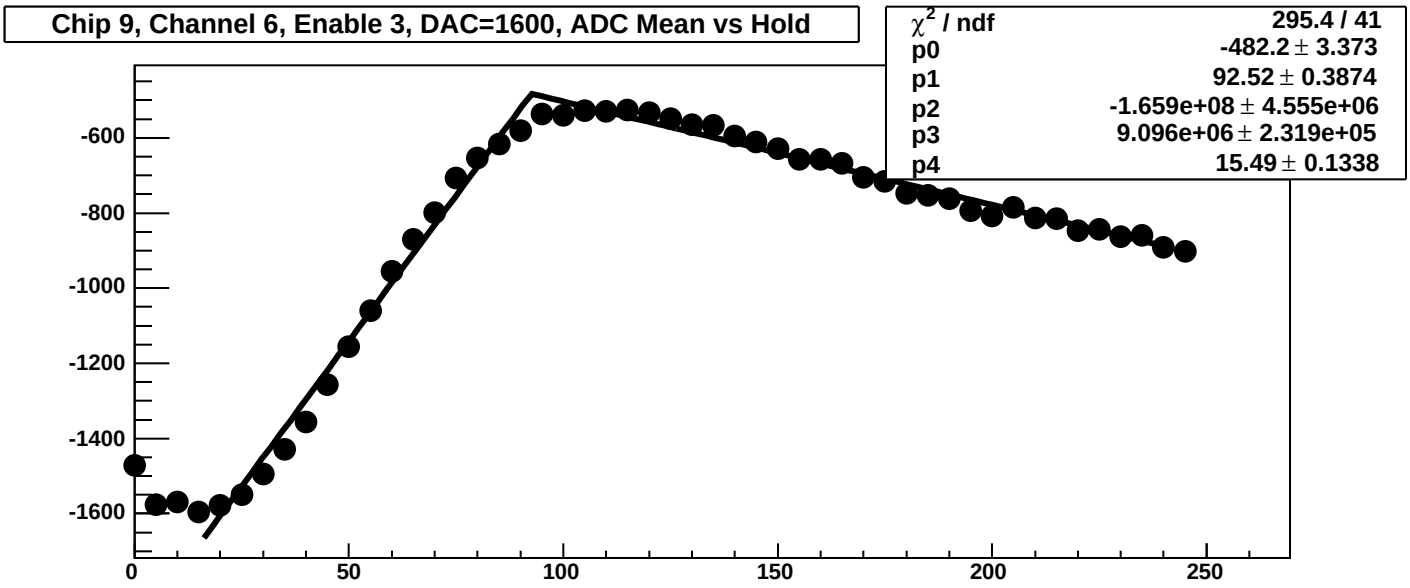


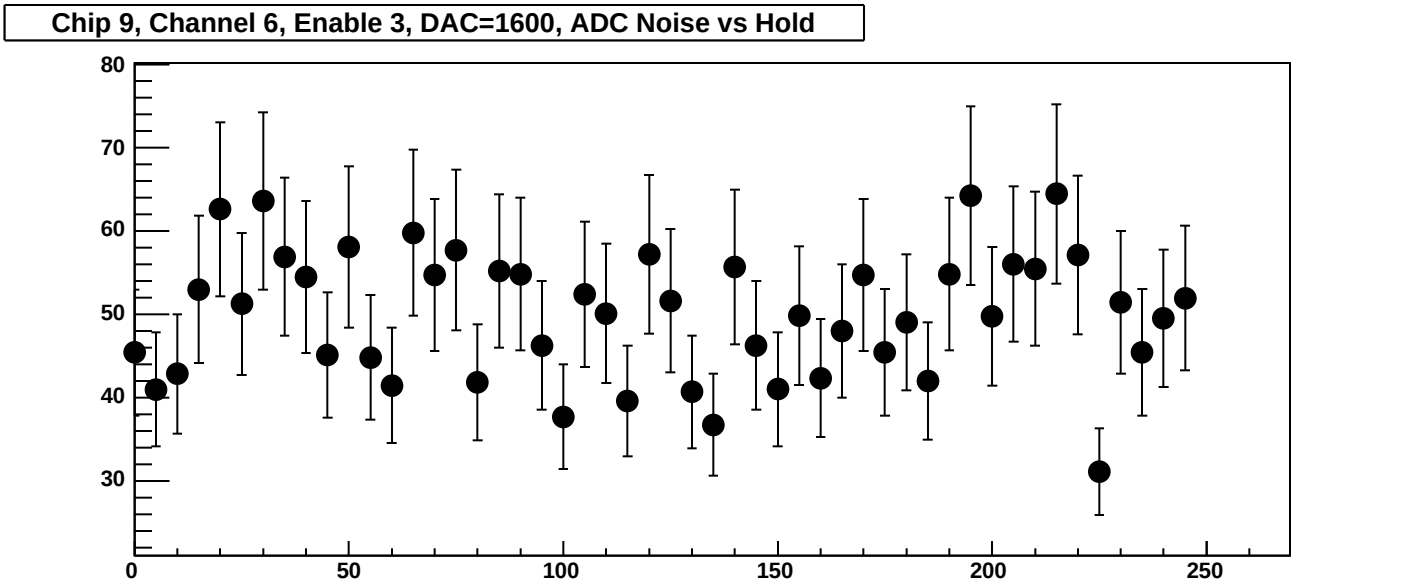
Chip 9, Channel 6, Enable 2, DAC=1600, ADC Noise vs Hold

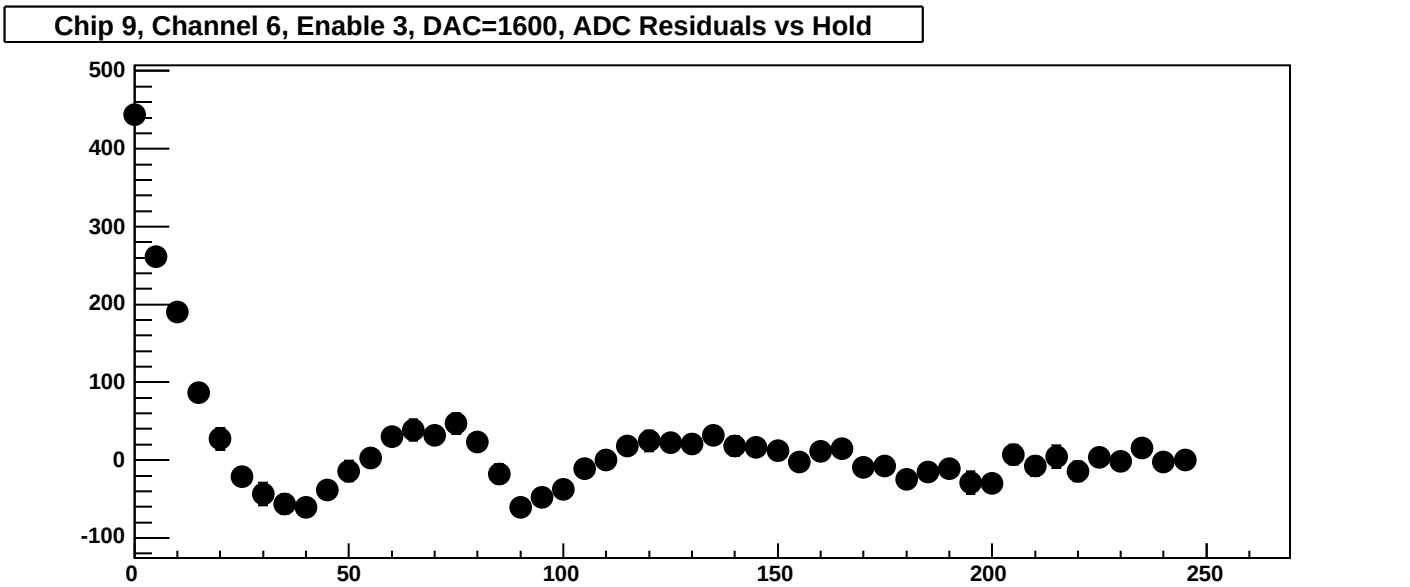


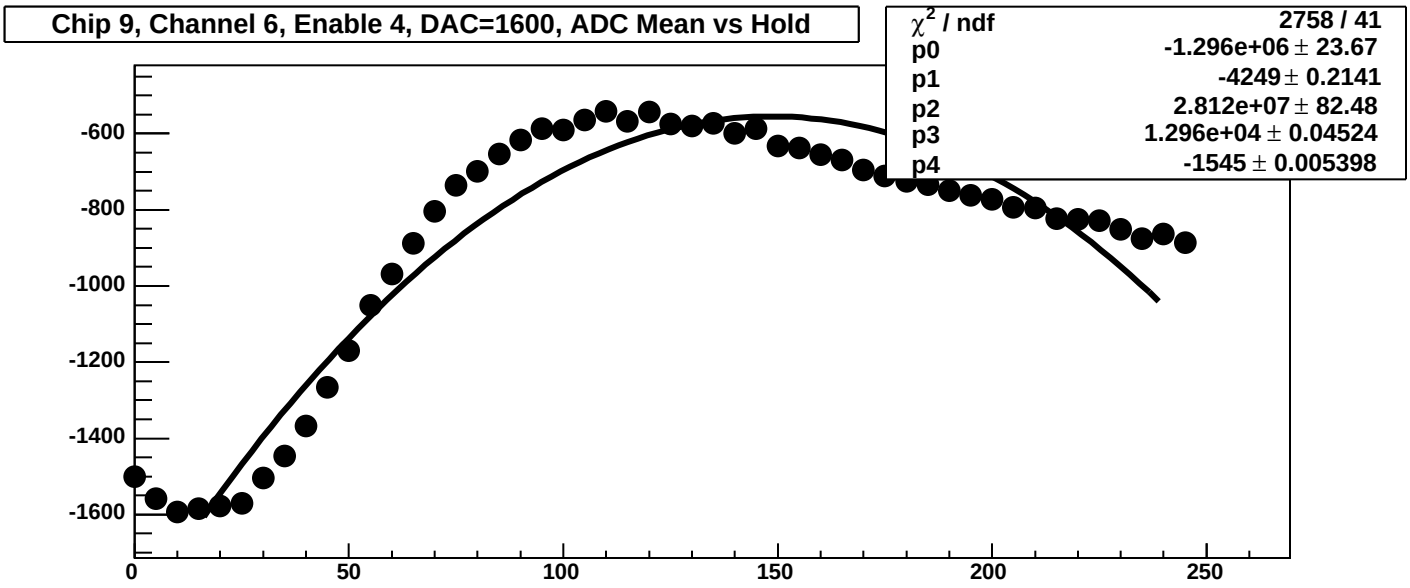
Chip 9, Channel 6, Enable 2, DAC=1600, ADC Residuals vs Hold

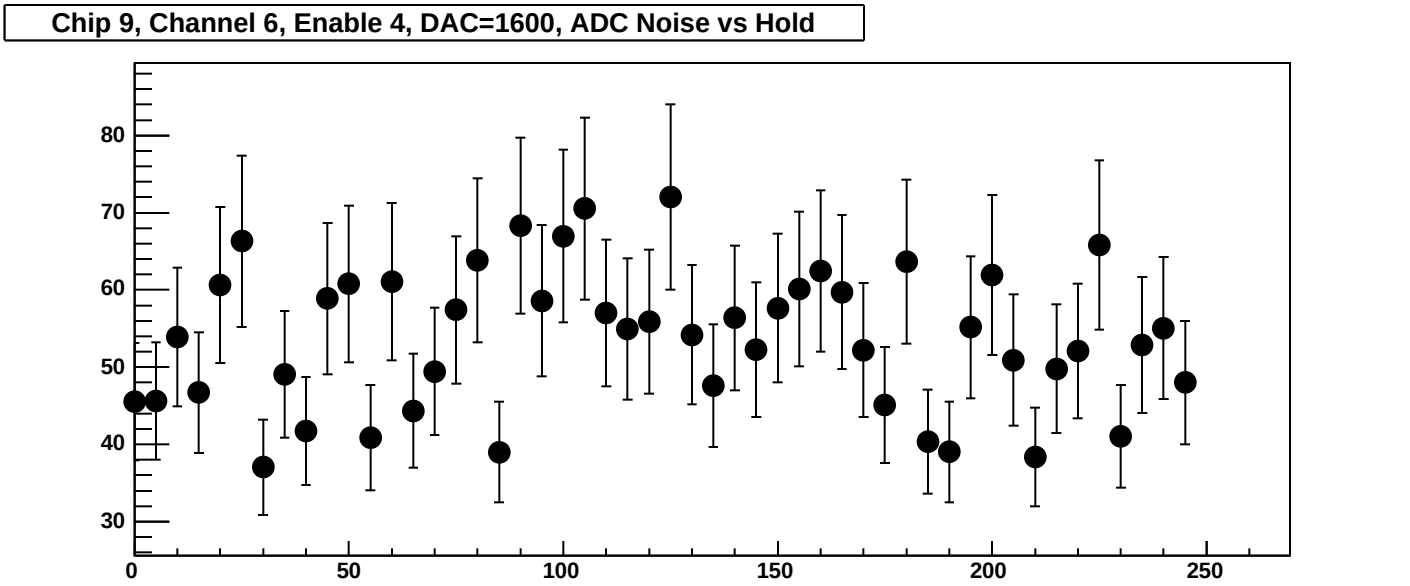


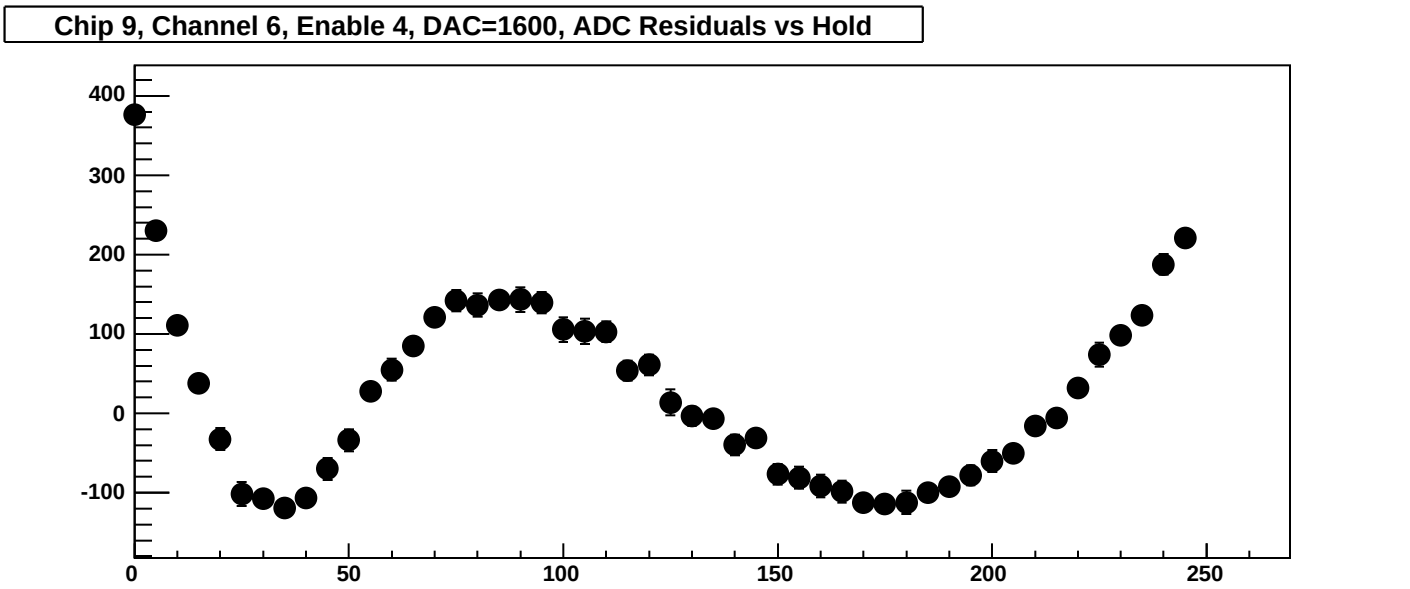

Chip 9, Channel 6, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 295.4 / 41
p0 -482.2 ± 3.373
p1 92.52 ± 0.3874
p2 -1.659e+08 ± 4.555e+06
p3 9.096e+06 ± 2.319e+05
p4 15.49 ± 0.1338
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250


Chip 9, Channel 6, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

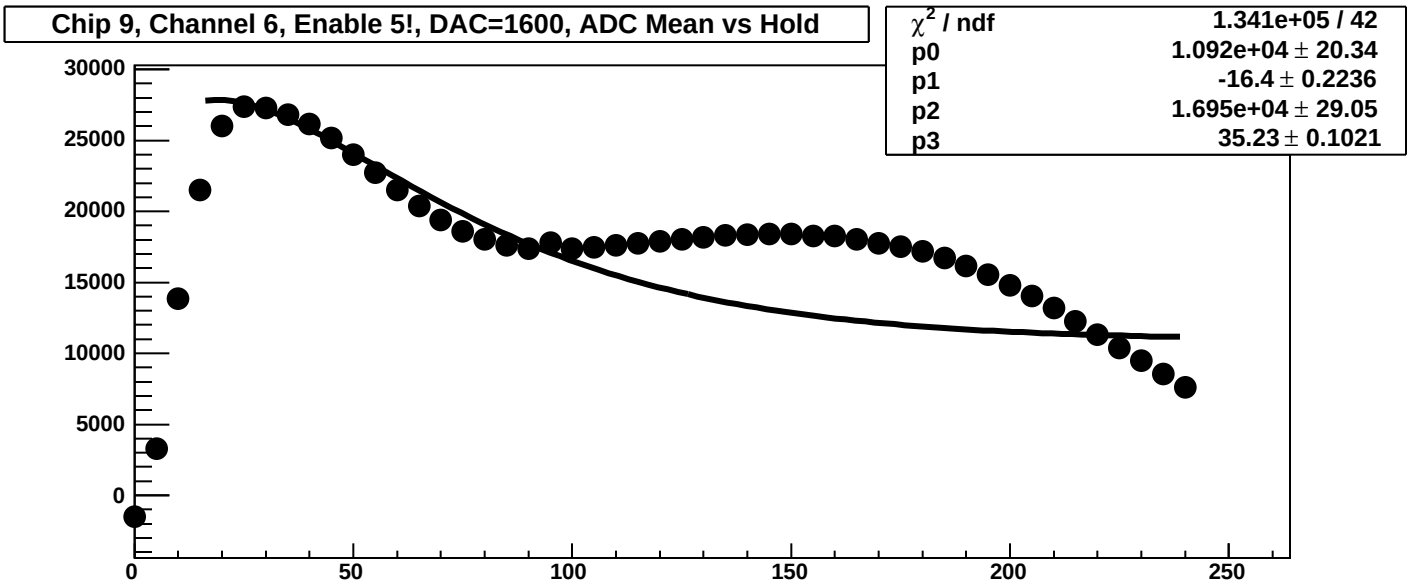

Chip 9, Channel 6, Enable 3, DAC=1600, ADC Residuals vs Hold
500
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 9, Channel 6, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 2758 / 41
p0 -1.296e+06 ± 23.67
p1 -4249 ± 0.2141
p2 2.812e+07 ± 82.48
p3 1.296e+04 ± 0.04524
p4 -1545 ± 0.005398
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250

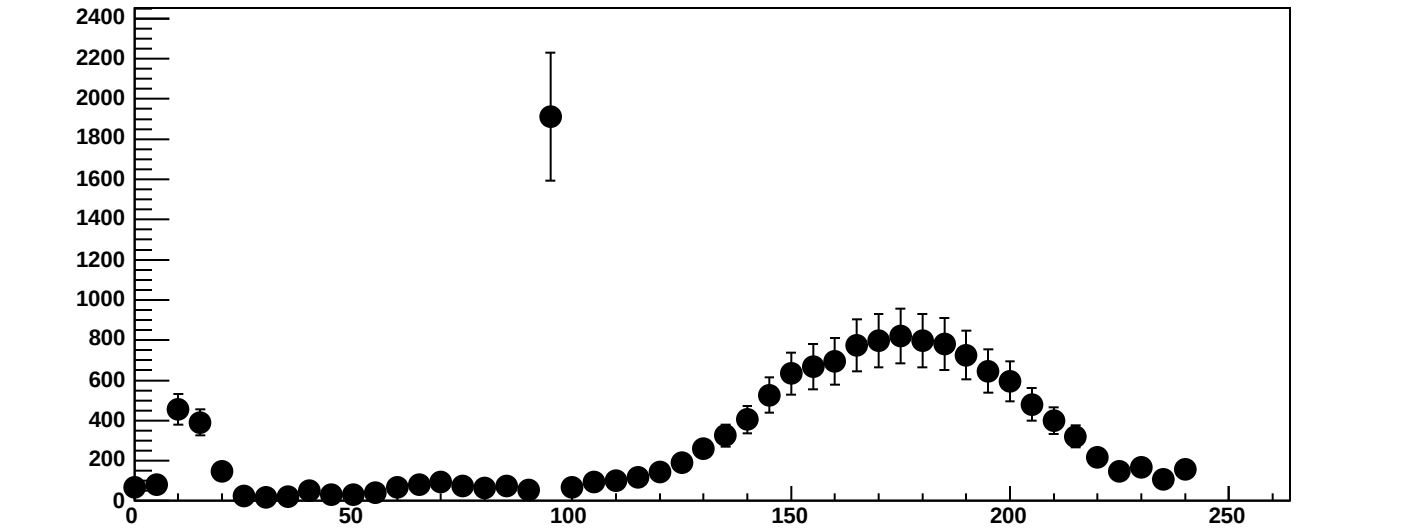

Chip 9, Channel 6, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 9, Channel 6, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

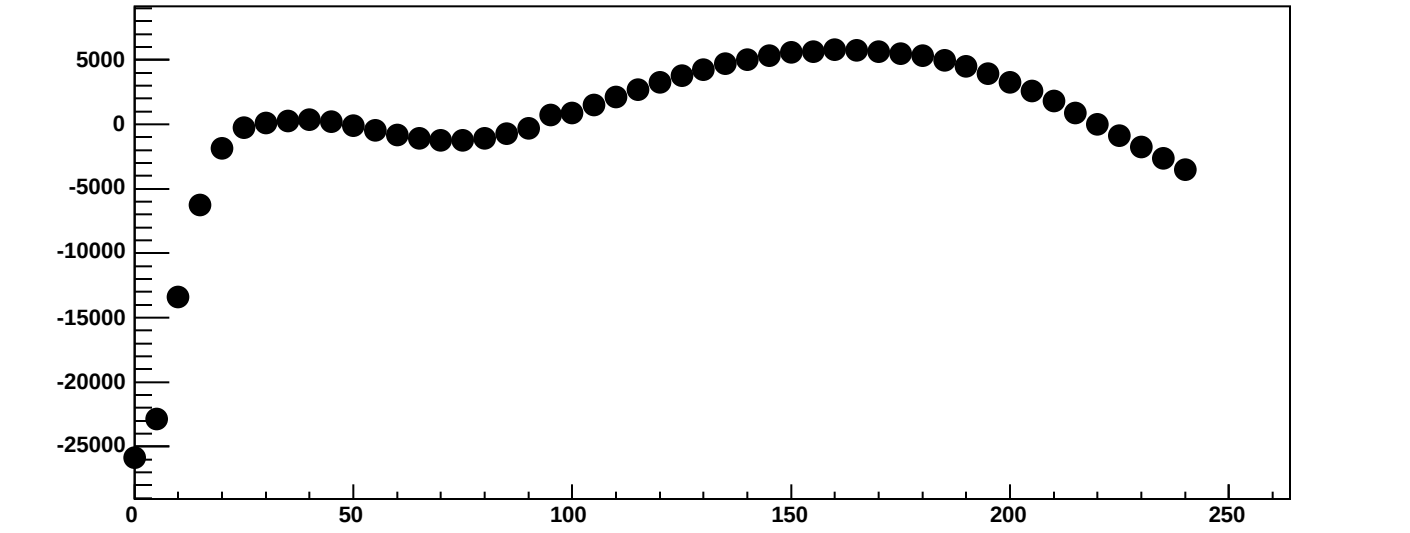
Chip 9, Channel 6, Enable 5!, DAC=1600, ADC Mean vs Hold



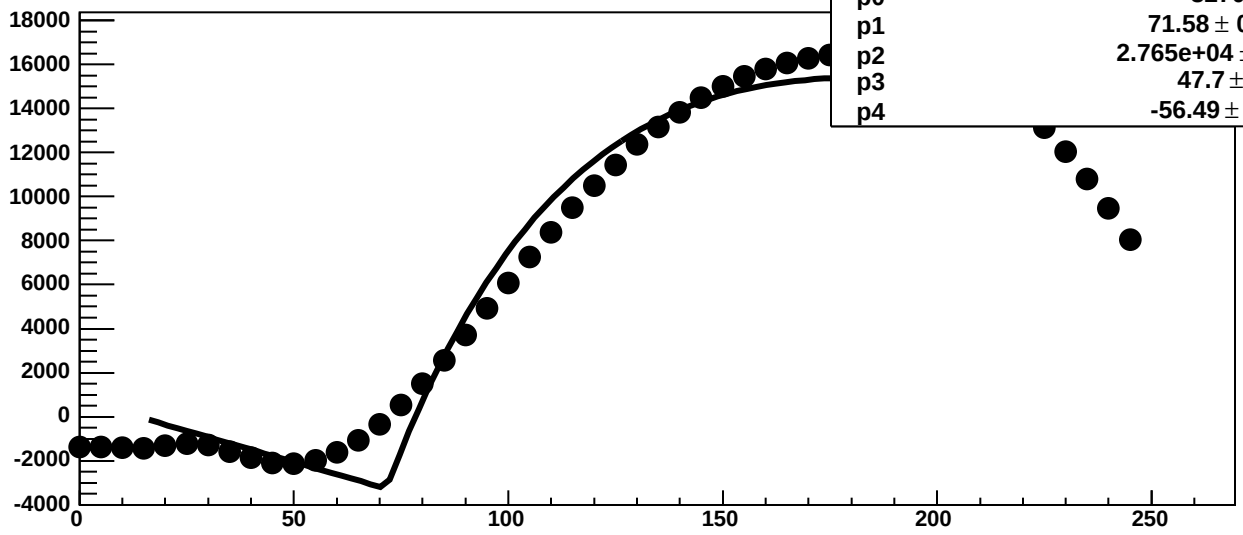
Chip 9, Channel 6, Enable 5!, DAC=1600, ADC Noise vs Hold



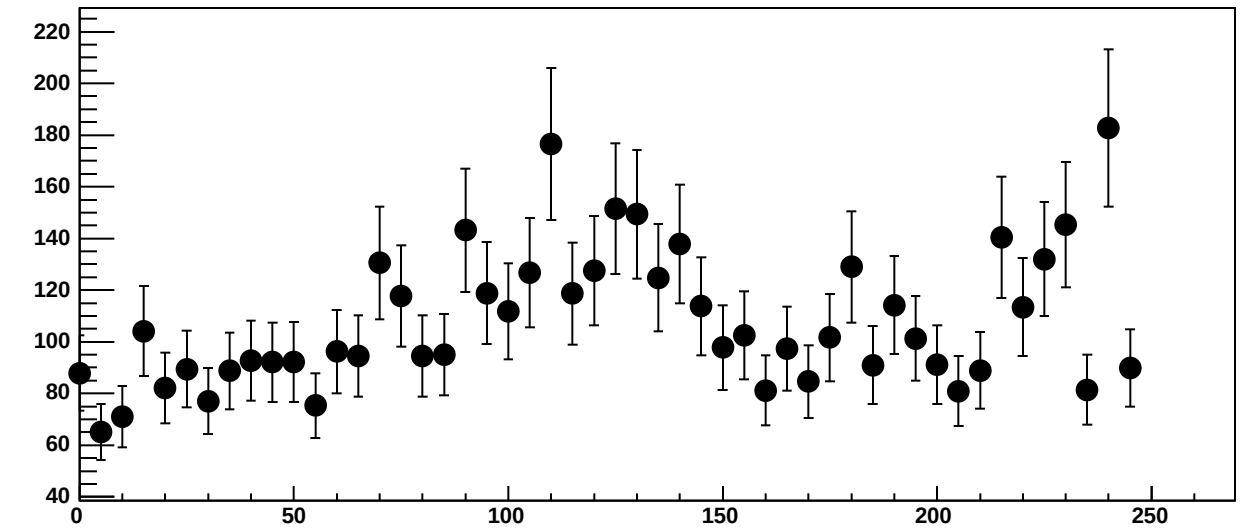
Chip 9, Channel 6, Enable 5!, DAC=1600, ADC Residuals vs Hold



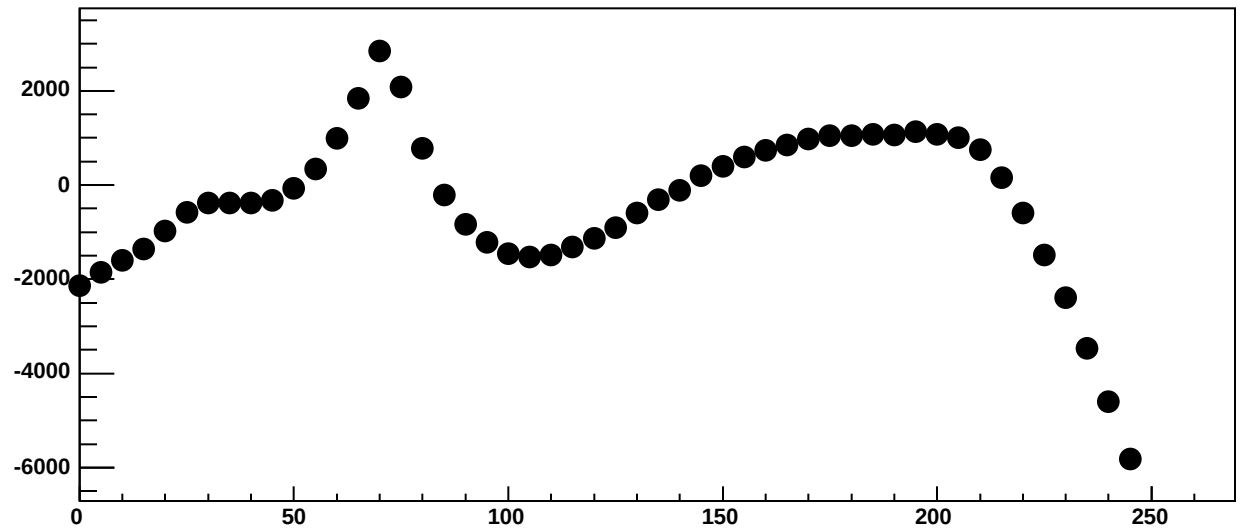
Chip 9, Channel 7, Enable 0, DAC=1600, ADC Mean vs Hold



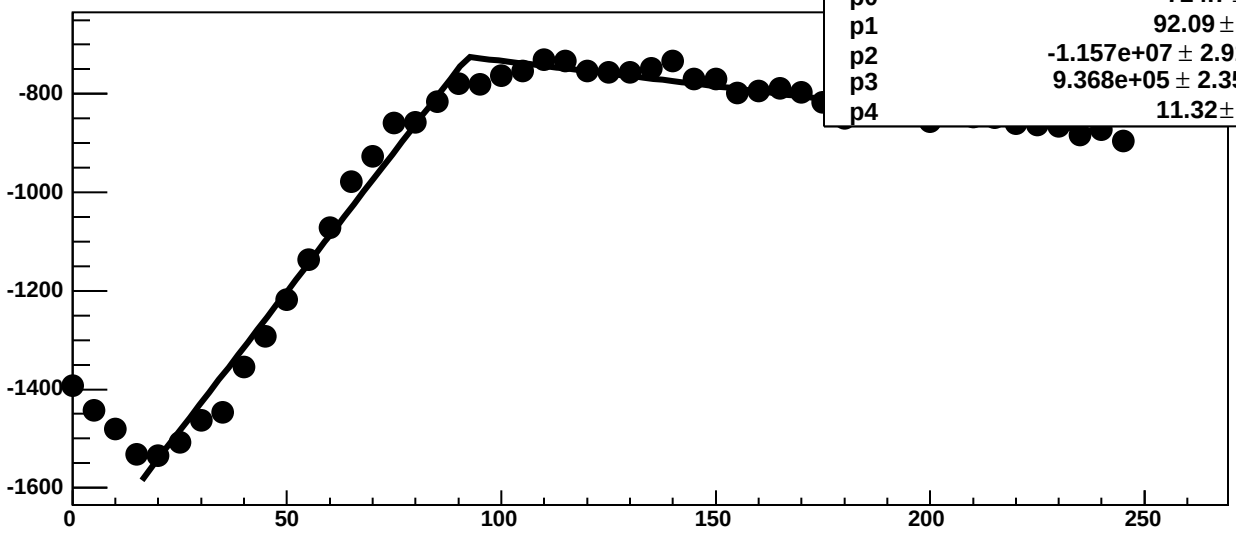
Chip 9, Channel 7, Enable 0, DAC=1600, ADC Noise vs Hold



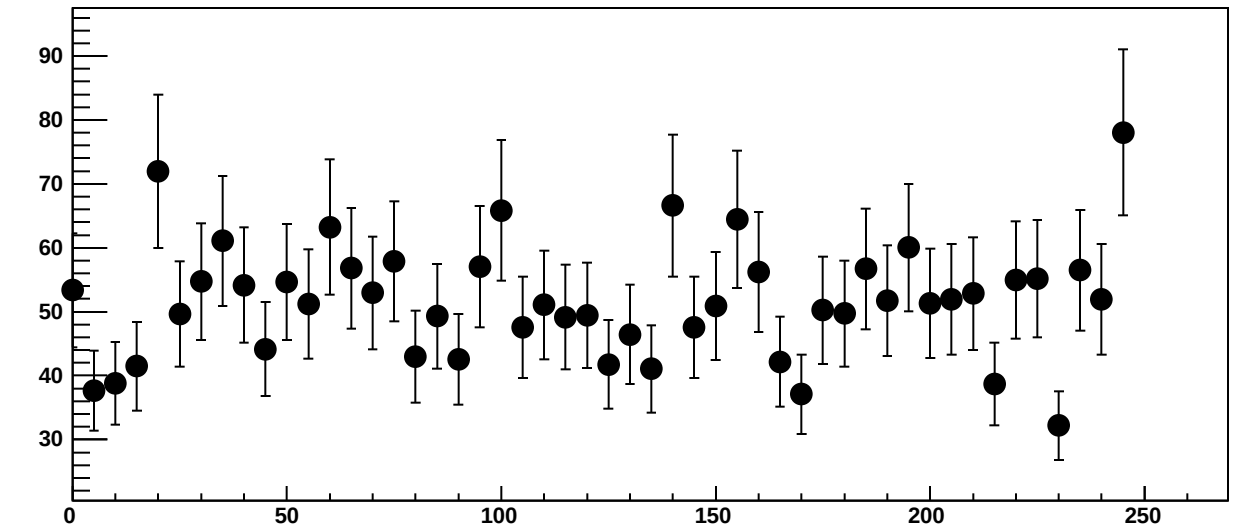
Chip 9, Channel 7, Enable 0, DAC=1600, ADC Residuals vs Hold



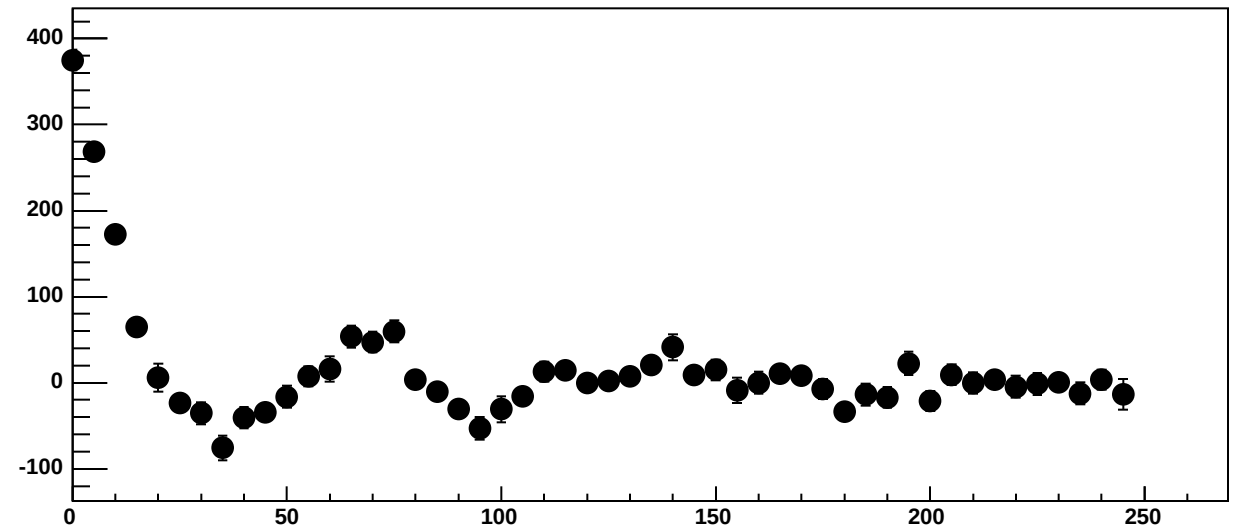
Chip 9, Channel 7, Enable 1, DAC=1600, ADC Mean vs Hold



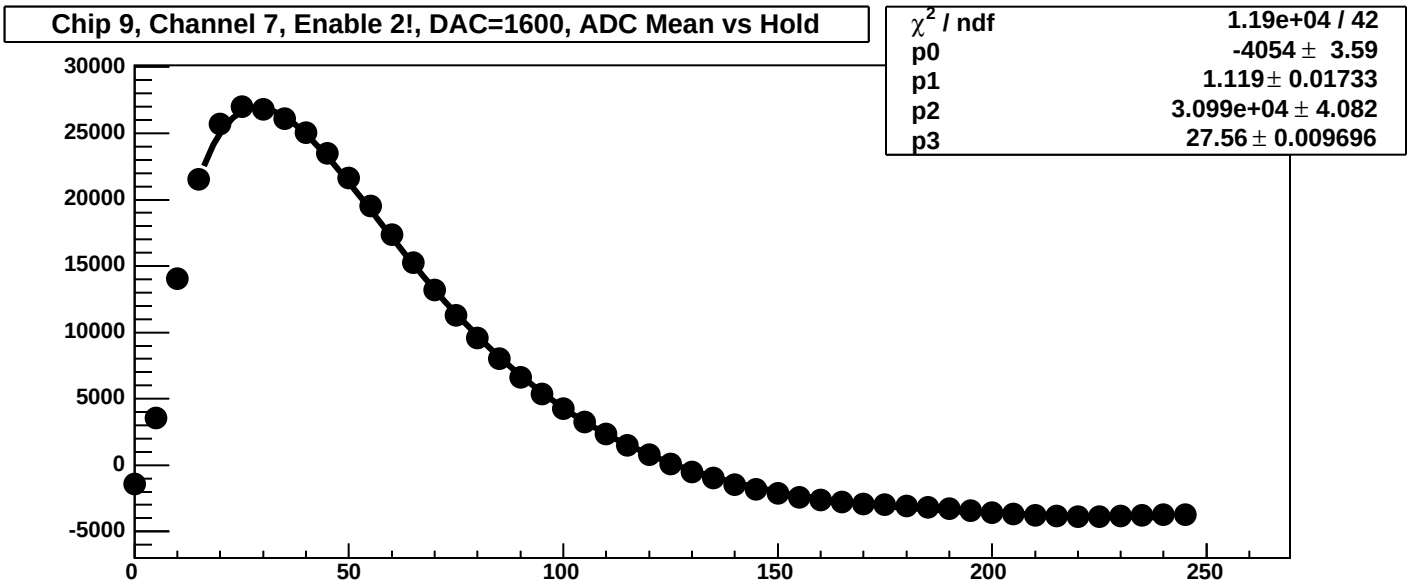
Chip 9, Channel 7, Enable 1, DAC=1600, ADC Noise vs Hold



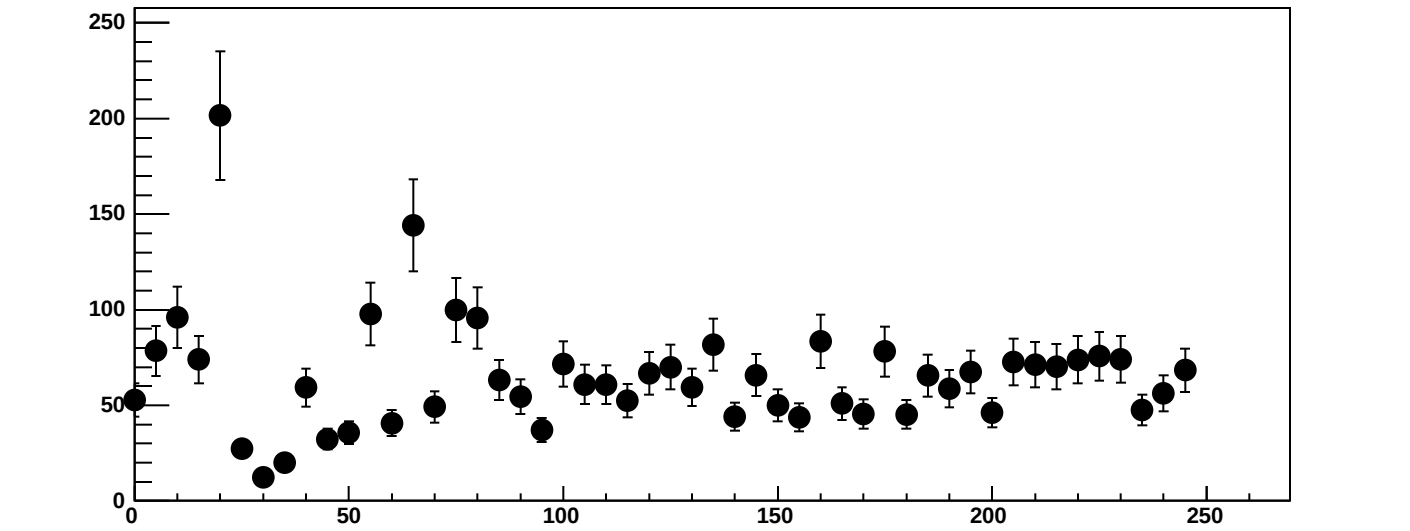
Chip 9, Channel 7, Enable 1, DAC=1600, ADC Residuals vs Hold



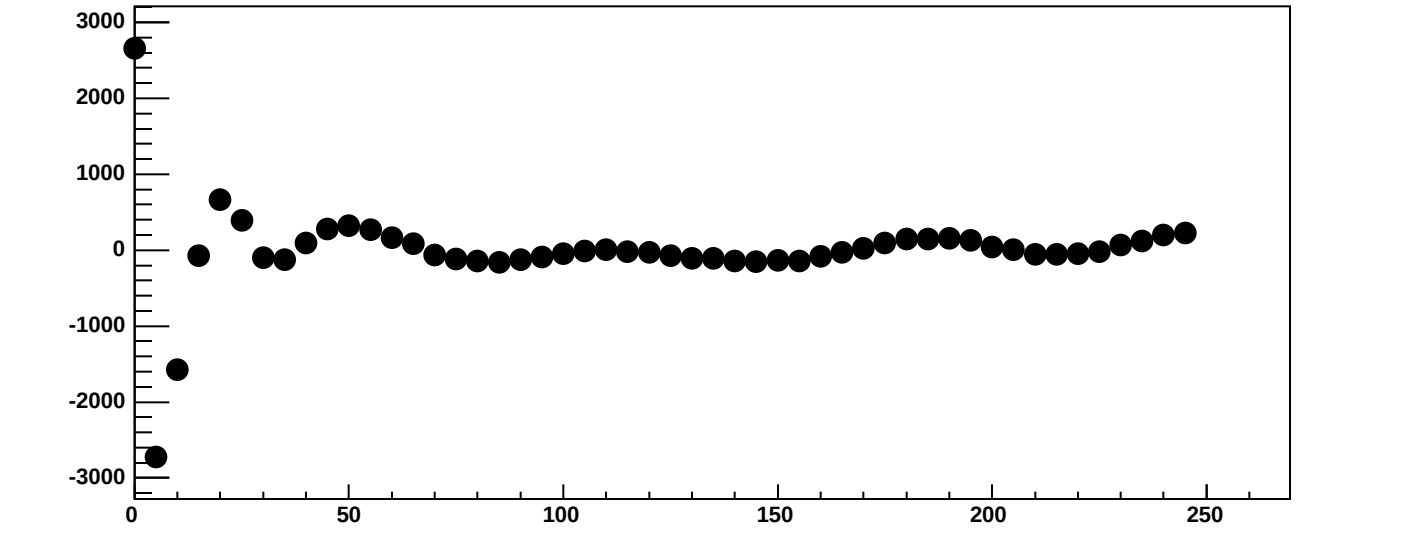
Chip 9, Channel 7, Enable 2!, DAC=1600, ADC Mean vs Hold



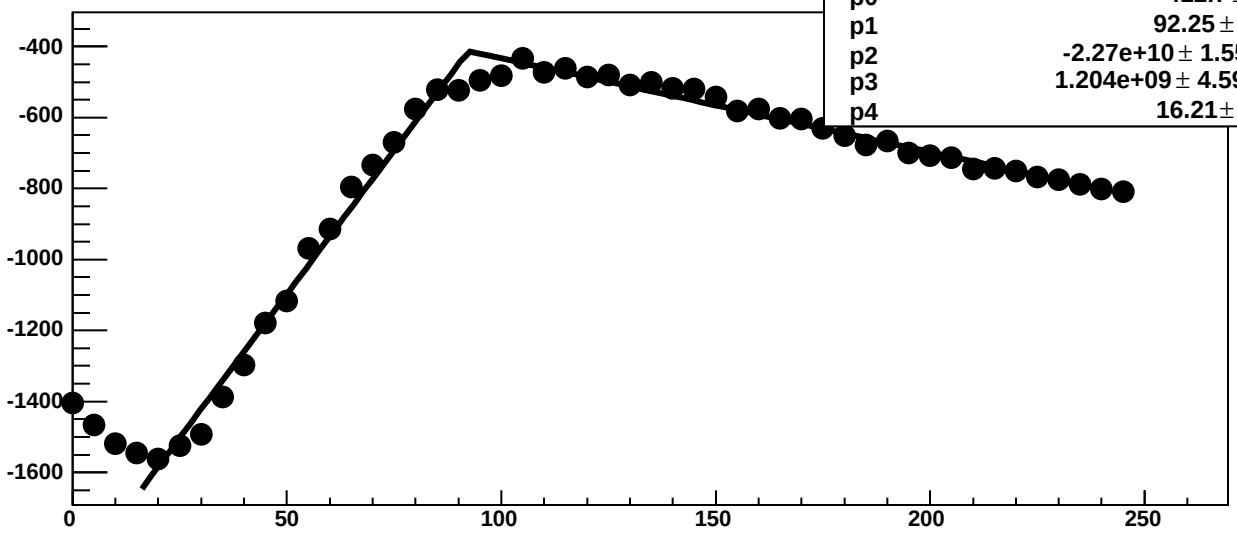
Chip 9, Channel 7, Enable 2!, DAC=1600, ADC Noise vs Hold



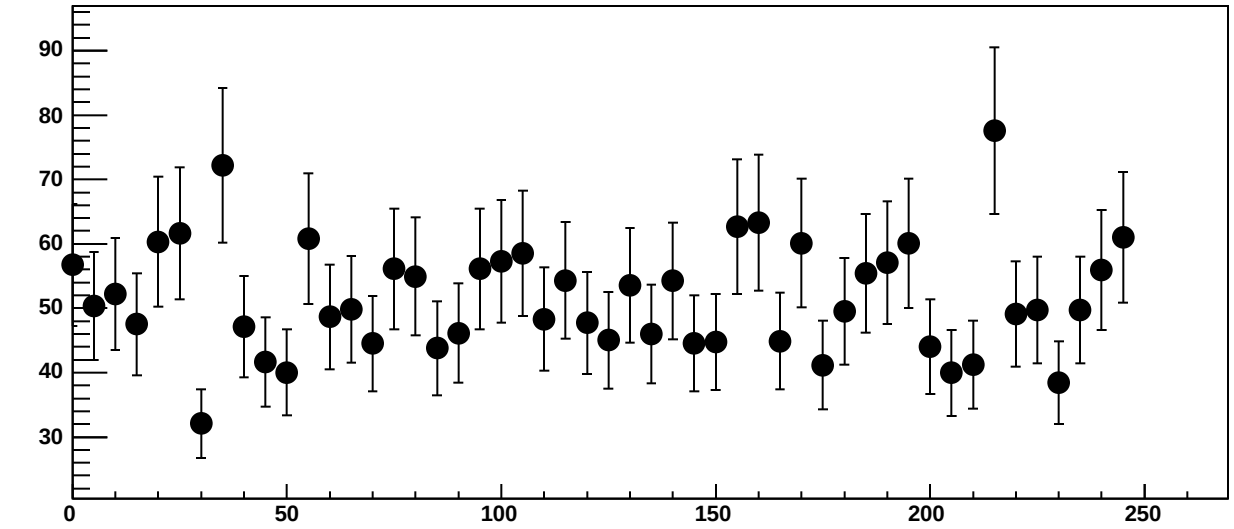
Chip 9, Channel 7, Enable 2!, DAC=1600, ADC Residuals vs Hold



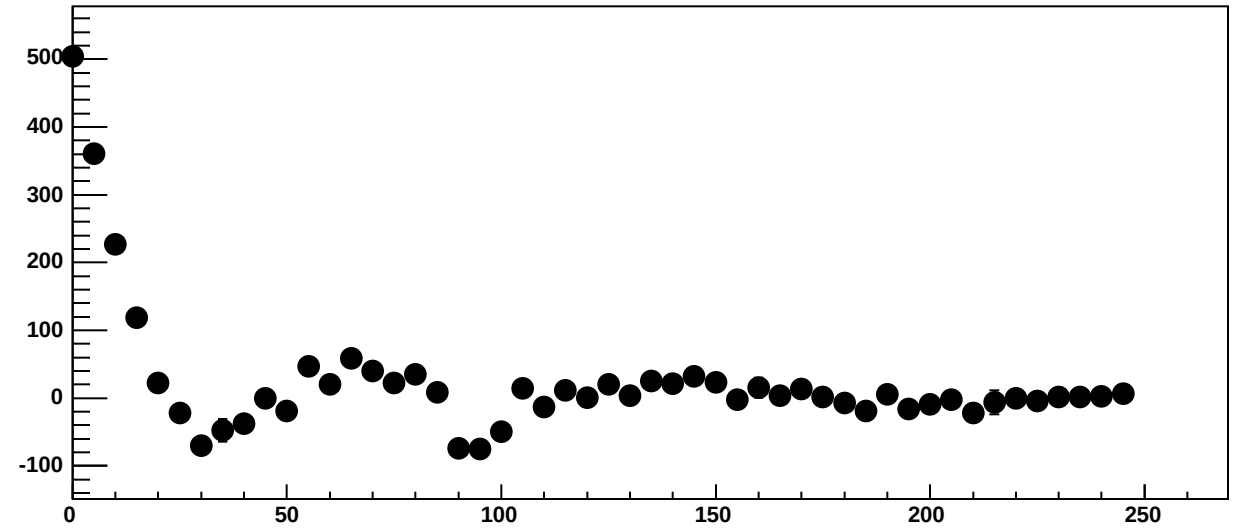
Chip 9, Channel 7, Enable 3, DAC=1600, ADC Mean vs Hold

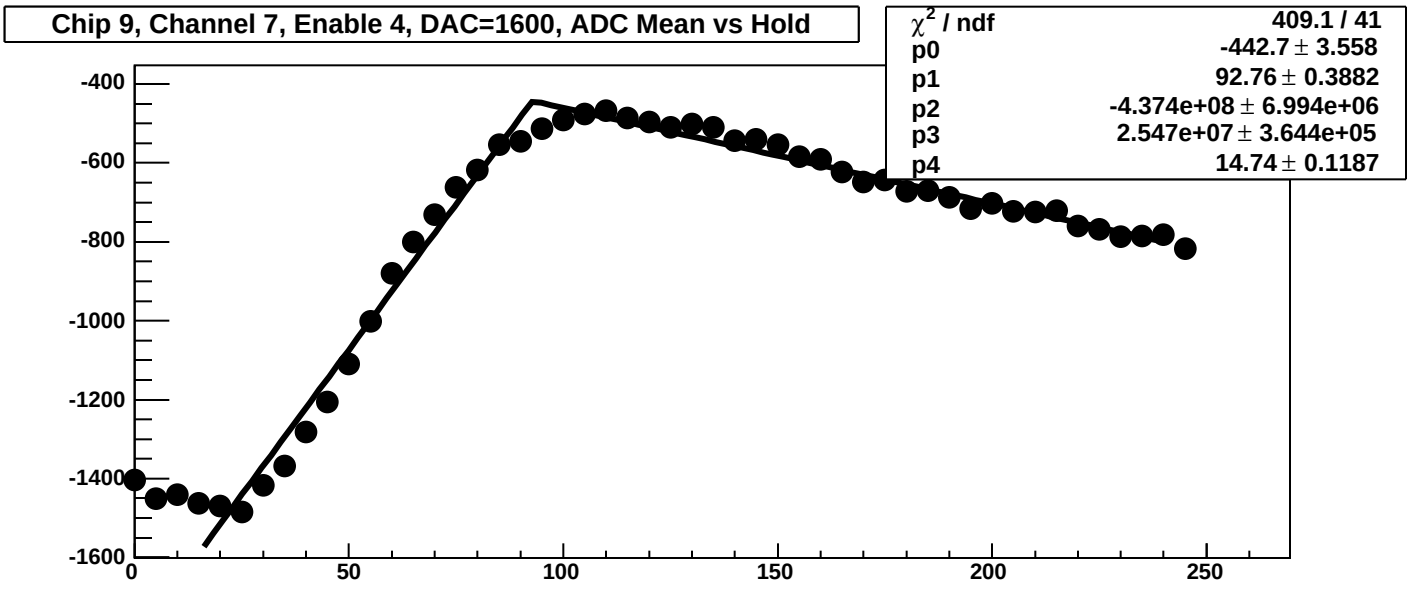


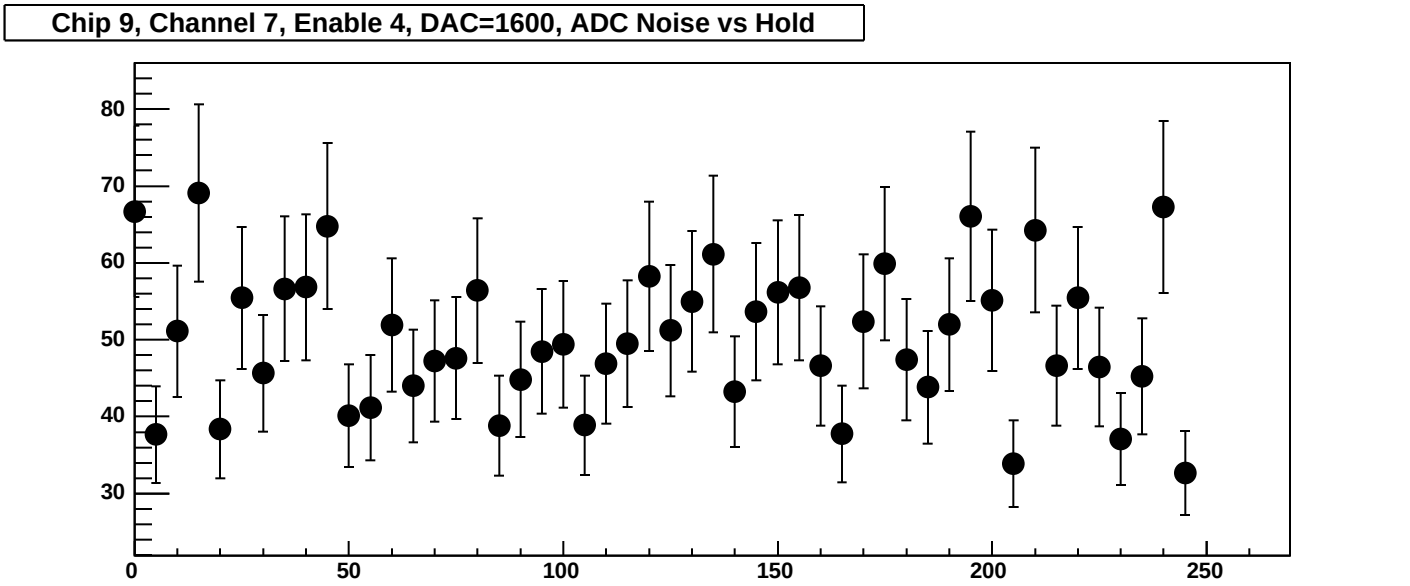
Chip 9, Channel 7, Enable 3, DAC=1600, ADC Noise vs Hold

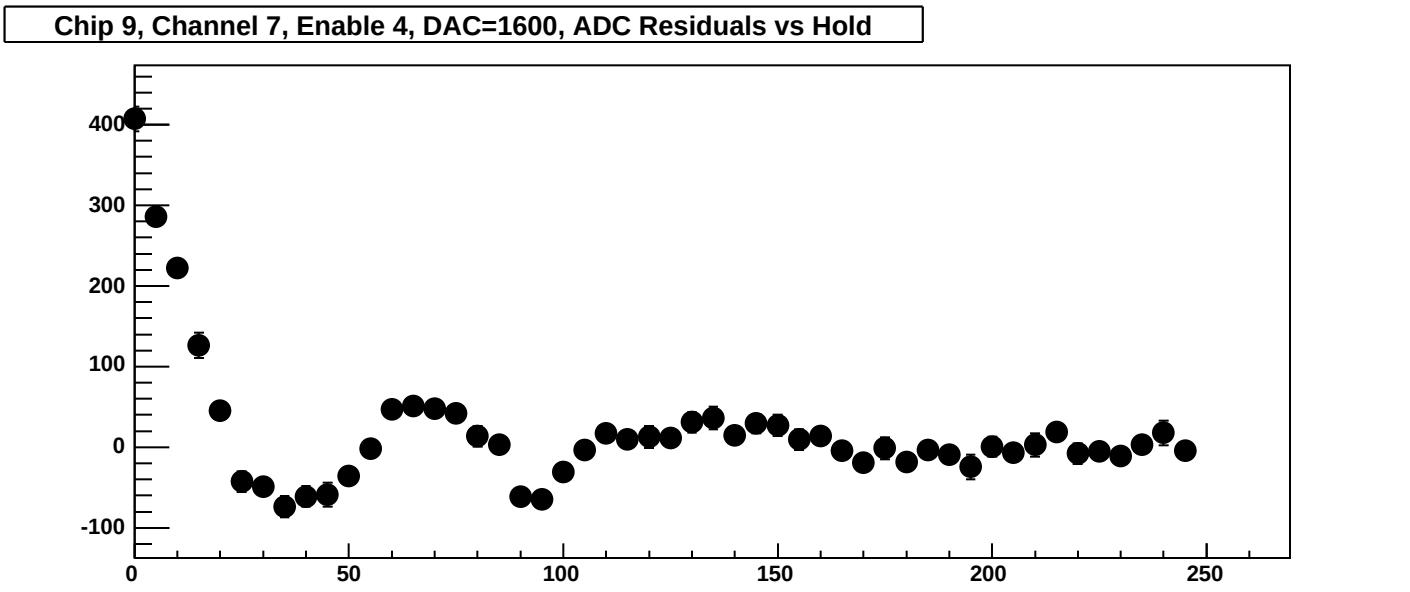


Chip 9, Channel 7, Enable 3, DAC=1600, ADC Residuals vs Hold

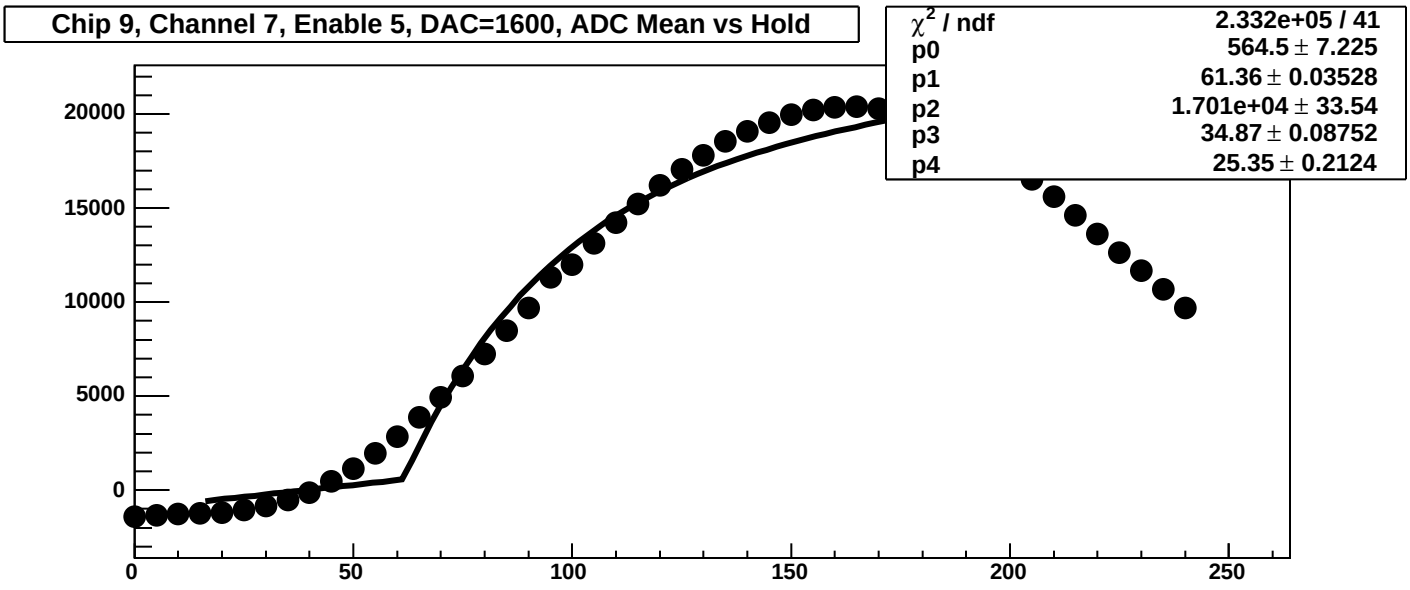



Chip 9, Channel 7, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 409.1 / 41
p0 -442.7 ± 3.558
p1 92.76 ± 0.3882
p2 -4.374e+08 ± 6.994e+06
p3 2.547e+07 ± 3.644e+05
p4 14.74 ± 0.1187
-400
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250

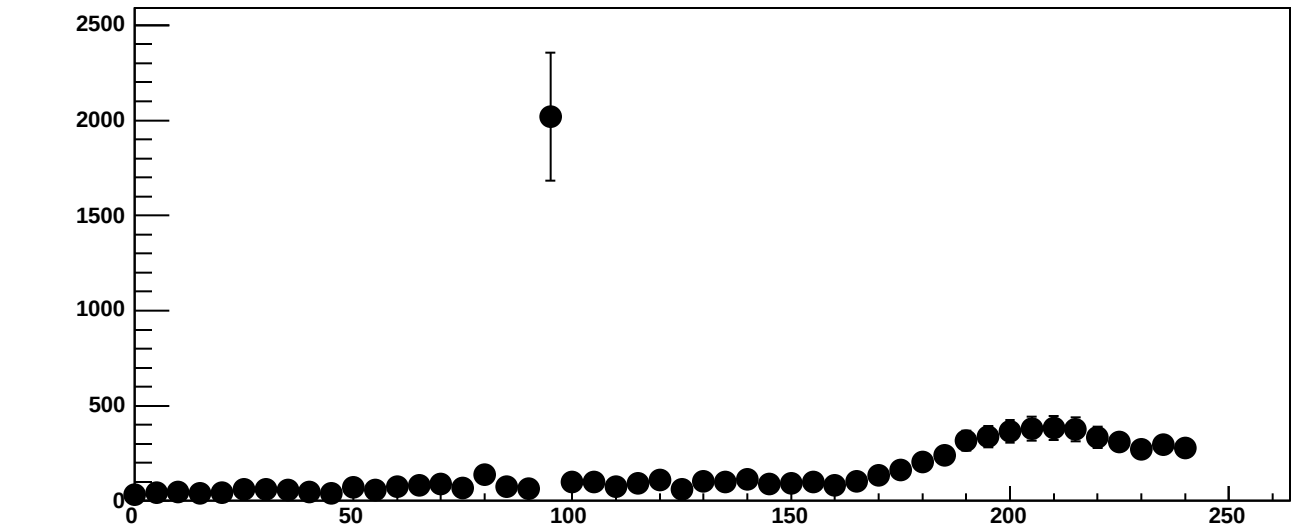

Chip 9, Channel 7, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 9, Channel 7, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

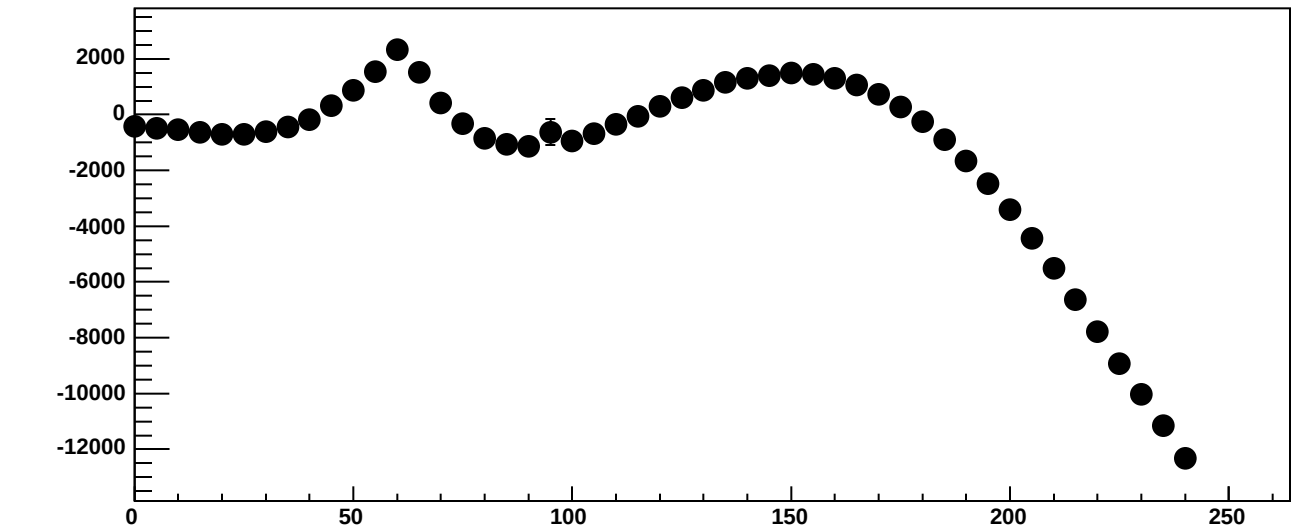
Chip 9, Channel 7, Enable 5, DAC=1600, ADC Mean vs Hold

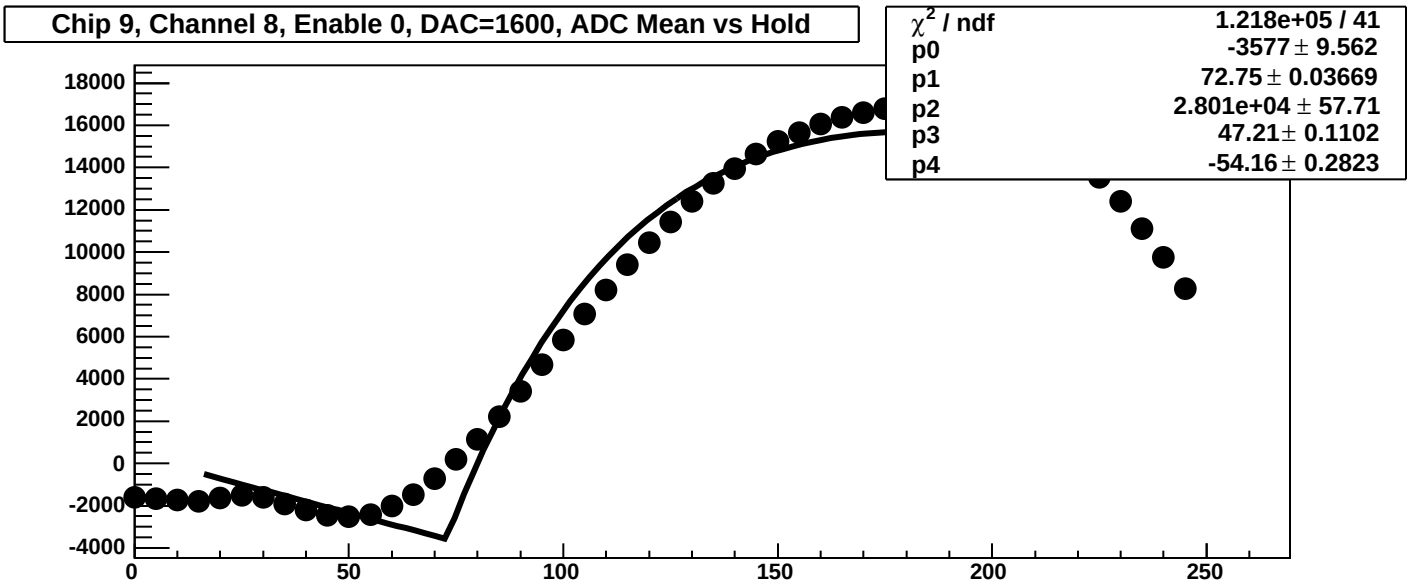


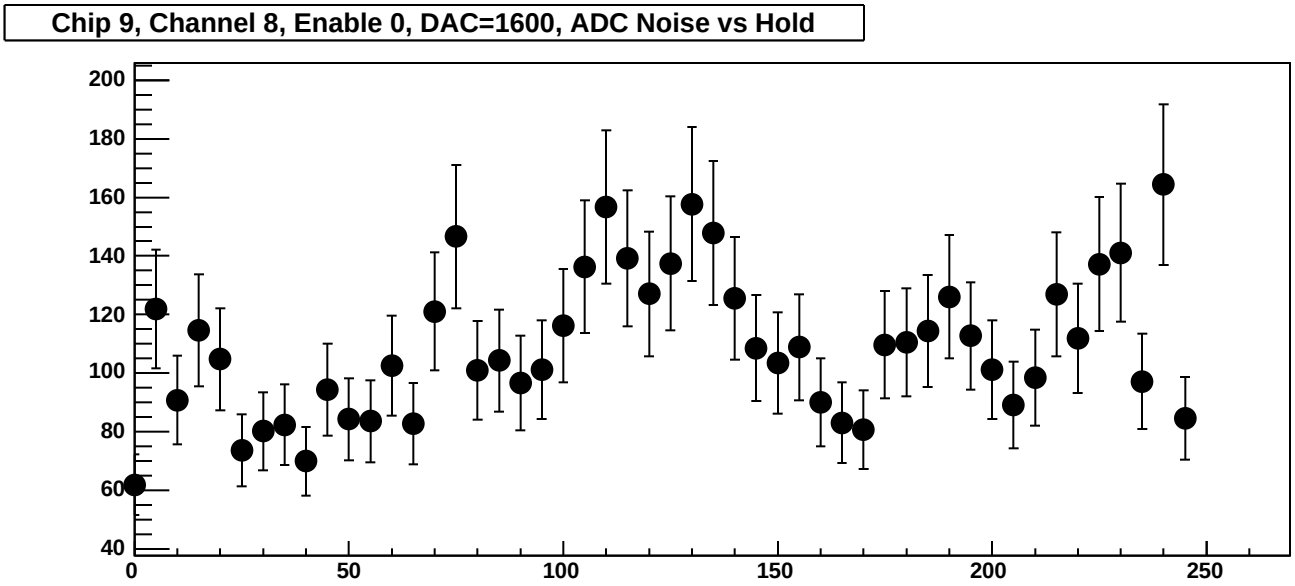
Chip 9, Channel 7, Enable 5, DAC=1600, ADC Noise vs Hold

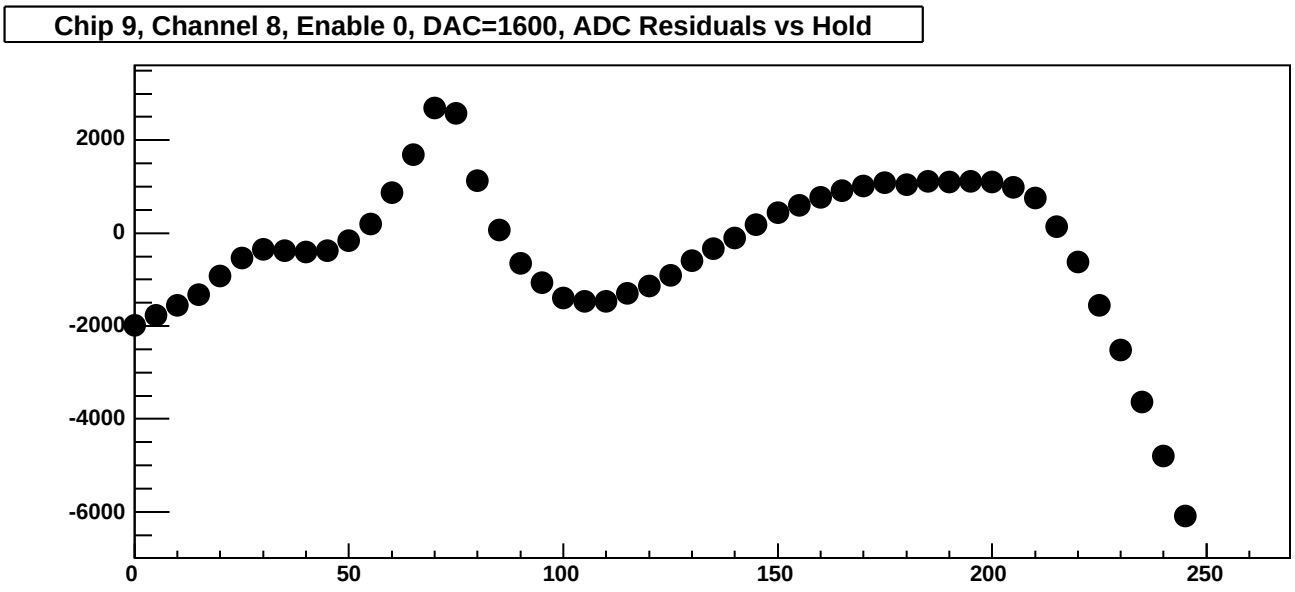


Chip 9, Channel 7, Enable 5, DAC=1600, ADC Residuals vs Hold

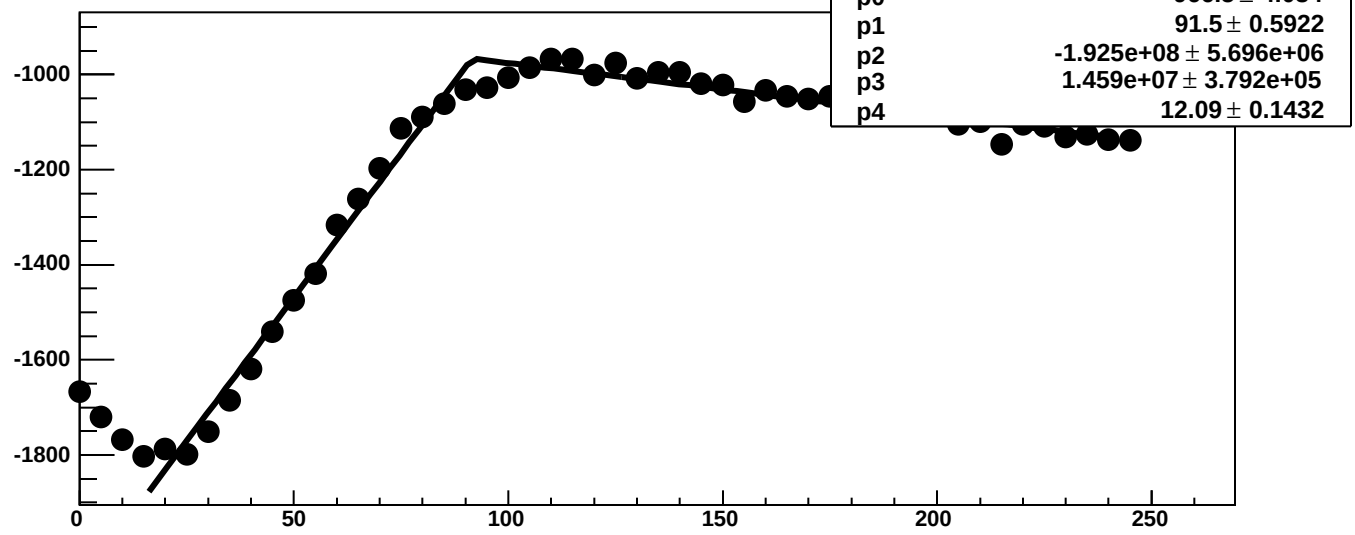



Chip 9, Channel 8, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.218e+05 / 41
p0 -3577 ± 9.562
p1 72.75 ± 0.03669
p2 2.801e+04 ± 57.71
p3 47.21 ± 0.1102
p4 -54.16 ± 0.2823
18000
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

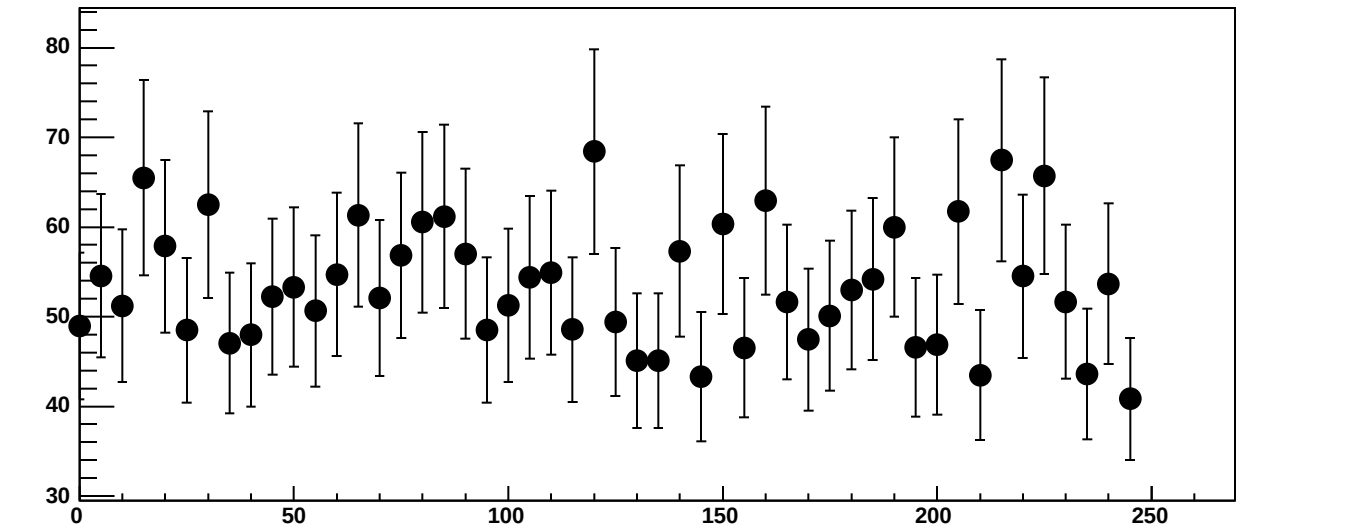

Chip 9, Channel 8, Enable 0, DAC=1600, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250


Chip 9, Channel 8, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

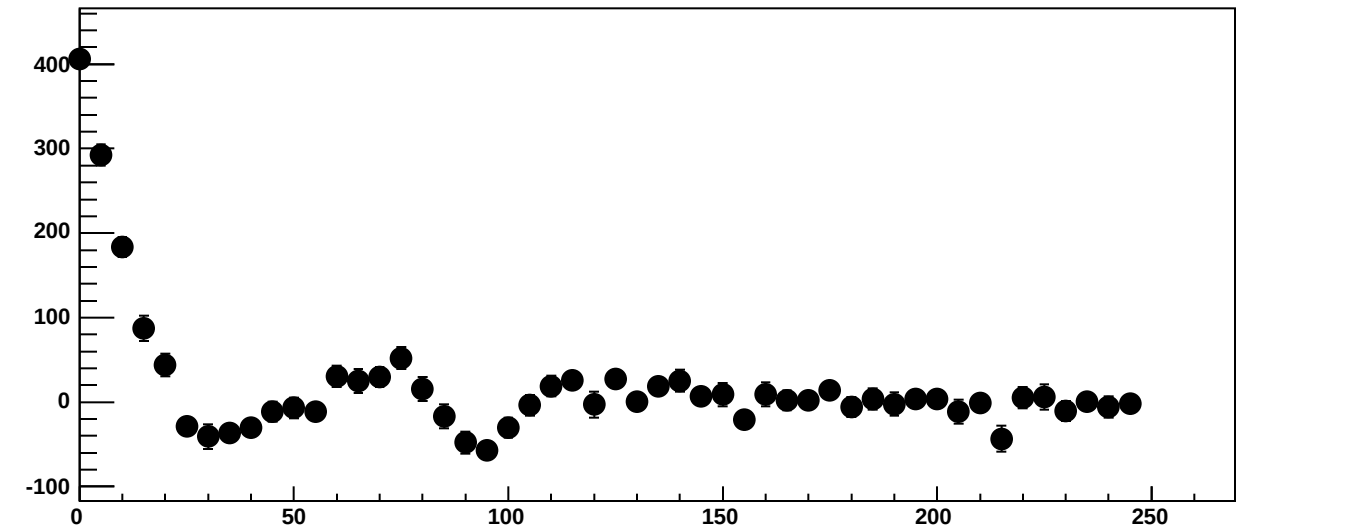
Chip 9, Channel 8, Enable 1, DAC=1600, ADC Mean vs Hold



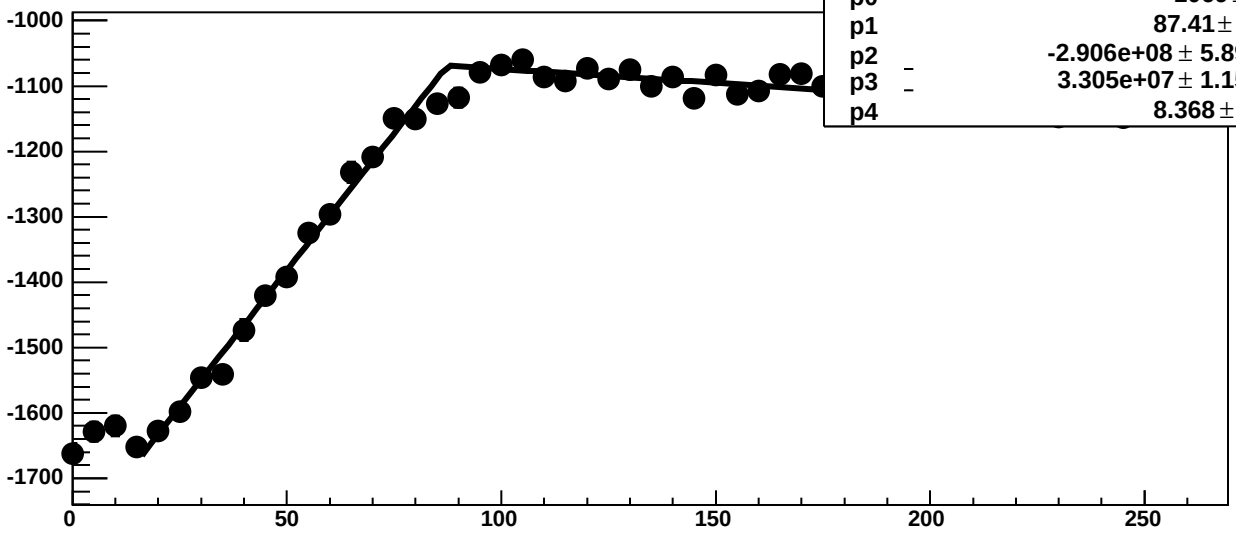
Chip 9, Channel 8, Enable 1, DAC=1600, ADC Noise vs Hold



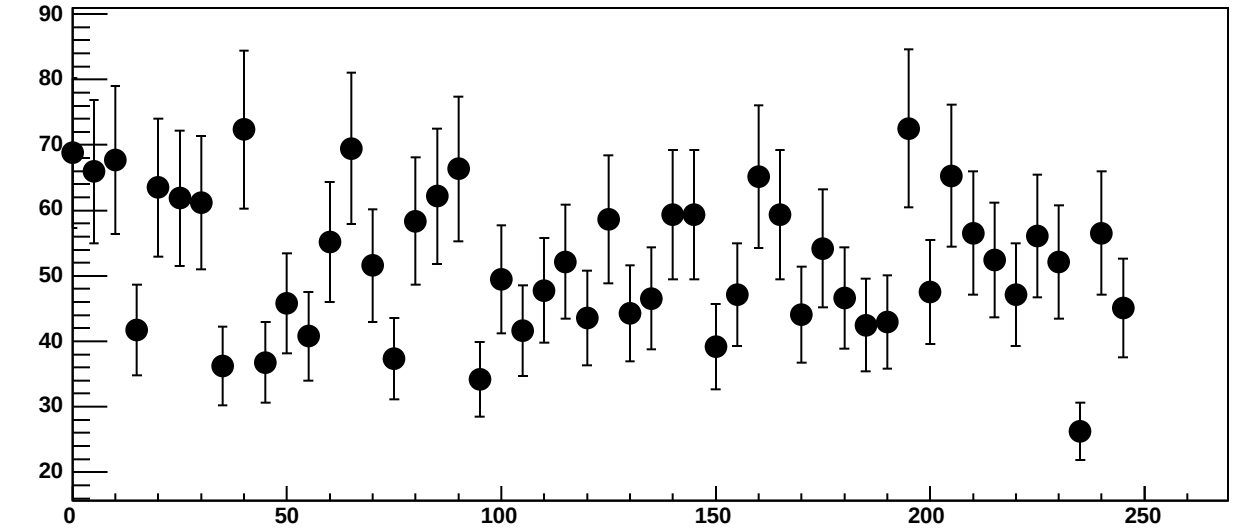
Chip 9, Channel 8, Enable 1, DAC=1600, ADC Residuals vs Hold



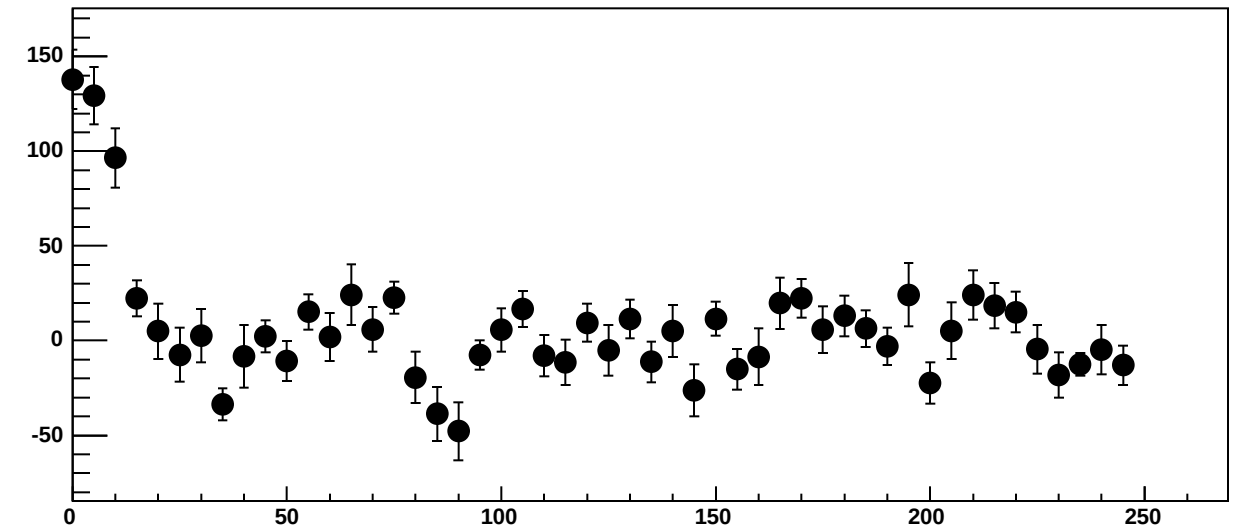
Chip 9, Channel 8, Enable 2, DAC=1600, ADC Mean vs Hold

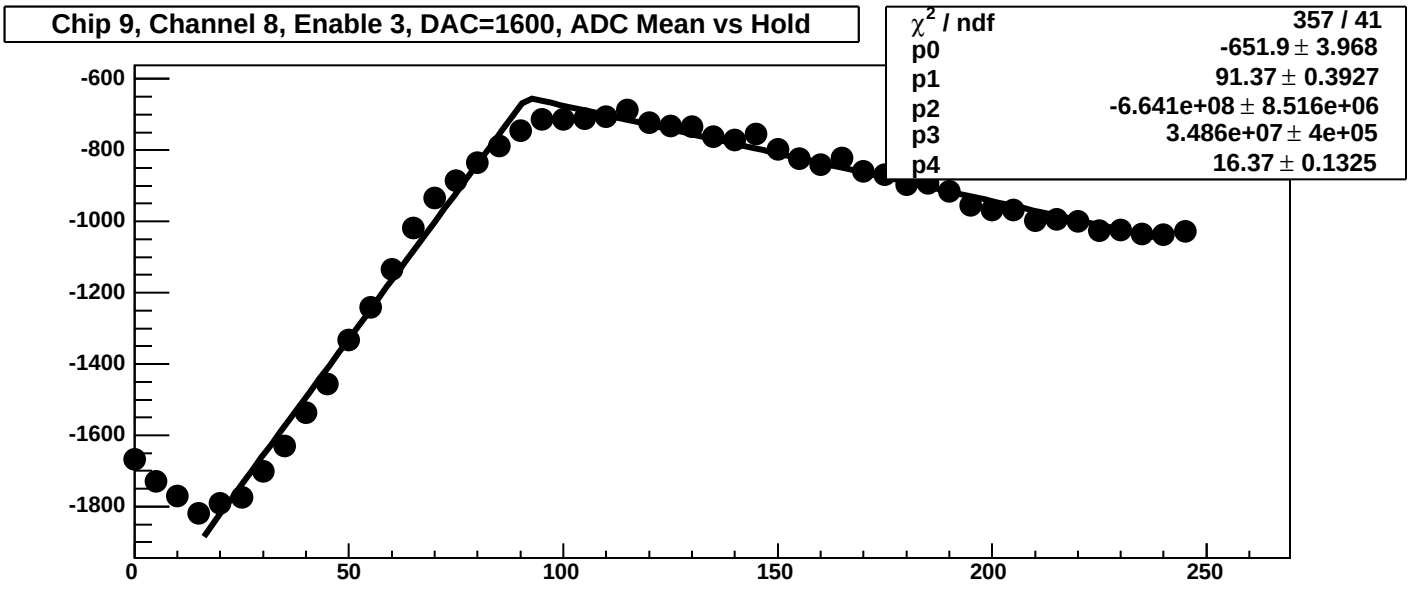


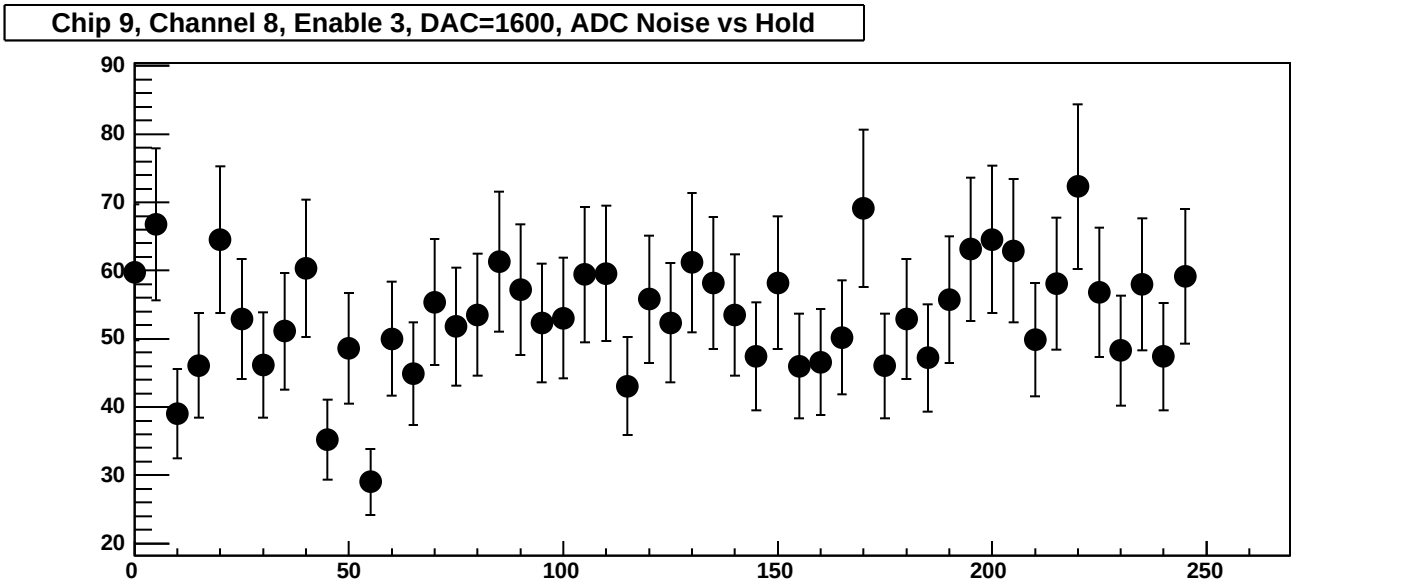
Chip 9, Channel 8, Enable 2, DAC=1600, ADC Noise vs Hold

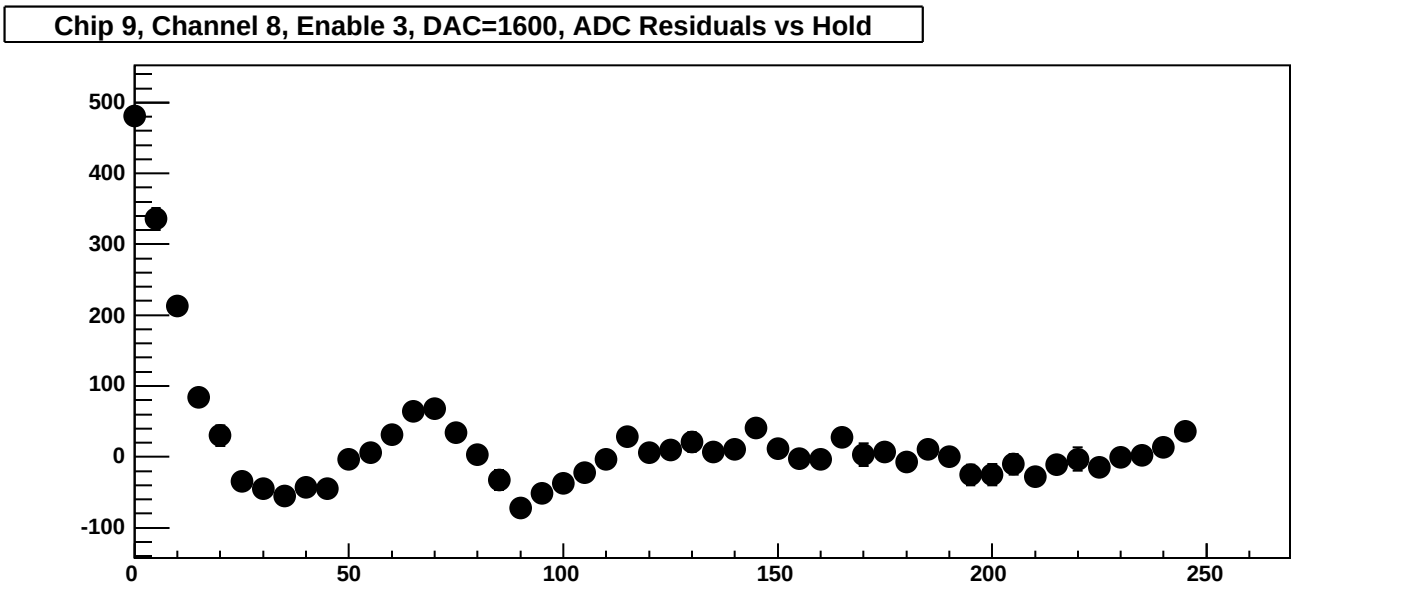


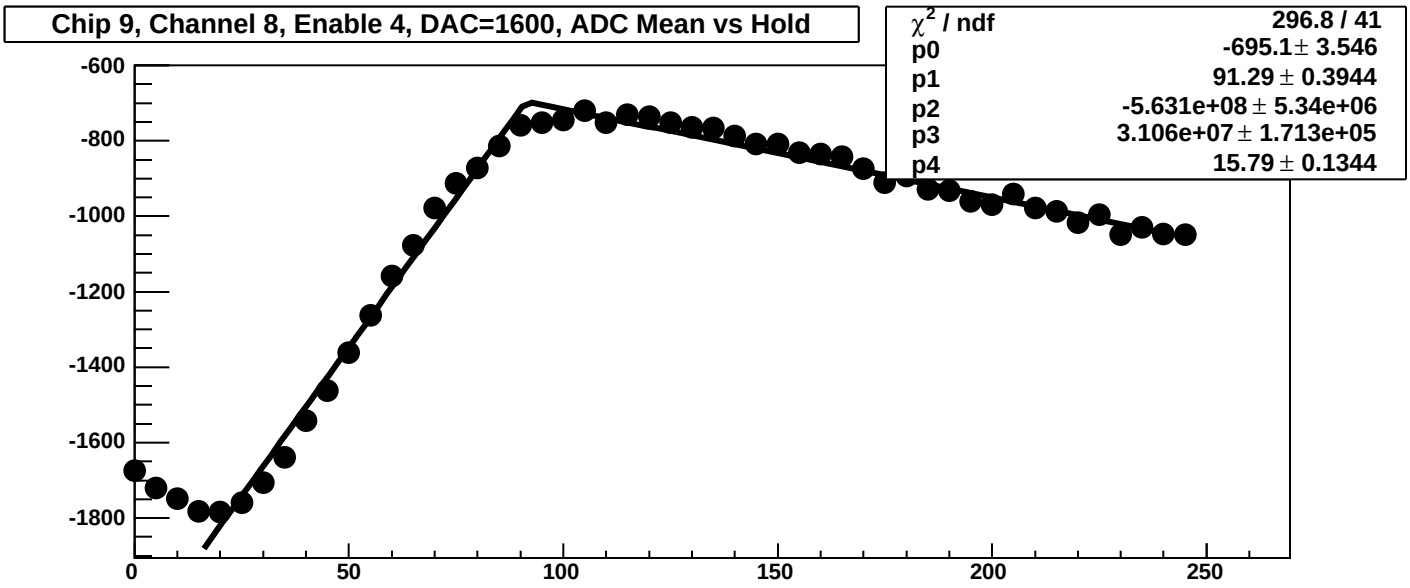
Chip 9, Channel 8, Enable 2, DAC=1600, ADC Residuals vs Hold

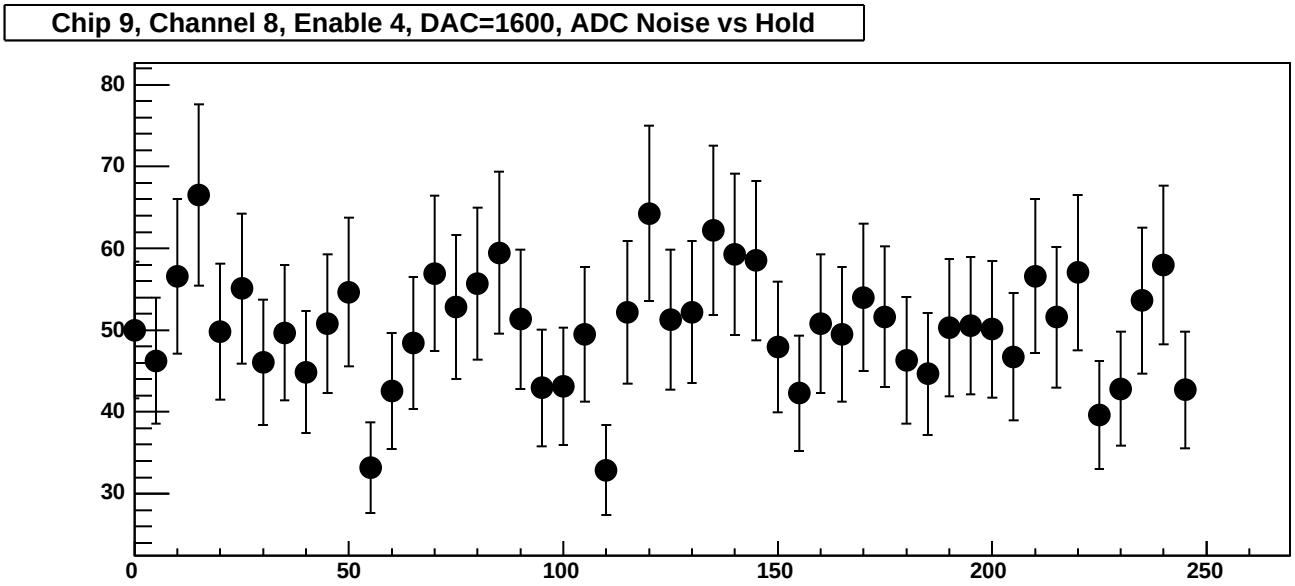


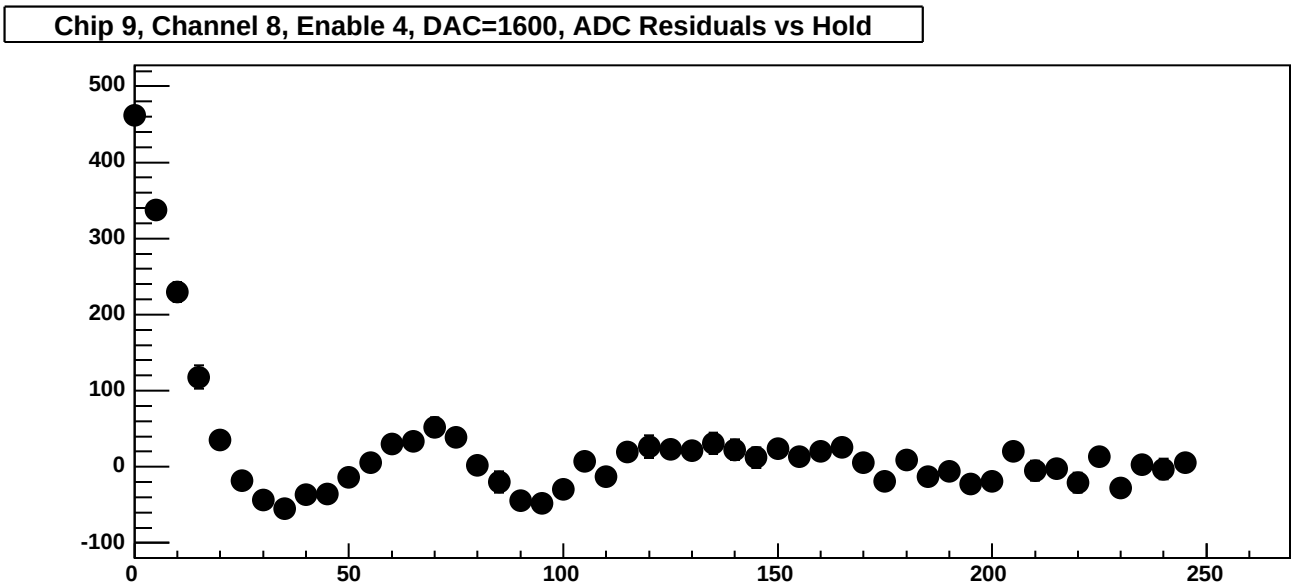

Chip 9, Channel 8, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 357 / 41
p0 -651.9 ± 3.968
p1 91.37 ± 0.3927
p2 -6.641e+08 ± 8.516e+06
p3 3.486e+07 ± 4e+05
p4 16.37 ± 0.1325
-600
-800
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250


Chip 9, Channel 8, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
20
0
50
100
150
200
250

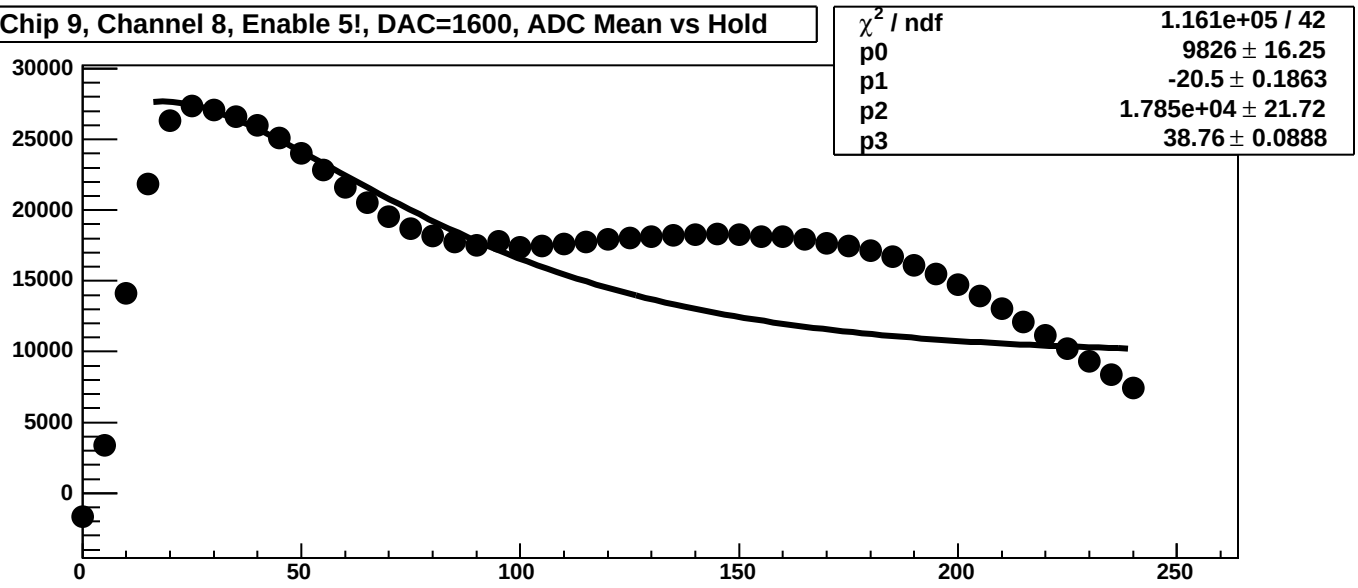

Chip 9, Channel 8, Enable 3, DAC=1600, ADC Residuals vs Hold
500
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 9, Channel 8, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 296.8 / 41
p0 -695.1 ± 3.546
p1 91.29 ± 0.3944
p2 -5.631e+08 ± 5.34e+06
p3 3.106e+07 ± 1.713e+05
p4 15.79 ± 0.1344
-600
-800
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250

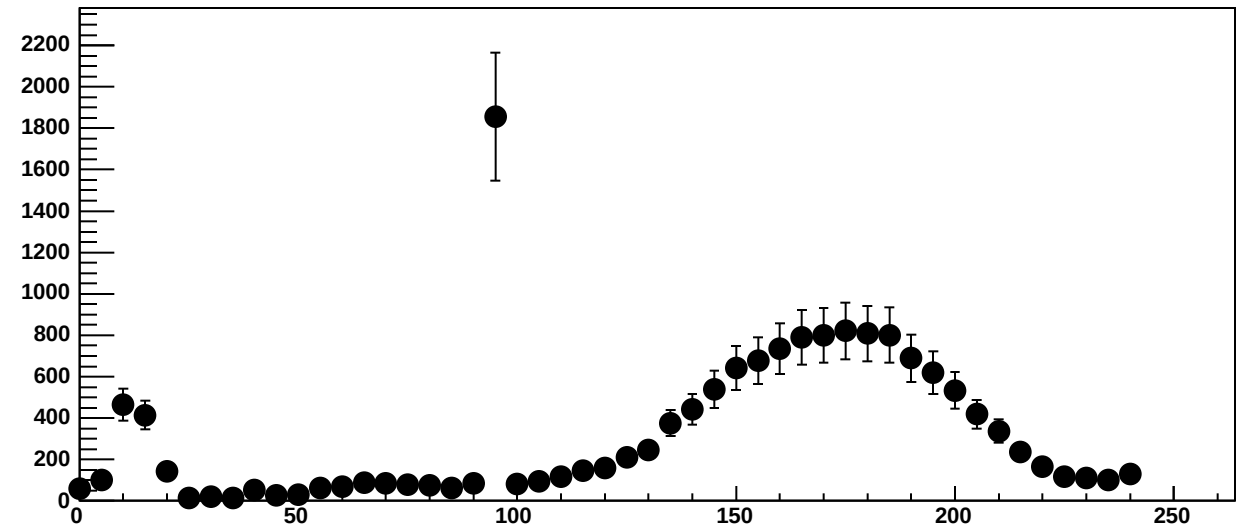

Chip 9, Channel 8, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 9, Channel 8, Enable 4, DAC=1600, ADC Residuals vs Hold
500
400
300
200
100
0
-100
0
50
100
150
200
250

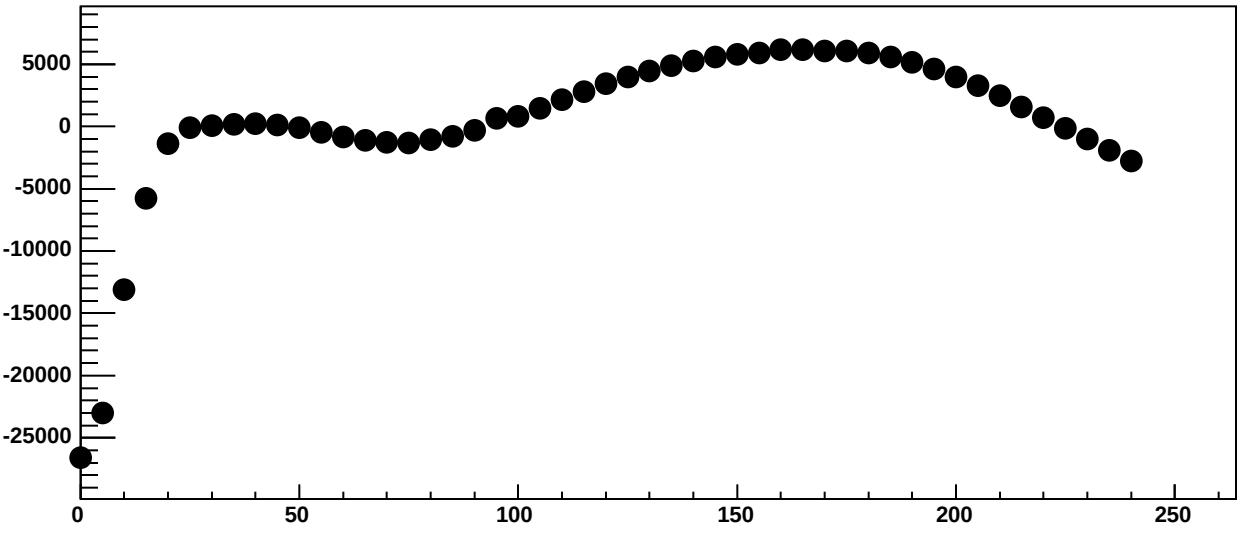
Chip 9, Channel 8, Enable 5!, DAC=1600, ADC Mean vs Hold



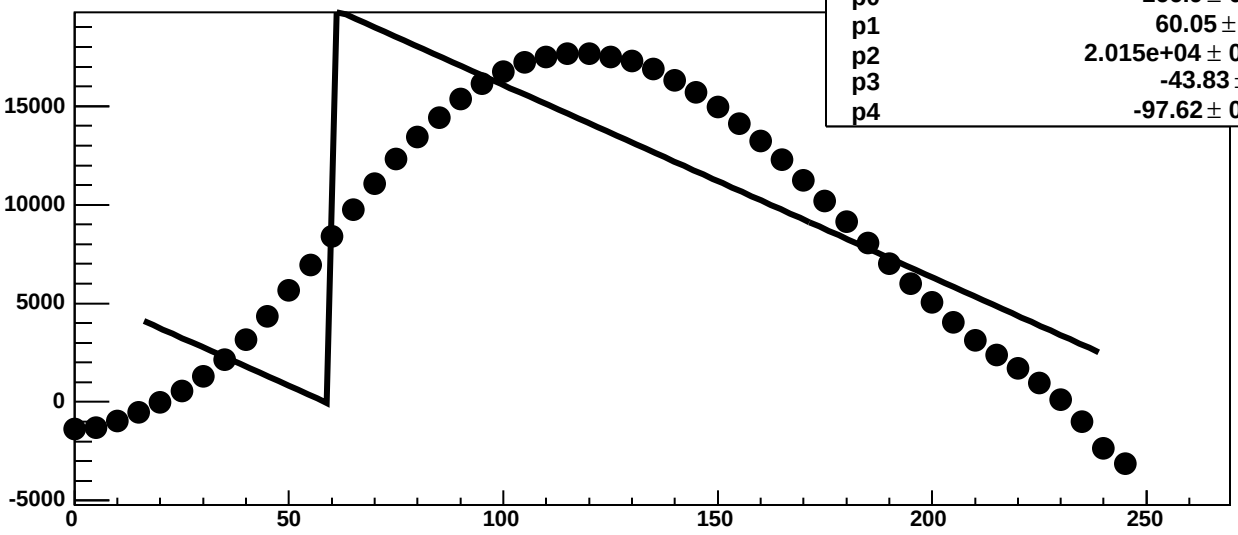
Chip 9, Channel 8, Enable 5!, DAC=1600, ADC Noise vs Hold



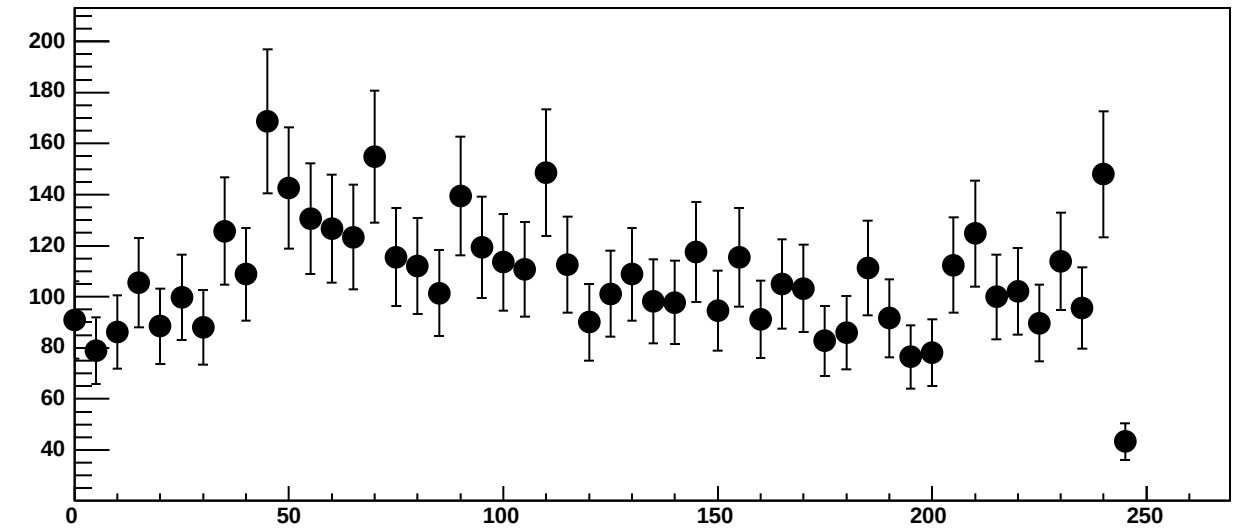
Chip 9, Channel 8, Enable 5!, DAC=1600, ADC Residuals vs Hold



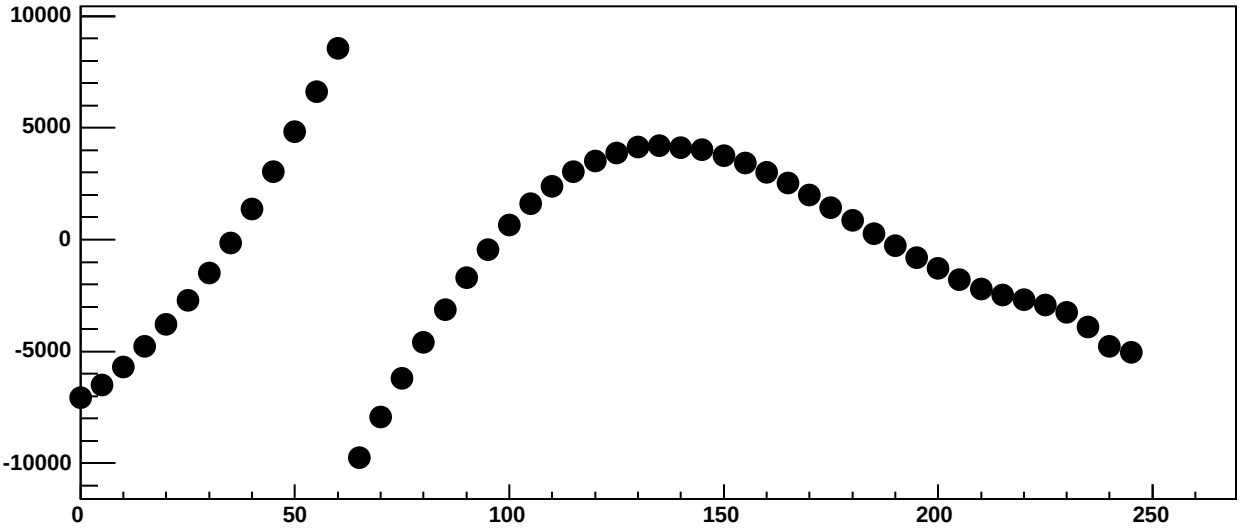
Chip 9, Channel 9, Enable 0, DAC=1600, ADC Mean vs Hold



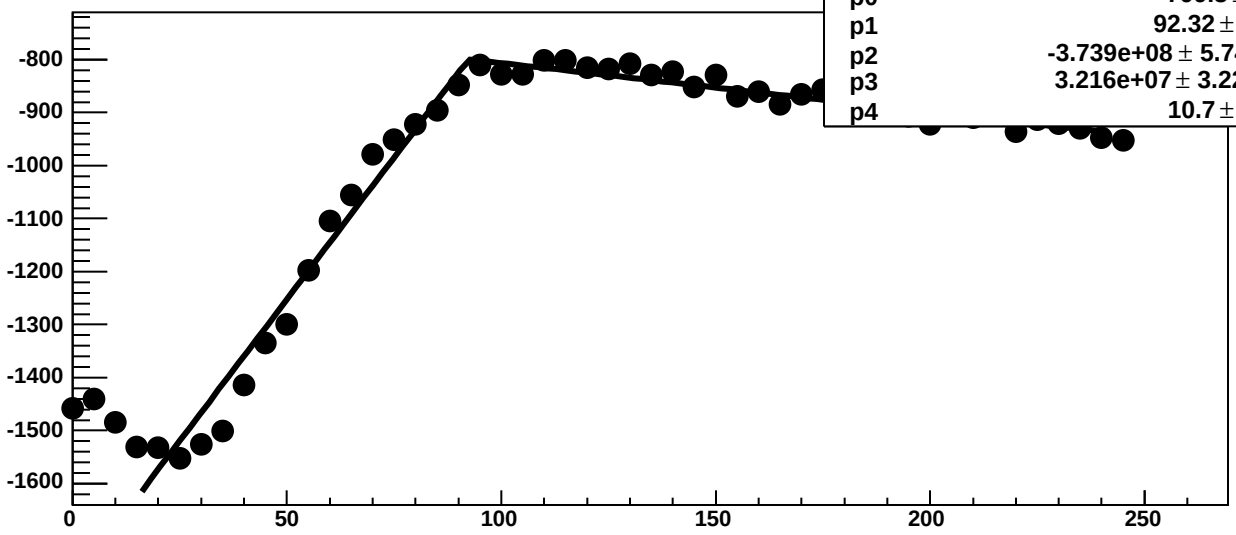
Chip 9, Channel 9, Enable 0, DAC=1600, ADC Noise vs Hold



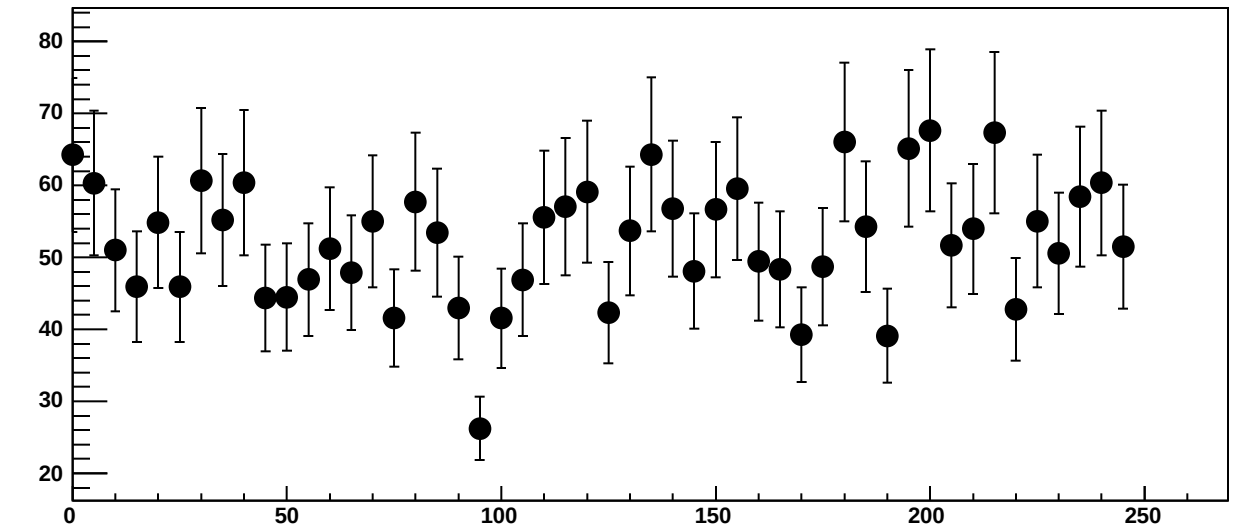
Chip 9, Channel 9, Enable 0, DAC=1600, ADC Residuals vs Hold



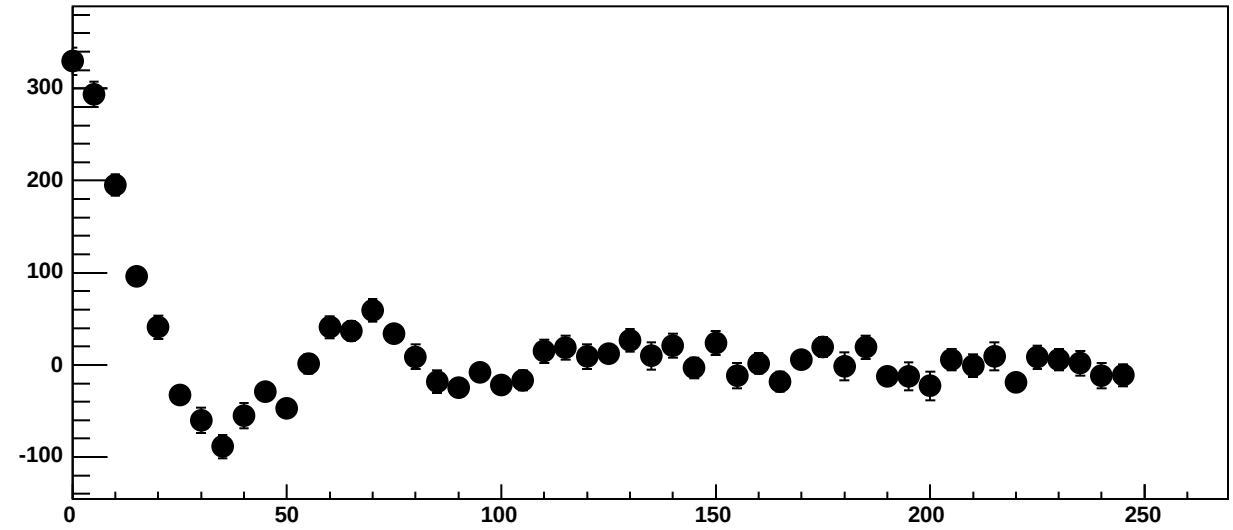
Chip 9, Channel 9, Enable 1, DAC=1600, ADC Mean vs Hold



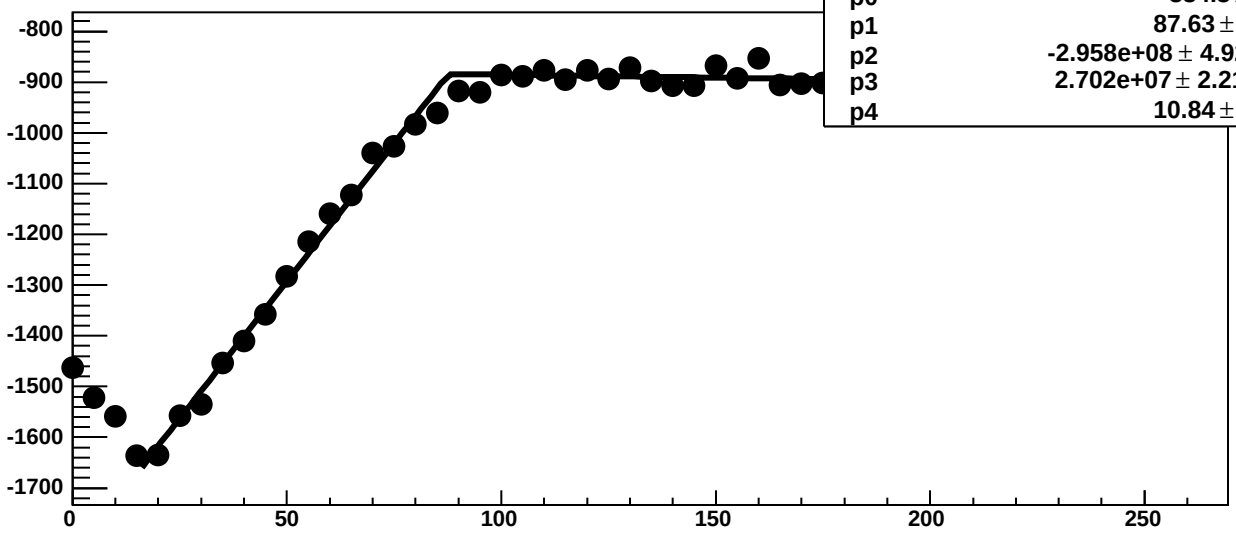
Chip 9, Channel 9, Enable 1, DAC=1600, ADC Noise vs Hold



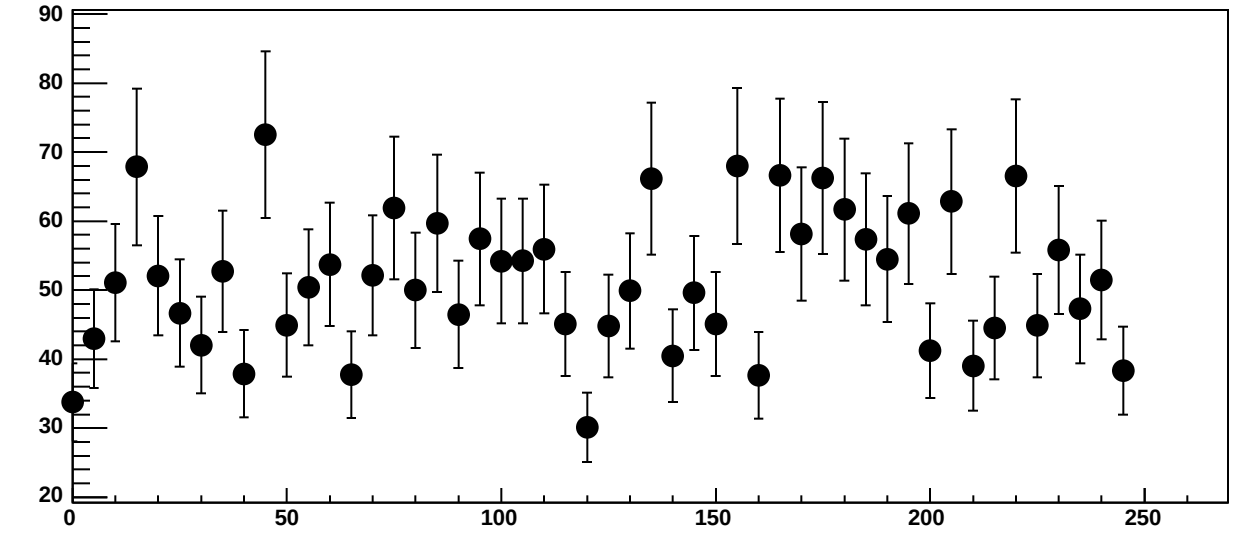
Chip 9, Channel 9, Enable 1, DAC=1600, ADC Residuals vs Hold



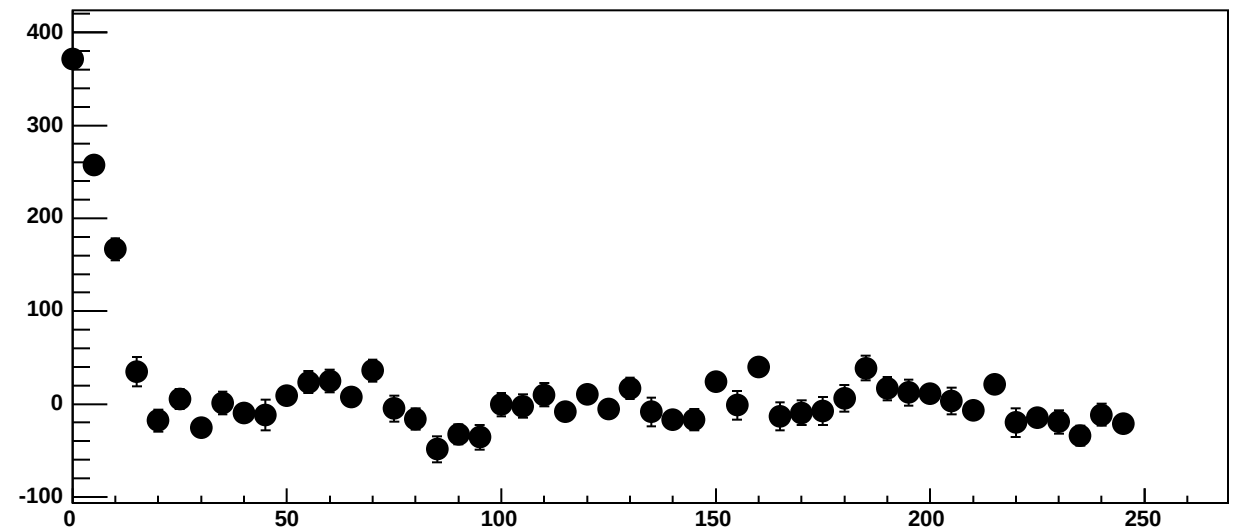
Chip 9, Channel 9, Enable 2, DAC=1600, ADC Mean vs Hold



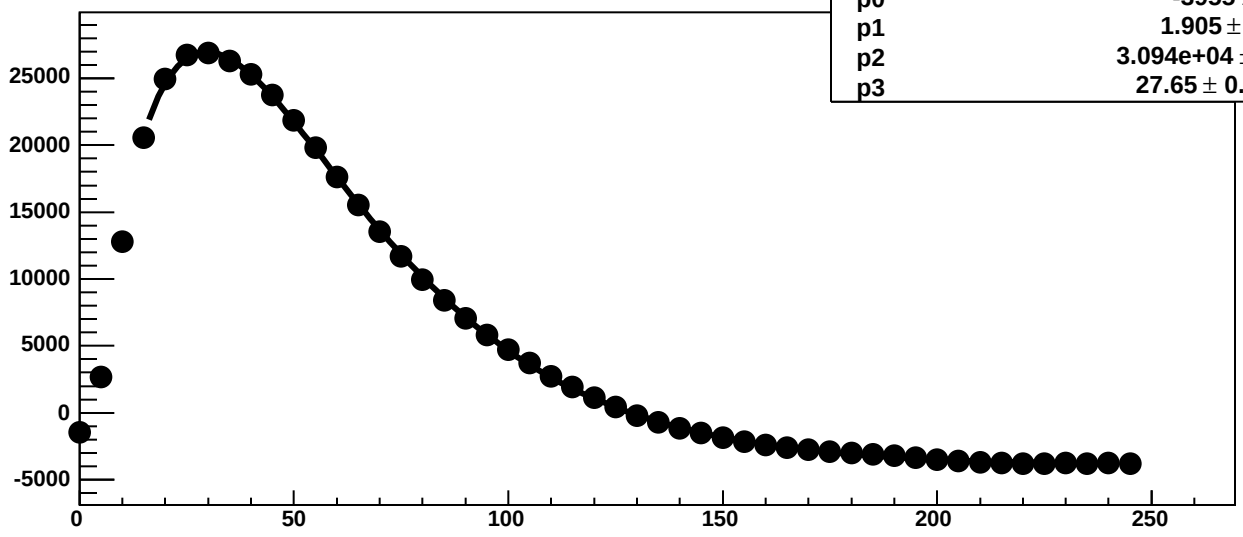
Chip 9, Channel 9, Enable 2, DAC=1600, ADC Noise vs Hold



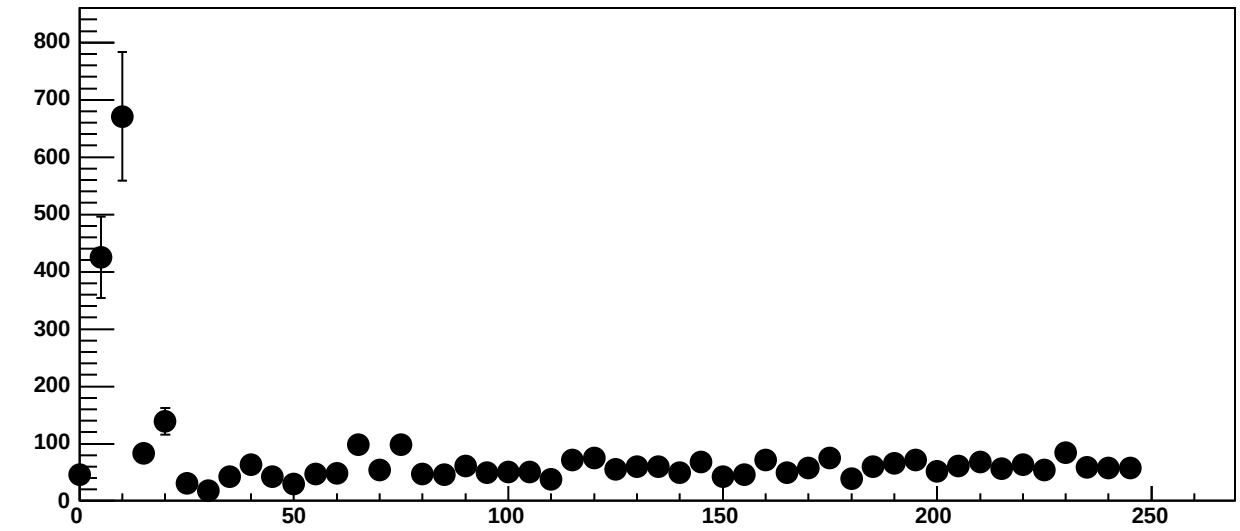
Chip 9, Channel 9, Enable 2, DAC=1600, ADC Residuals vs Hold



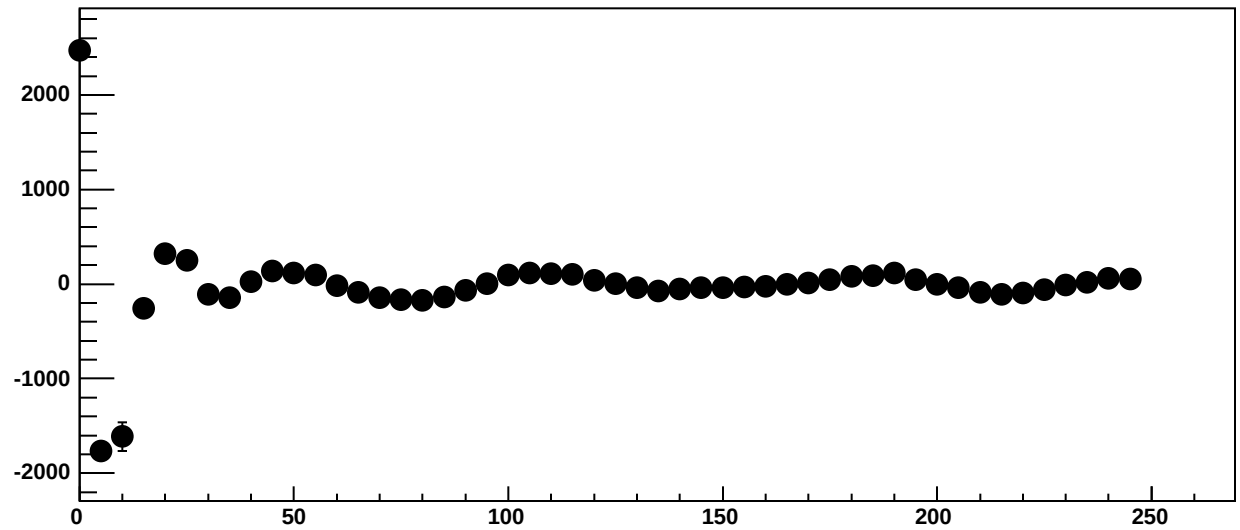
Chip 9, Channel 9, Enable 3!, DAC=1600, ADC Mean vs Hold



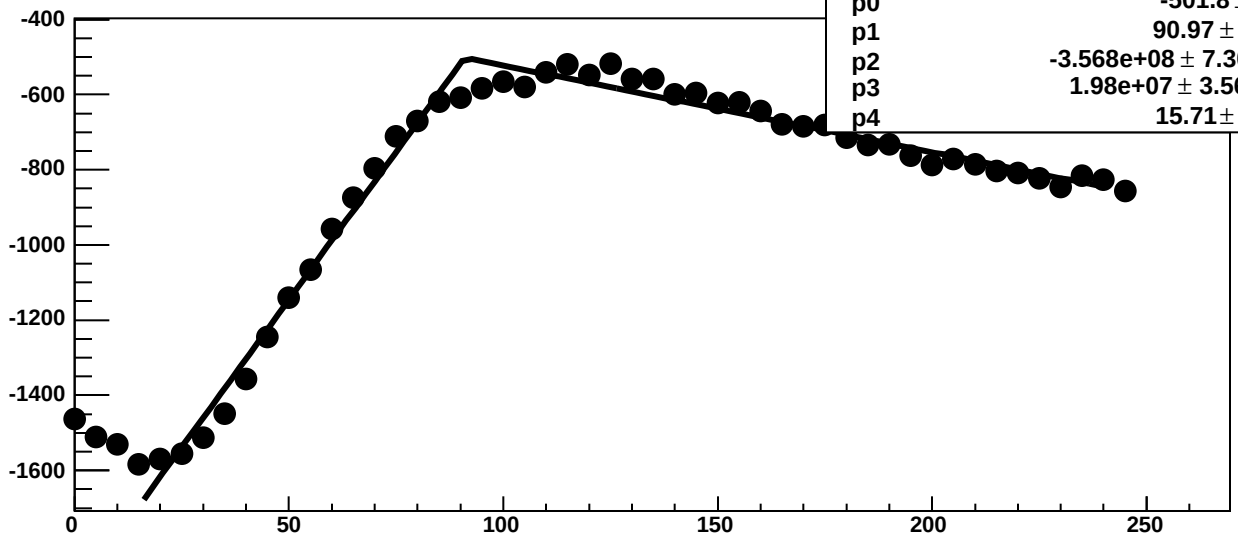
Chip 9, Channel 9, Enable 3!, DAC=1600, ADC Noise vs Hold



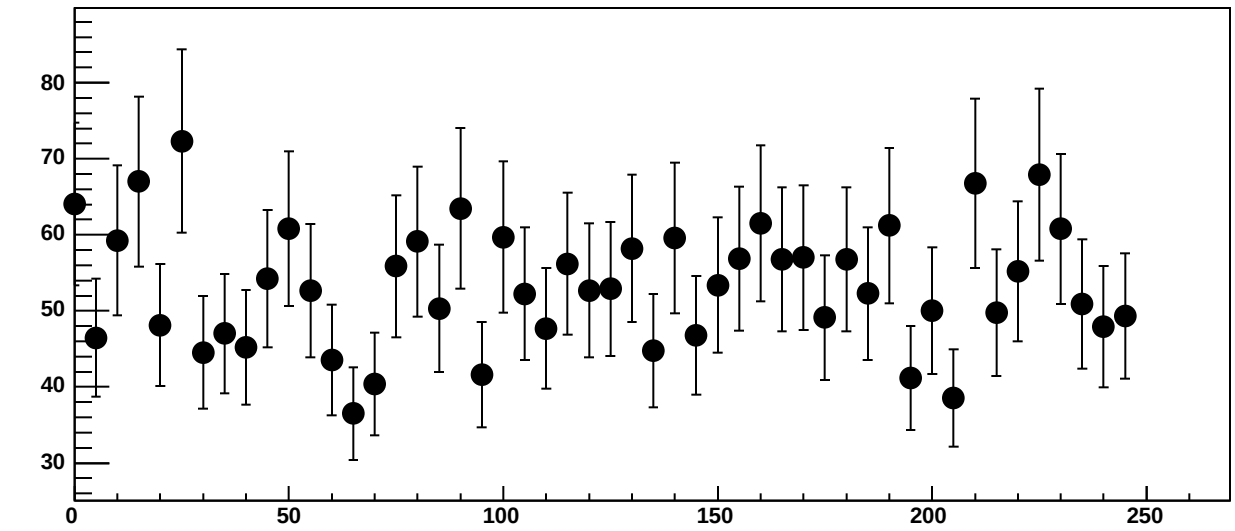
Chip 9, Channel 9, Enable 3!, DAC=1600, ADC Residuals vs Hold



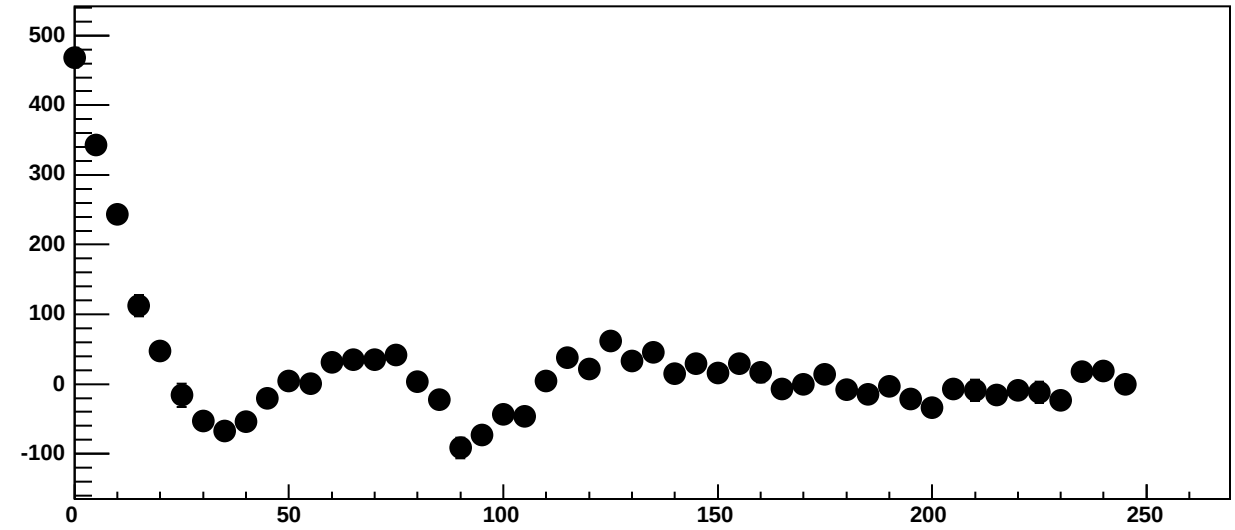
Chip 9, Channel 9, Enable 4, DAC=1600, ADC Mean vs Hold



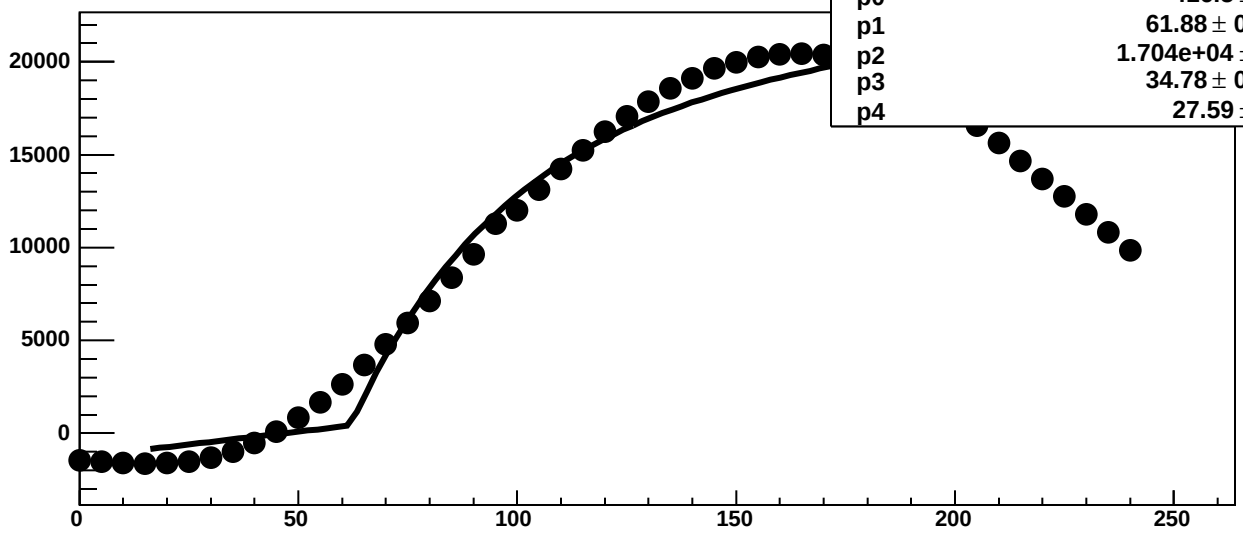
Chip 9, Channel 9, Enable 4, DAC=1600, ADC Noise vs Hold



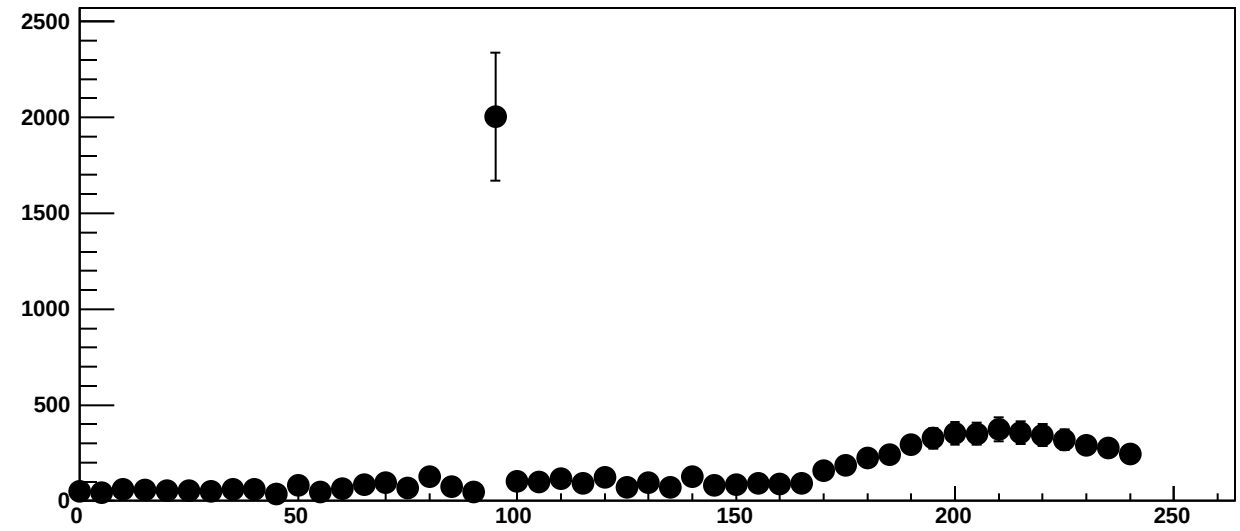
Chip 9, Channel 9, Enable 4, DAC=1600, ADC Residuals vs Hold



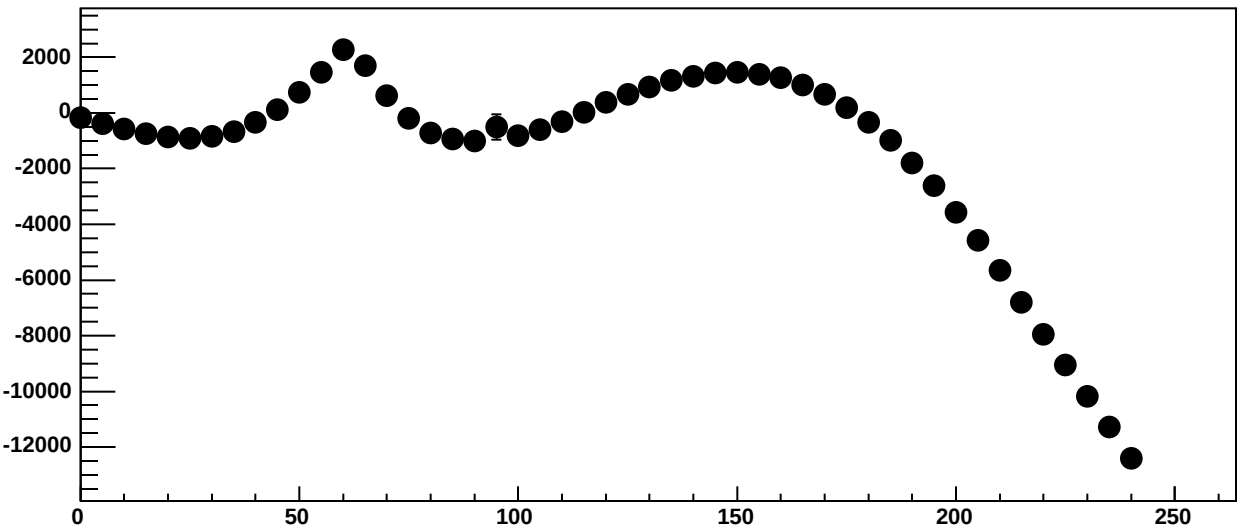
Chip 9, Channel 9, Enable 5, DAC=1600, ADC Mean vs Hold

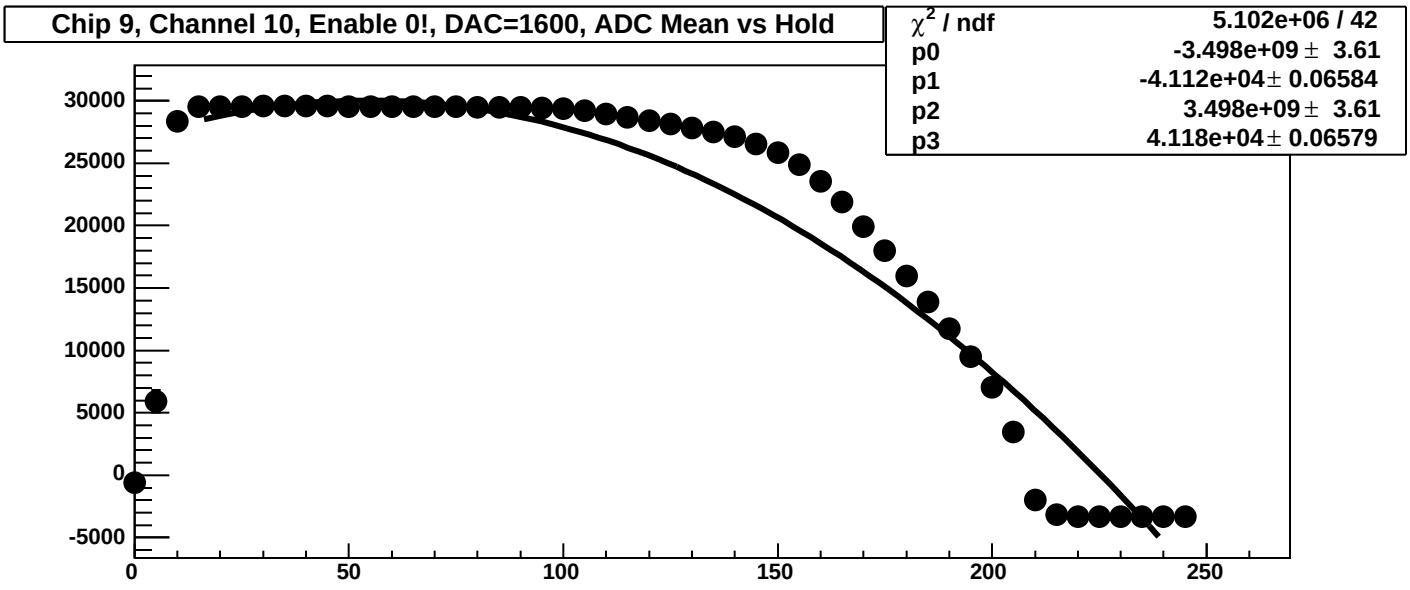


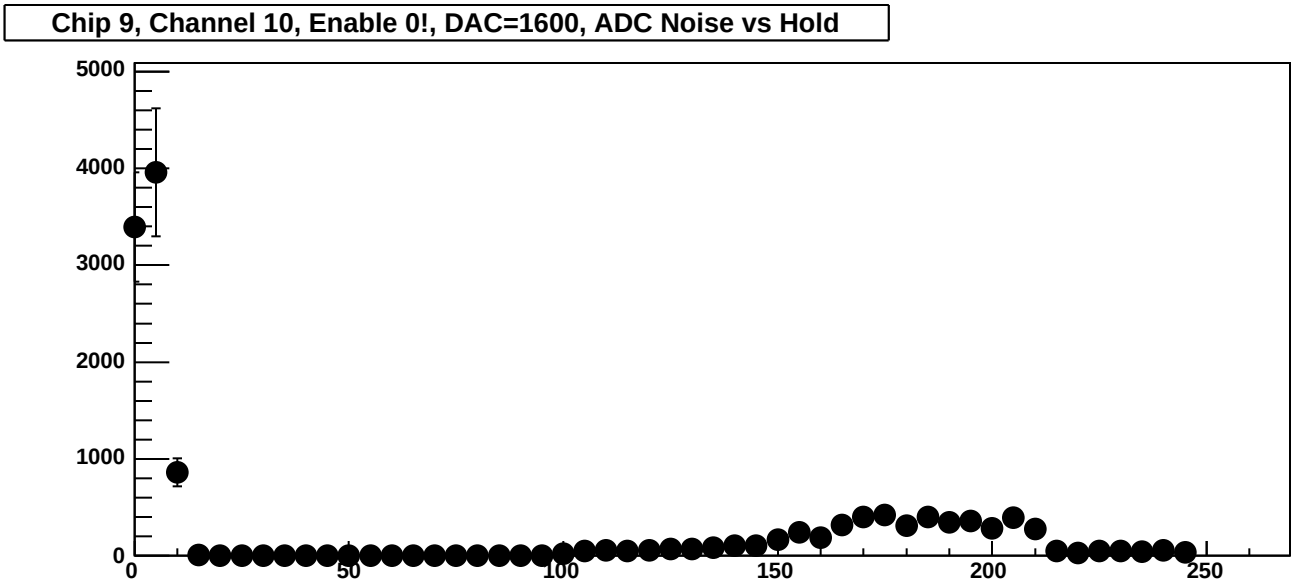
Chip 9, Channel 9, Enable 5, DAC=1600, ADC Noise vs Hold

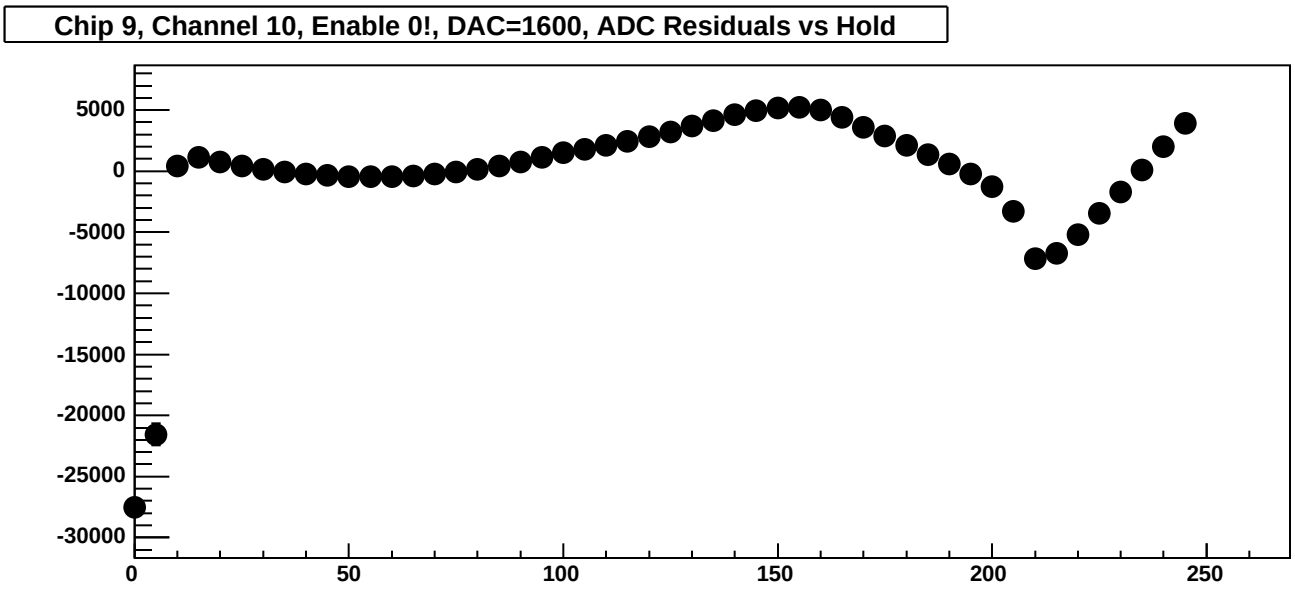


Chip 9, Channel 9, Enable 5, DAC=1600, ADC Residuals vs Hold

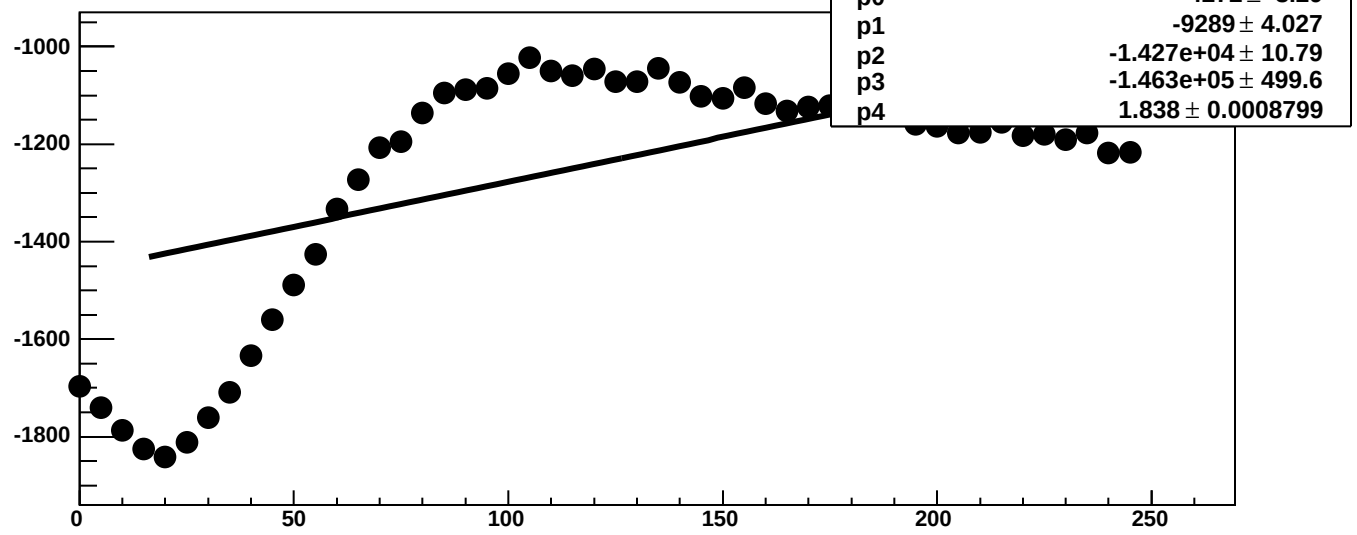



Chip 9, Channel 10, Enable 0!, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.102e+06 / 42
p0 -3.498e+09 ± 3.61
p1 -4.112e+04 ± 0.06584
p2 3.498e+09 ± 3.61
p3 4.118e+04 ± 0.06579
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

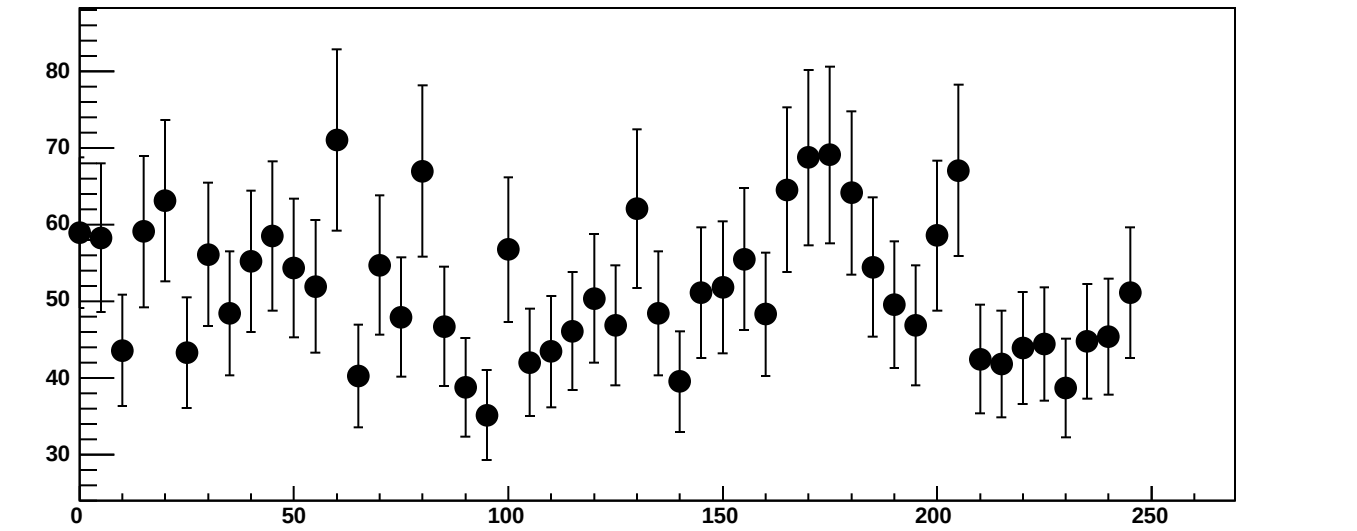

Chip 9, Channel 10, Enable 0!, DAC=1600, ADC Noise vs Hold
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 9, Channel 10, Enable 0!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
0
50
100
150
200
250

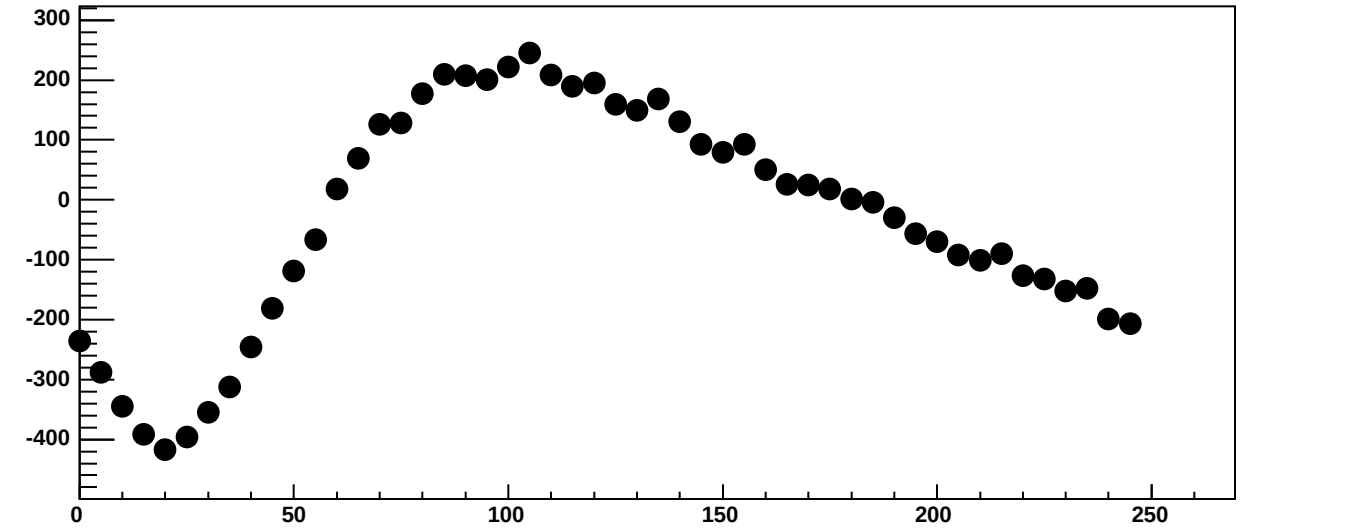
Chip 9, Channel 10, Enable 1, DAC=1600, ADC Mean vs Hold

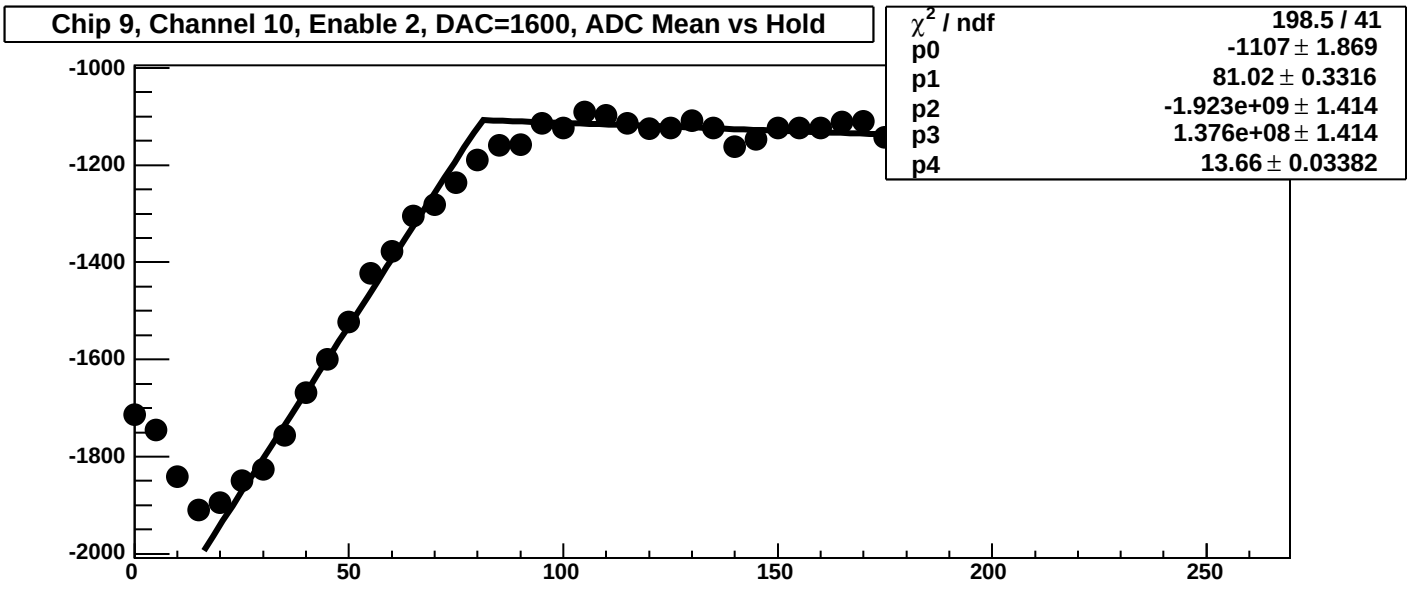


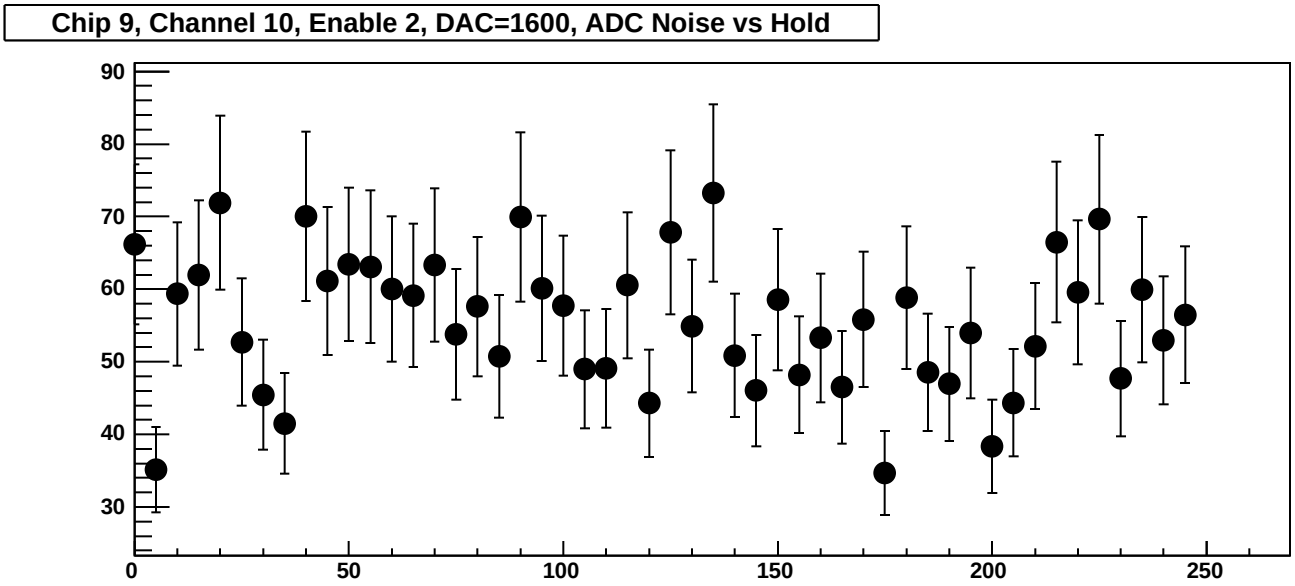
Chip 9, Channel 10, Enable 1, DAC=1600, ADC Noise vs Hold

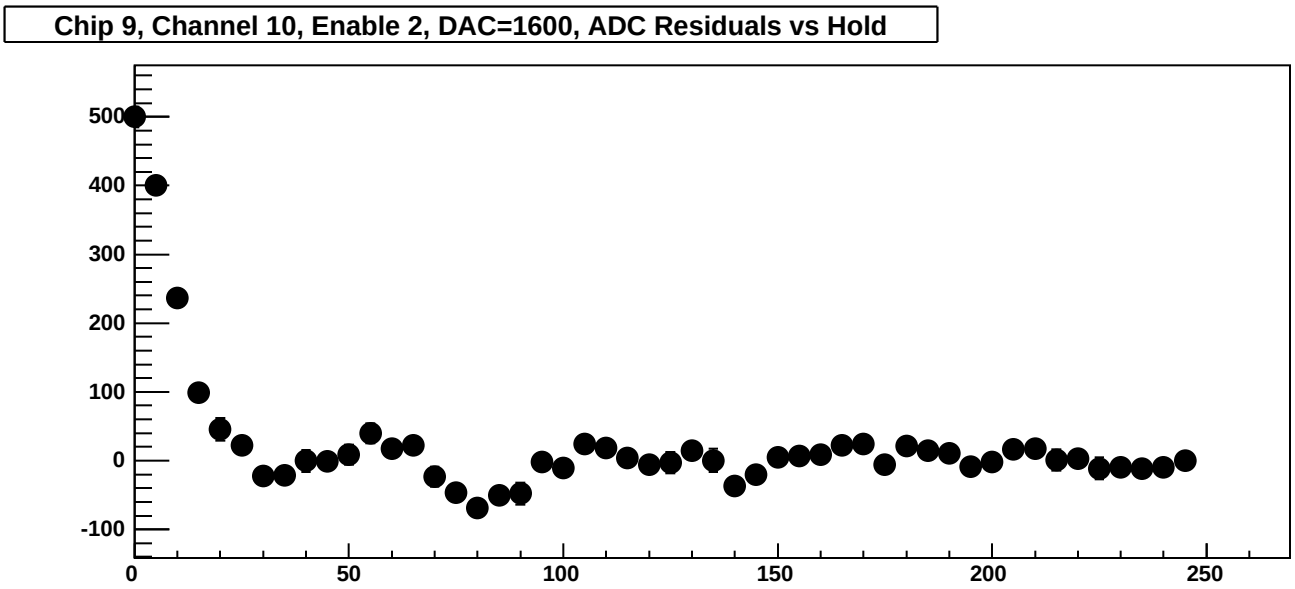


Chip 9, Channel 10, Enable 1, DAC=1600, ADC Residuals vs Hold

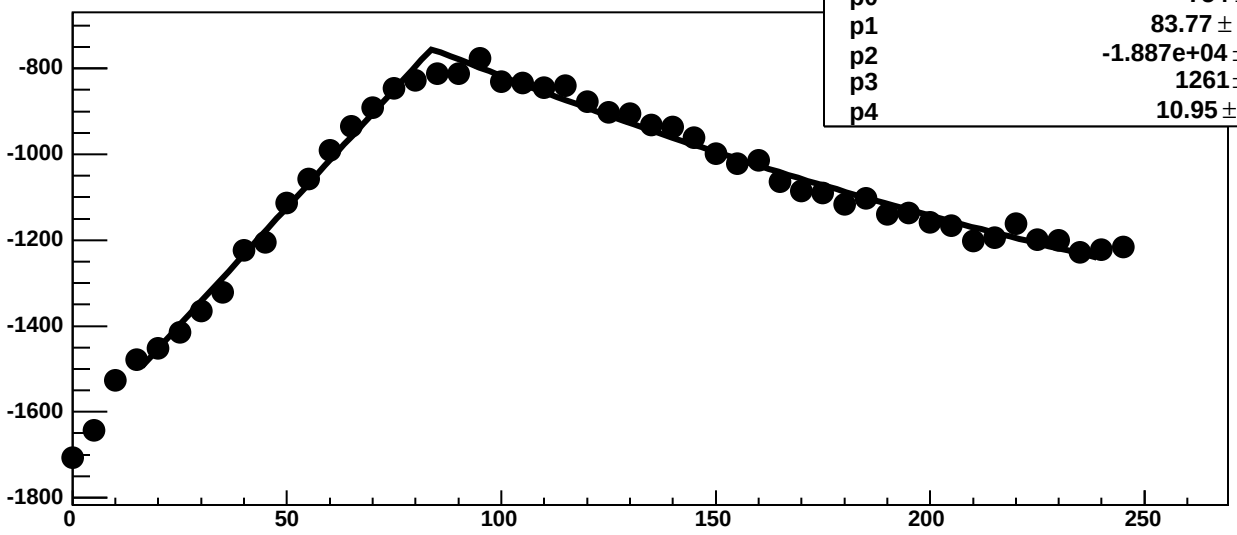



Chip 9, Channel 10, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 198.5 / 41
p0 -1107 ± 1.869
p1 81.02 ± 0.3316
p2 -1.923e+09 ± 1.414
p3 1.376e+08 ± 1.414
p4 13.66 ± 0.03382
-1000
-1200
-1400
-1600
-1800
-2000
0
50
100
150
200
250

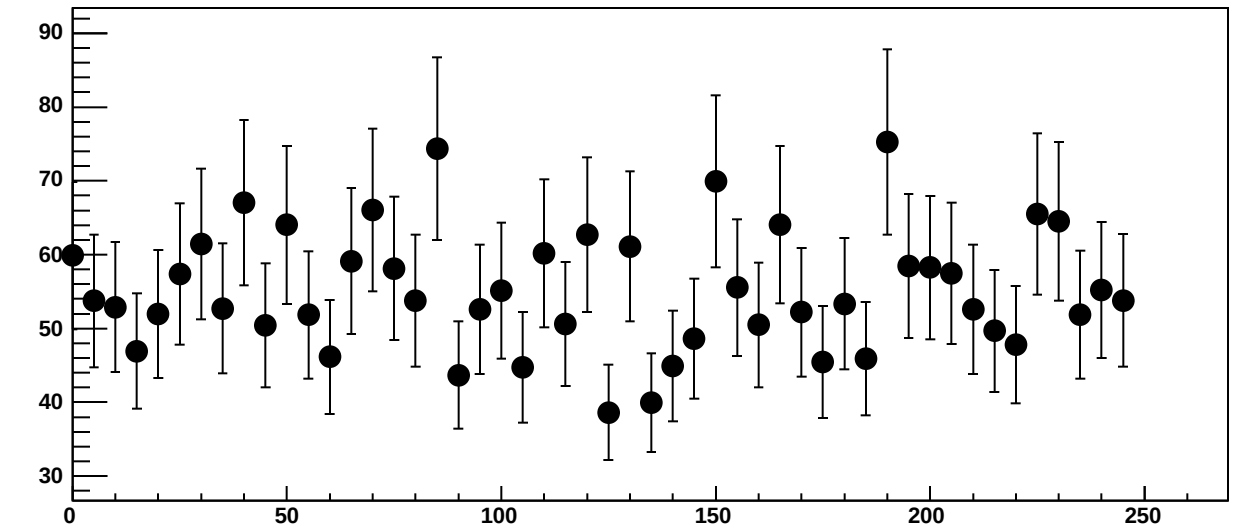

Chip 9, Channel 10, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 9, Channel 10, Enable 2, DAC=1600, ADC Residuals vs Hold
500
400
300
200
100
0
-100
0
50
100
150
200
250

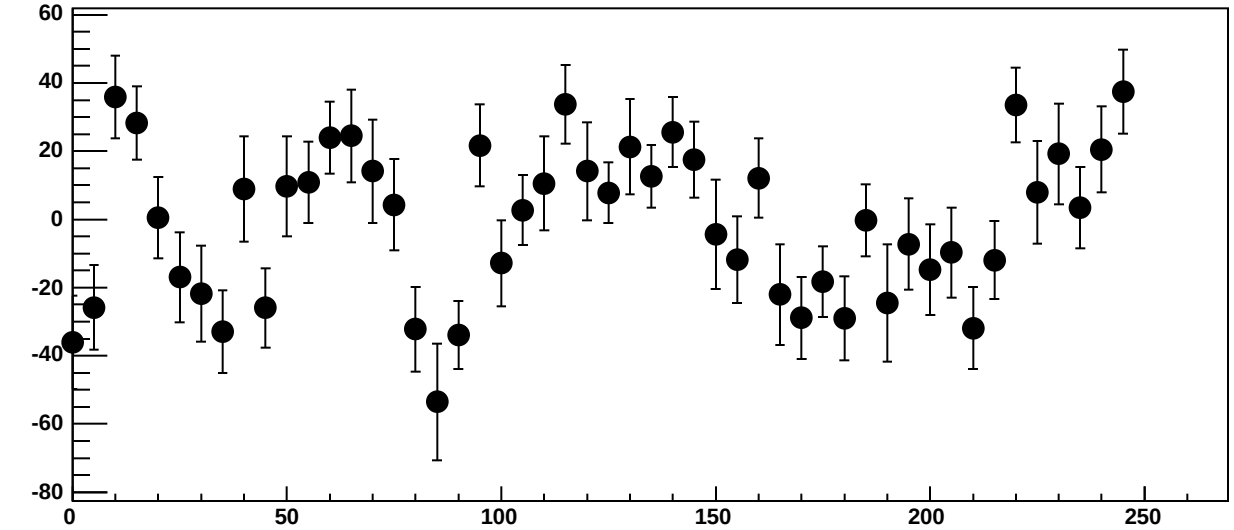
Chip 9, Channel 10, Enable 3, DAC=1600, ADC Mean vs Hold



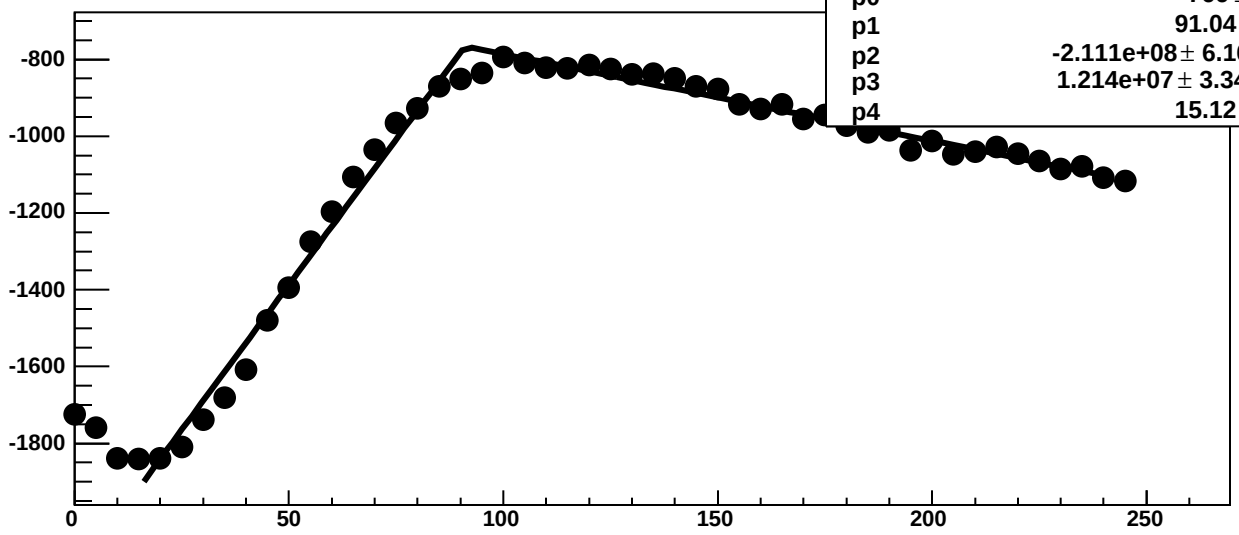
Chip 9, Channel 10, Enable 3, DAC=1600, ADC Noise vs Hold



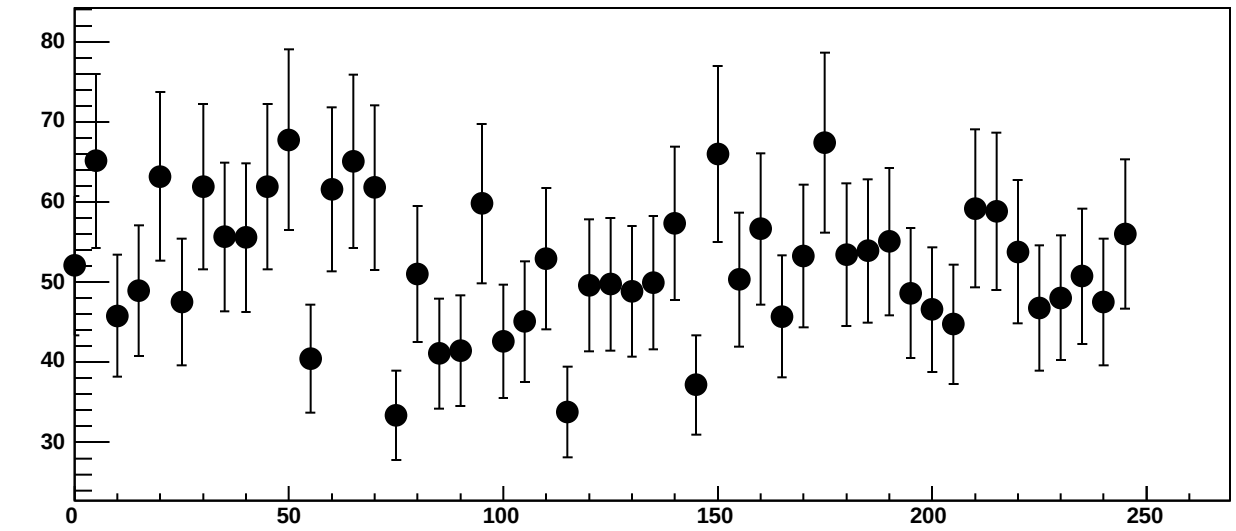
Chip 9, Channel 10, Enable 3, DAC=1600, ADC Residuals vs Hold



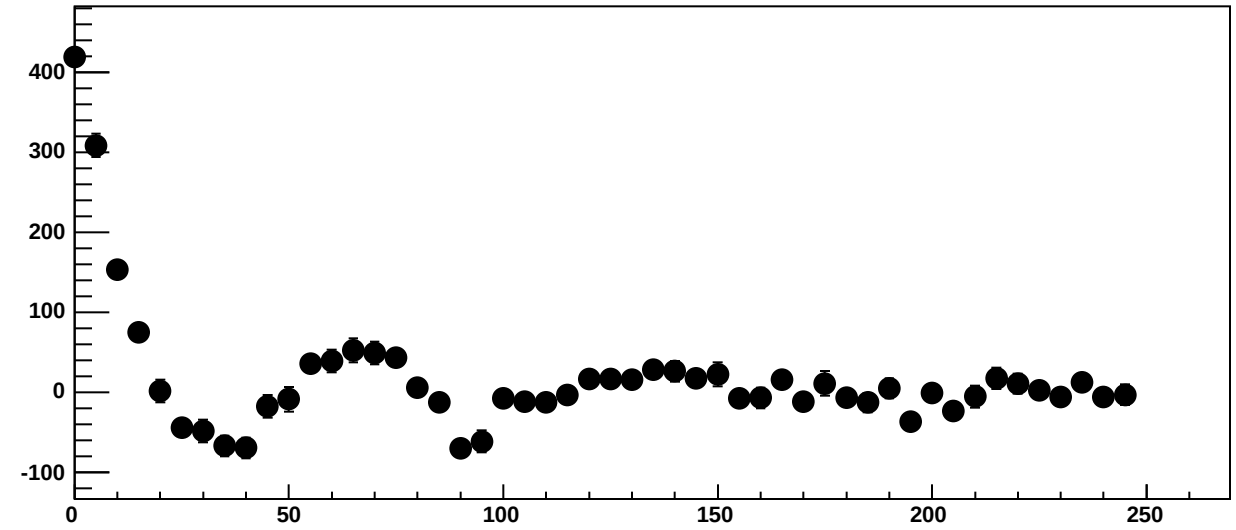
Chip 9, Channel 10, Enable 4, DAC=1600, ADC Mean vs Hold

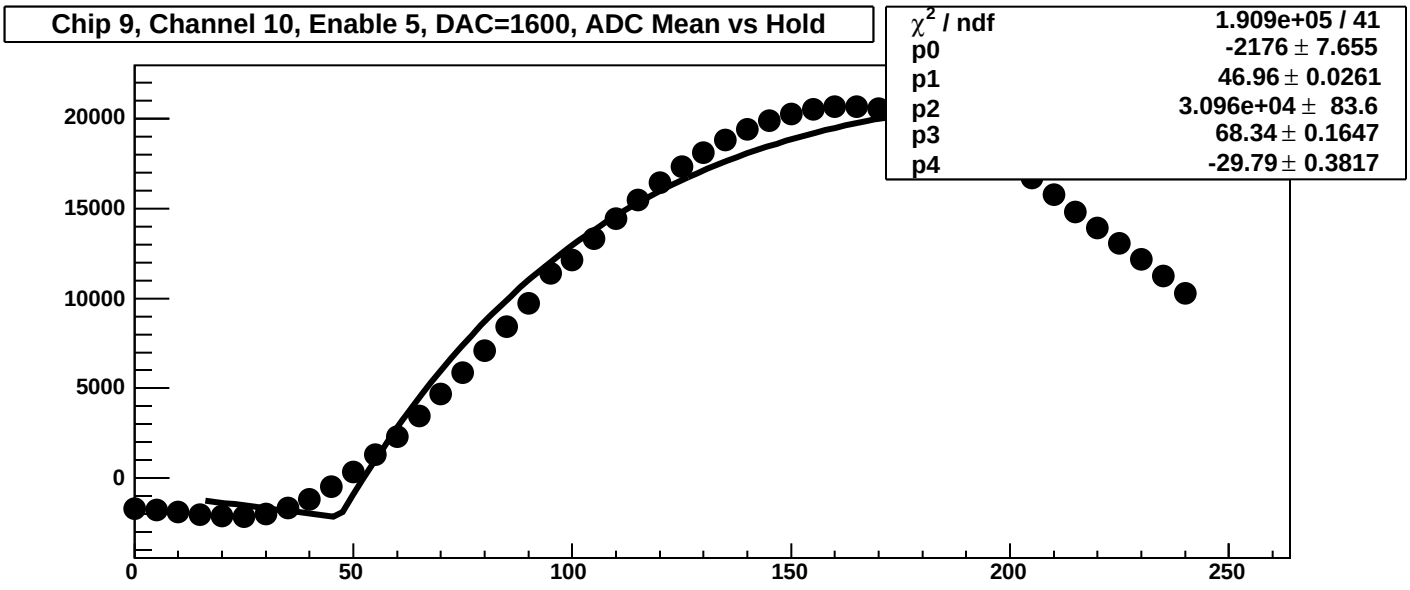


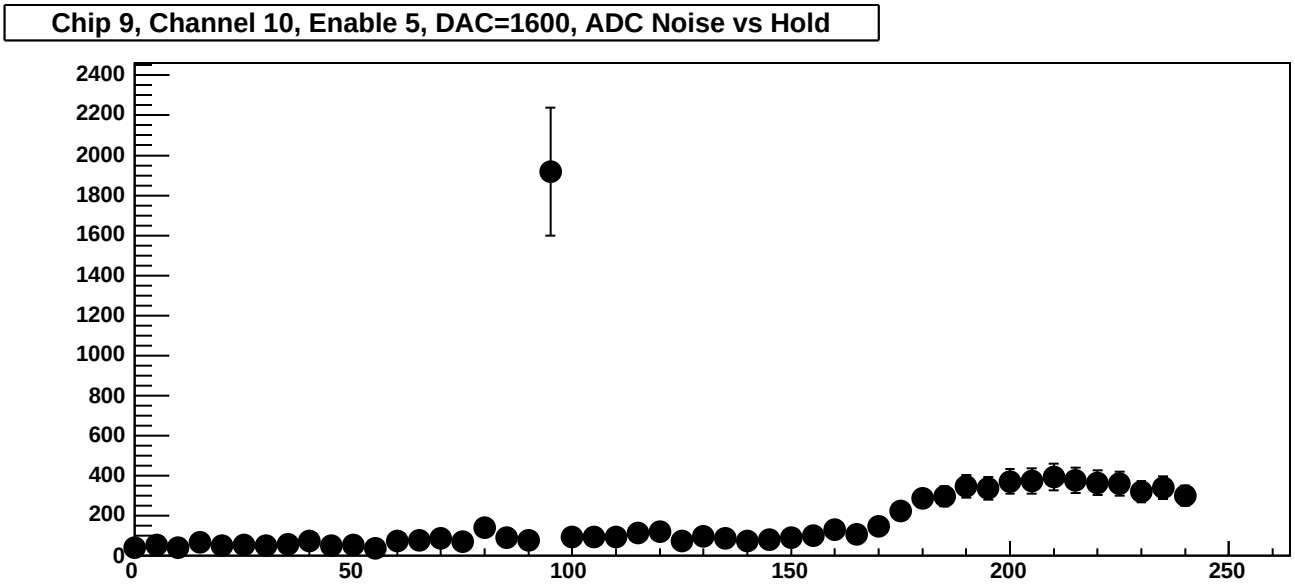
Chip 9, Channel 10, Enable 4, DAC=1600, ADC Noise vs Hold

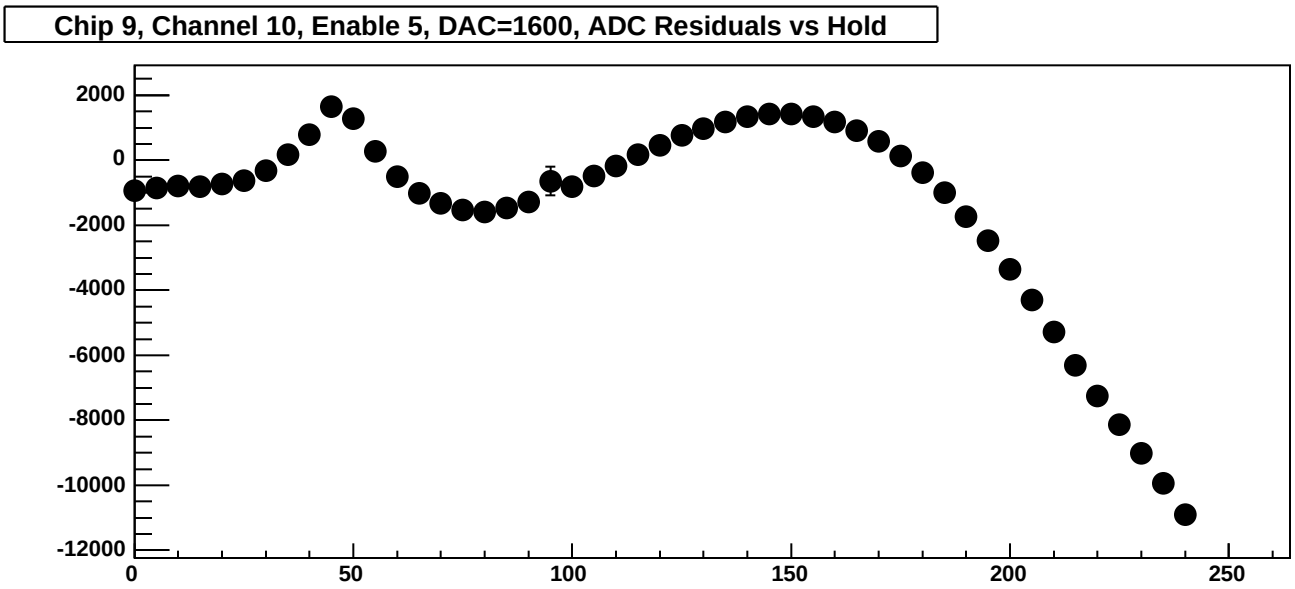


Chip 9, Channel 10, Enable 4, DAC=1600, ADC Residuals vs Hold

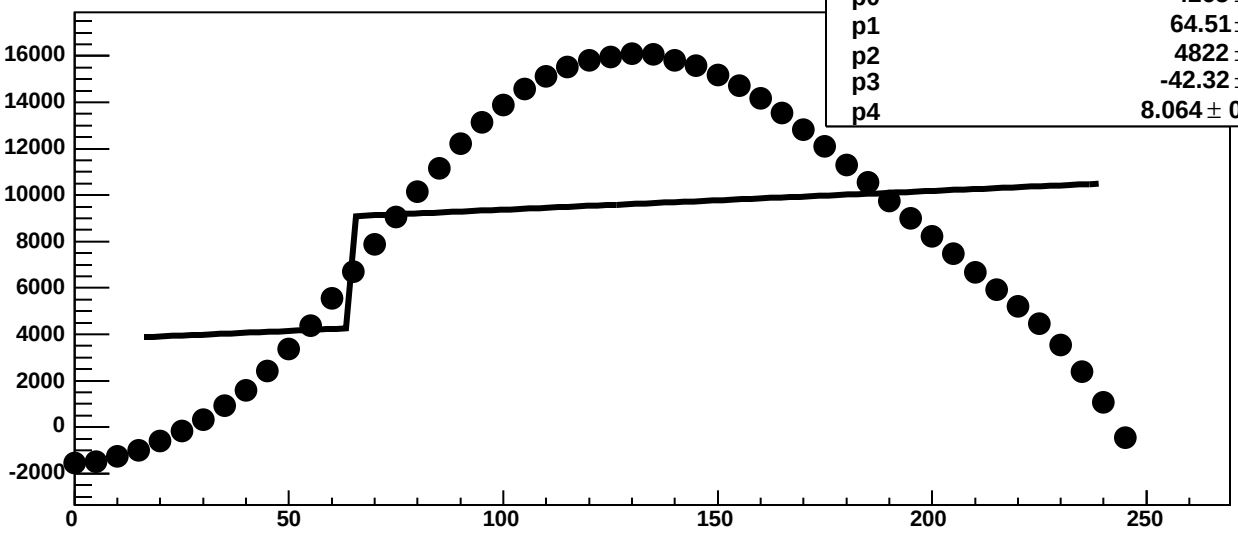



Chip 9, Channel 10, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.909e+05 / 41
p0 -2176 ± 7.655
p1 46.96 ± 0.0261
p2 3.096e+04 ± 83.6
p3 68.34 ± 0.1647
p4 -29.79 ± 0.3817
20000
15000
10000
5000
0
0
50
100
150
200
250

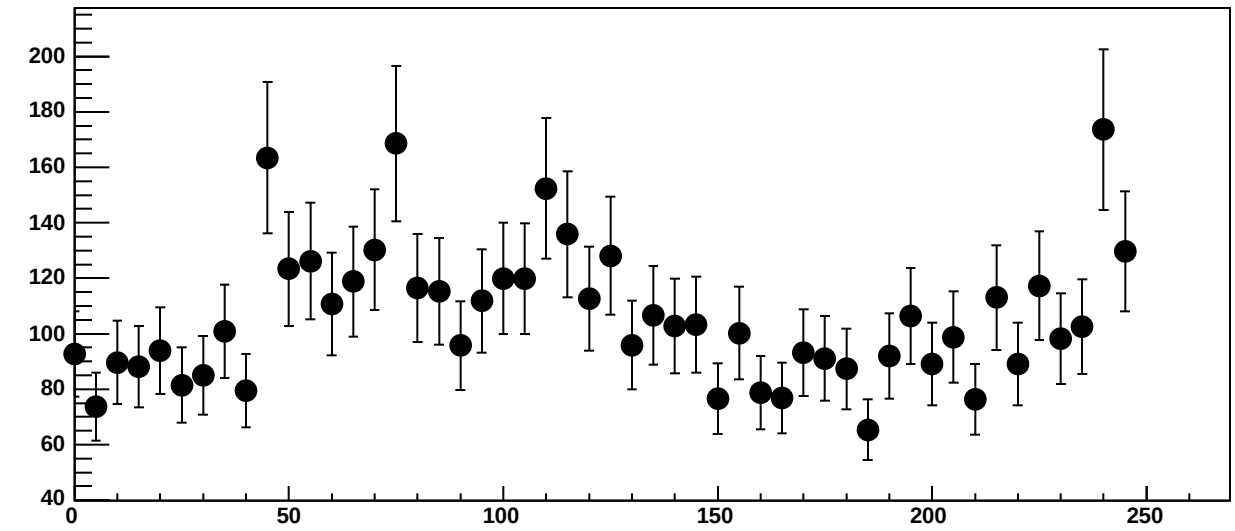

Chip 9, Channel 10, Enable 5, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 9, Channel 10, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
0
50
100
150
200
250

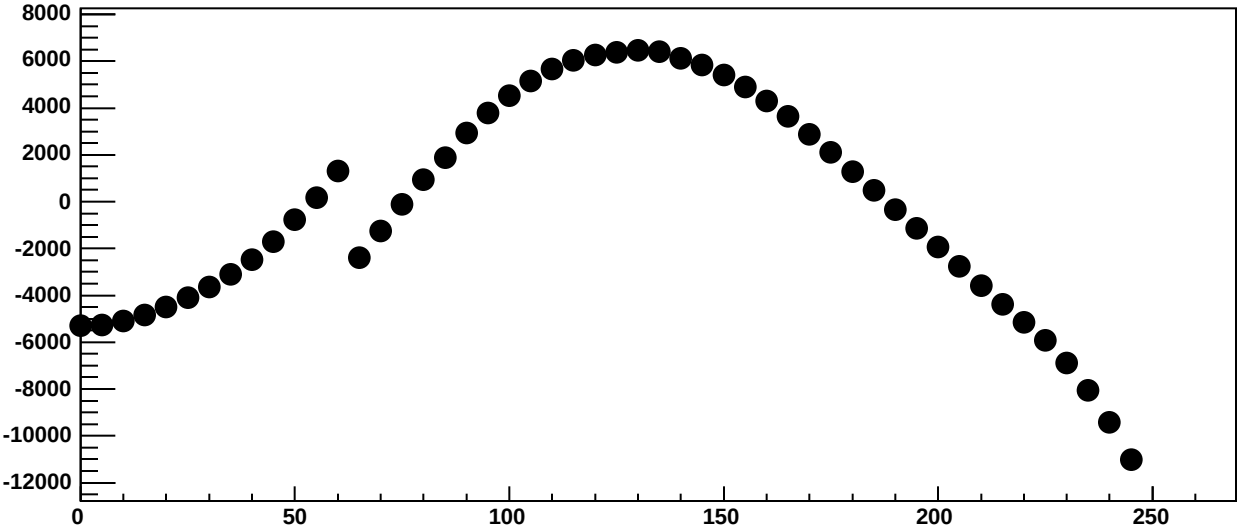
Chip 9, Channel 11, Enable 0, DAC=1600, ADC Mean vs Hold

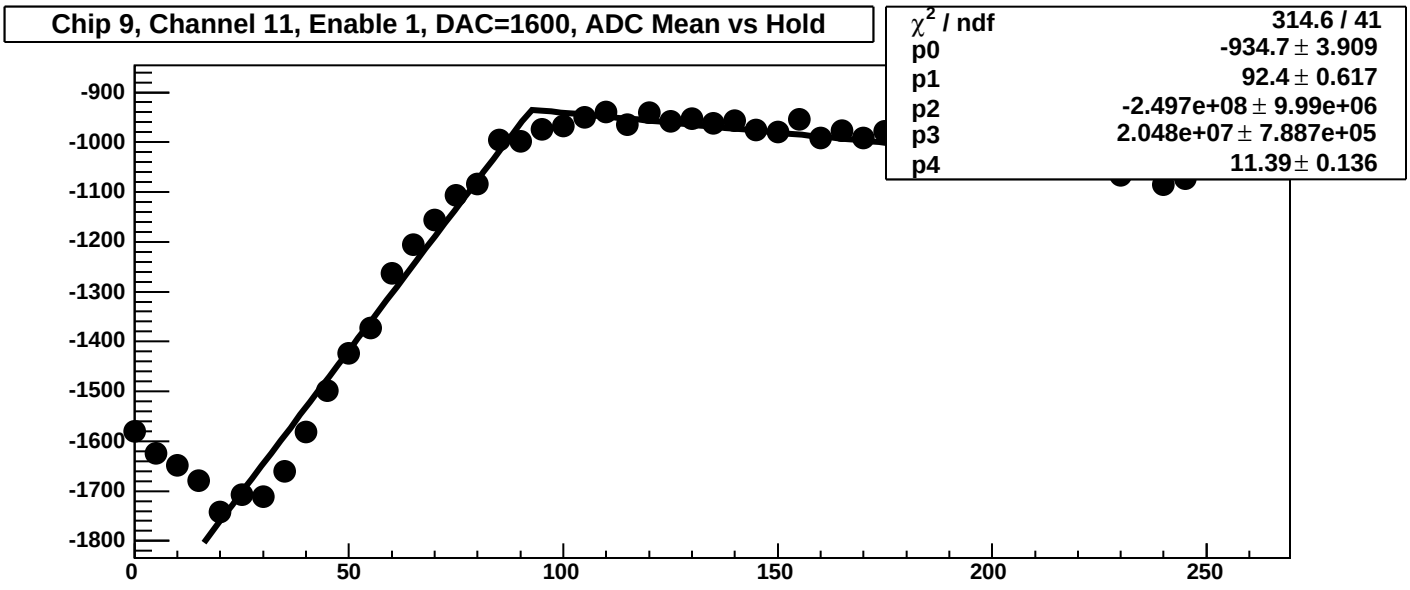


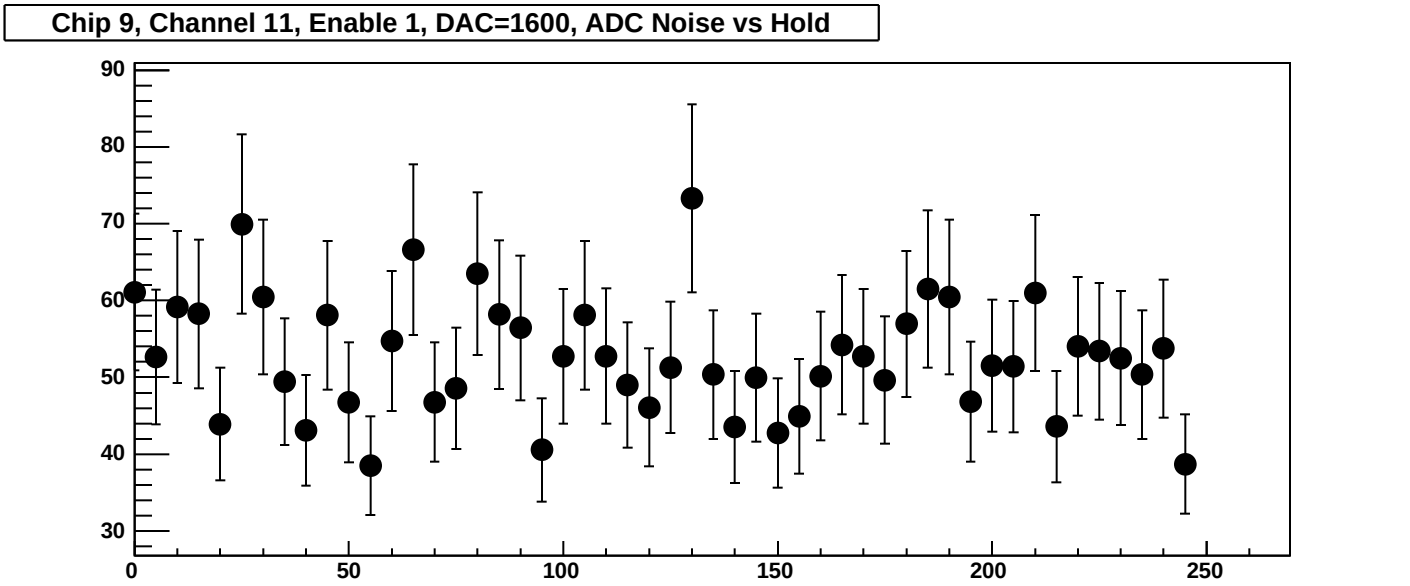
Chip 9, Channel 11, Enable 0, DAC=1600, ADC Noise vs Hold

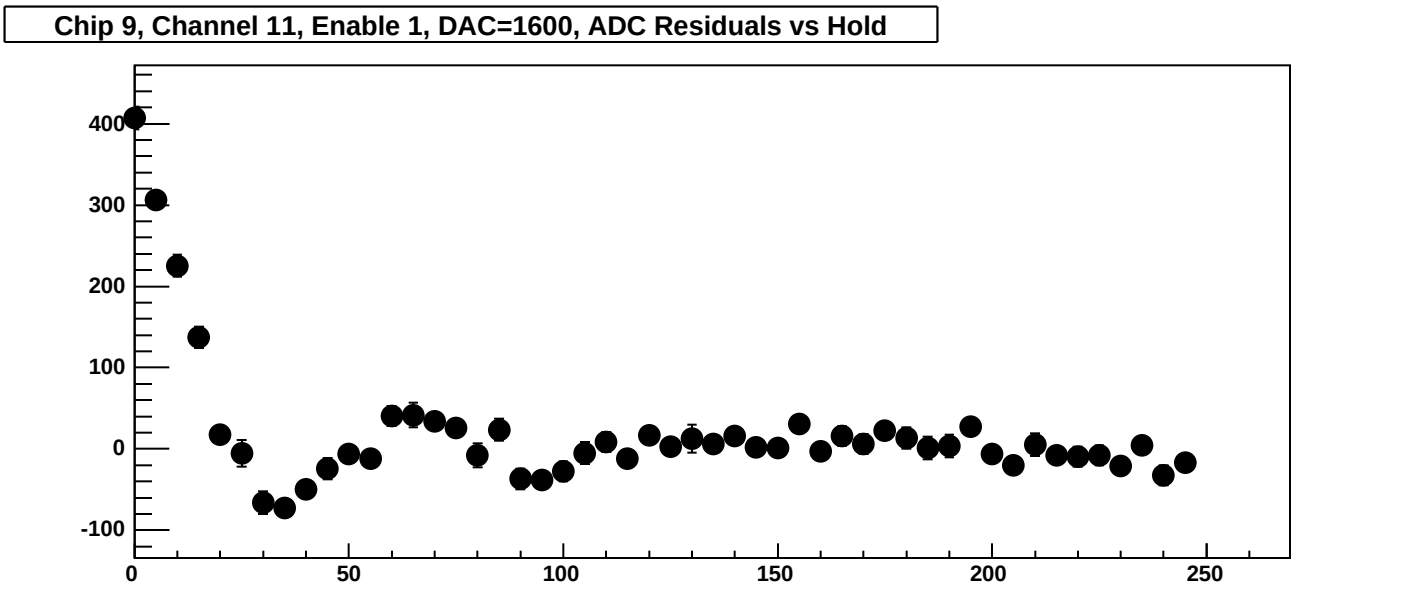


Chip 9, Channel 11, Enable 0, DAC=1600, ADC Residuals vs Hold

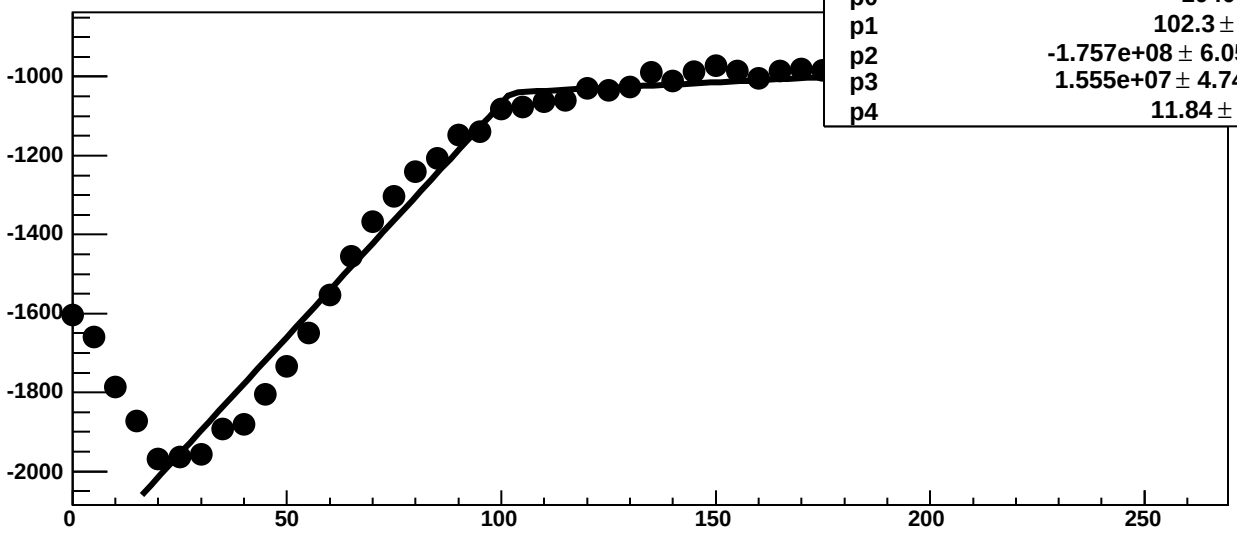



Chip 9, Channel 11, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 314.6 / 41
p0 -934.7 ± 3.909
p1 92.4 ± 0.617
p2 -2.497e+08 ± 9.99e+06
p3 2.048e+07 ± 7.887e+05
p4 11.39 ± 0.136
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
-1800
0
50
100
150
200
250

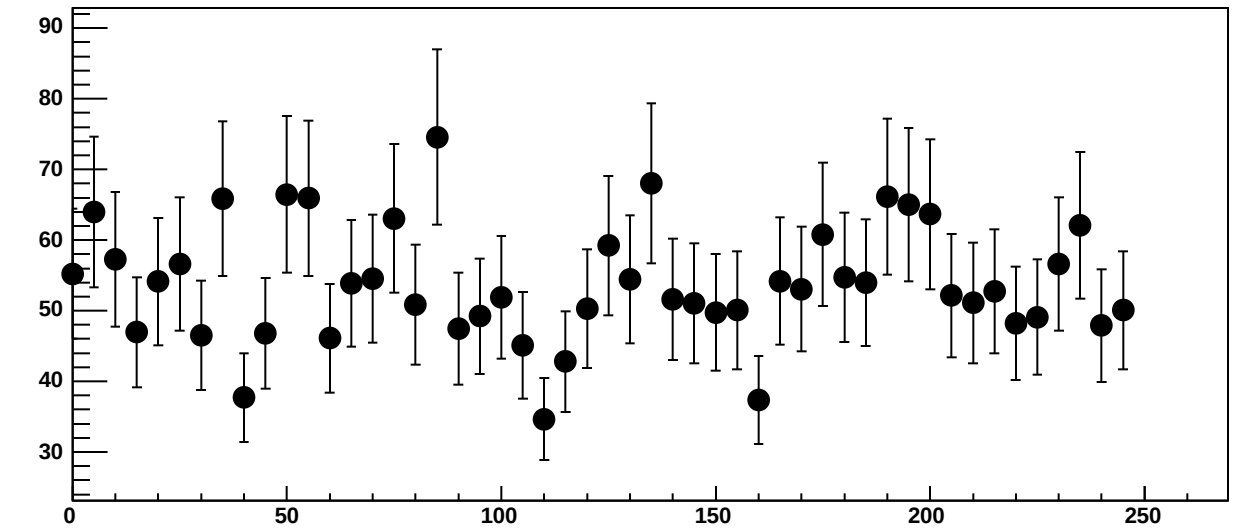

Chip 9, Channel 11, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 9, Channel 11, Enable 1, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

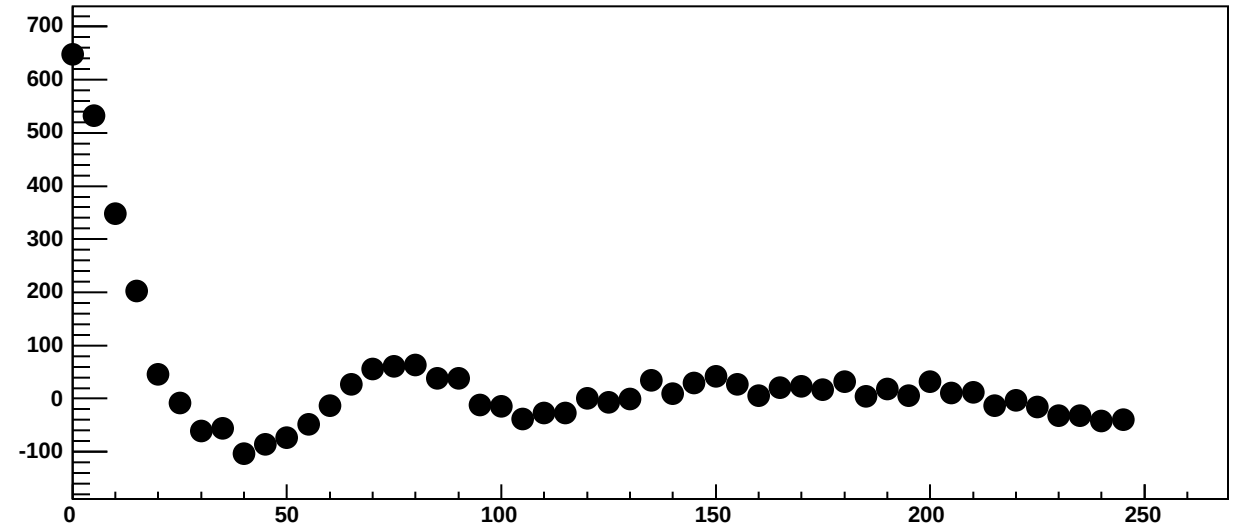
Chip 9, Channel 11, Enable 2, DAC=1600, ADC Mean vs Hold

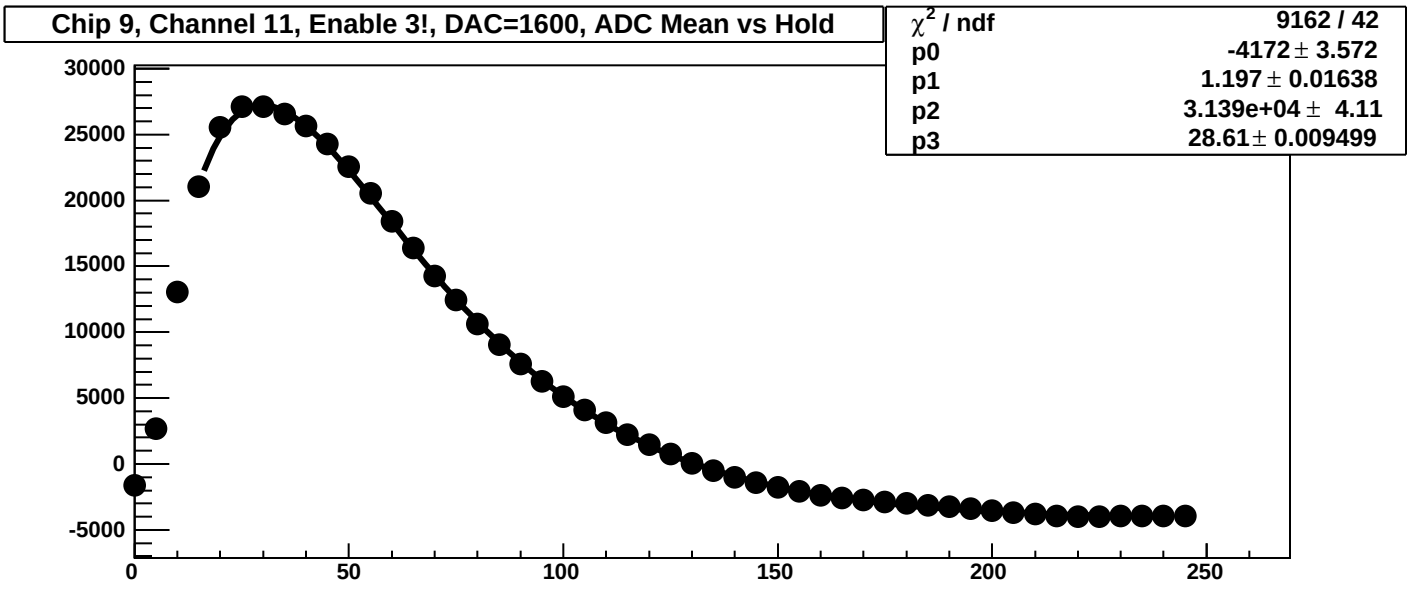


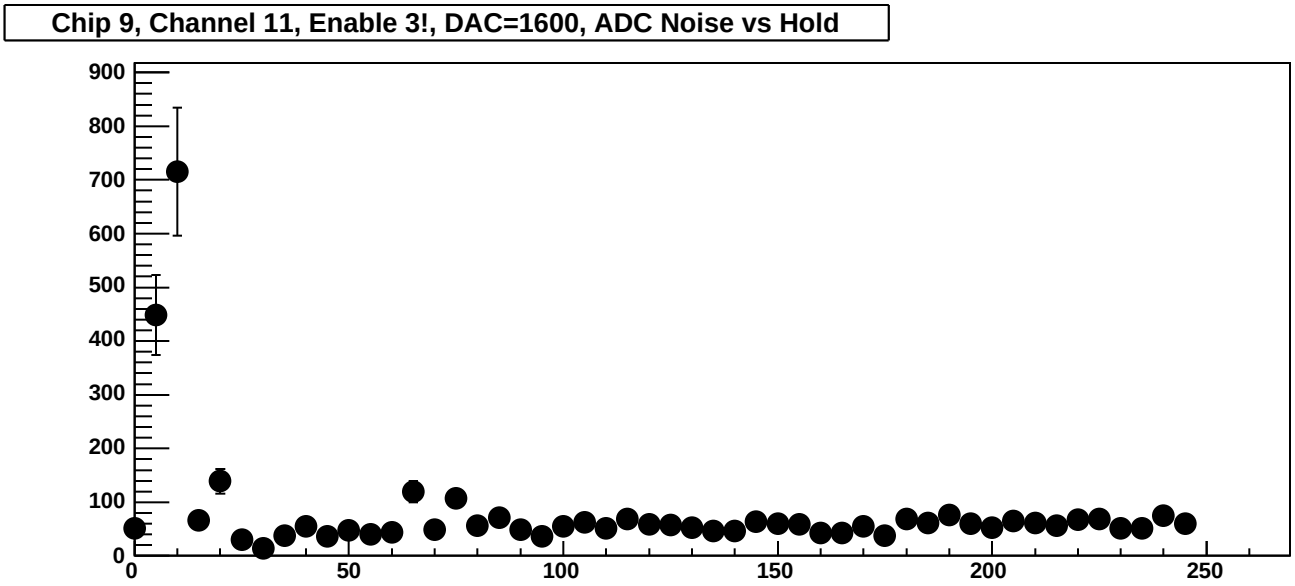
Chip 9, Channel 11, Enable 2, DAC=1600, ADC Noise vs Hold

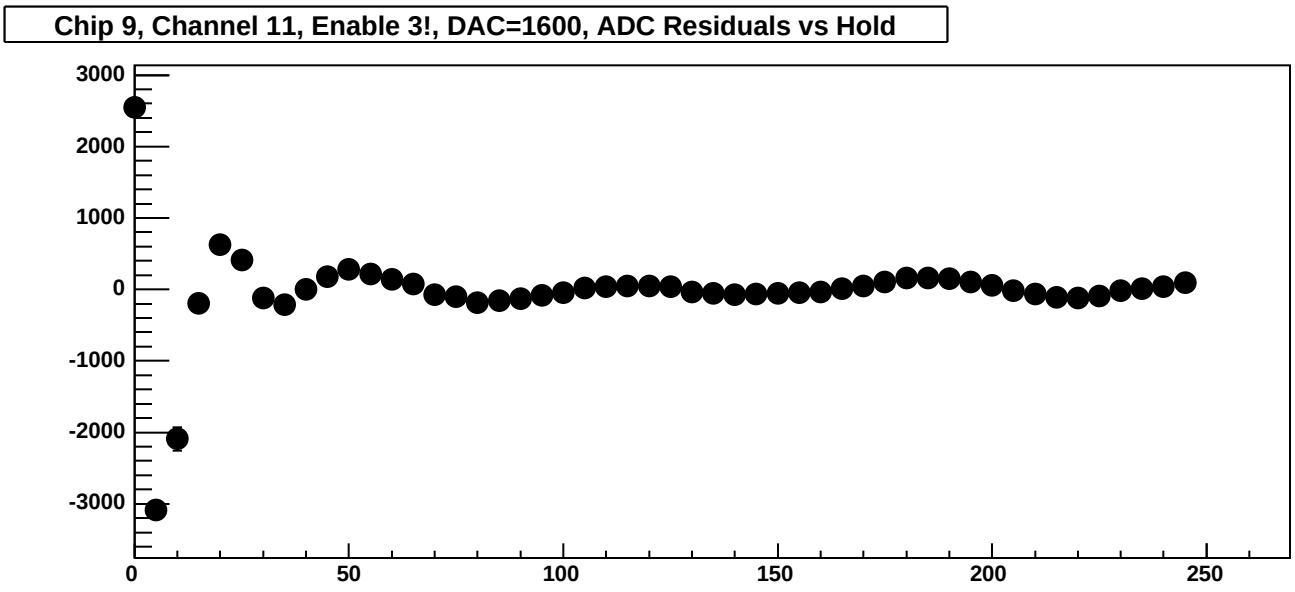


Chip 9, Channel 11, Enable 2, DAC=1600, ADC Residuals vs Hold

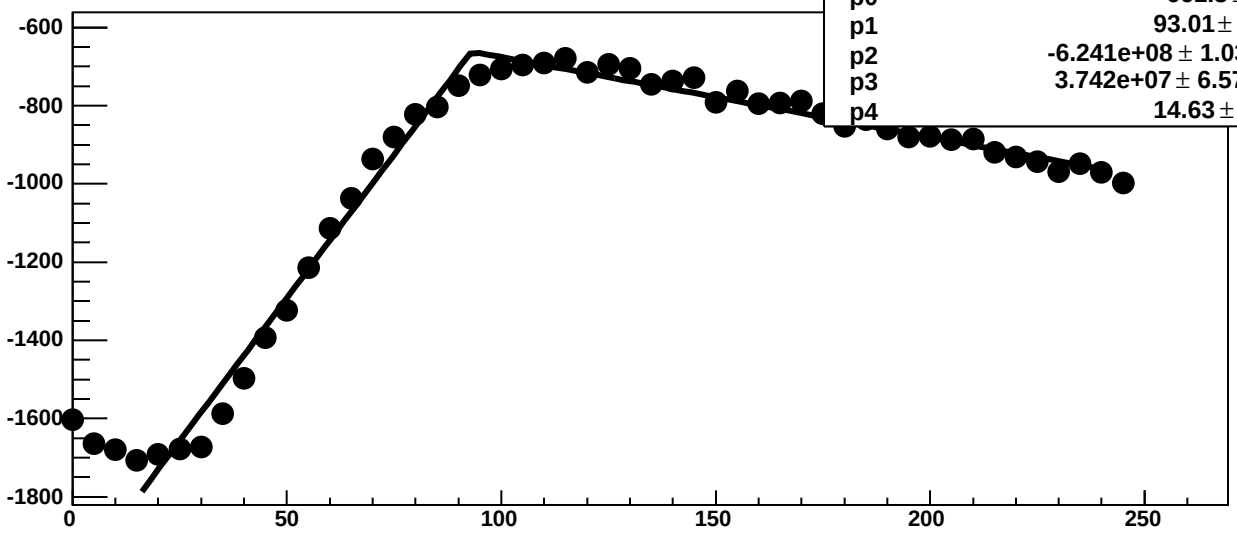



Chip 9, Channel 11, Enable 3!, DAC=1600, ADC Mean vs Hold
χ² / ndf 9162 / 42
p0 -4172 ± 3.572
p1 1.197 ± 0.01638
p2 3.139e+04 ± 4.11
p3 28.61 ± 0.009499
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

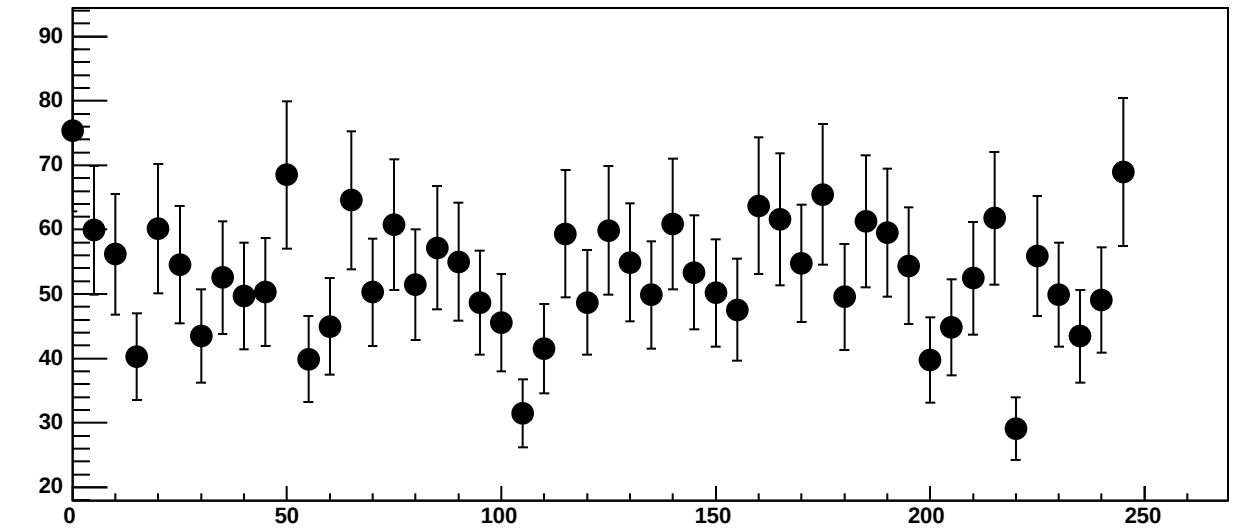

Chip 9, Channel 11, Enable 3!, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 9, Channel 11, Enable 3!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

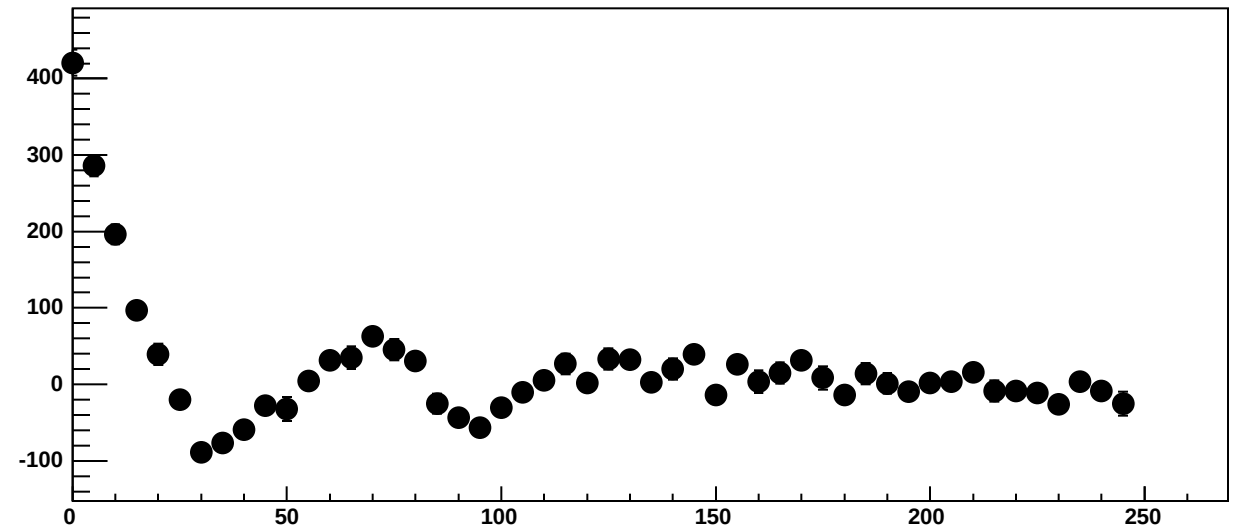
Chip 9, Channel 11, Enable 4, DAC=1600, ADC Mean vs Hold

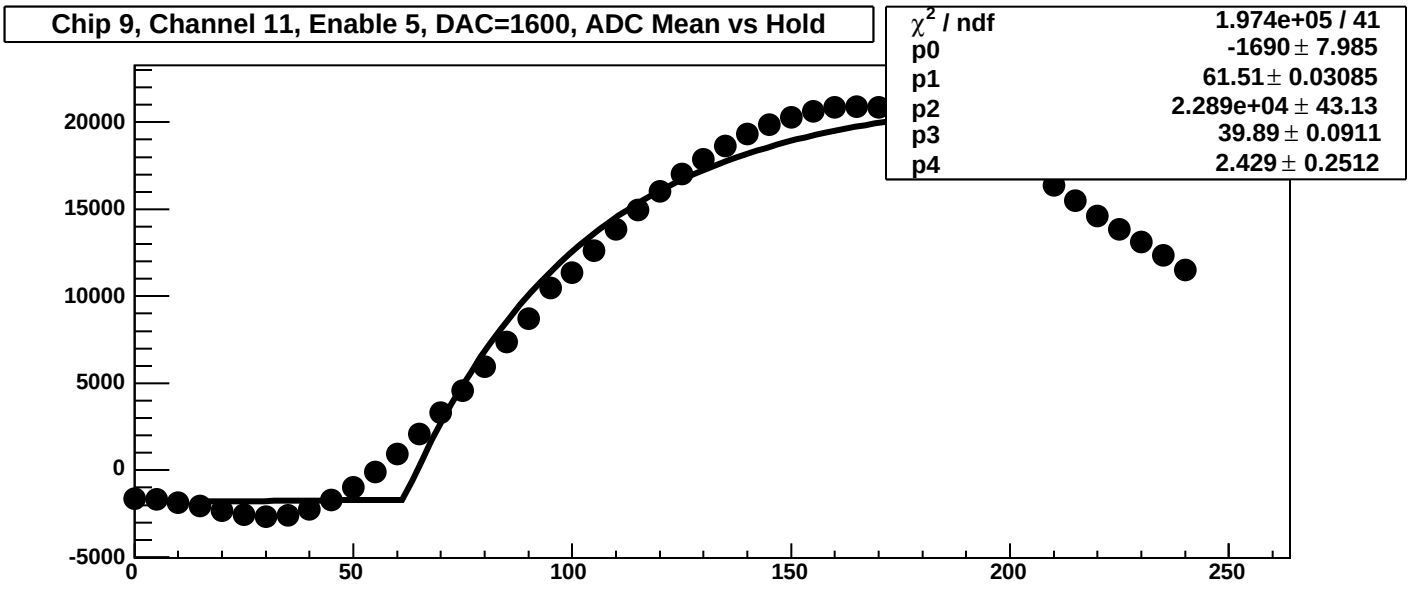


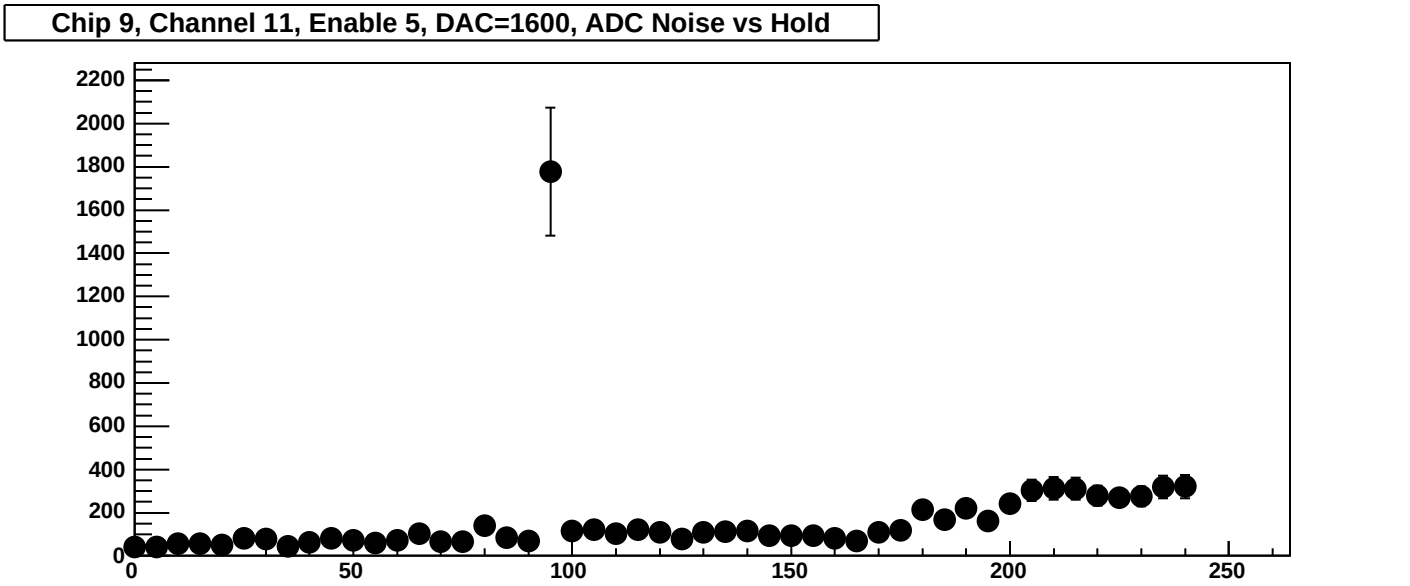
Chip 9, Channel 11, Enable 4, DAC=1600, ADC Noise vs Hold

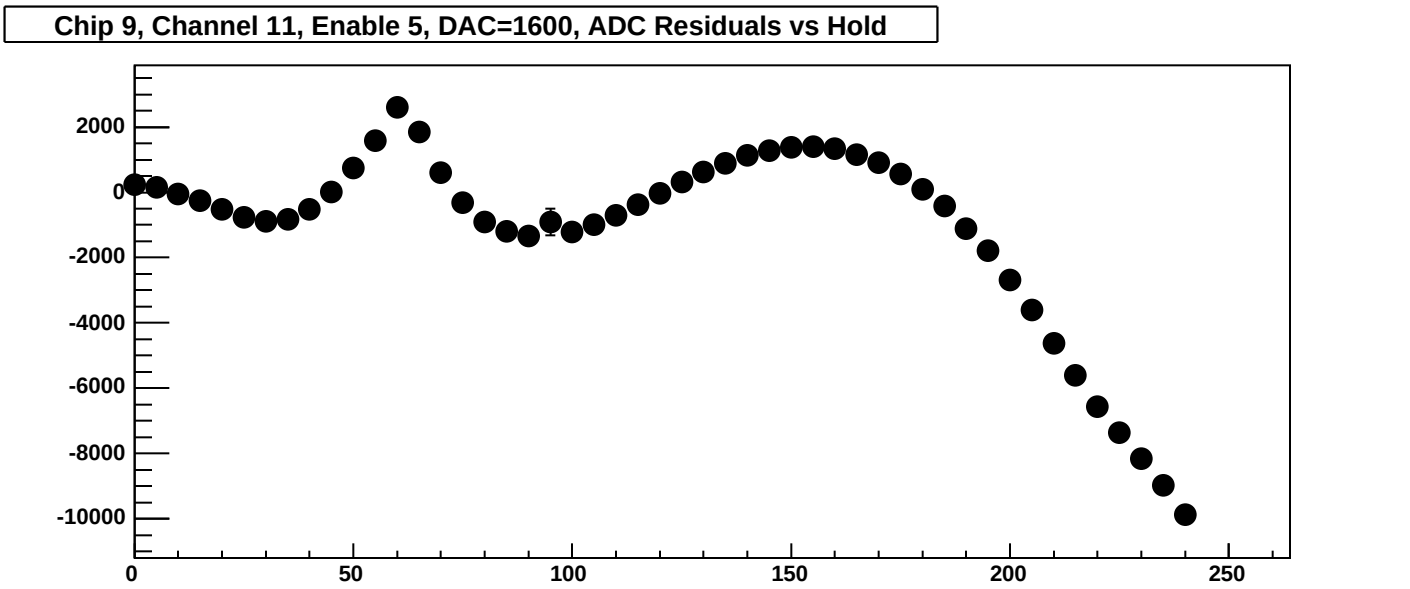


Chip 9, Channel 11, Enable 4, DAC=1600, ADC Residuals vs Hold

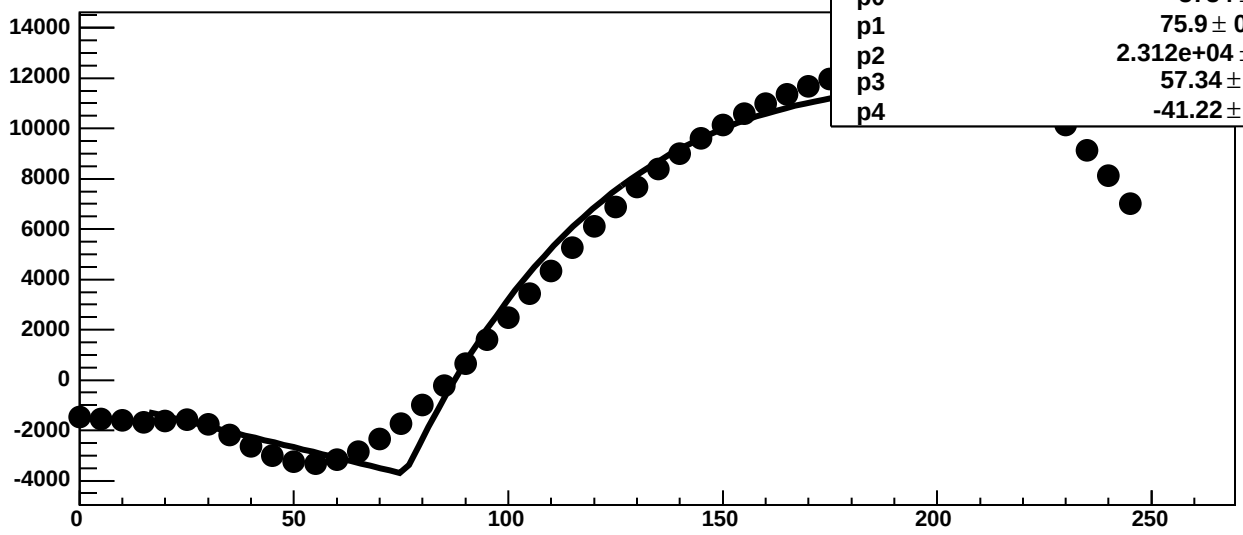



Chip 9, Channel 11, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.974e+05 / 41
p0 -1690 ± 7.985
p1 61.51 ± 0.03085
p2 2.289e+04 ± 43.13
p3 39.89 ± 0.0911
p4 2.429 ± 0.2512
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

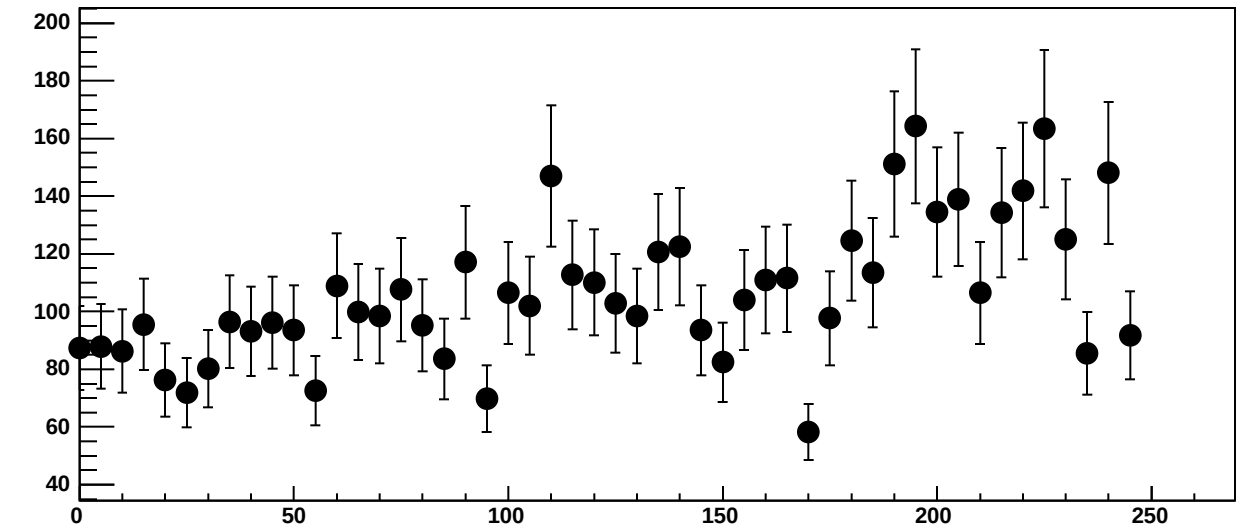

Chip 9, Channel 11, Enable 5, DAC=1600, ADC Noise vs Hold
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 9, Channel 11, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

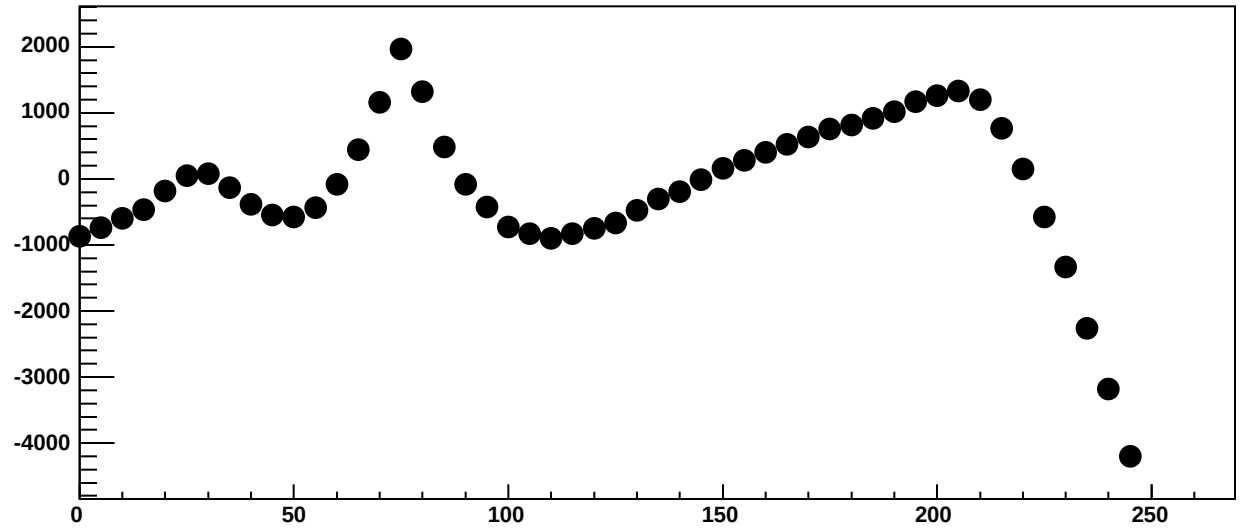
Chip 9, Channel 12, Enable 0, DAC=1600, ADC Mean vs Hold

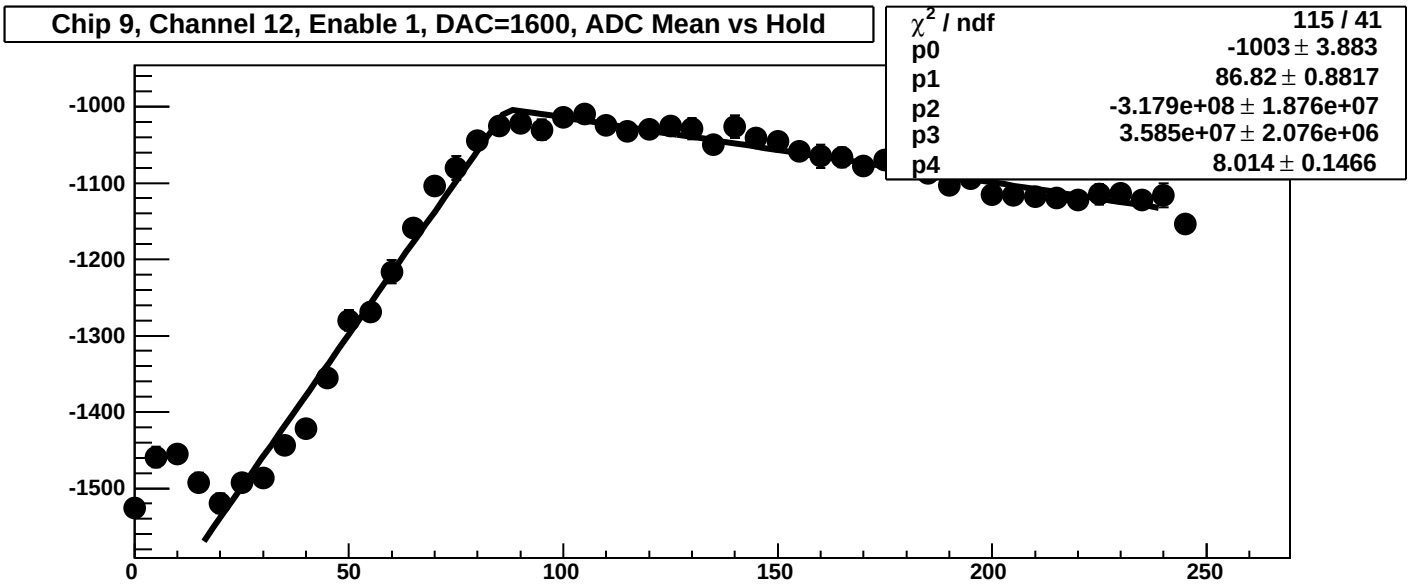


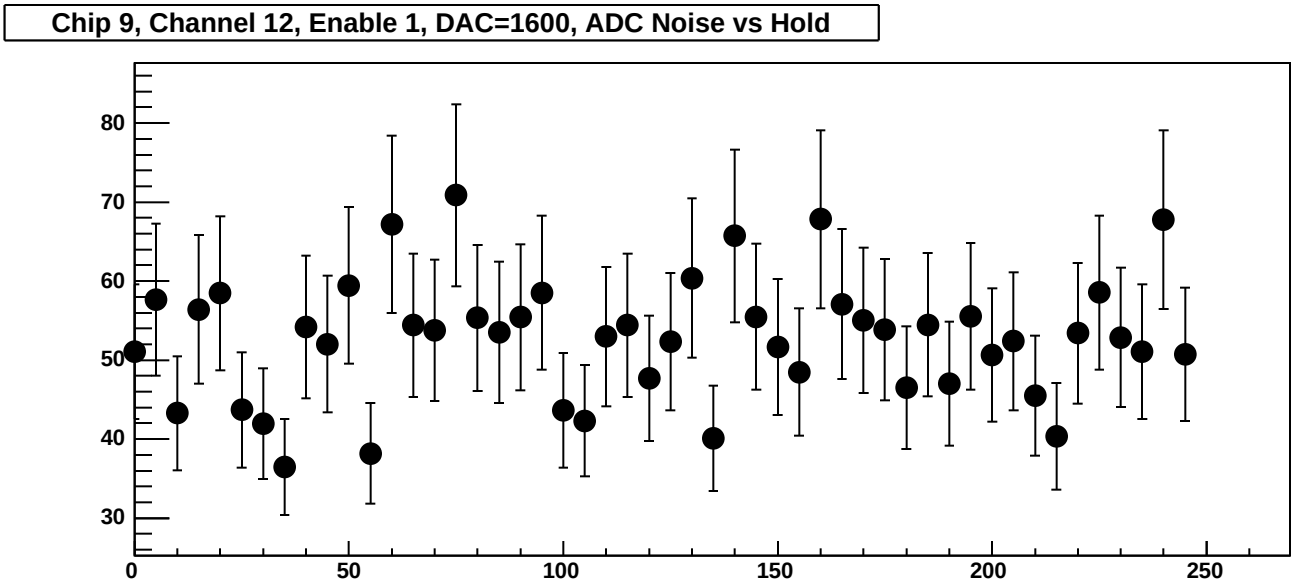
Chip 9, Channel 12, Enable 0, DAC=1600, ADC Noise vs Hold

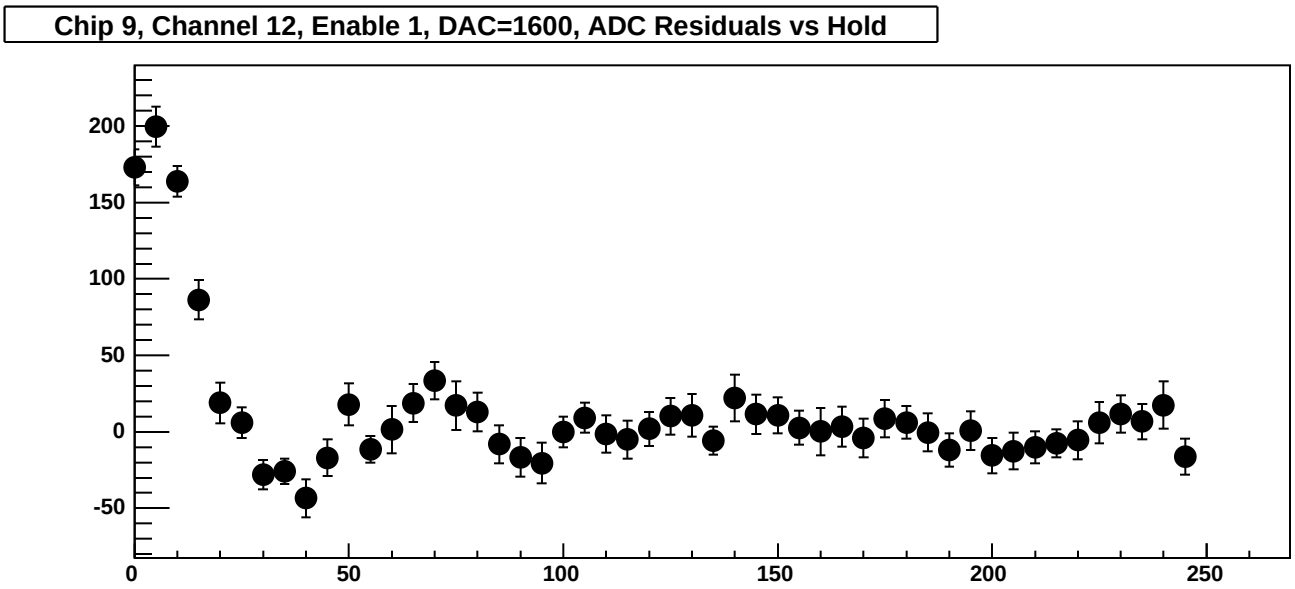


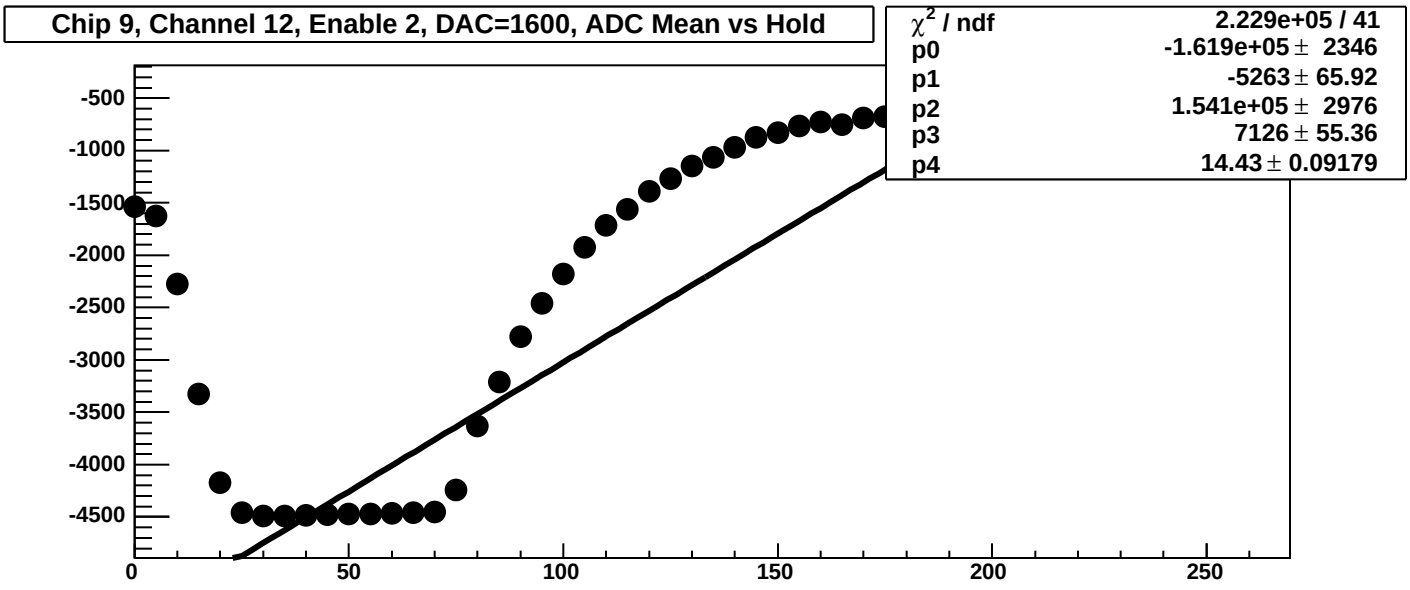
Chip 9, Channel 12, Enable 0, DAC=1600, ADC Residuals vs Hold

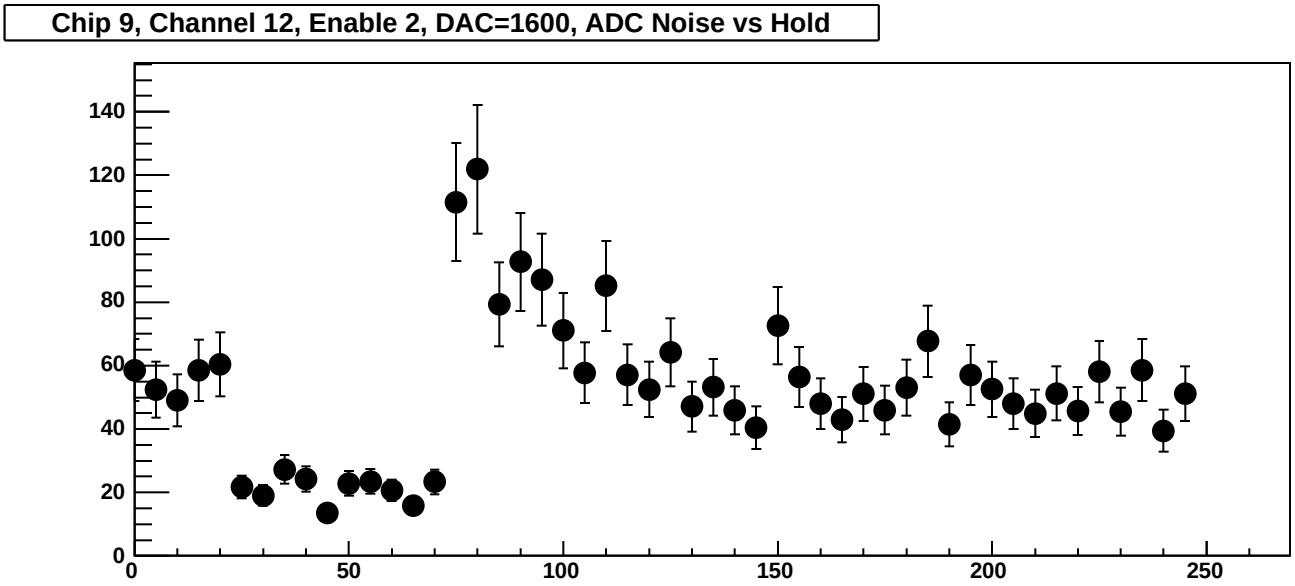


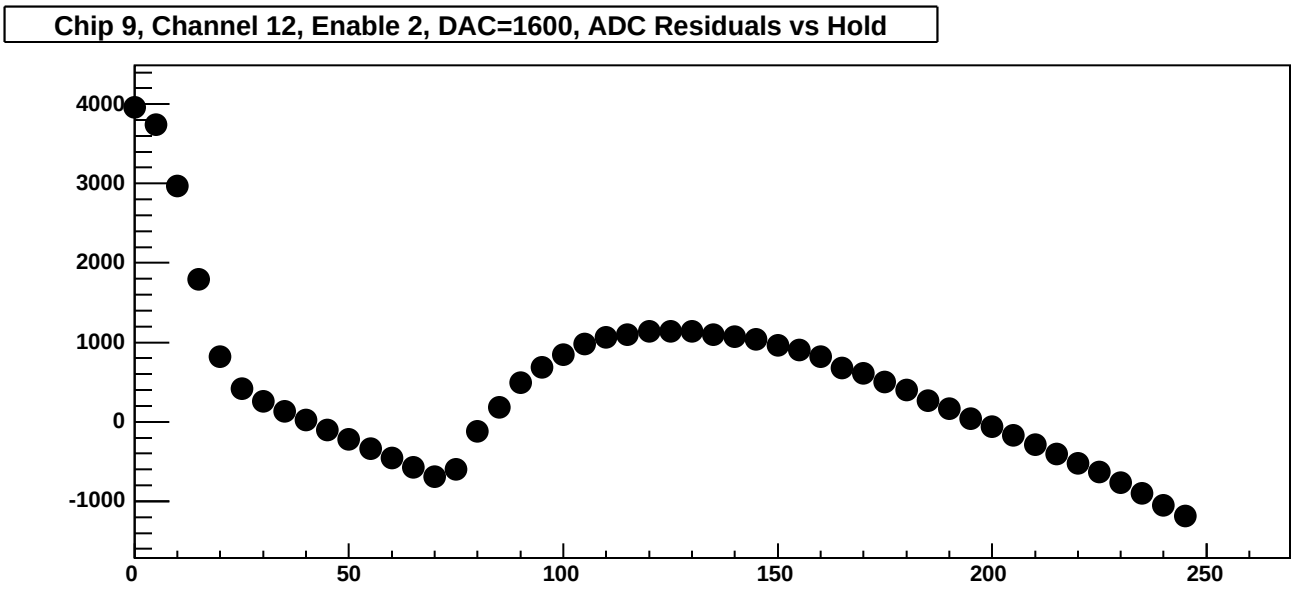

Chip 9, Channel 12, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 115 / 41
p0 -1003 ± 3.883
p1 86.82 ± 0.8817
p2 -3.179e+08 ± 1.876e+07
p3 3.585e+07 ± 2.076e+06
p4 8.014 ± 0.1466
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250


Chip 9, Channel 12, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

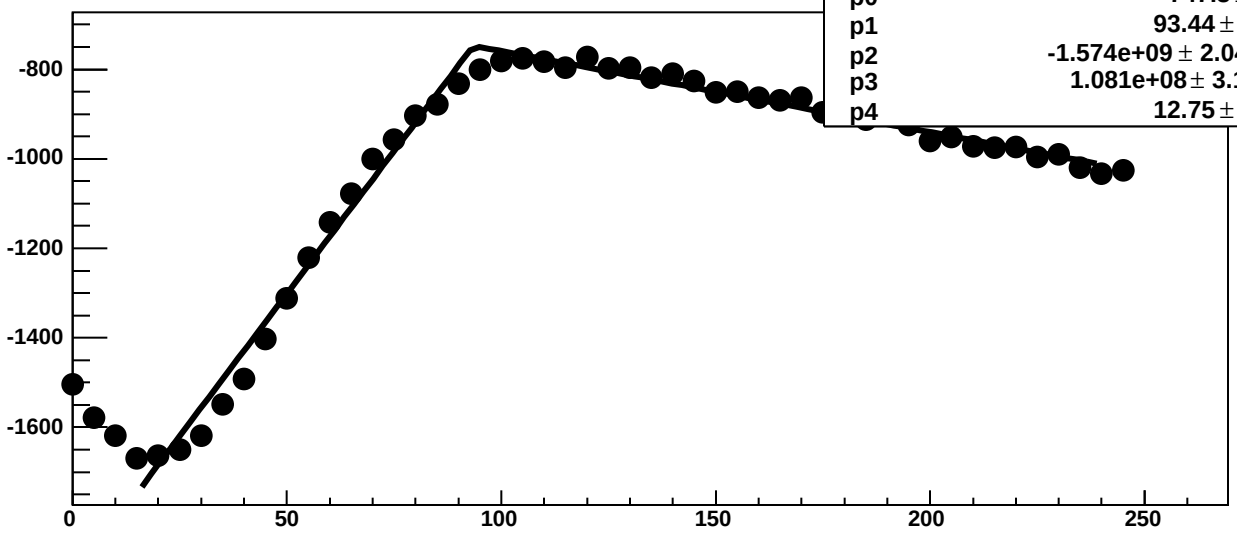

Chip 9, Channel 12, Enable 1, DAC=1600, ADC Residuals vs Hold
200
150
100
50
0
-50
0
50
100
150
200
250


Chip 9, Channel 12, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.229e+05 / 41
p0 -1.619e+05 ± 2346
p1 -5263 ± 65.92
p2 1.541e+05 ± 2976
p3 7126 ± 55.36
p4 14.43 ± 0.09179
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
-4500
0
50
100
150
200
250

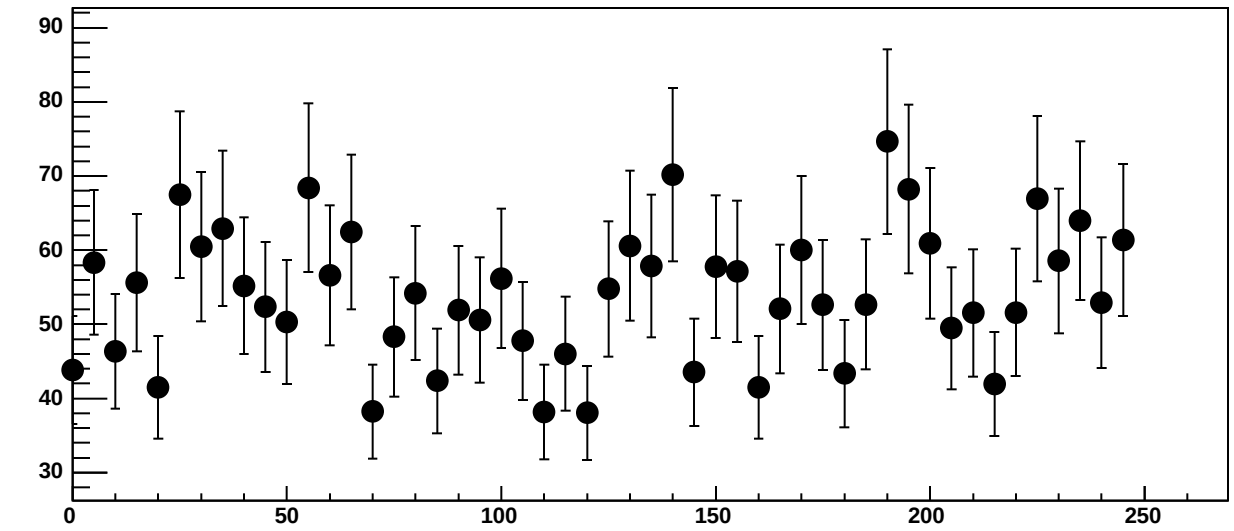

Chip 9, Channel 12, Enable 2, DAC=1600, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 9, Channel 12, Enable 2, DAC=1600, ADC Residuals vs Hold
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

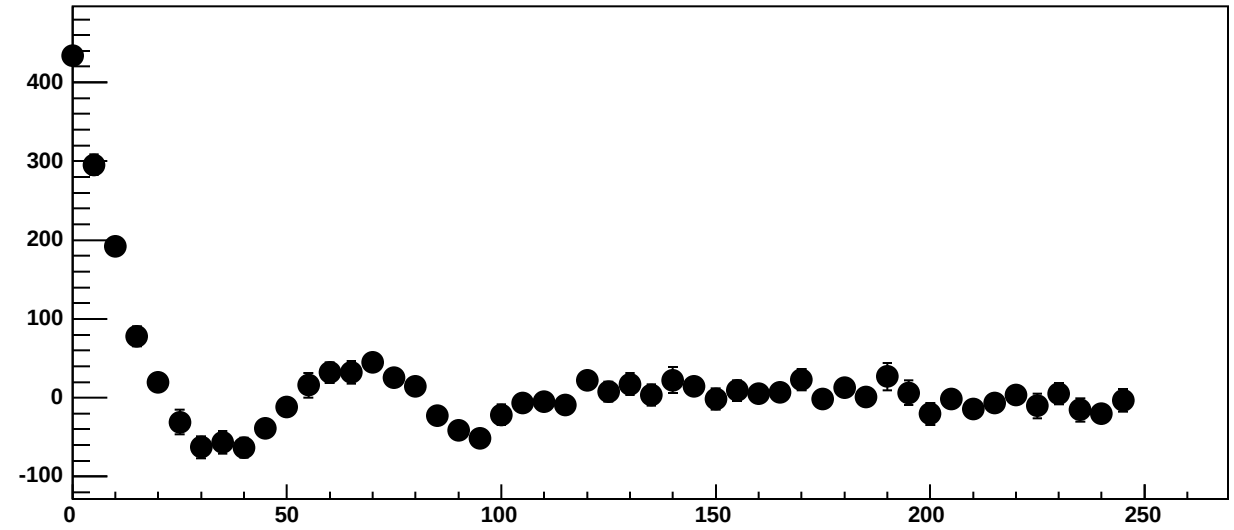
Chip 9, Channel 12, Enable 3, DAC=1600, ADC Mean vs Hold



Chip 9, Channel 12, Enable 3, DAC=1600, ADC Noise vs Hold

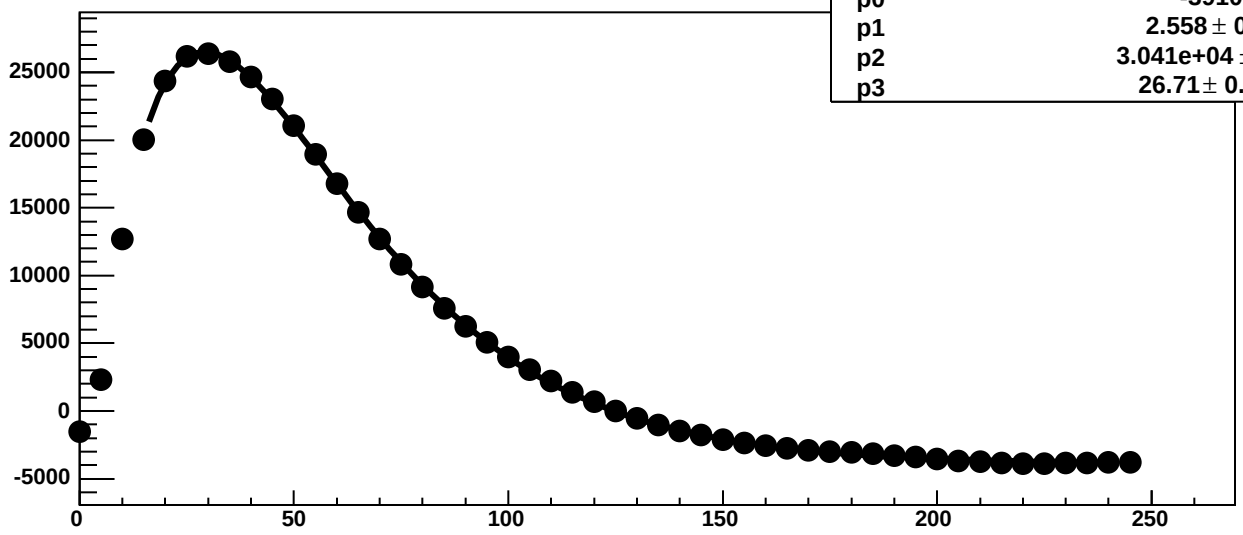


Chip 9, Channel 12, Enable 3, DAC=1600, ADC Residuals vs Hold

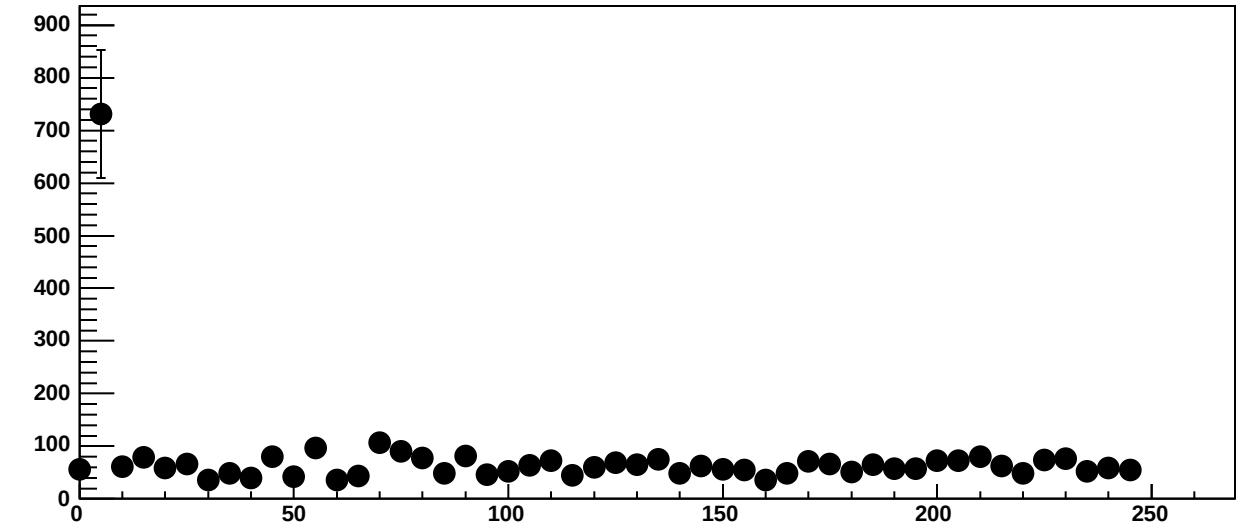


Chip 9, Channel 12, Enable 4!, DAC=1600, ADC Mean vs Hold

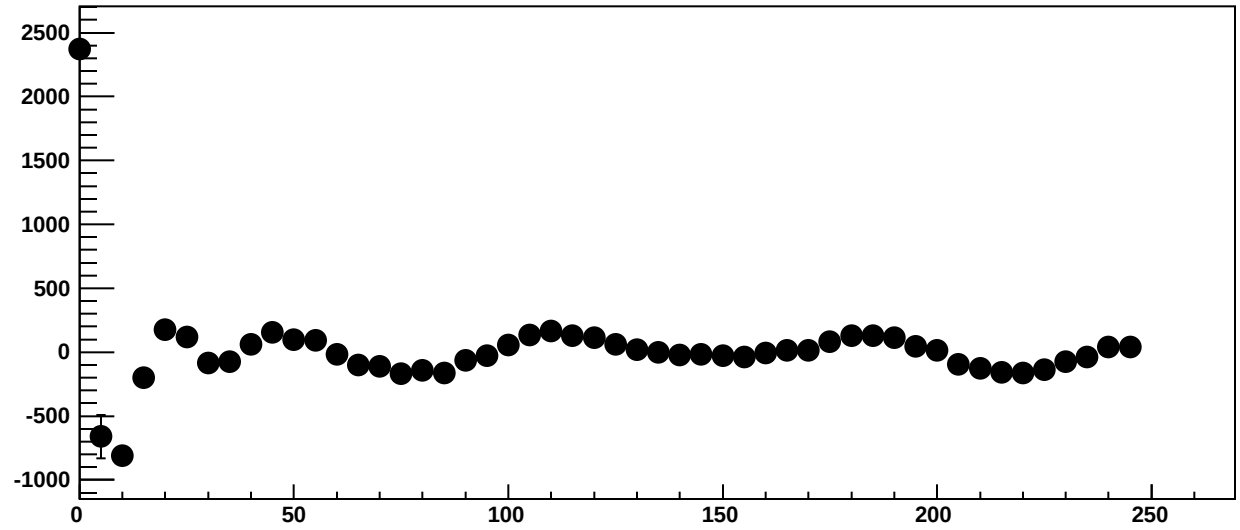
χ^2 / ndf	2504 / 42
p0	-3910 ± 3.52
p1	2.558 ± 0.01639
p2	3.041e+04 ± 5.234
p3	26.71 ± 0.009751

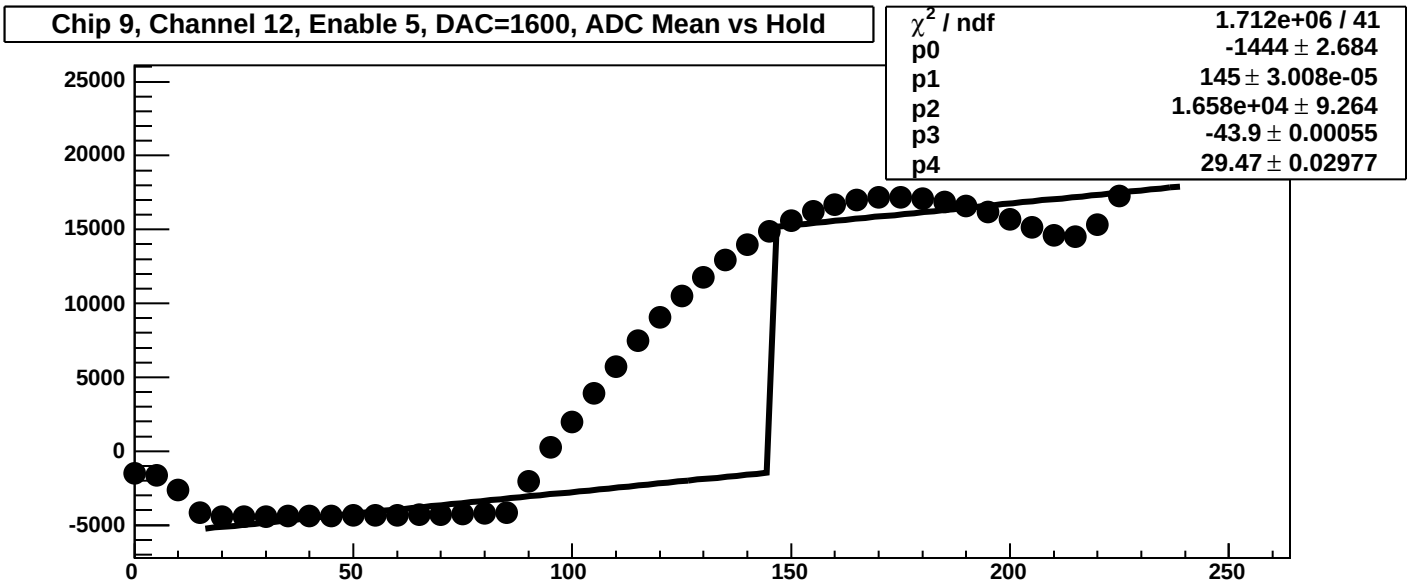


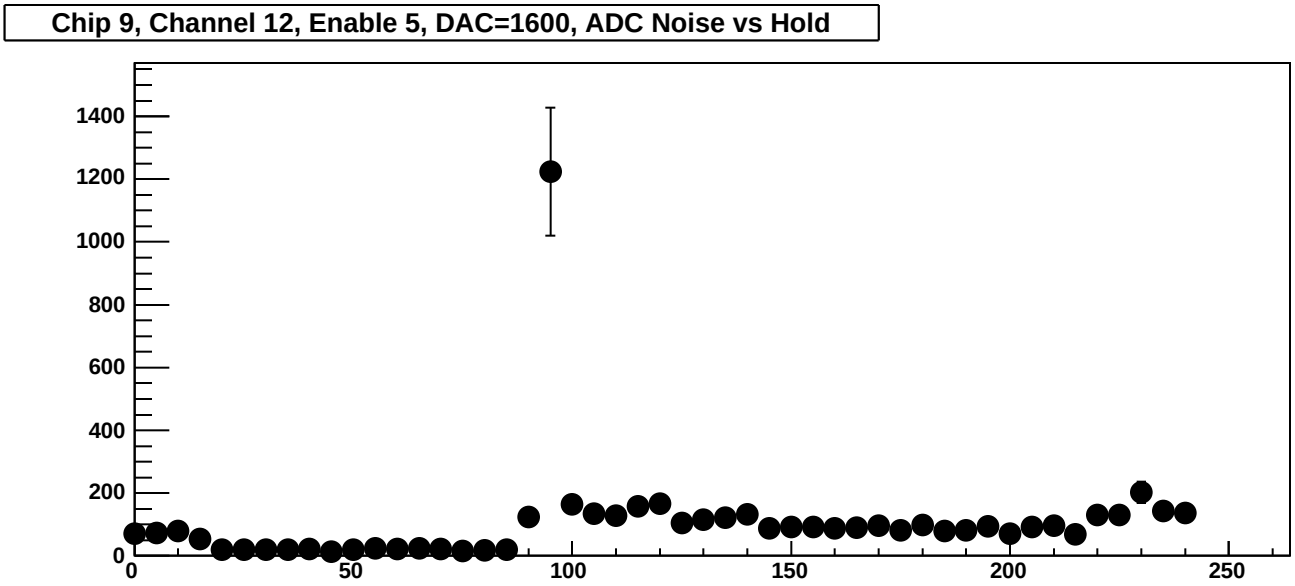
Chip 9, Channel 12, Enable 4!, DAC=1600, ADC Noise vs Hold

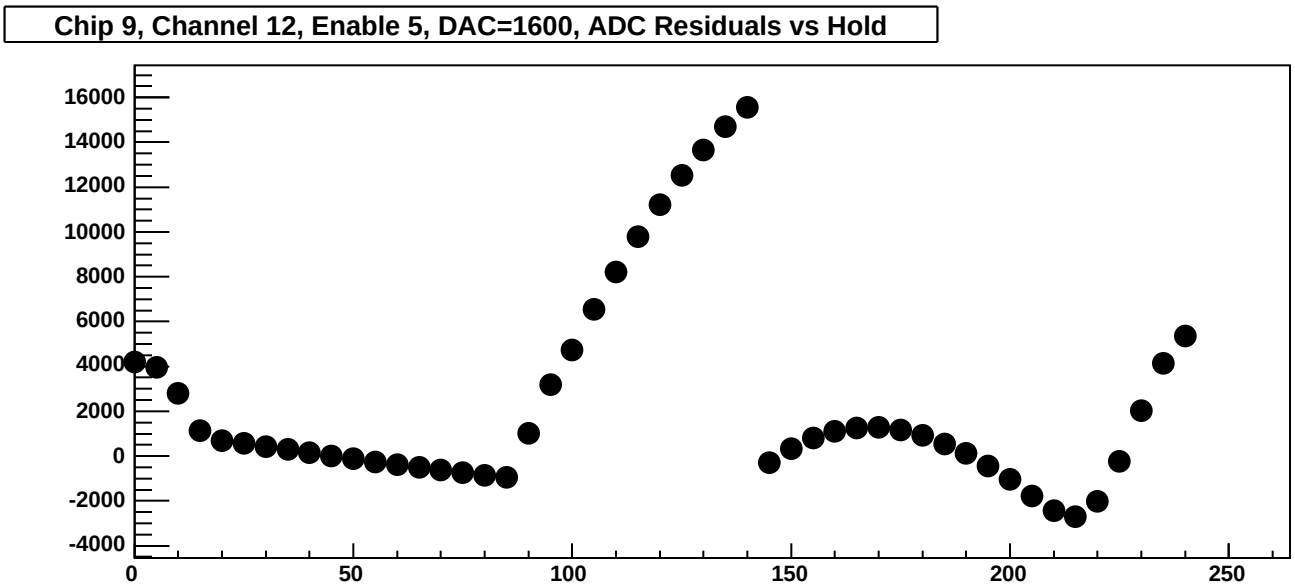


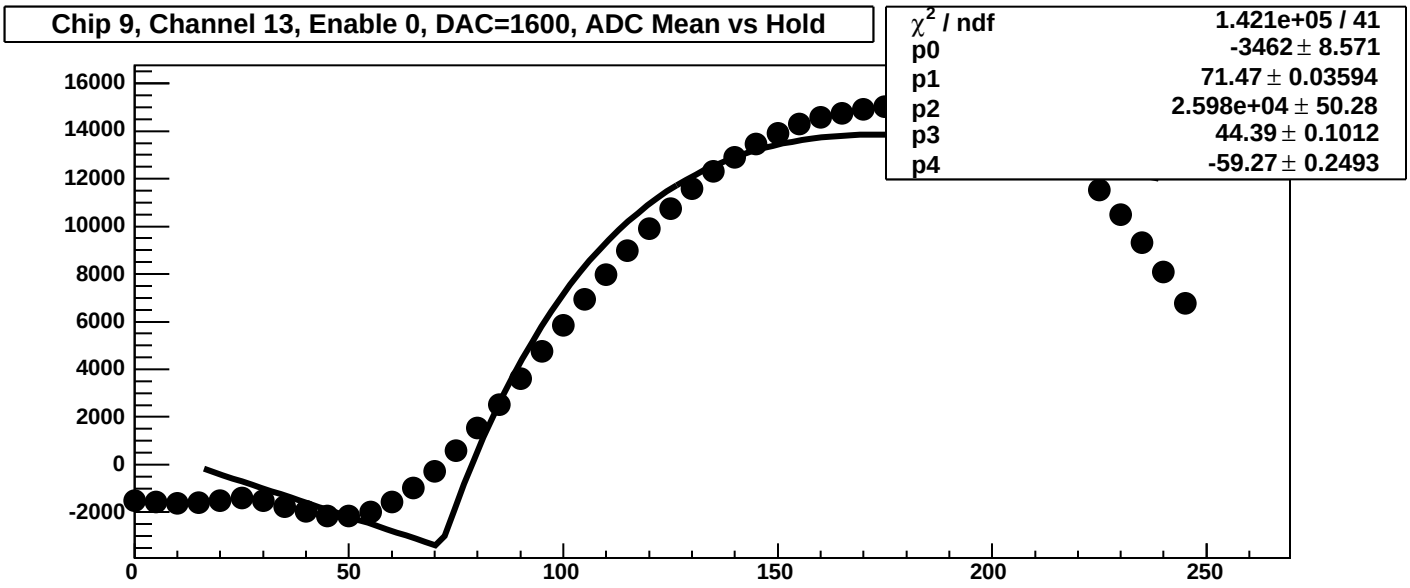
Chip 9, Channel 12, Enable 4!, DAC=1600, ADC Residuals vs Hold

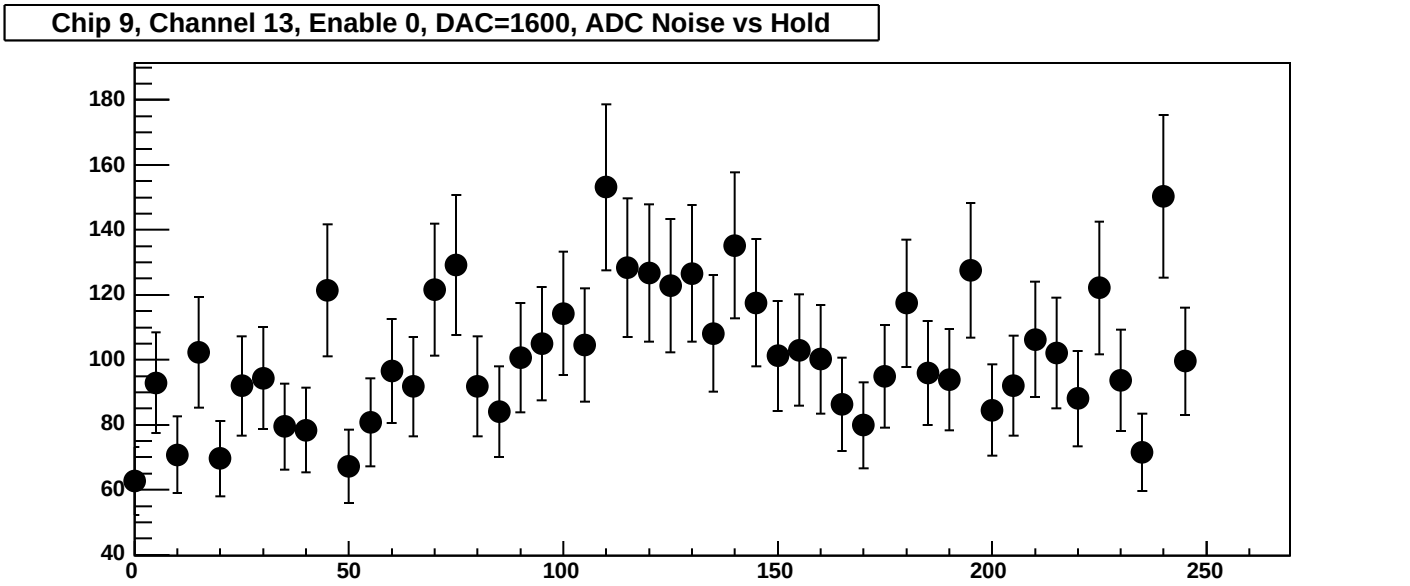


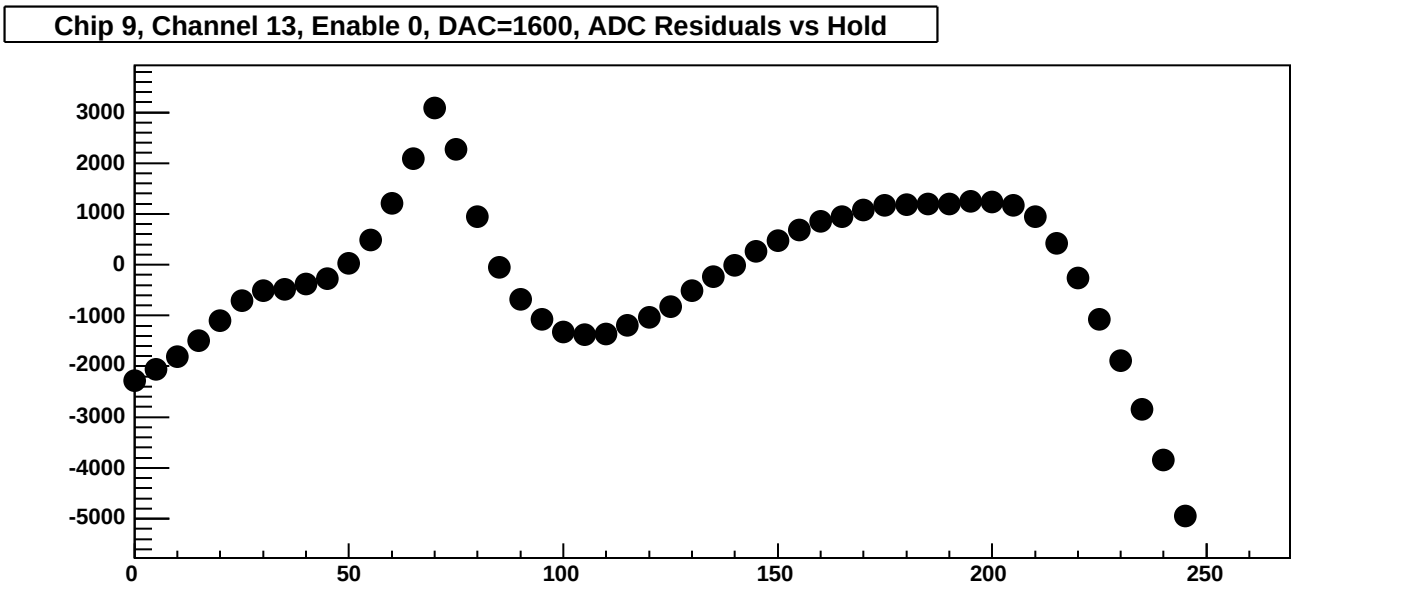

Chip 9, Channel 12, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.712e+06 / 41
p0 -1444 ± 2.684
p1 145 ± 3.008e-05
p2 1.658e+04 ± 9.264
p3 -43.9 ± 0.00055
p4 29.47 ± 0.02977
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 9, Channel 12, Enable 5, DAC=1600, ADC Noise vs Hold
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

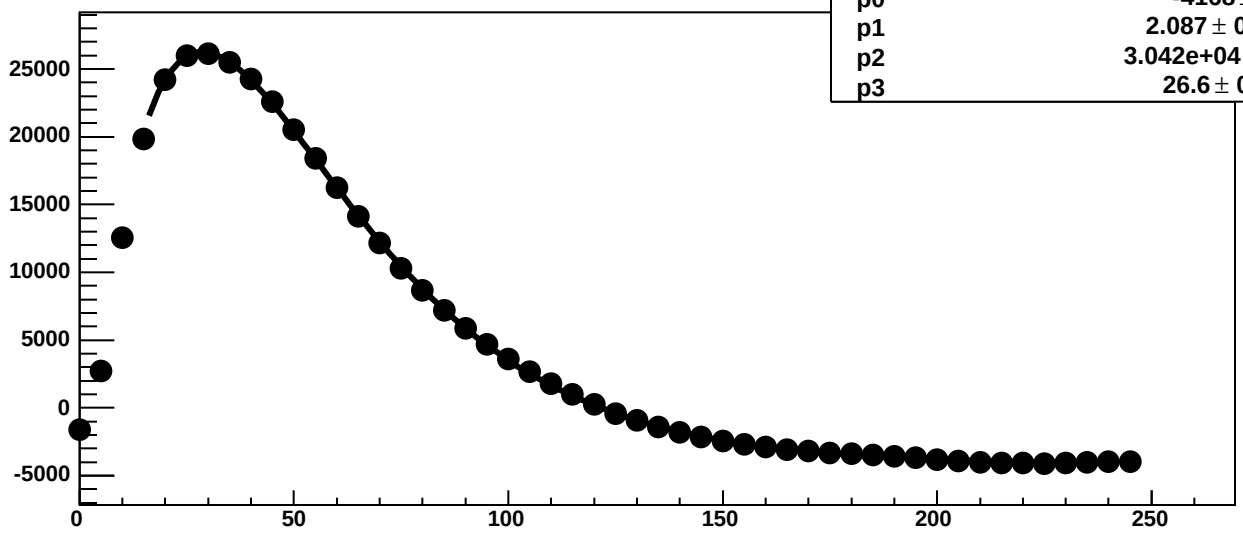

Chip 9, Channel 12, Enable 5, DAC=1600, ADC Residuals vs Hold
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250


Chip 9, Channel 13, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.421e+05 / 41
p0 -3462 ± 8.571
p1 71.47 ± 0.03594
p2 2.598e+04 ± 50.28
p3 44.39 ± 0.1012
p4 -59.27 ± 0.2493
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

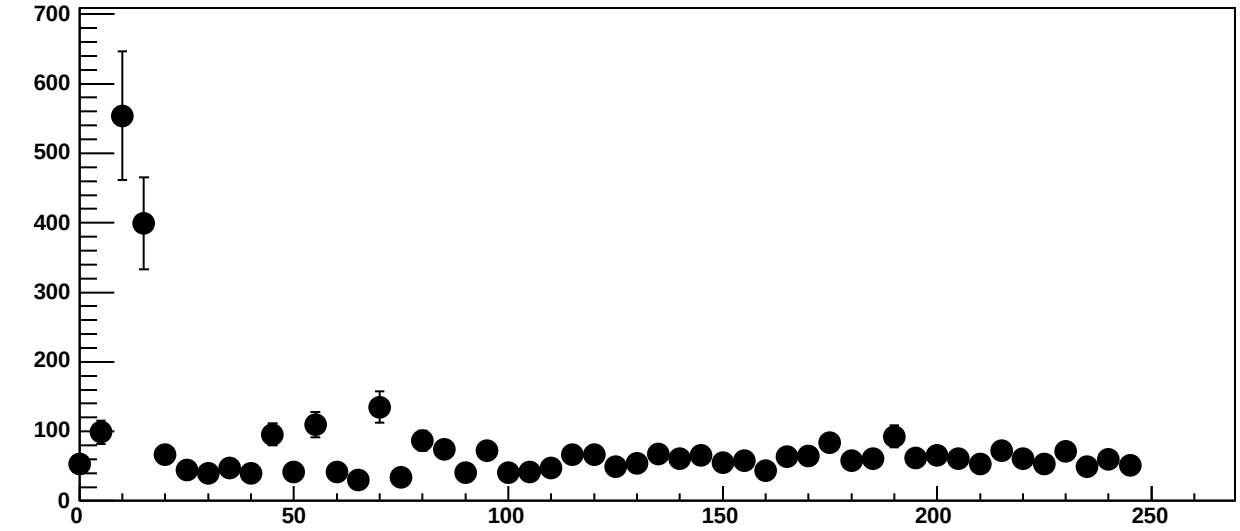

Chip 9, Channel 13, Enable 0, DAC=1600, ADC Noise vs Hold
180
160
140
120
100
80
60
40
0
50
100
150
200
250


Chip 9, Channel 13, Enable 0, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

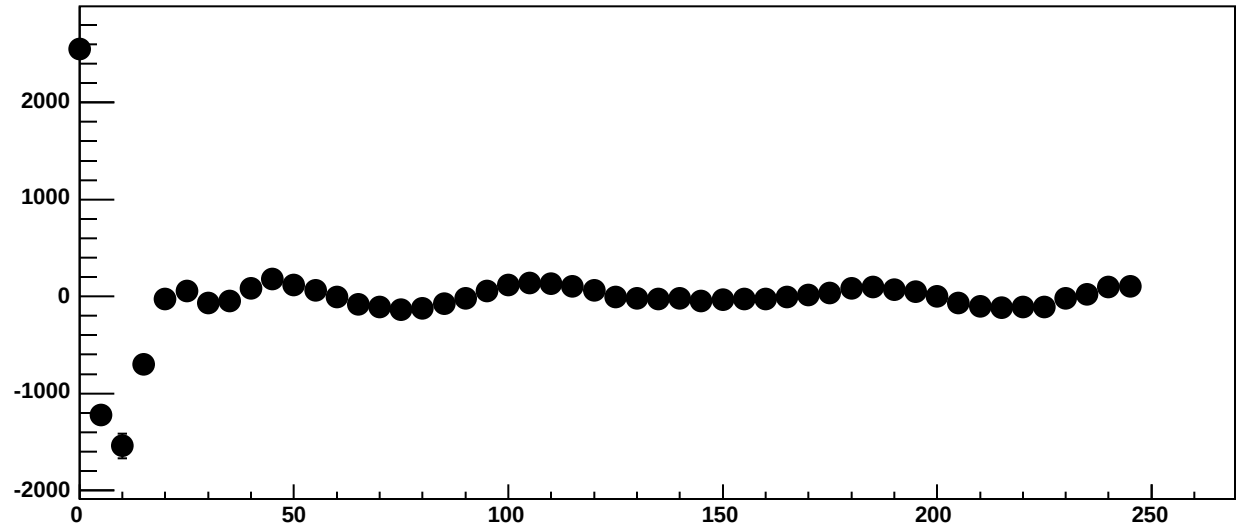
Chip 9, Channel 13, Enable 1!, DAC=1600, ADC Mean vs Hold

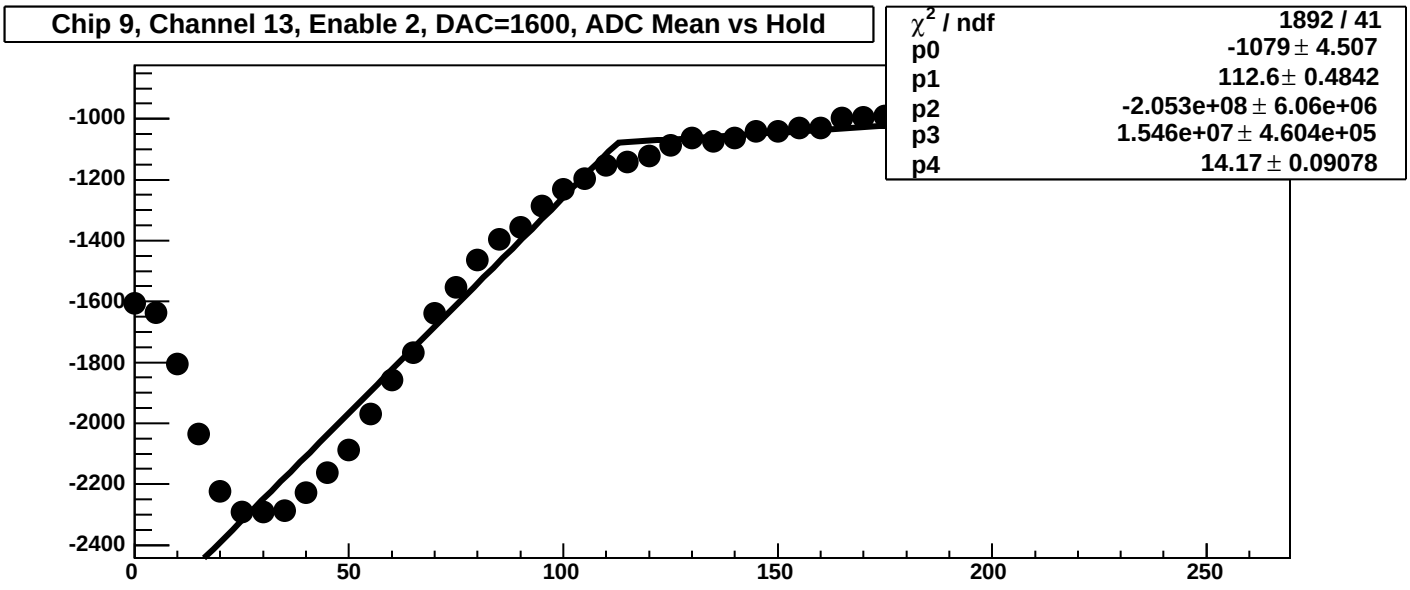


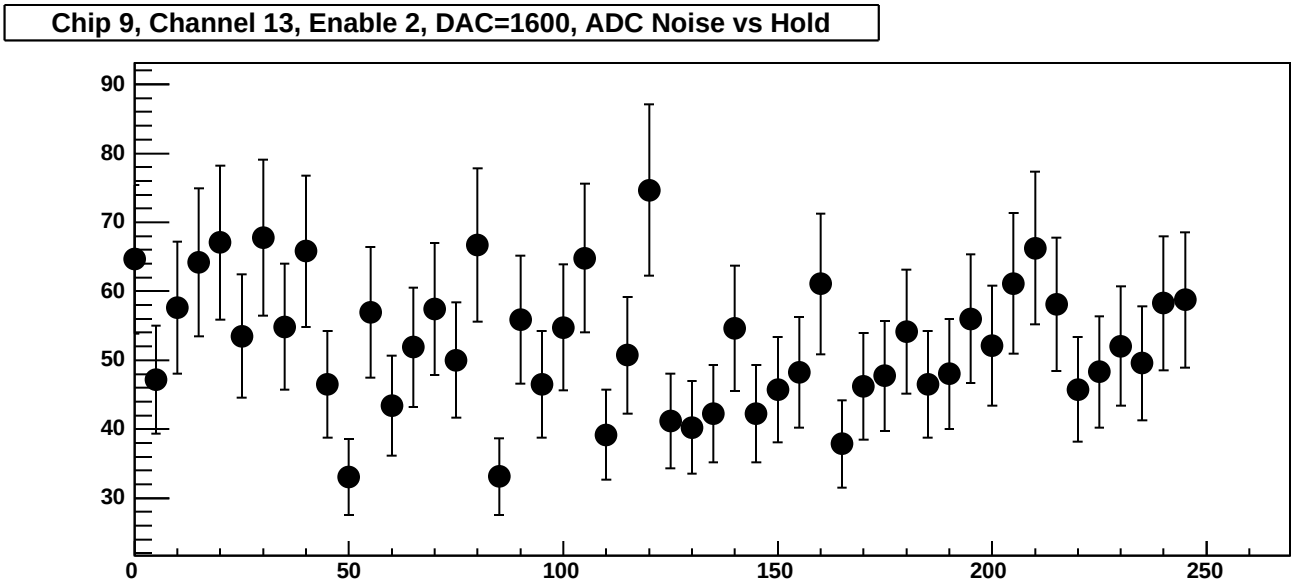
Chip 9, Channel 13, Enable 1!, DAC=1600, ADC Noise vs Hold

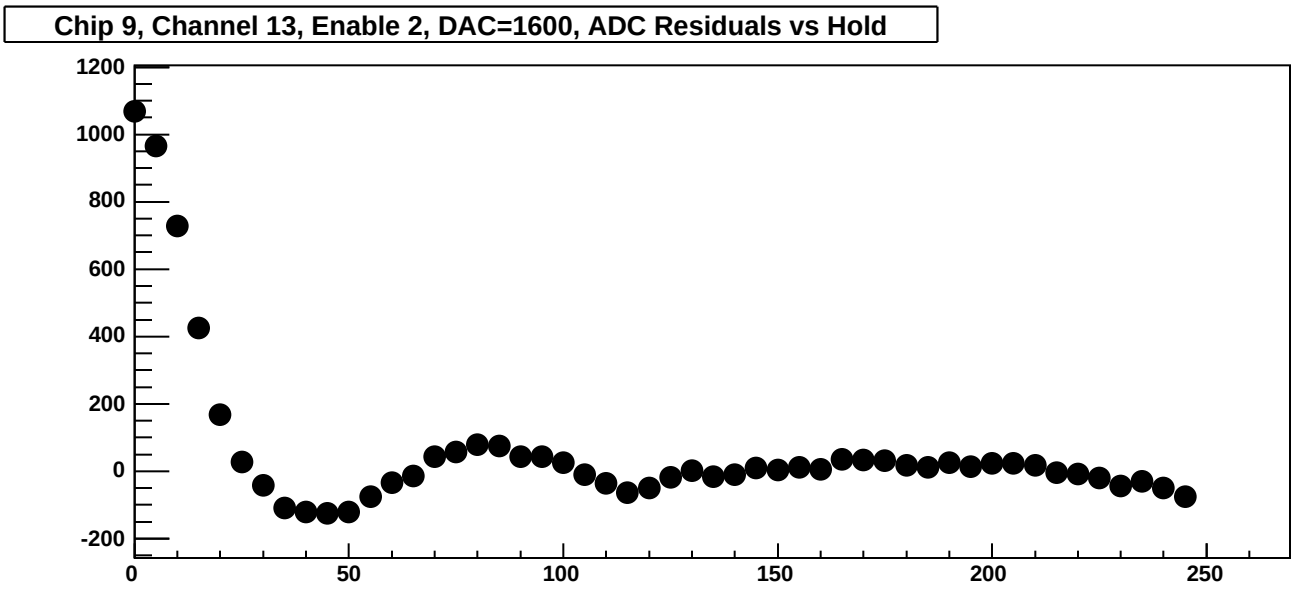


Chip 9, Channel 13, Enable 1!, DAC=1600, ADC Residuals vs Hold

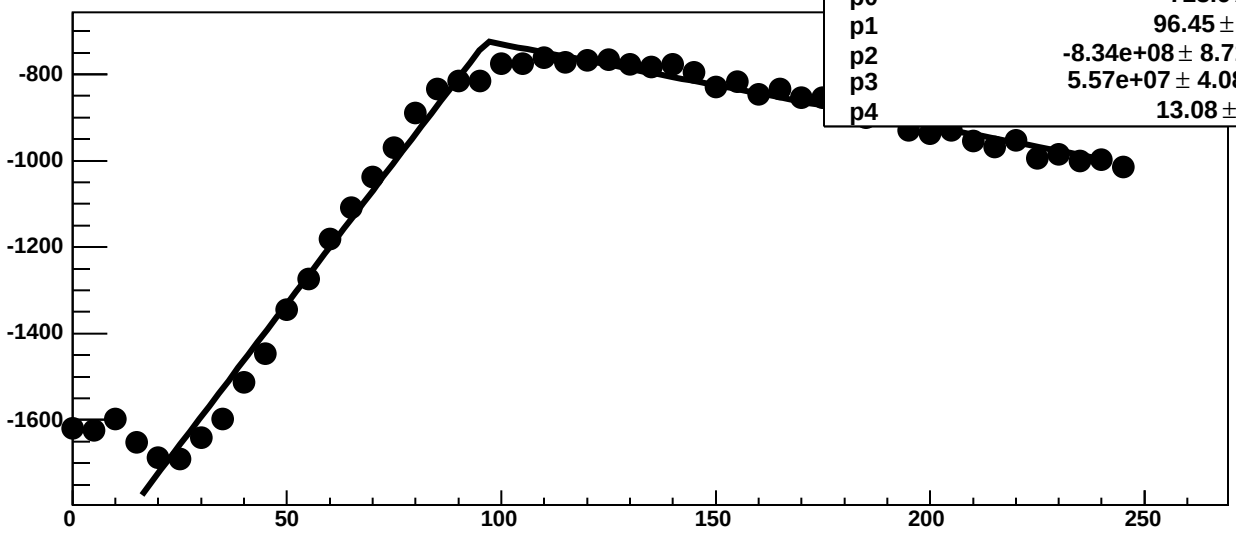



Chip 9, Channel 13, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 1892 / 41
p0 -1079 ± 4.507
p1 112.6 ± 0.4842
p2 -2.053e+08 ± 6.06e+06
p3 1.546e+07 ± 4.604e+05
p4 14.17 ± 0.09078
-1000
-1200
-1400
-1600
-1800
-2000
-2200
-2400
0
50
100
150
200
250

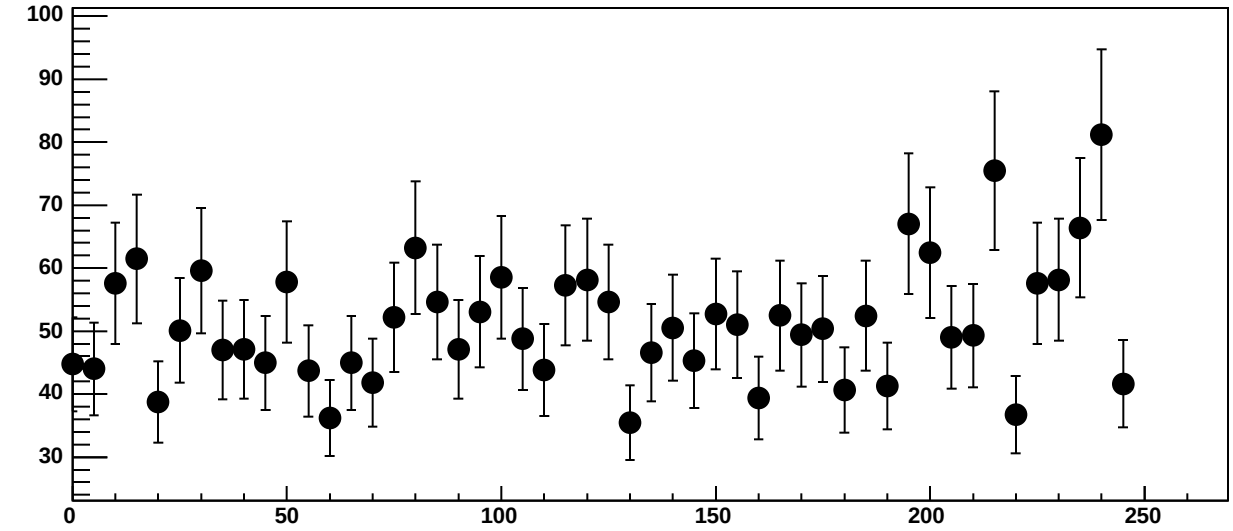

Chip 9, Channel 13, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 9, Channel 13, Enable 2, DAC=1600, ADC Residuals vs Hold
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

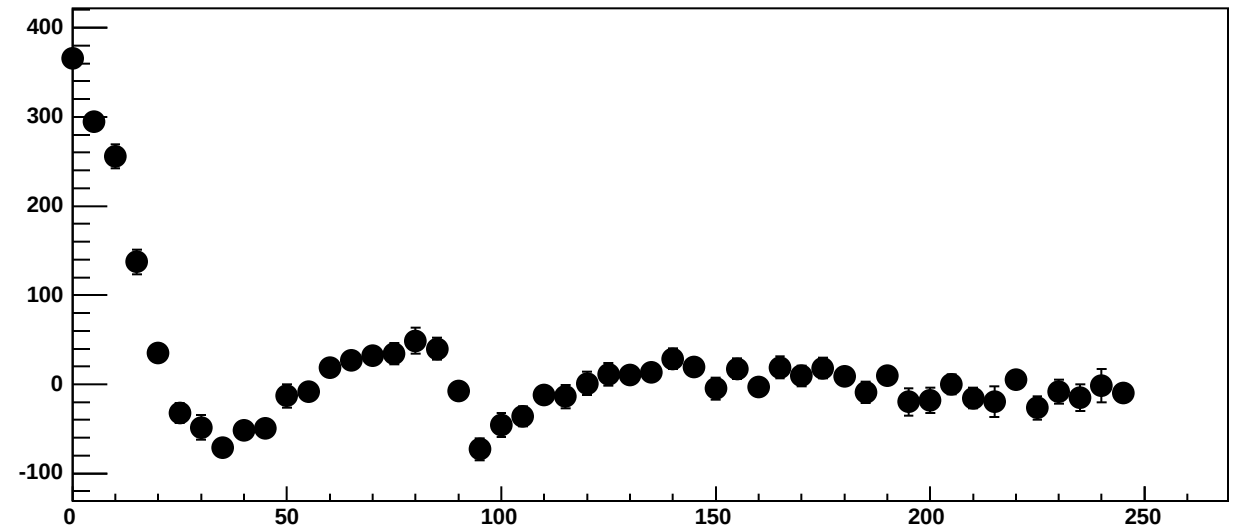
Chip 9, Channel 13, Enable 3, DAC=1600, ADC Mean vs Hold



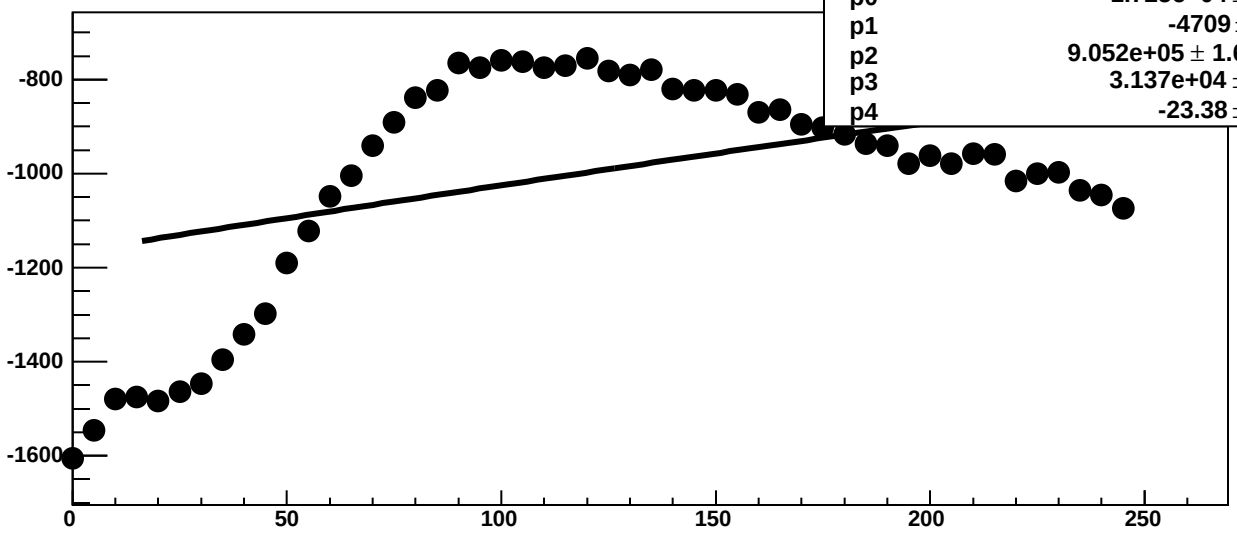
Chip 9, Channel 13, Enable 3, DAC=1600, ADC Noise vs Hold



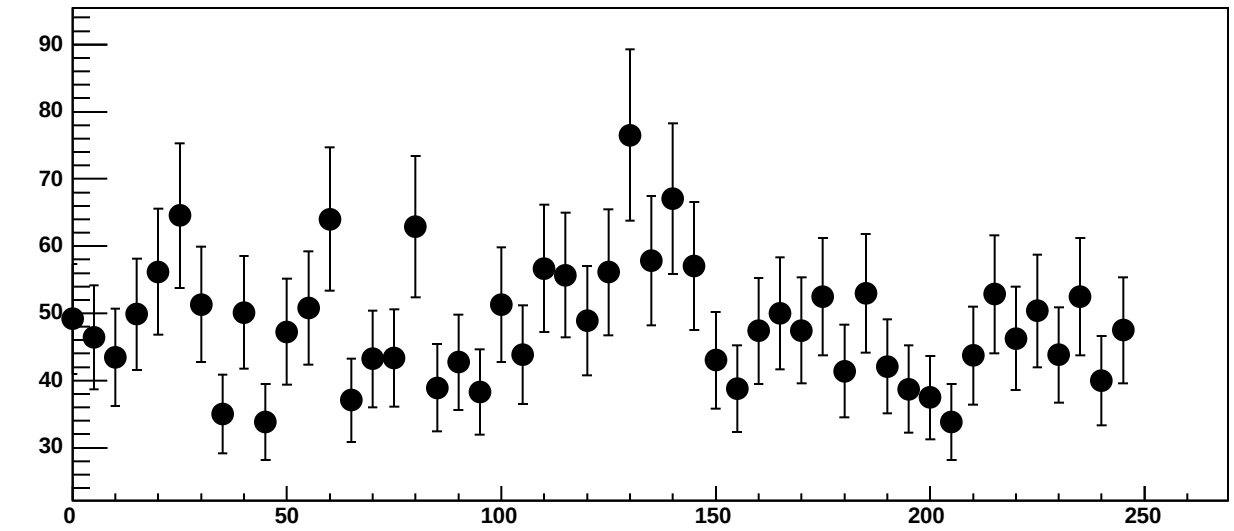
Chip 9, Channel 13, Enable 3, DAC=1600, ADC Residuals vs Hold



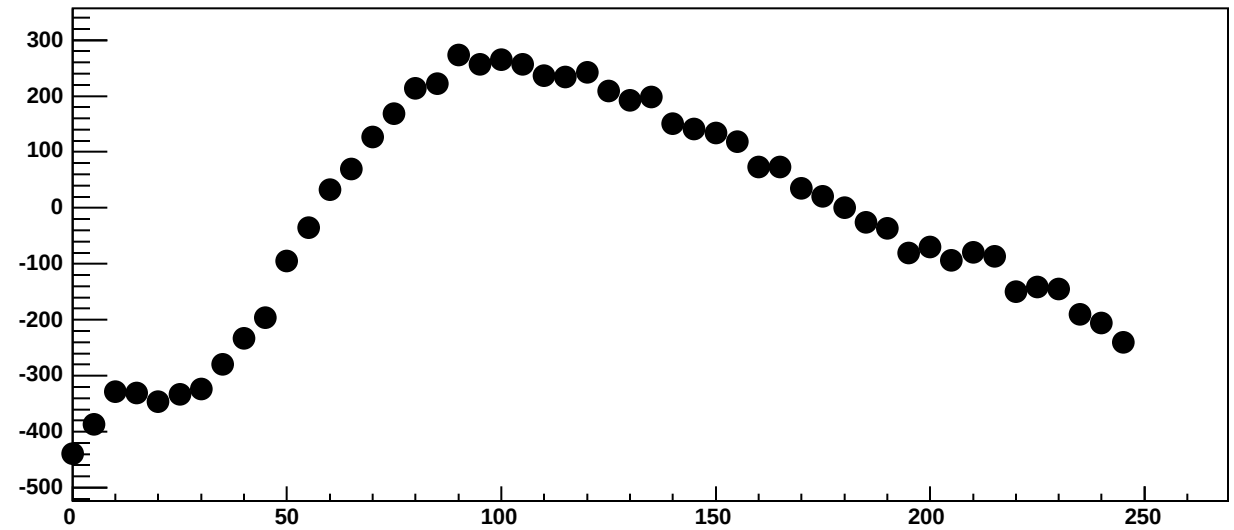
Chip 9, Channel 13, Enable 4, DAC=1600, ADC Mean vs Hold



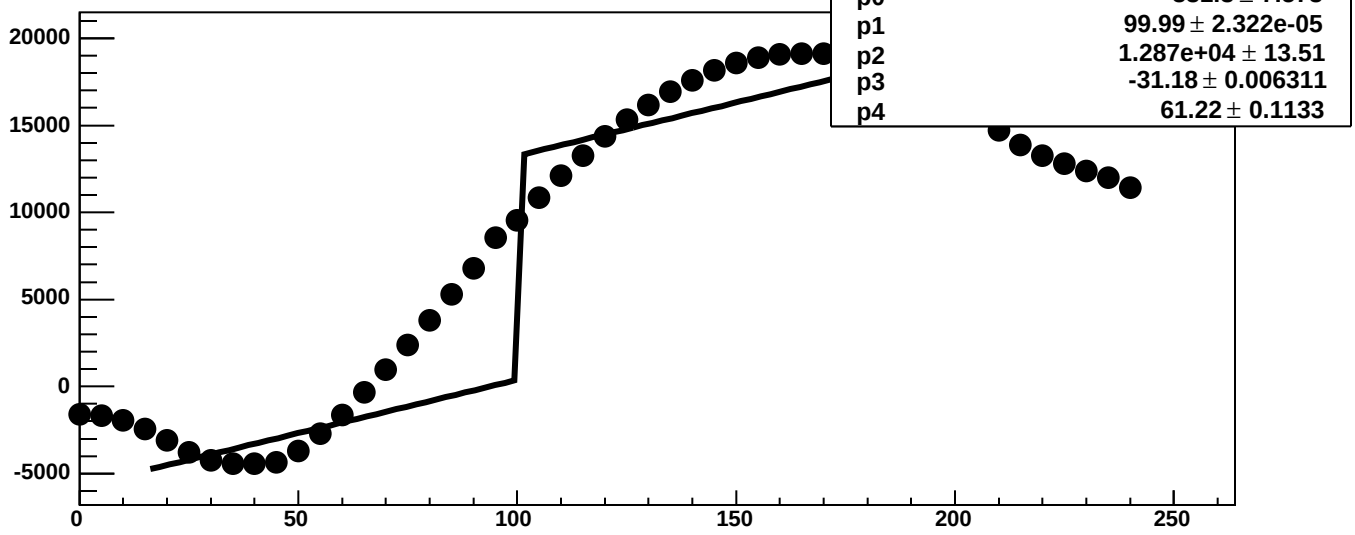
Chip 9, Channel 13, Enable 4, DAC=1600, ADC Noise vs Hold



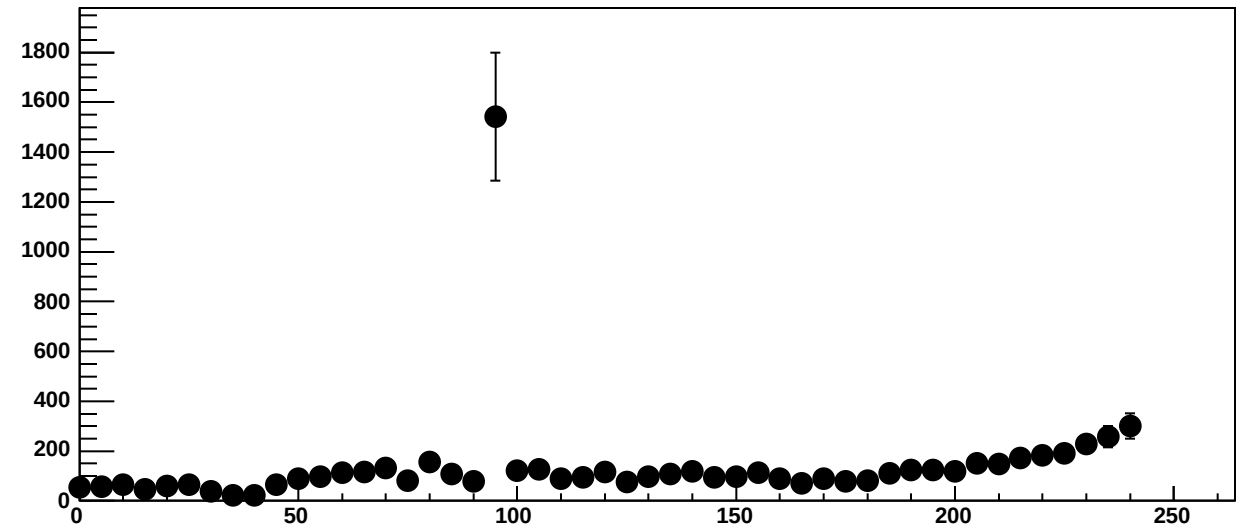
Chip 9, Channel 13, Enable 4, DAC=1600, ADC Residuals vs Hold



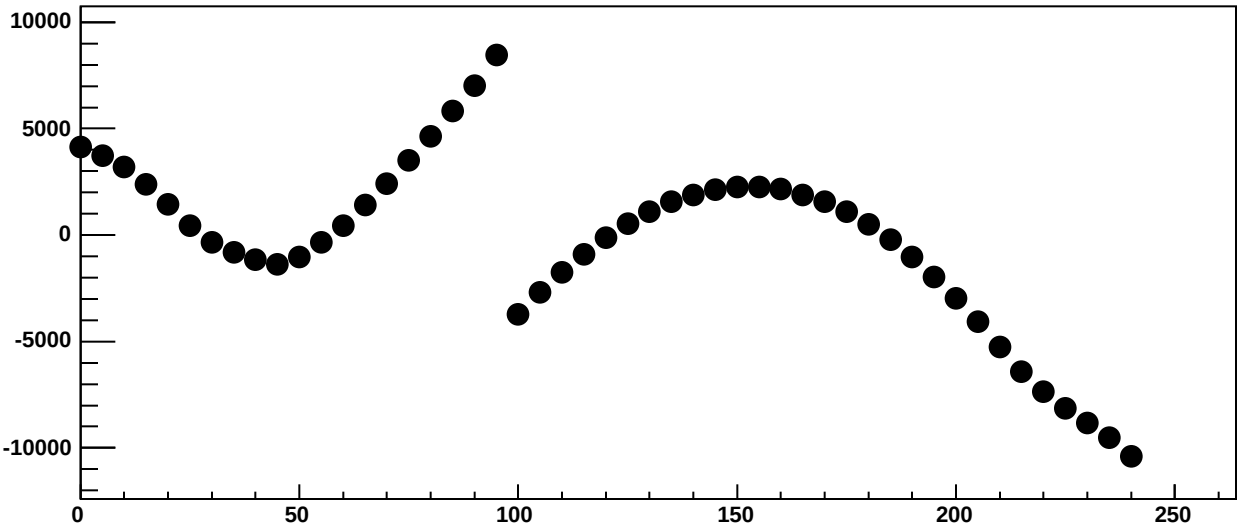
Chip 9, Channel 13, Enable 5, DAC=1600, ADC Mean vs Hold



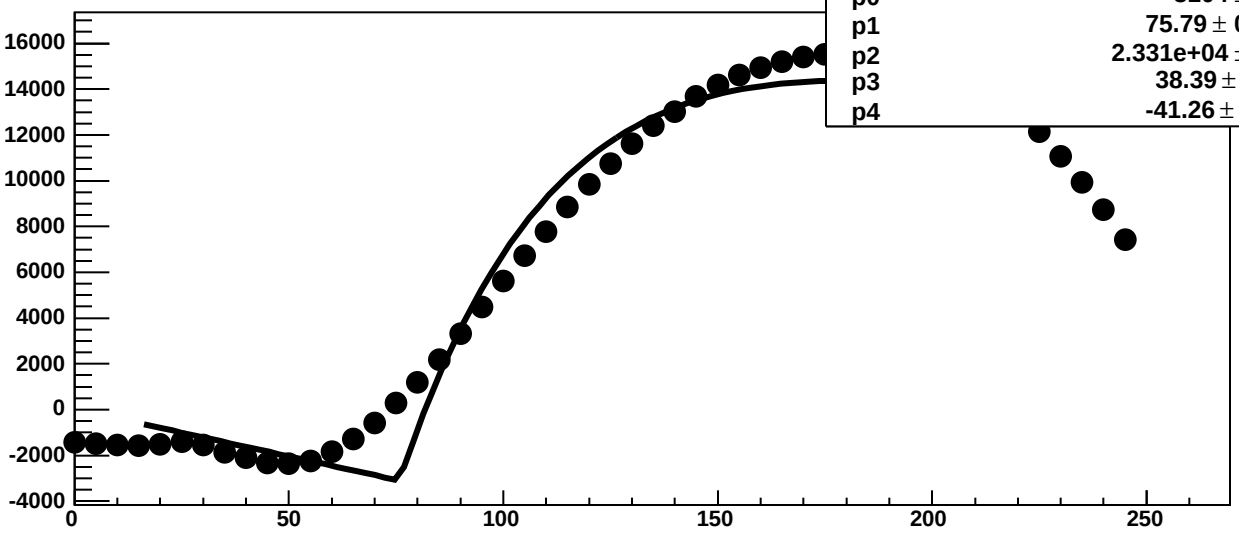
Chip 9, Channel 13, Enable 5, DAC=1600, ADC Noise vs Hold



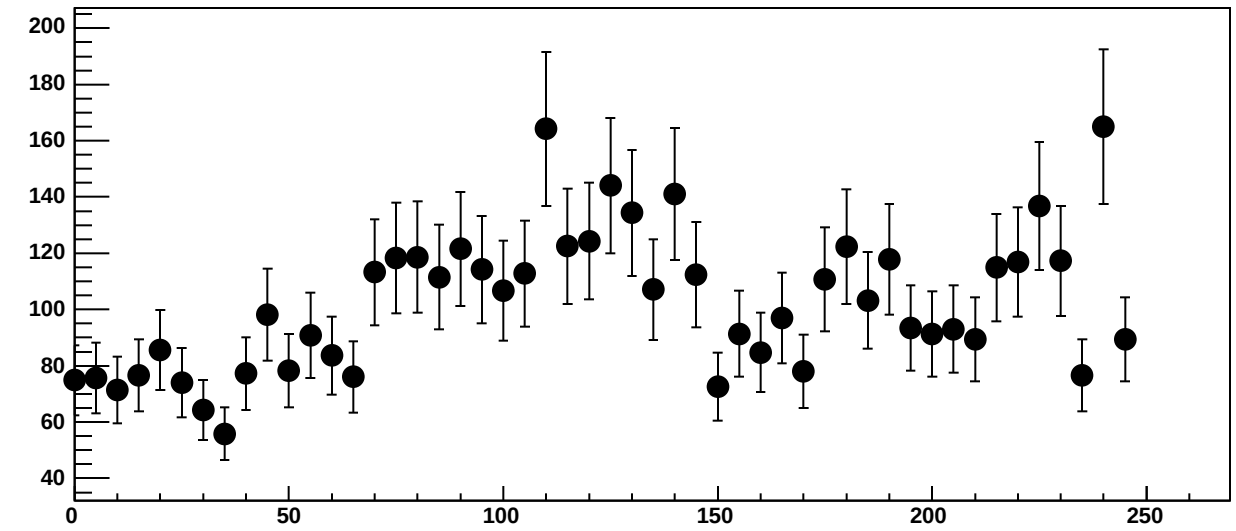
Chip 9, Channel 13, Enable 5, DAC=1600, ADC Residuals vs Hold



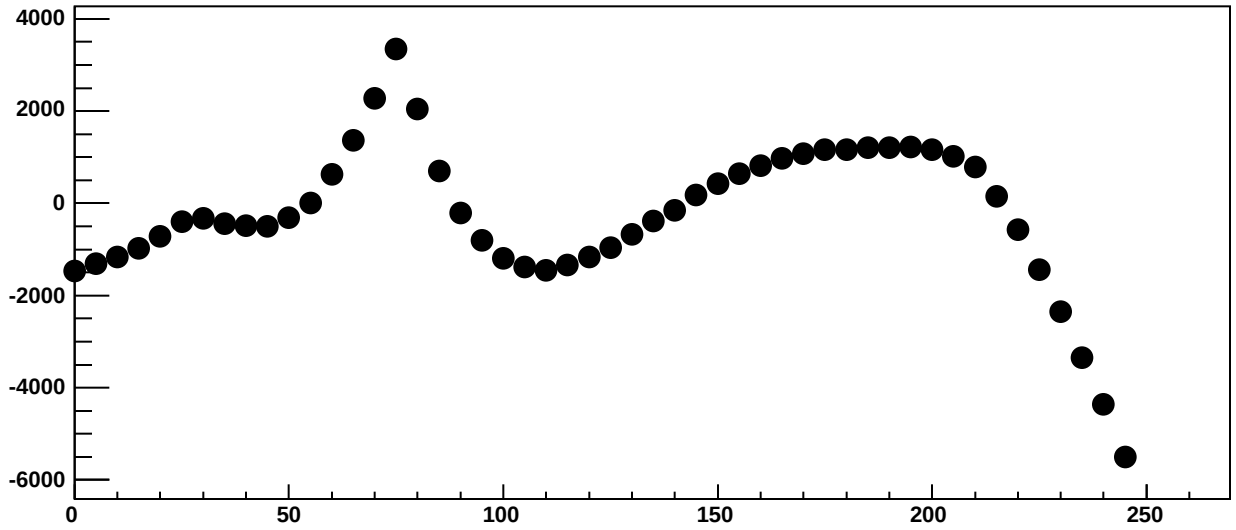
Chip 9, Channel 14, Enable 0, DAC=1600, ADC Mean vs Hold

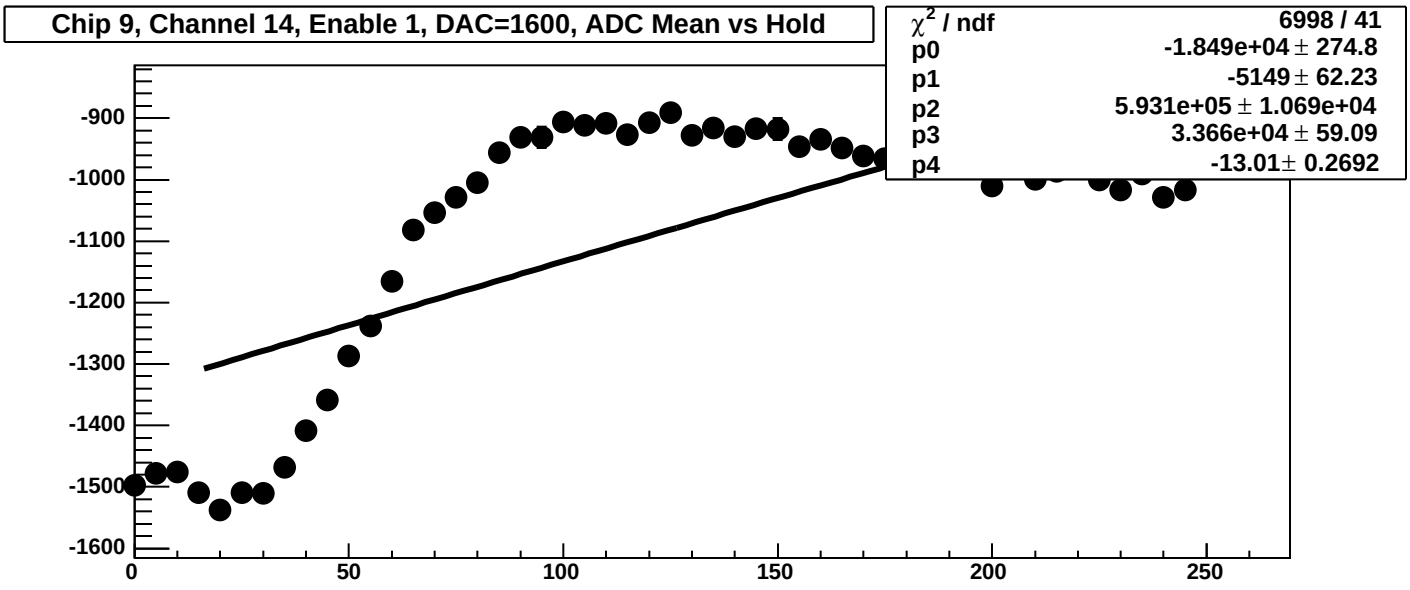


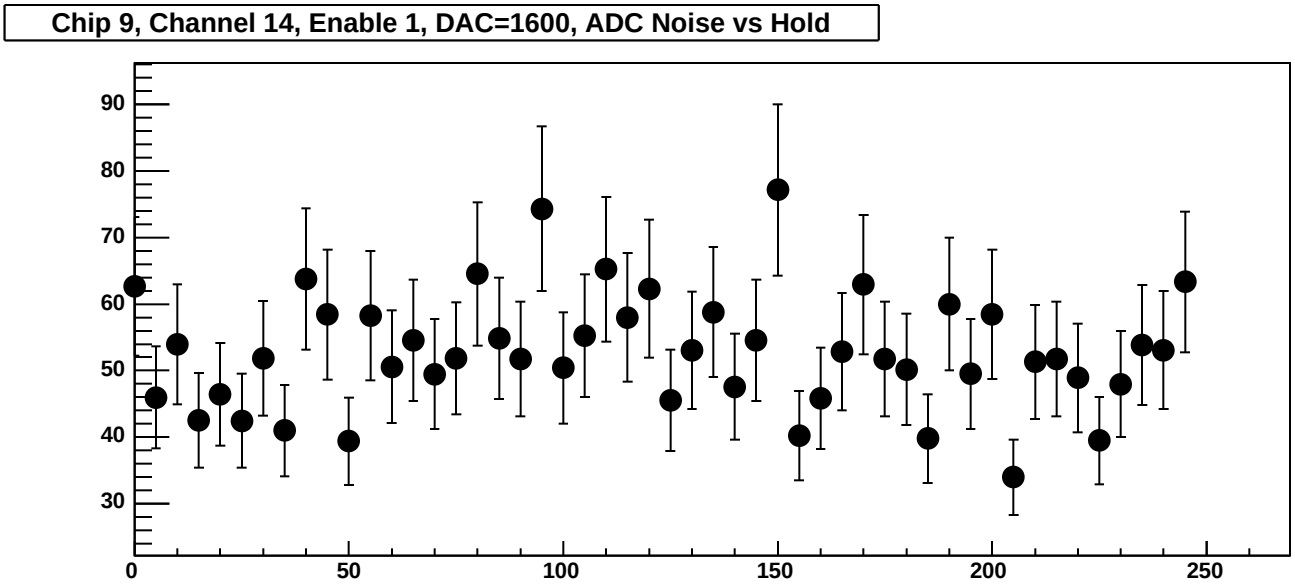
Chip 9, Channel 14, Enable 0, DAC=1600, ADC Noise vs Hold

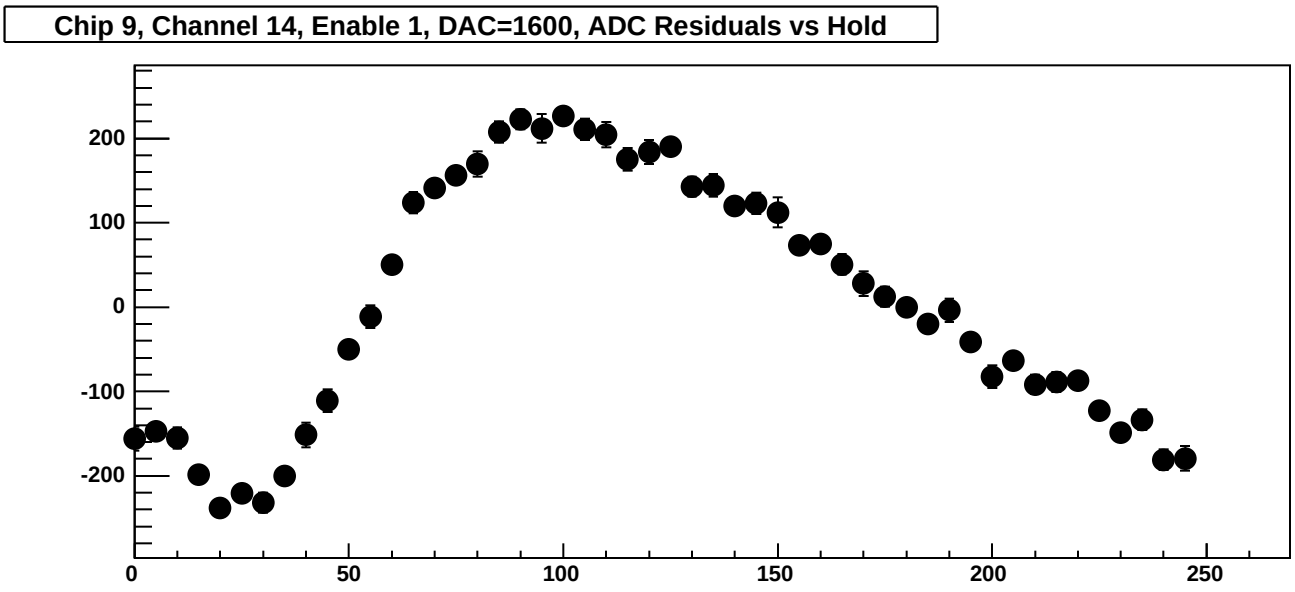


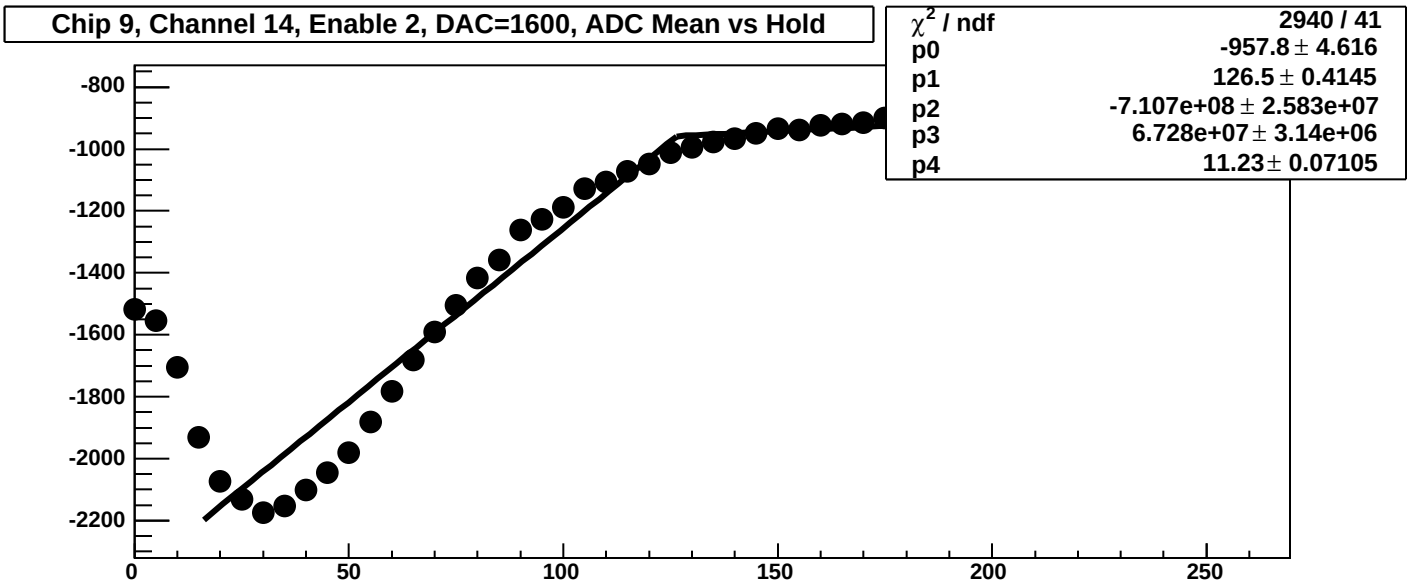
Chip 9, Channel 14, Enable 0, DAC=1600, ADC Residuals vs Hold

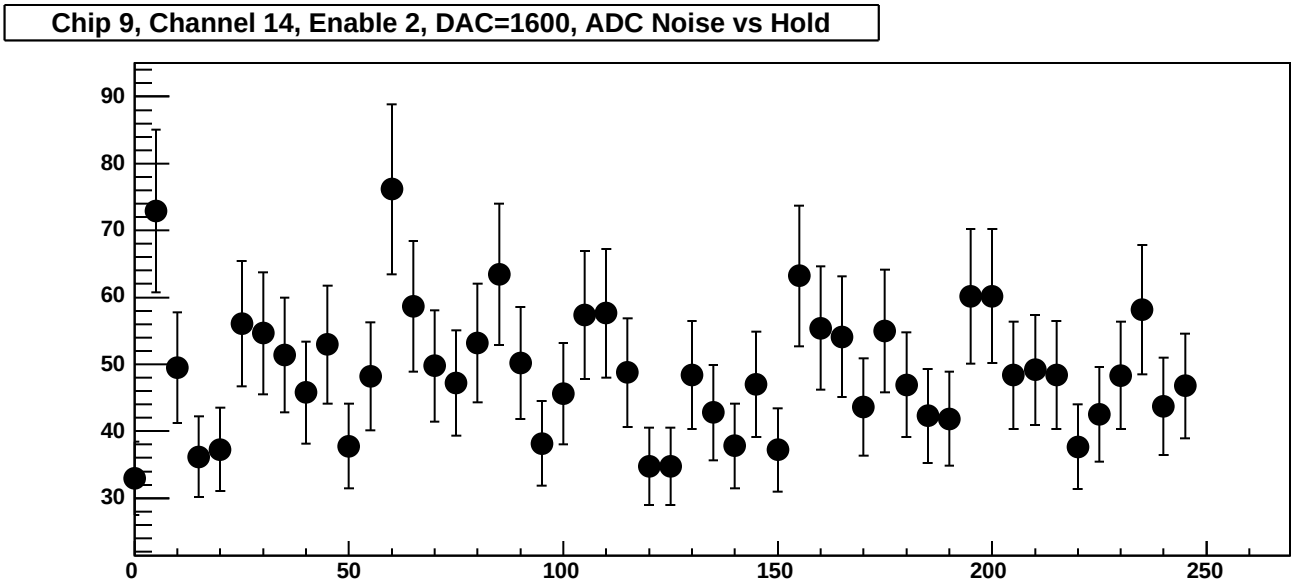


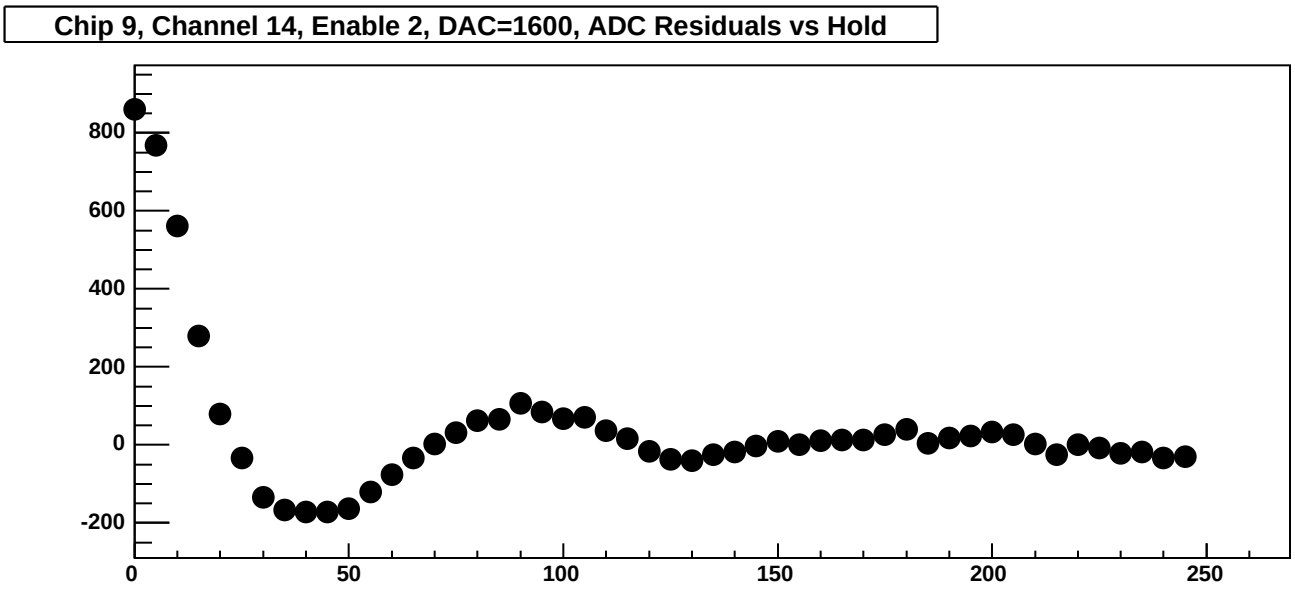

Chip 9, Channel 14, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 6998 / 41
p0 -1.849e+04 ± 274.8
p1 -5149 ± 62.23
p2 5.931e+05 ± 1.069e+04
p3 3.366e+04 ± 59.09
p4 -13.01 ± 0.2692
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
50
100
150
200
250

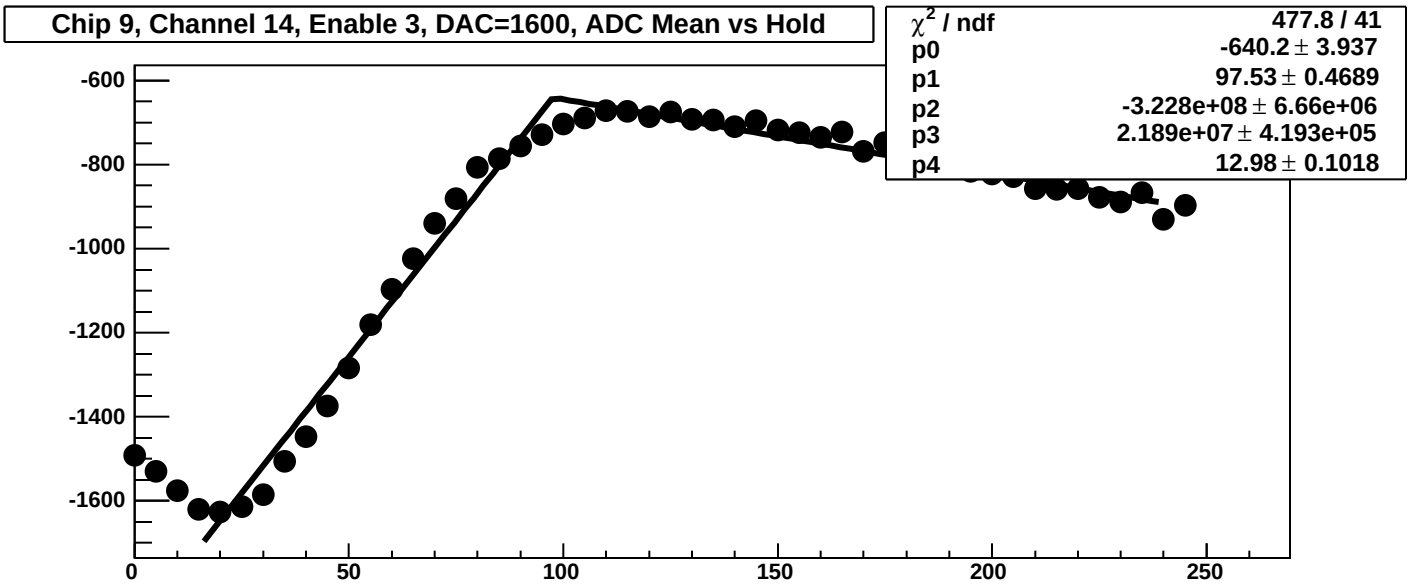

Chip 9, Channel 14, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

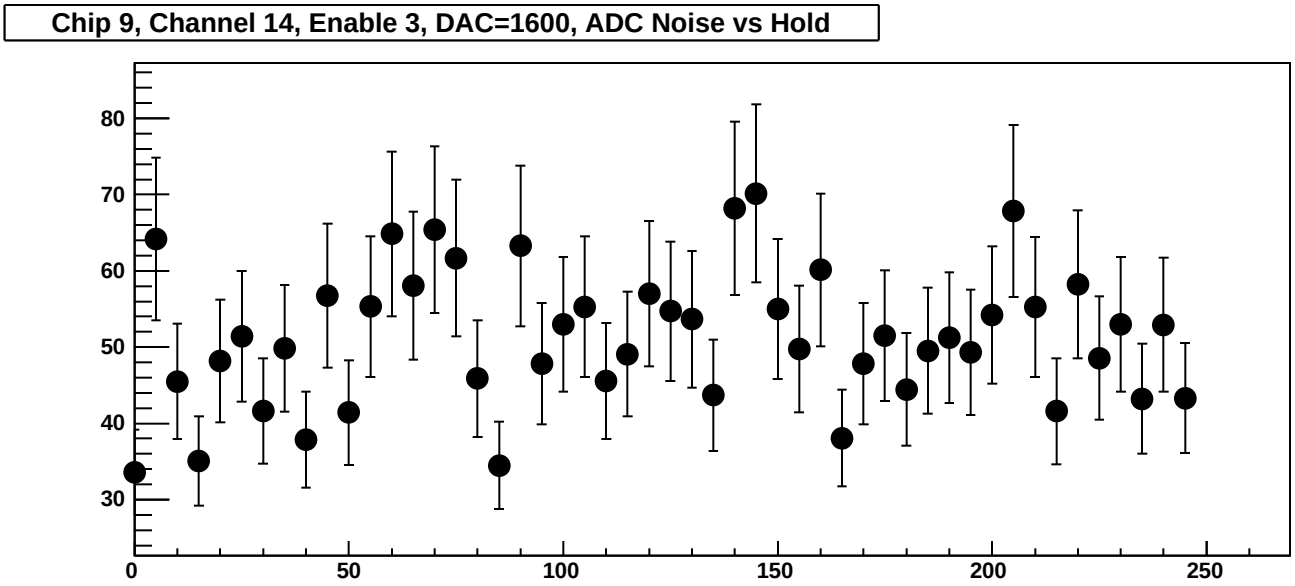

Chip 9, Channel 14, Enable 1, DAC=1600, ADC Residuals vs Hold
200
100
0
-100
-200
0
50
100
150
200
250

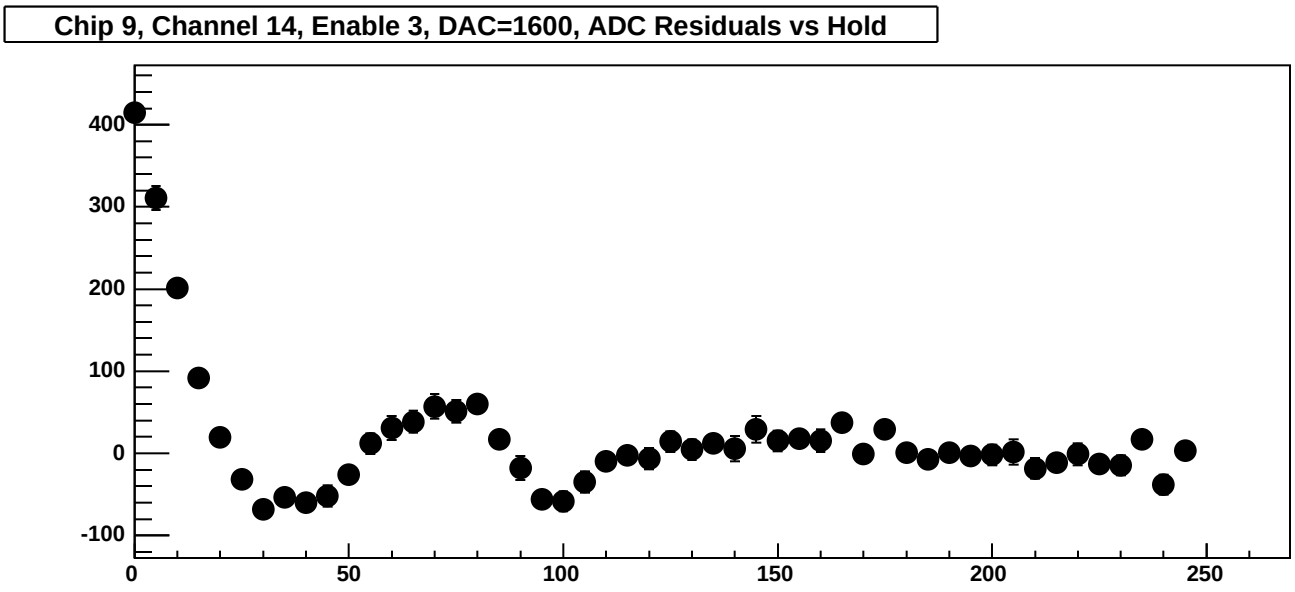

Chip 9, Channel 14, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 2940 / 41
p0 -957.8 ± 4.616
p1 126.5 ± 0.4145
p2 -7.107e+08 ± 2.583e+07
p3 6.728e+07 ± 3.14e+06
p4 11.23 ± 0.07105
-800
-1000
-1200
-1400
-1600
-1800
-2000
-2200
0
50
100
150
200
250


Chip 9, Channel 14, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 9, Channel 14, Enable 2, DAC=1600, ADC Residuals vs Hold
800
600
400
200
0
-200
0
50
100
150
200
250

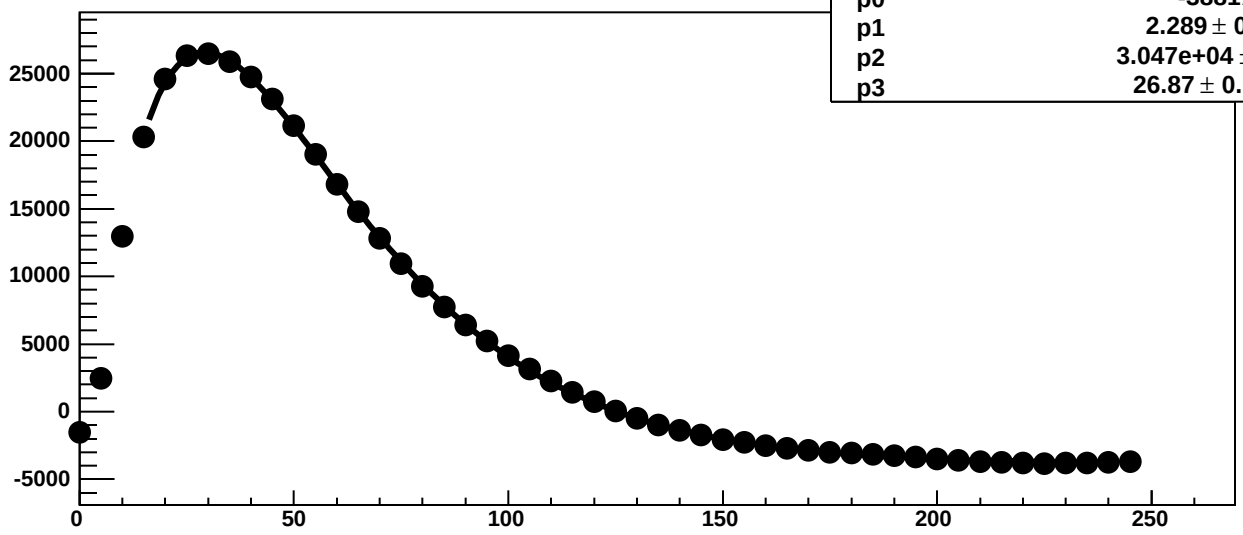

Chip 9, Channel 14, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 477.8 / 41
p0 -640.2 ± 3.937
p1 97.53 ± 0.4689
p2 -3.228e+08 ± 6.66e+06
p3 2.189e+07 ± 4.193e+05
p4 12.98 ± 0.1018
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250


Chip 9, Channel 14, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

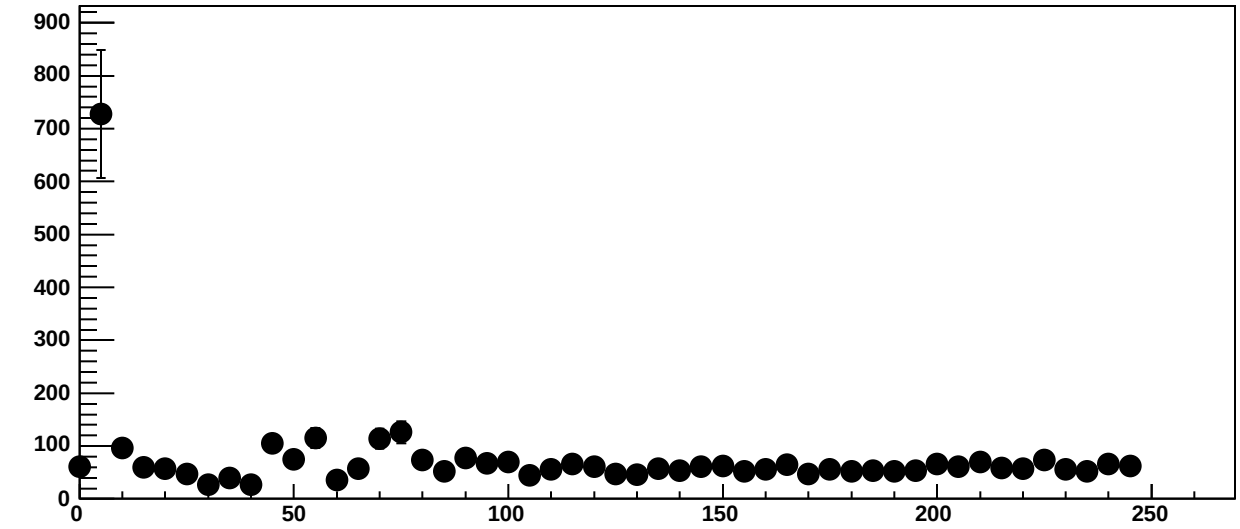

Chip 9, Channel 14, Enable 3, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

Chip 9, Channel 14, Enable 4!, DAC=1600, ADC Mean vs Hold

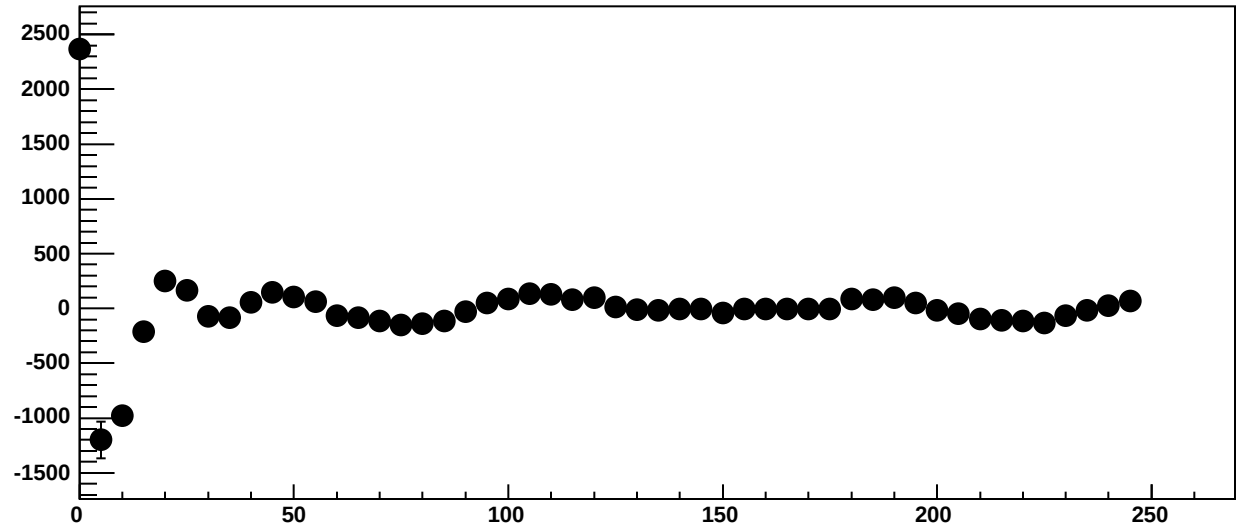
χ^2 / ndf	2393 / 42
p0	-3881 ± 3.481
p1	2.289 ± 0.01433
p2	3.047e+04 ± 4.603
p3	26.87 ± 0.009562

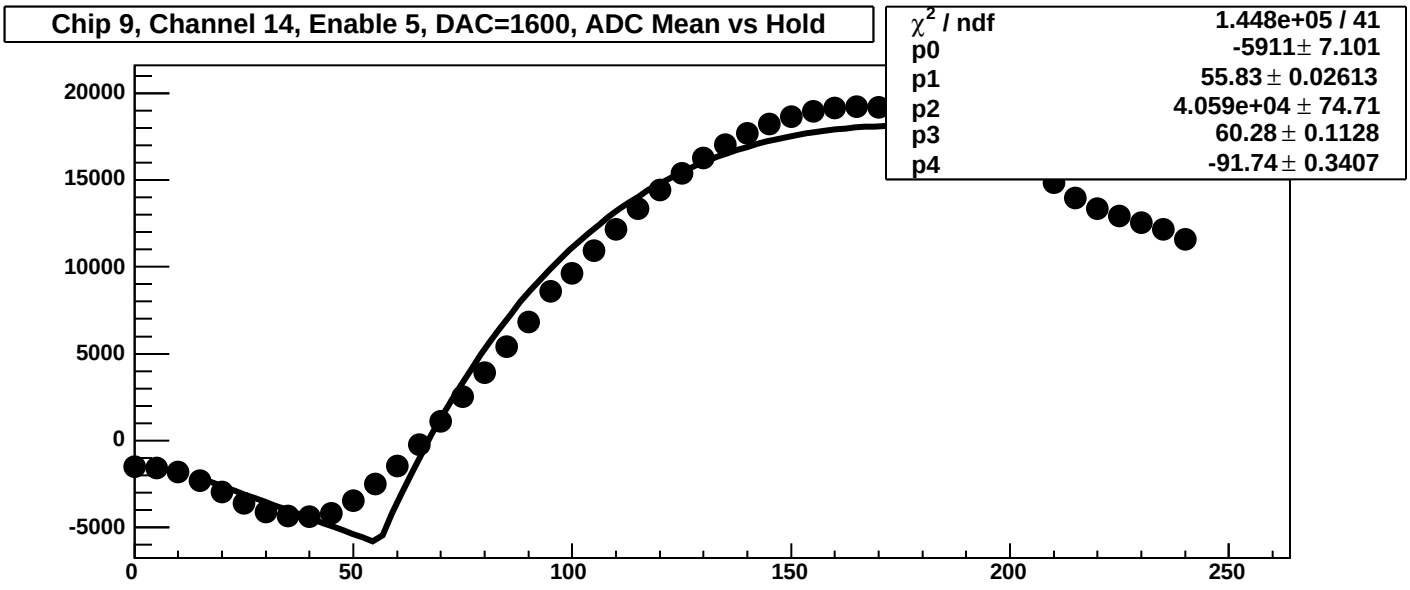


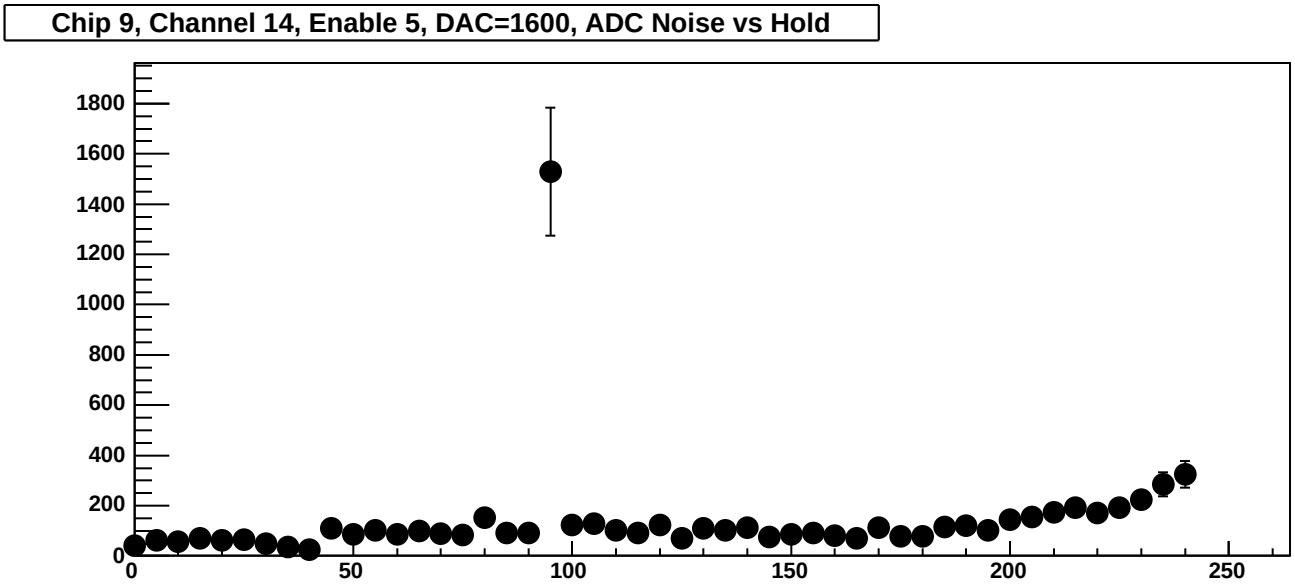
Chip 9, Channel 14, Enable 4!, DAC=1600, ADC Noise vs Hold

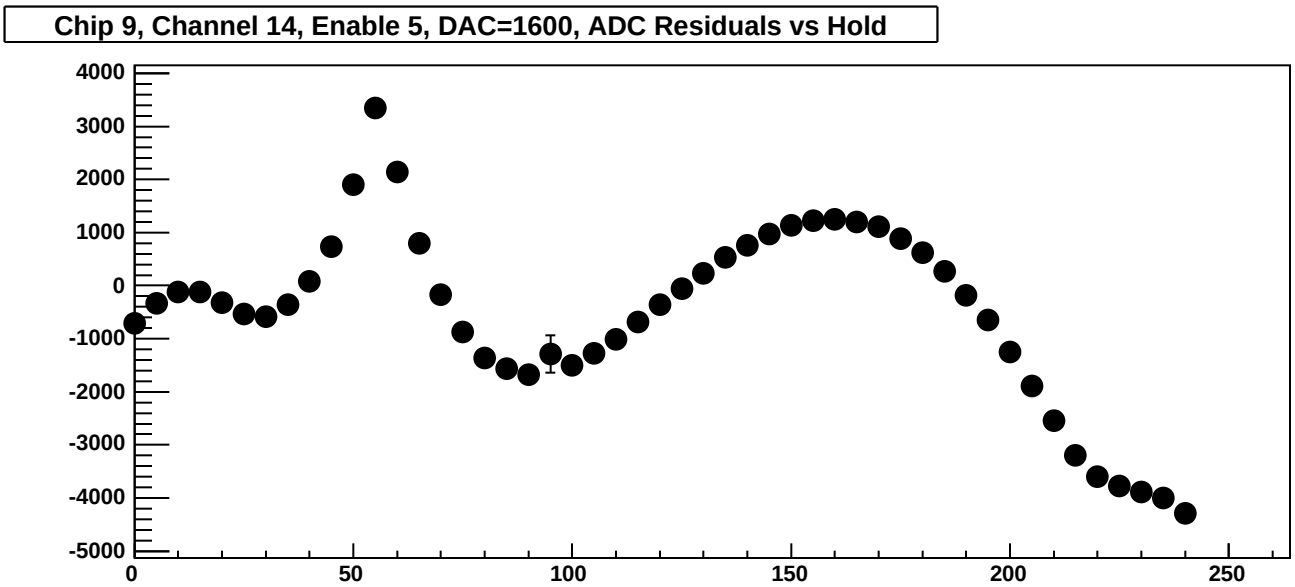


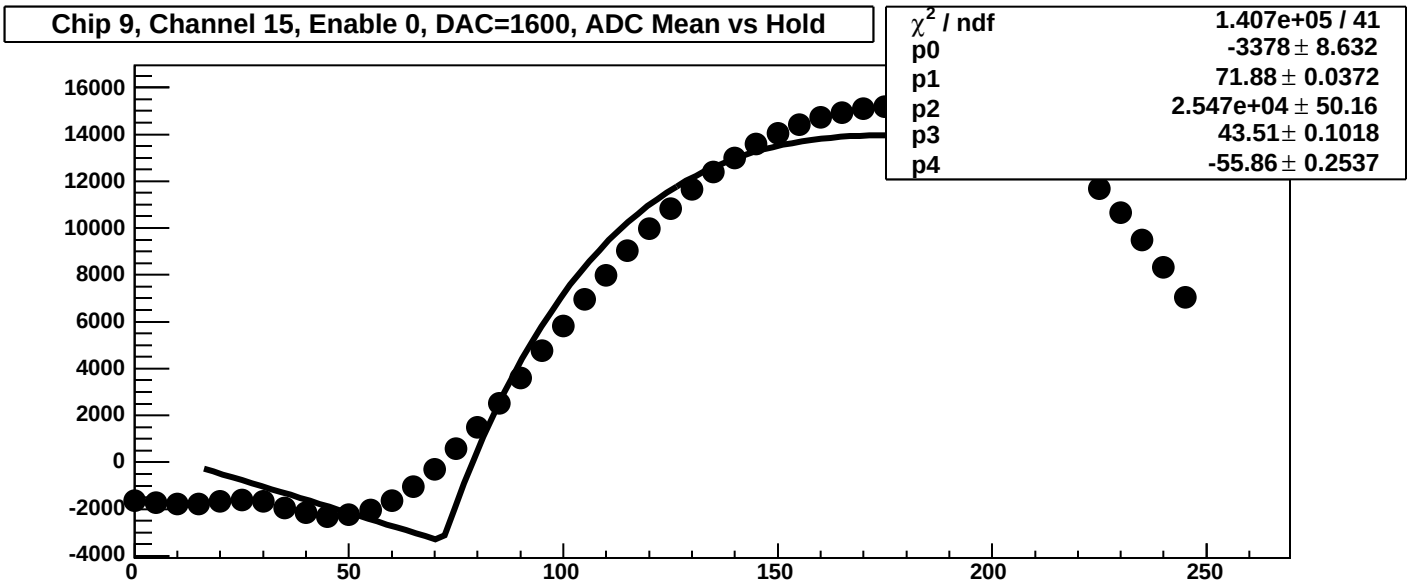
Chip 9, Channel 14, Enable 4!, DAC=1600, ADC Residuals vs Hold

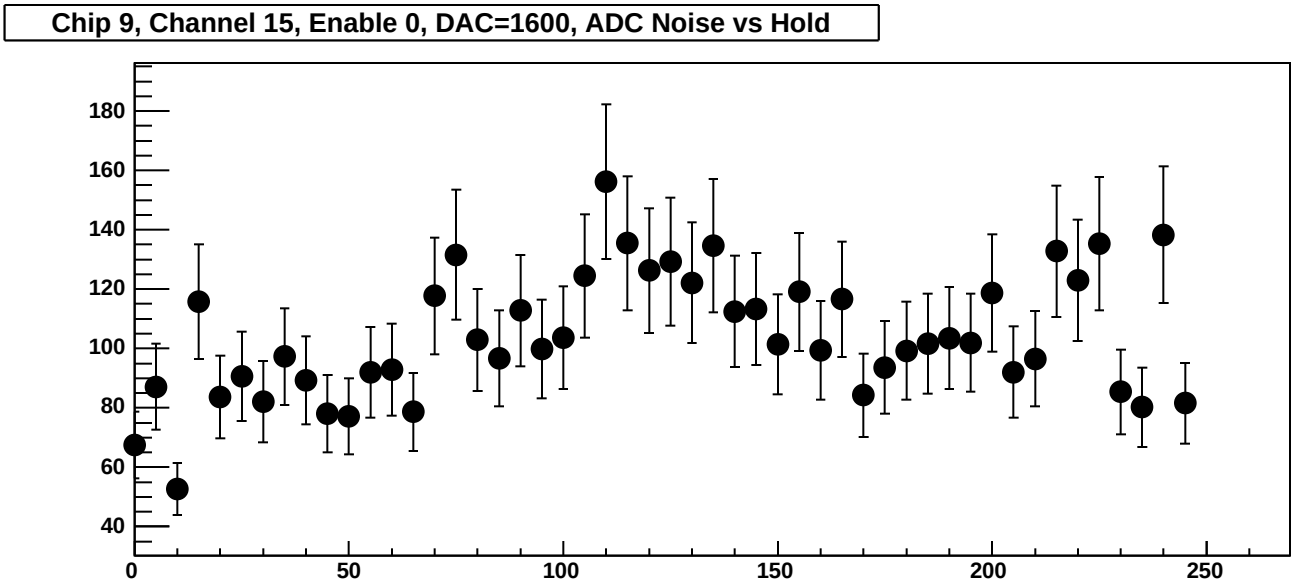


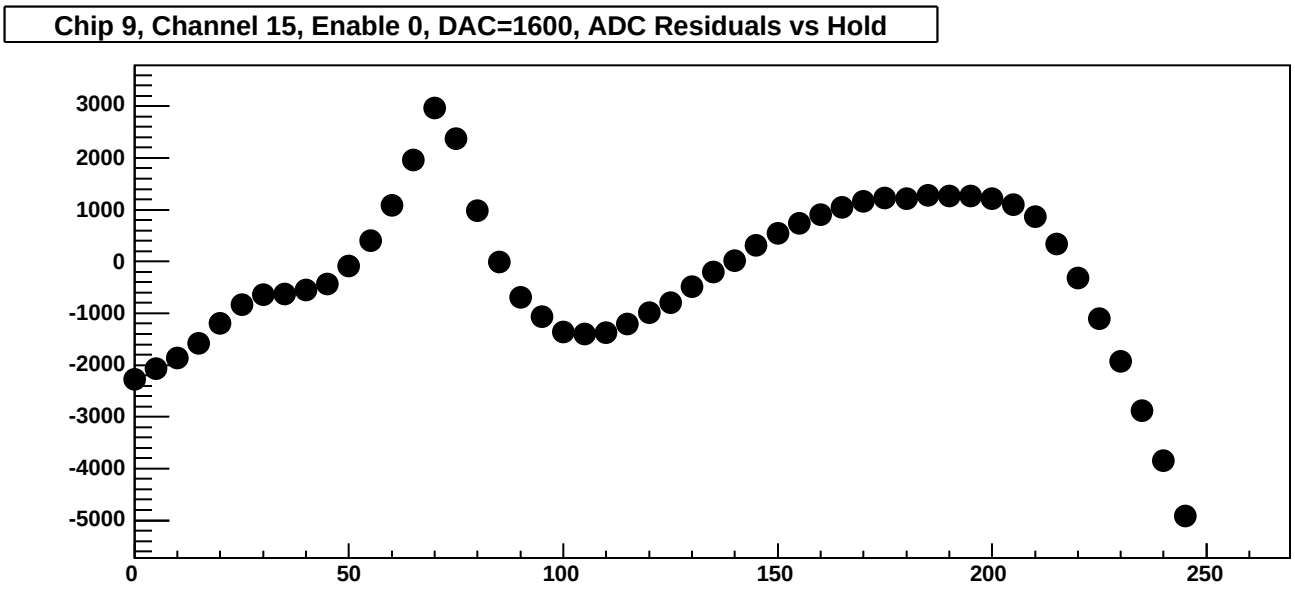

Chip 9, Channel 14, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.448e+05 / 41
p0 -5911 ± 7.101
p1 55.83 ± 0.02613
p2 4.059e+04 ± 74.71
p3 60.28 ± 0.1128
p4 -91.74 ± 0.3407
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

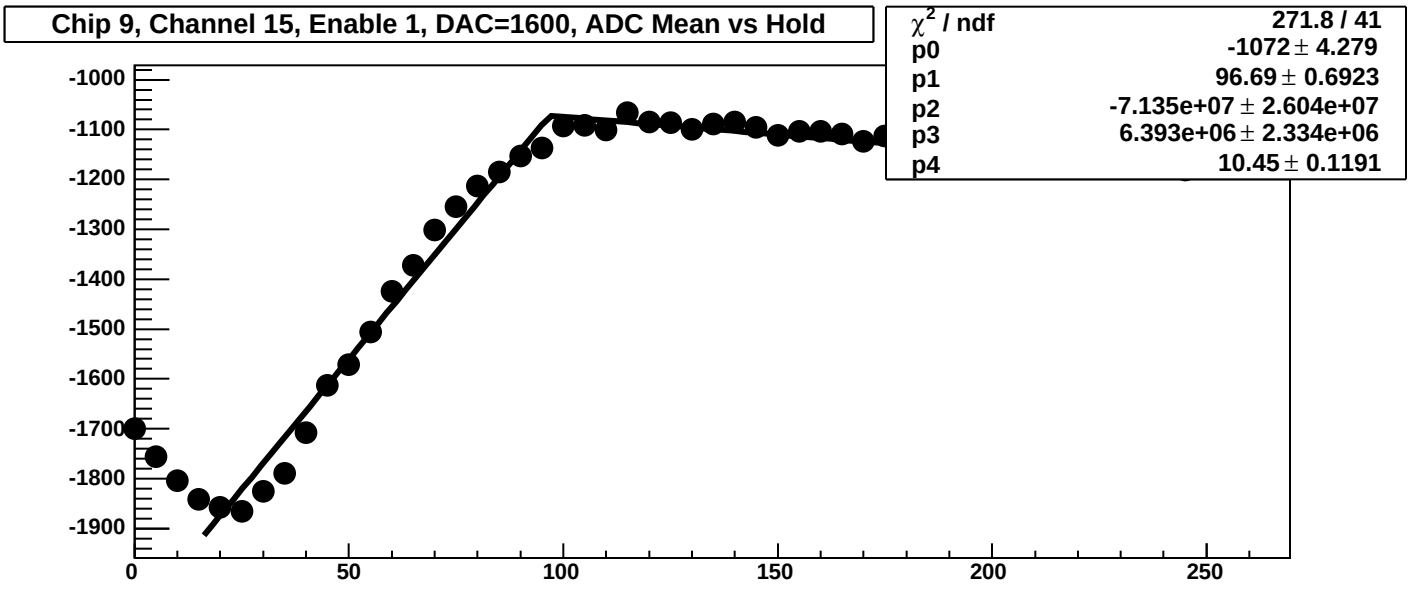

Chip 9, Channel 14, Enable 5, DAC=1600, ADC Noise vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

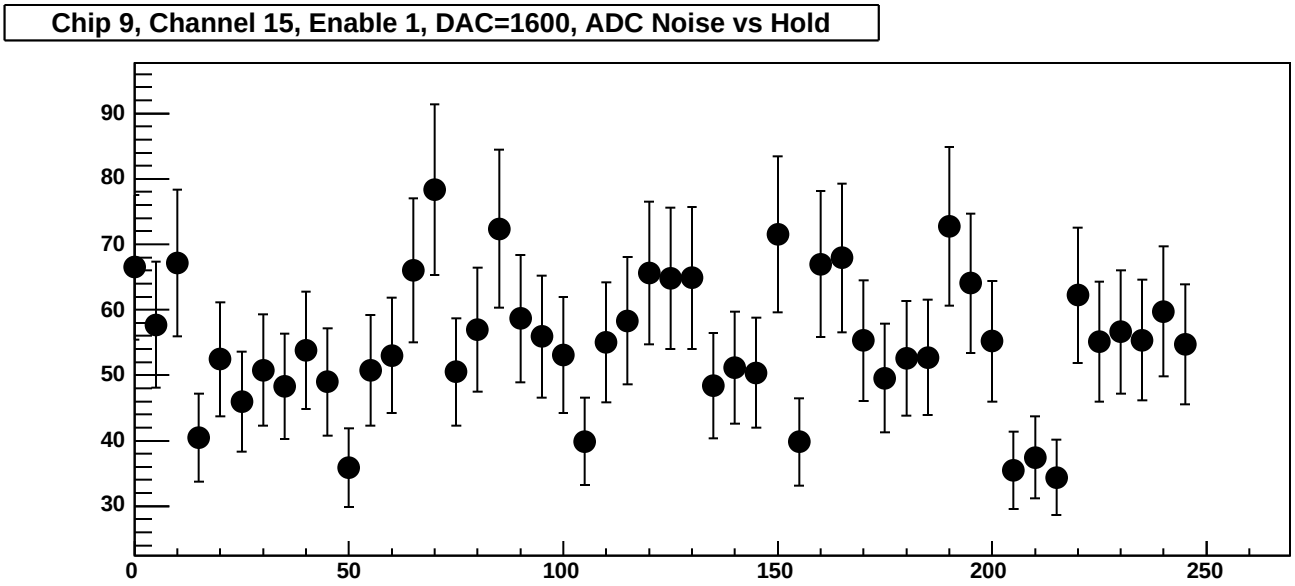

Chip 9, Channel 14, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

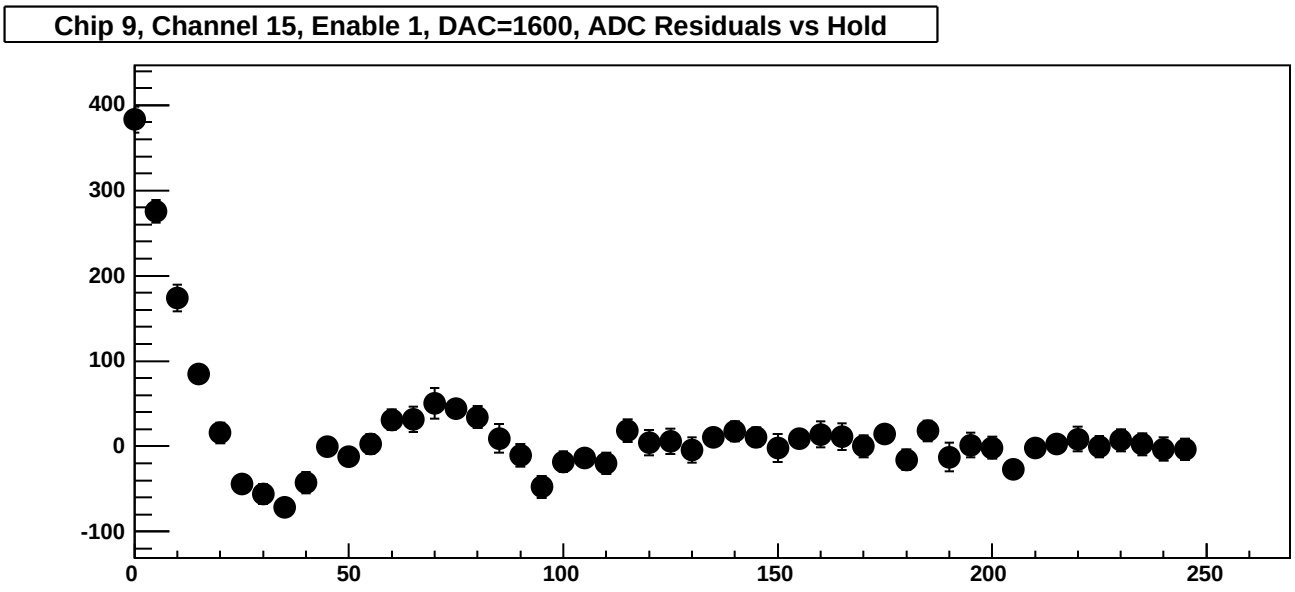

Chip 9, Channel 15, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.407e+05 / 41
p0 -3378 ± 8.632
p1 71.88 ± 0.0372
p2 2.547e+04 ± 50.16
p3 43.51 ± 0.1018
p4 -55.86 ± 0.2537
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

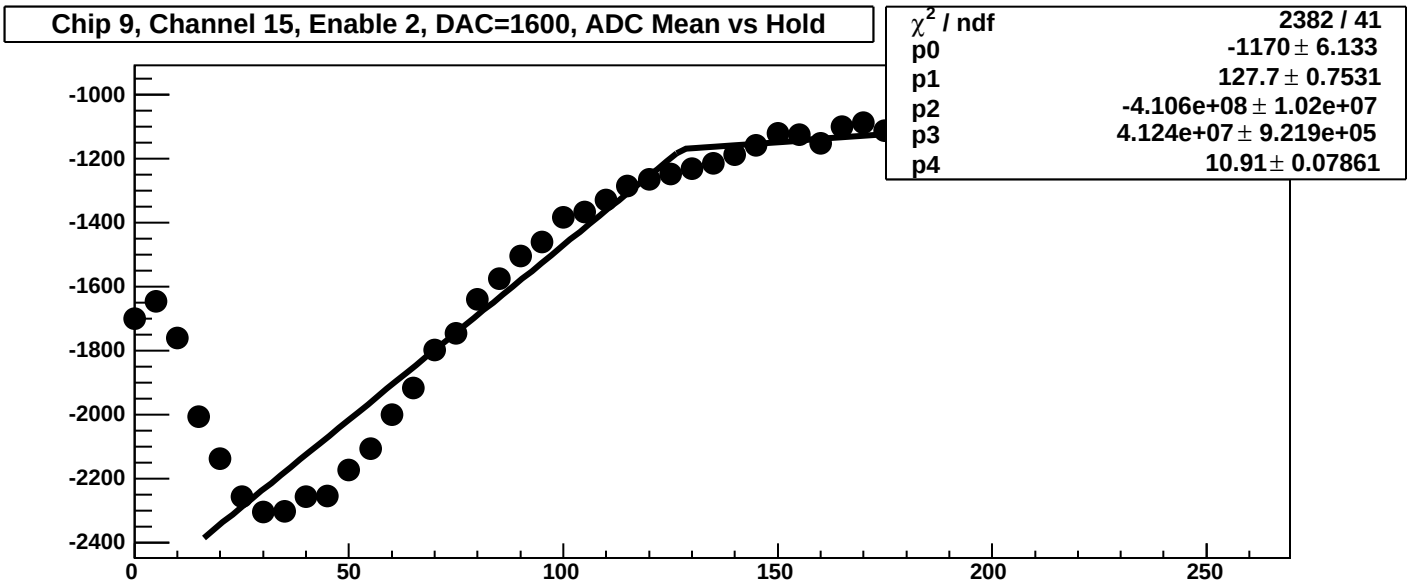

Chip 9, Channel 15, Enable 0, DAC=1600, ADC Noise vs Hold
180
160
140
120
100
80
60
40
0
50
100
150
200
250

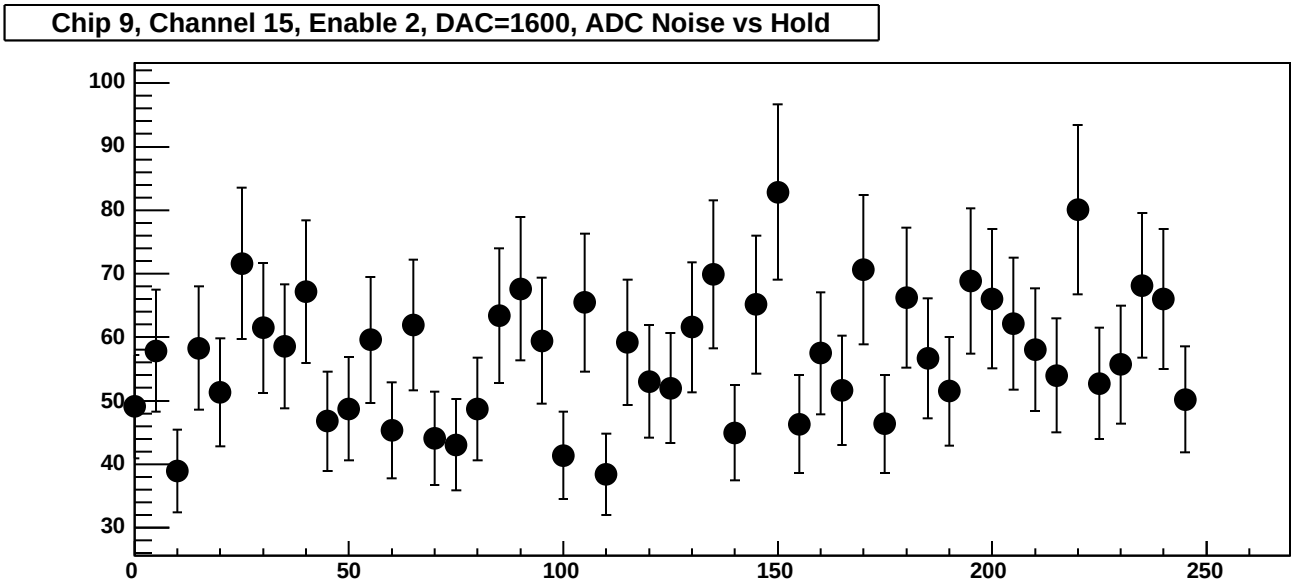

Chip 9, Channel 15, Enable 0, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

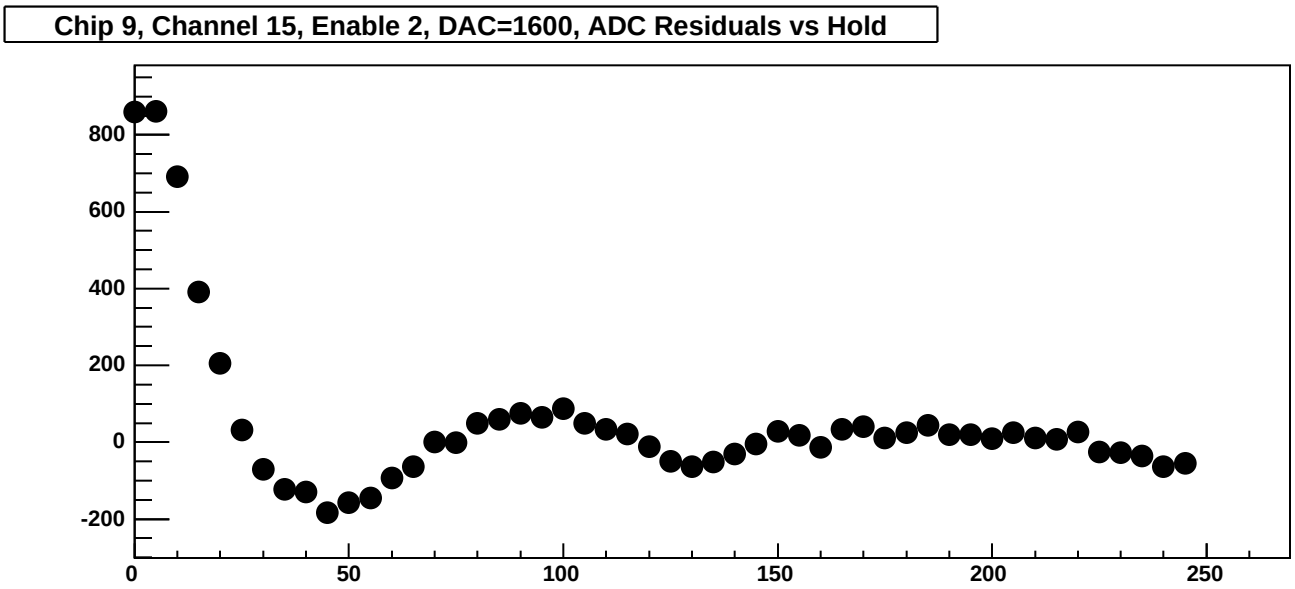

Chip 9, Channel 15, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 271.8 / 41
p0 -1072 ± 4.279
p1 96.69 ± 0.6923
p2 -7.135e+07 ± 2.604e+07
p3 6.393e+06 ± 2.334e+06
p4 10.45 ± 0.1191
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
-1800
-1900
0
50
100
150
200
250

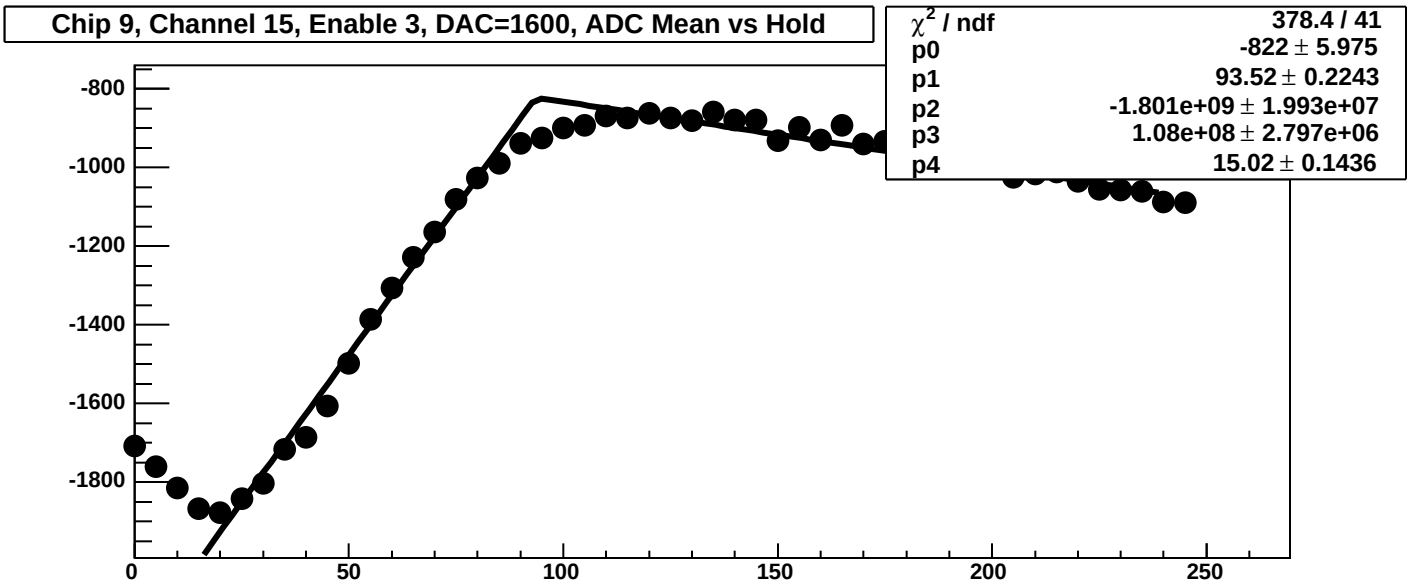

Chip 9, Channel 15, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

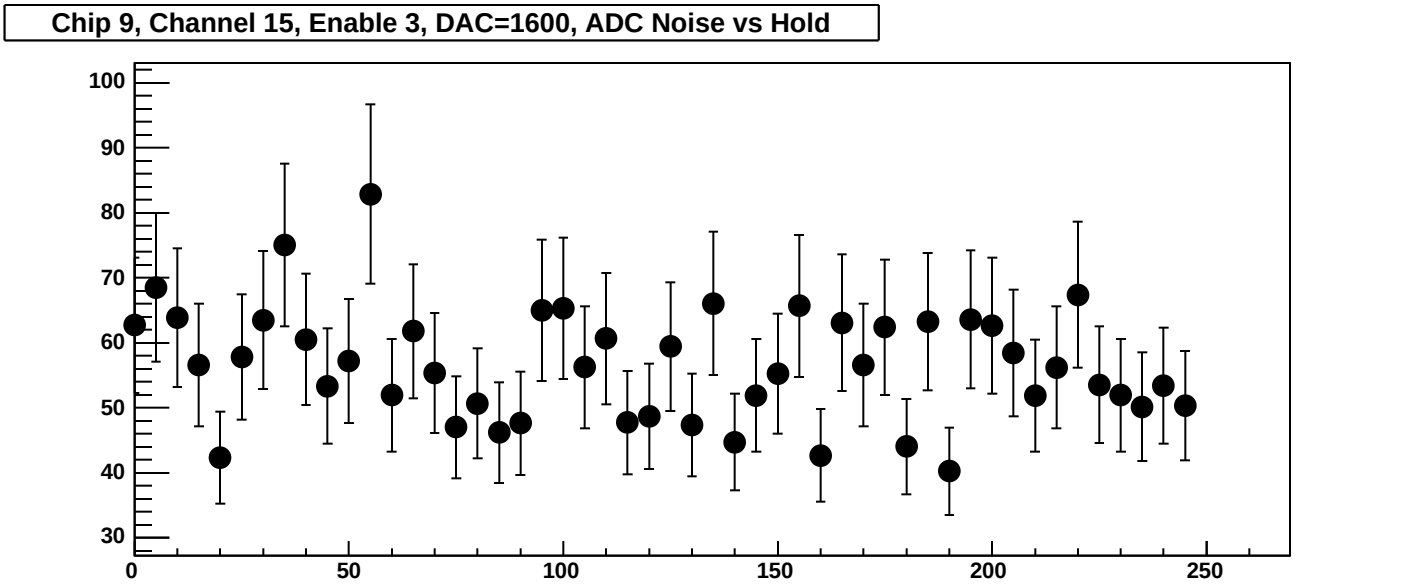

Chip 9, Channel 15, Enable 1, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

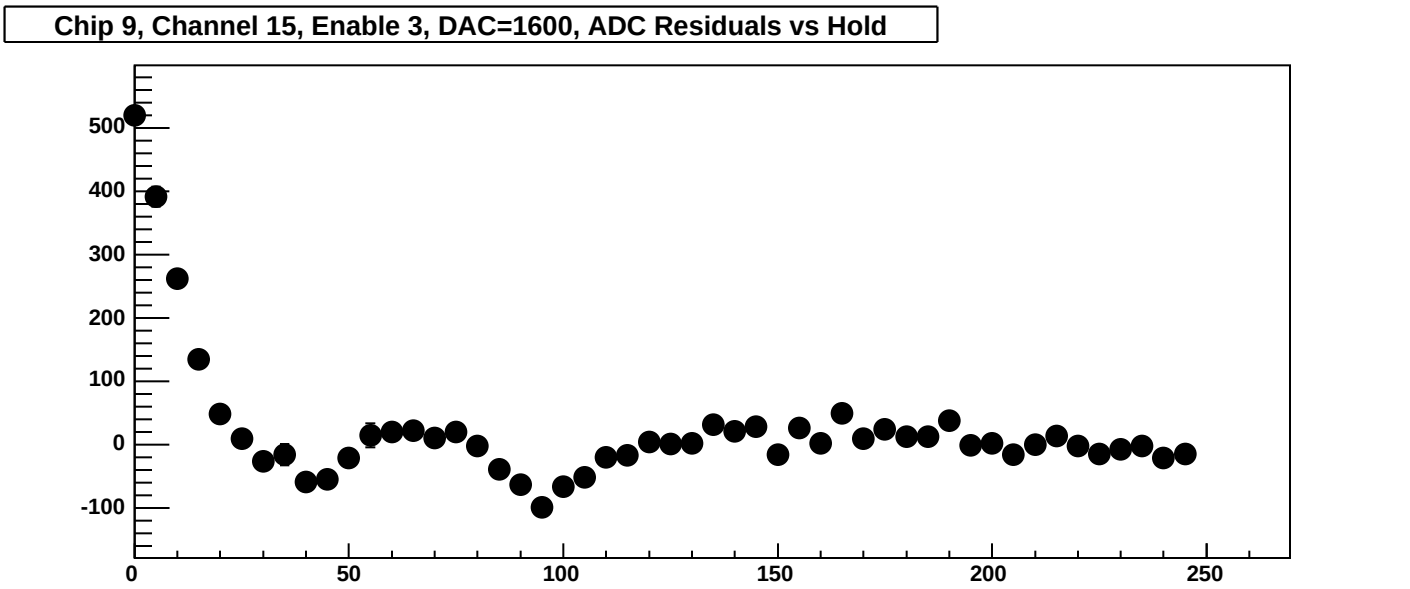

Chip 9, Channel 15, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 2382 / 41
p0 -1170 ± 6.133
p1 127.7 ± 0.7531
p2 -4.106e+08 ± 1.02e+07
p3 4.124e+07 ± 9.219e+05
p4 10.91 ± 0.07861
-1000
-1200
-1400
-1600
-1800
-2000
-2200
-2400
0
50
100
150
200
250

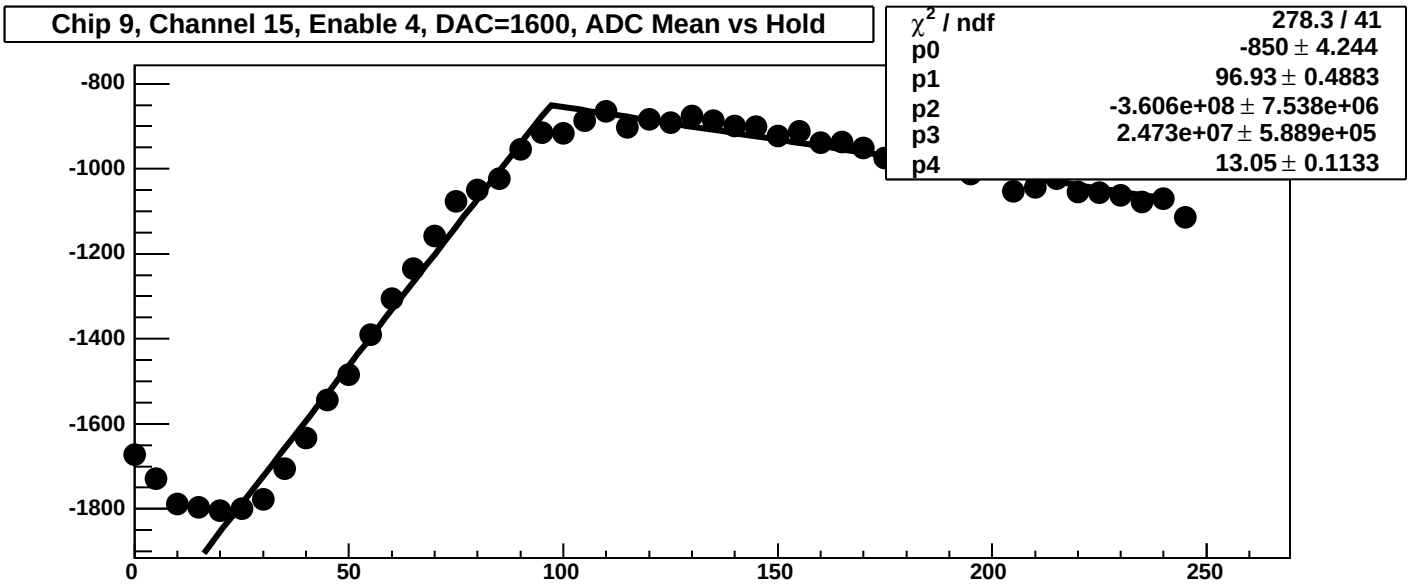

Chip 9, Channel 15, Enable 2, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250

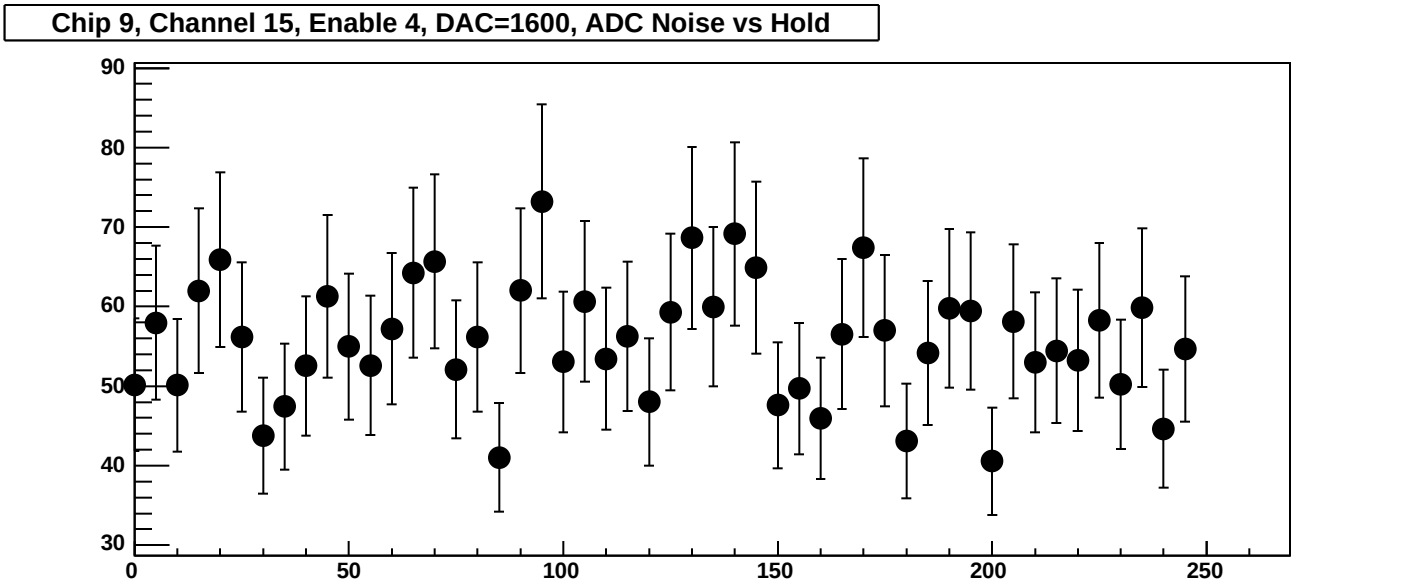

Chip 9, Channel 15, Enable 2, DAC=1600, ADC Residuals vs Hold
800
600
400
200
0
-200
0
50
100
150
200
250

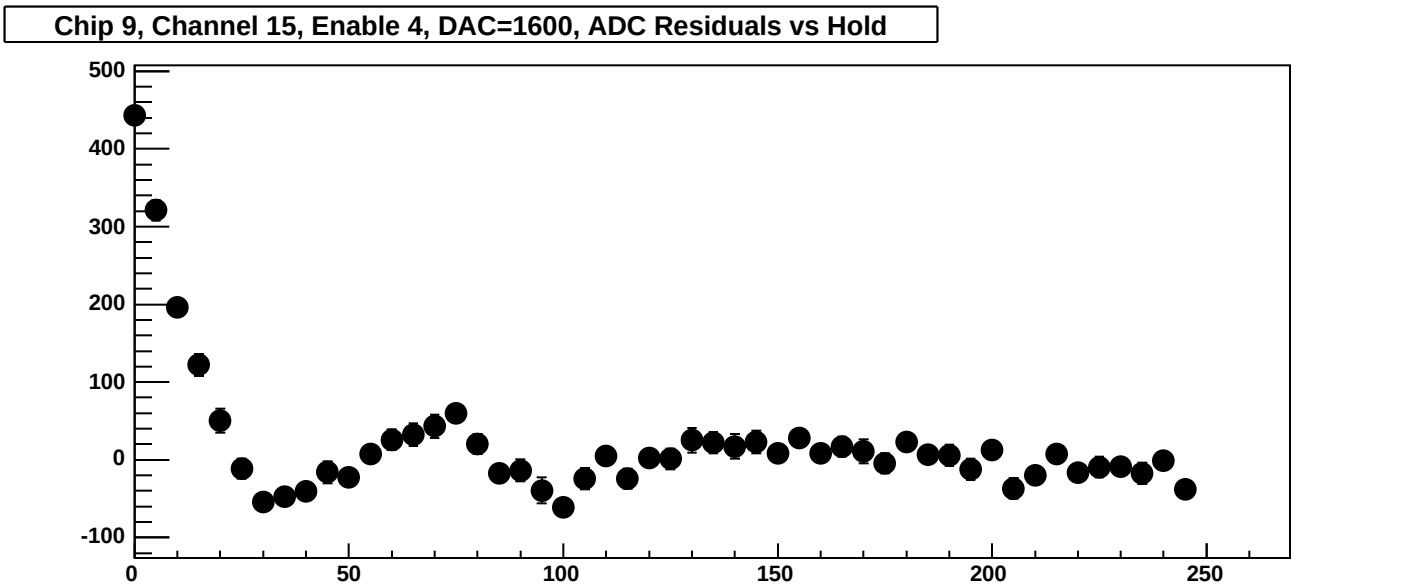

Chip 9, Channel 15, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 378.4 / 41
p0 -822 ± 5.975
p1 93.52 ± 0.2243
p2 -1.801e+09 ± 1.993e+07
p3 1.08e+08 ± 2.797e+06
p4 15.02 ± 0.1436
-800
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250


Chip 9, Channel 15, Enable 3, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250

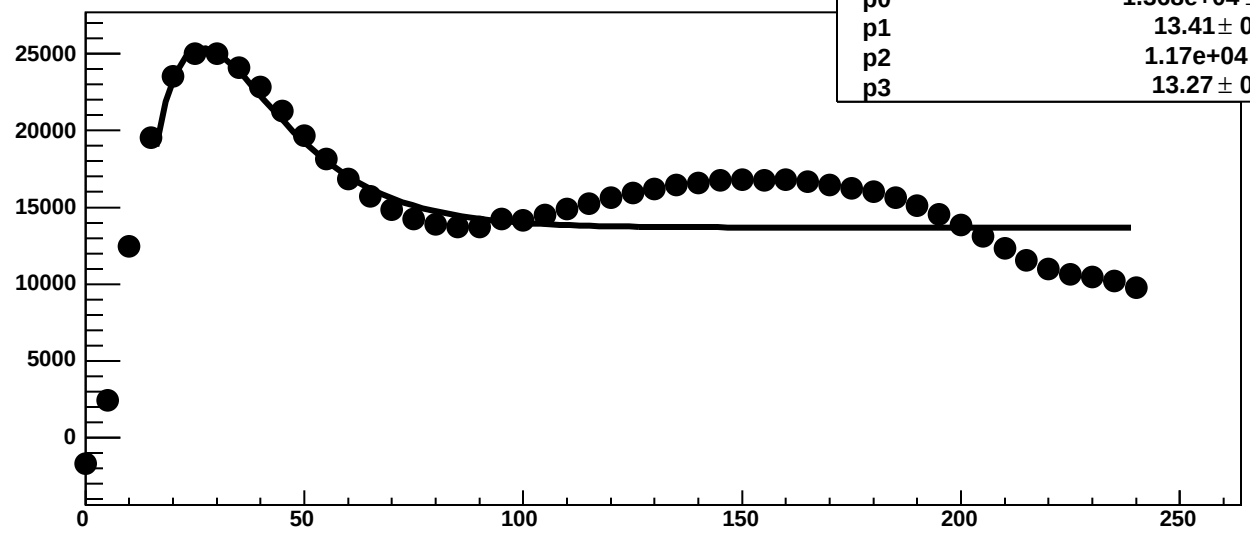

Chip 9, Channel 15, Enable 3, DAC=1600, ADC Residuals vs Hold
500
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 9, Channel 15, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 278.3 / 41
p0 -850 ± 4.244
p1 96.93 ± 0.4883
p2 -3.606e+08 ± 7.538e+06
p3 2.473e+07 ± 5.889e+05
p4 13.05 ± 0.1133
-800
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250

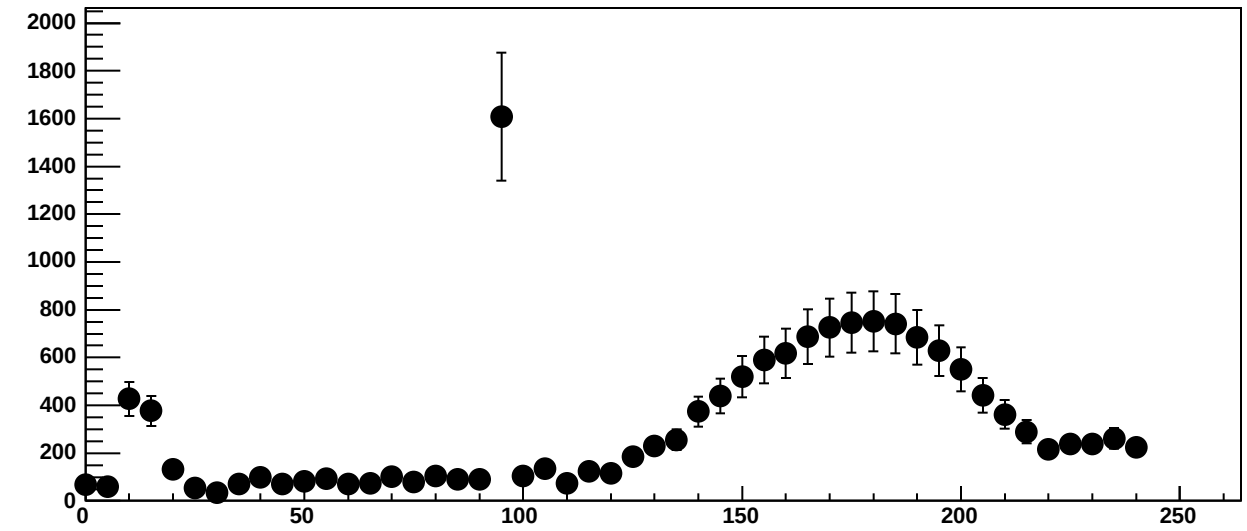

Chip 9, Channel 15, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 9, Channel 15, Enable 4, DAC=1600, ADC Residuals vs Hold
500
400
300
200
100
0
-100
0
50
100
150
200
250

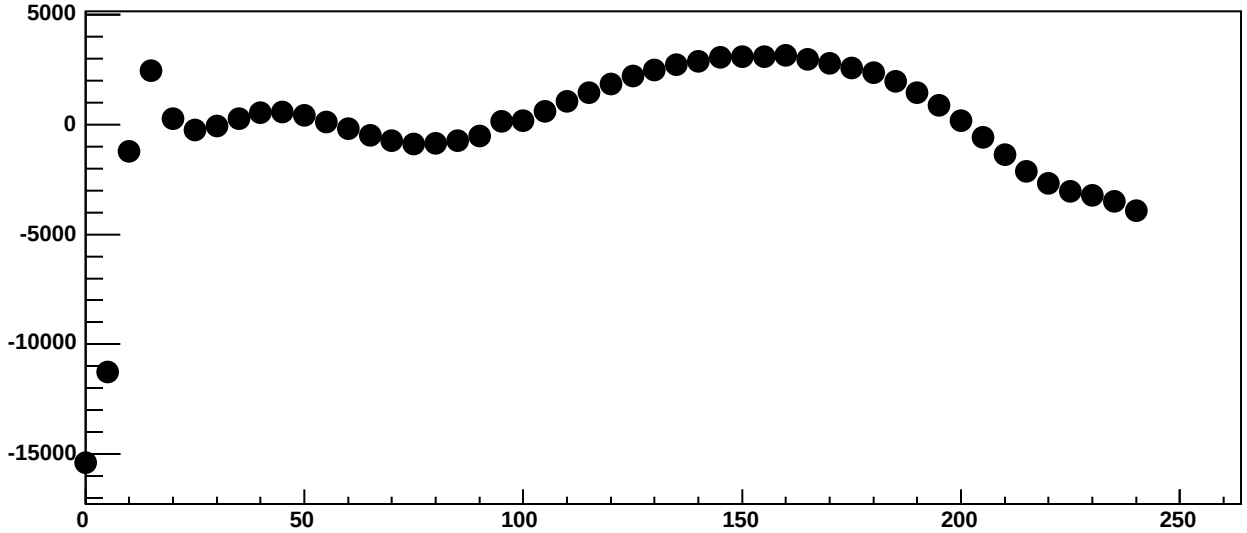
Chip 9, Channel 15, Enable 5!, DAC=1600, ADC Mean vs Hold



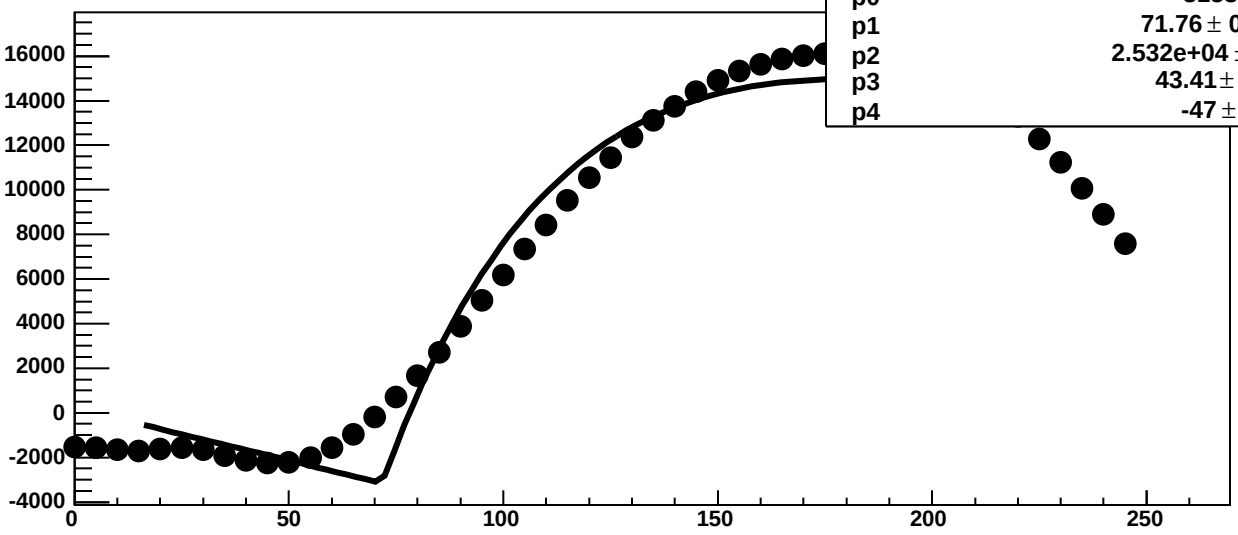
Chip 9, Channel 15, Enable 5!, DAC=1600, ADC Noise vs Hold



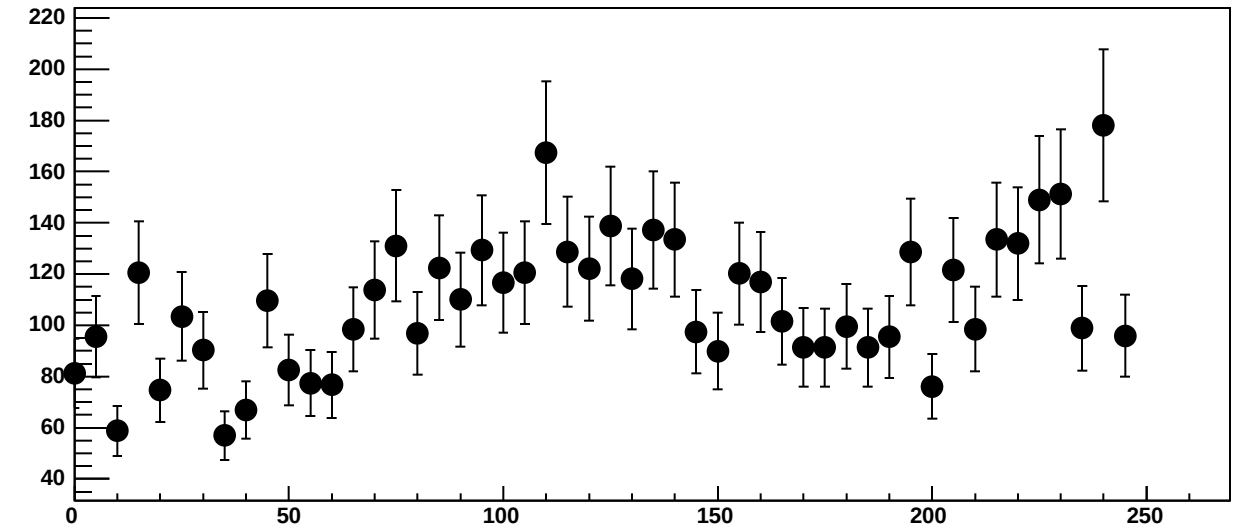
Chip 9, Channel 15, Enable 5!, DAC=1600, ADC Residuals vs Hold



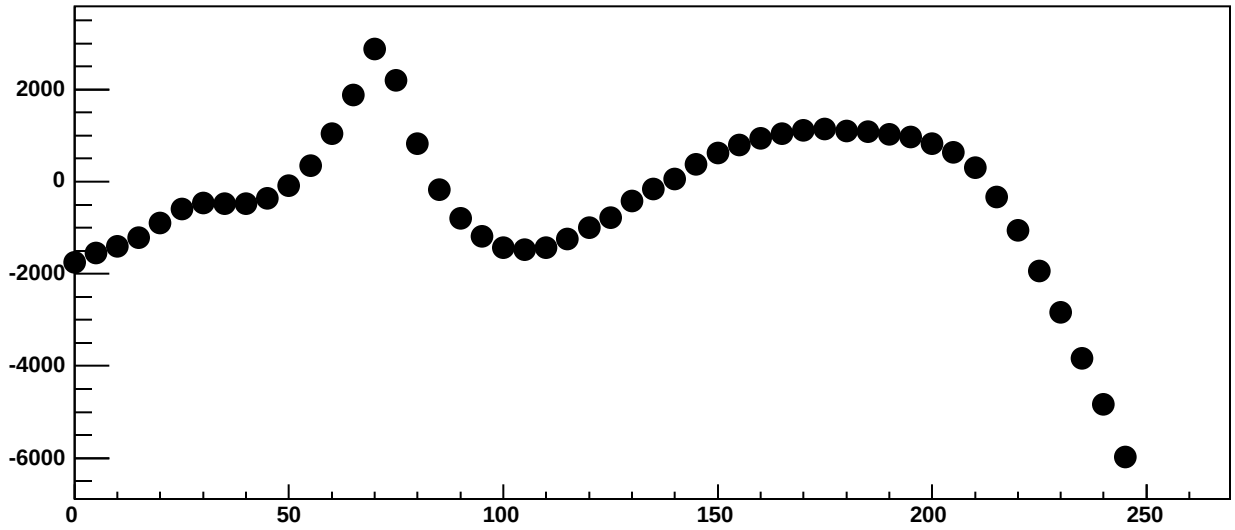
Chip 9, Channel 16, Enable 0, DAC=1600, ADC Mean vs Hold



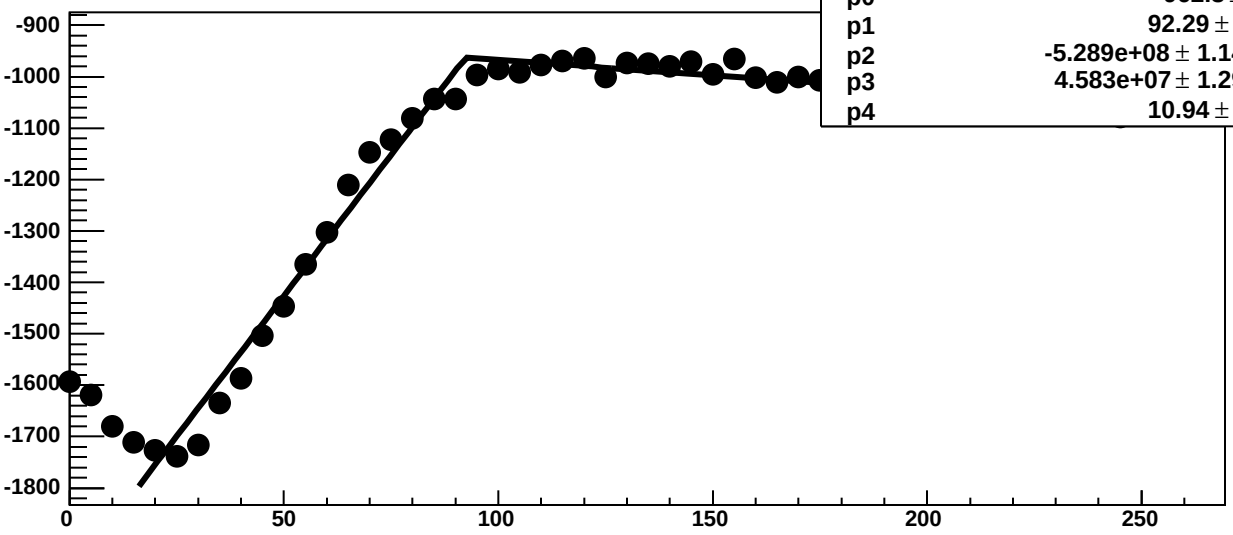
Chip 9, Channel 16, Enable 0, DAC=1600, ADC Noise vs Hold



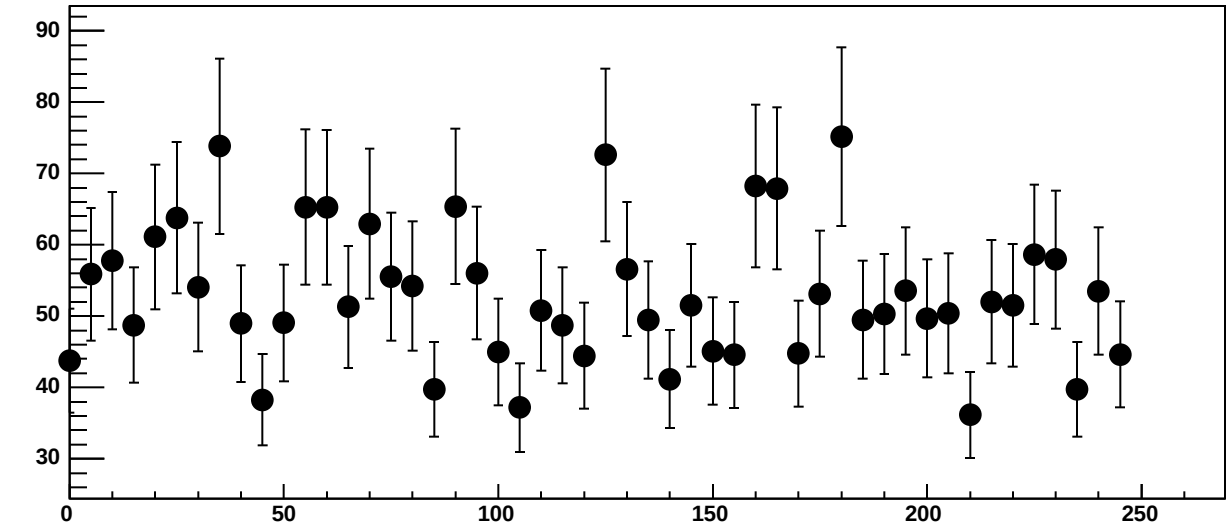
Chip 9, Channel 16, Enable 0, DAC=1600, ADC Residuals vs Hold



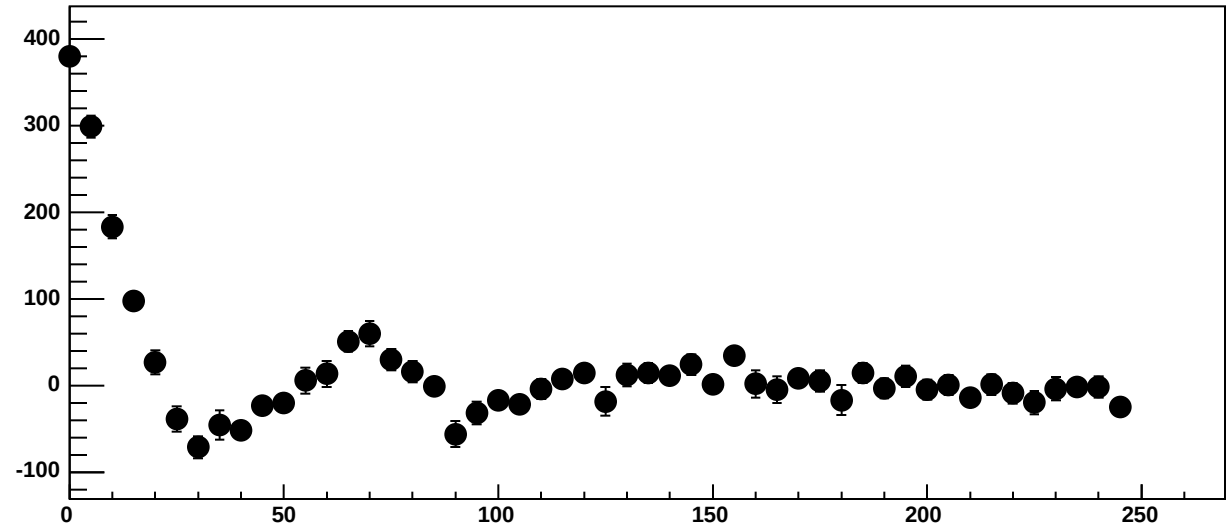
Chip 9, Channel 16, Enable 1, DAC=1600, ADC Mean vs Hold



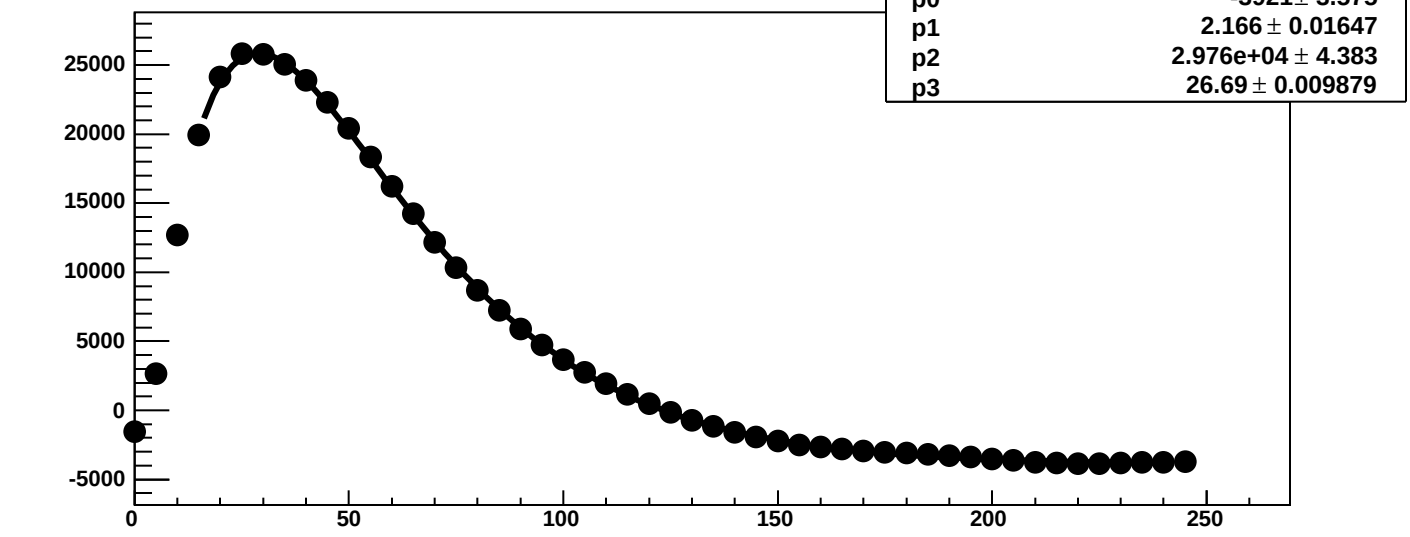
Chip 9, Channel 16, Enable 1, DAC=1600, ADC Noise vs Hold



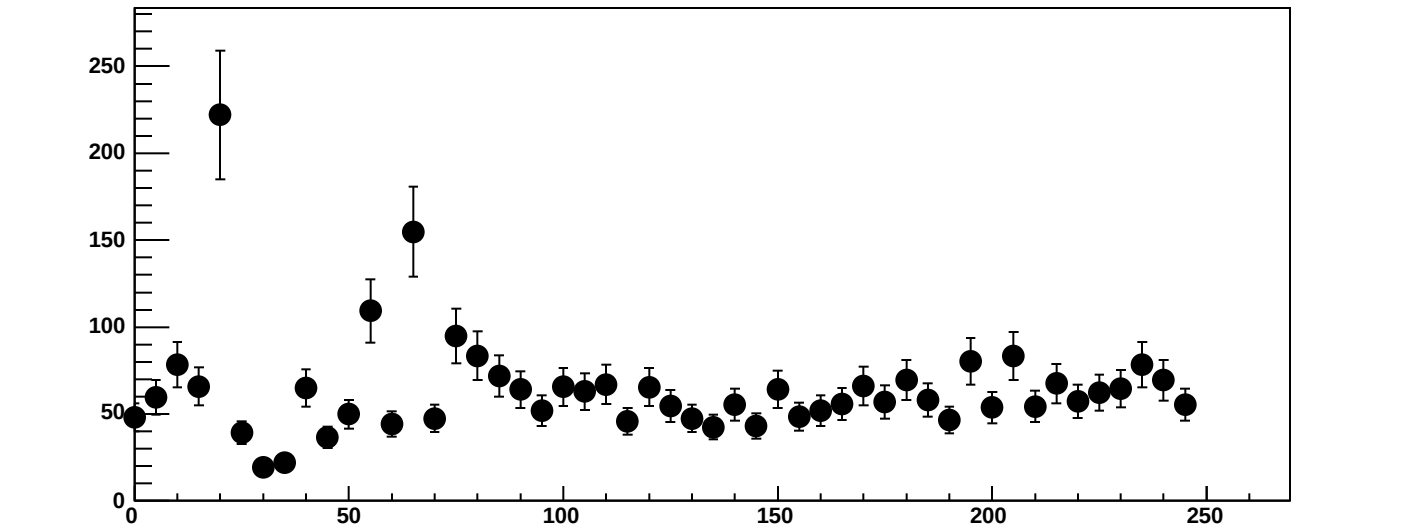
Chip 9, Channel 16, Enable 1, DAC=1600, ADC Residuals vs Hold



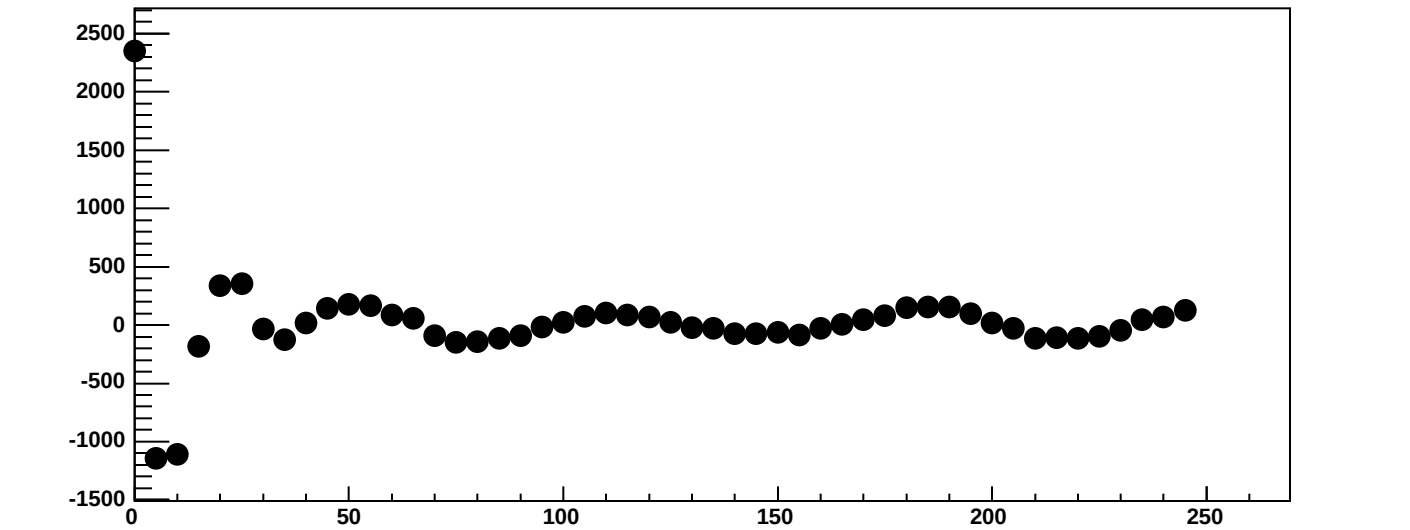
Chip 9, Channel 16, Enable 2!, DAC=1600, ADC Mean vs Hold



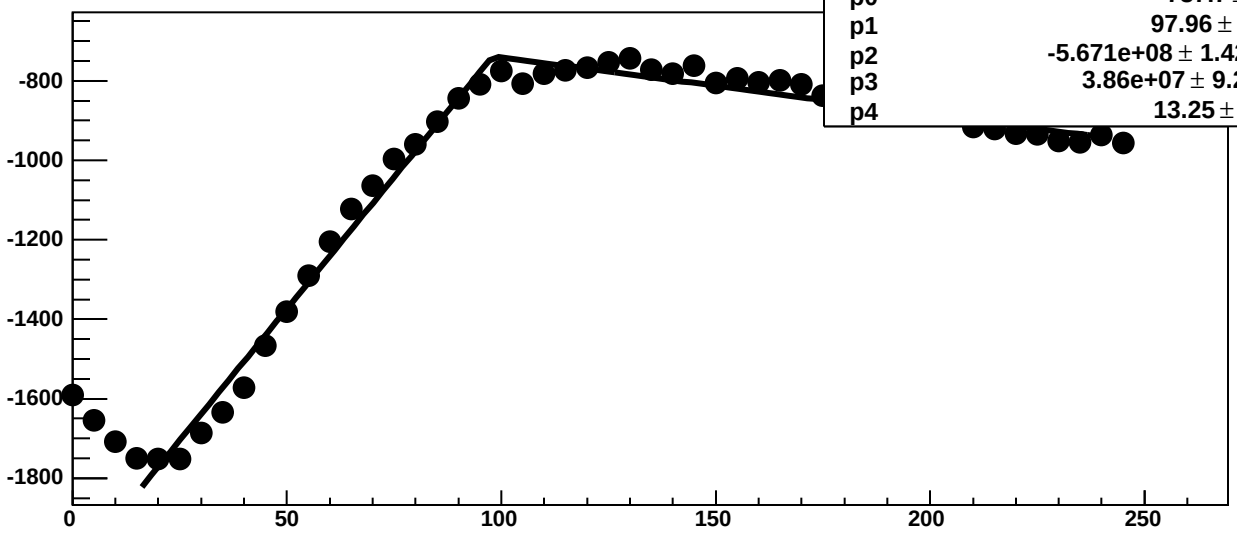
Chip 9, Channel 16, Enable 2!, DAC=1600, ADC Noise vs Hold



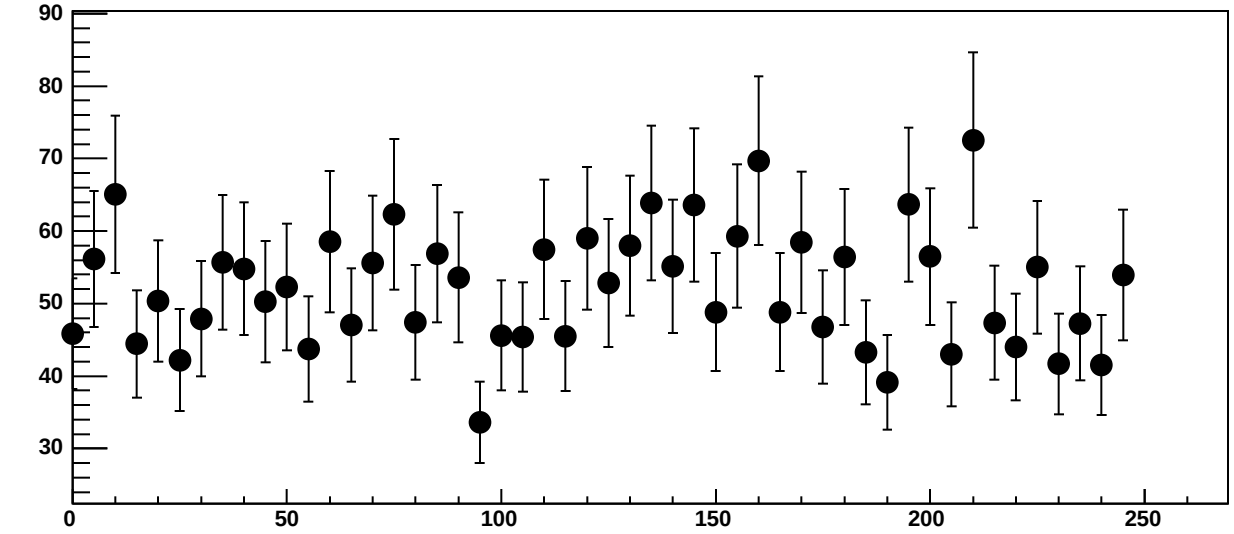
Chip 9, Channel 16, Enable 2!, DAC=1600, ADC Residuals vs Hold



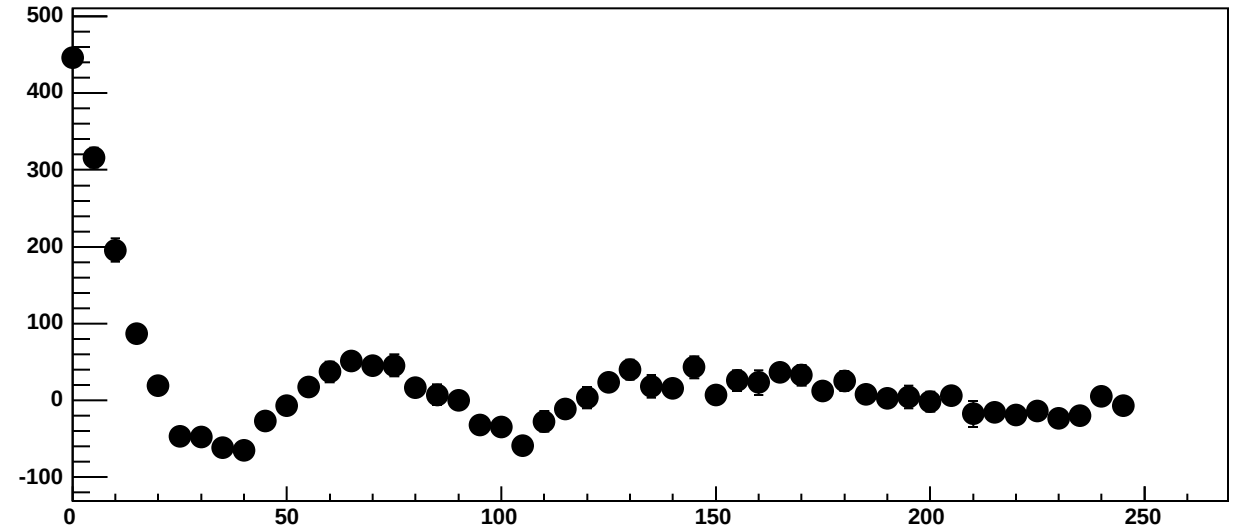
Chip 9, Channel 16, Enable 3, DAC=1600, ADC Mean vs Hold

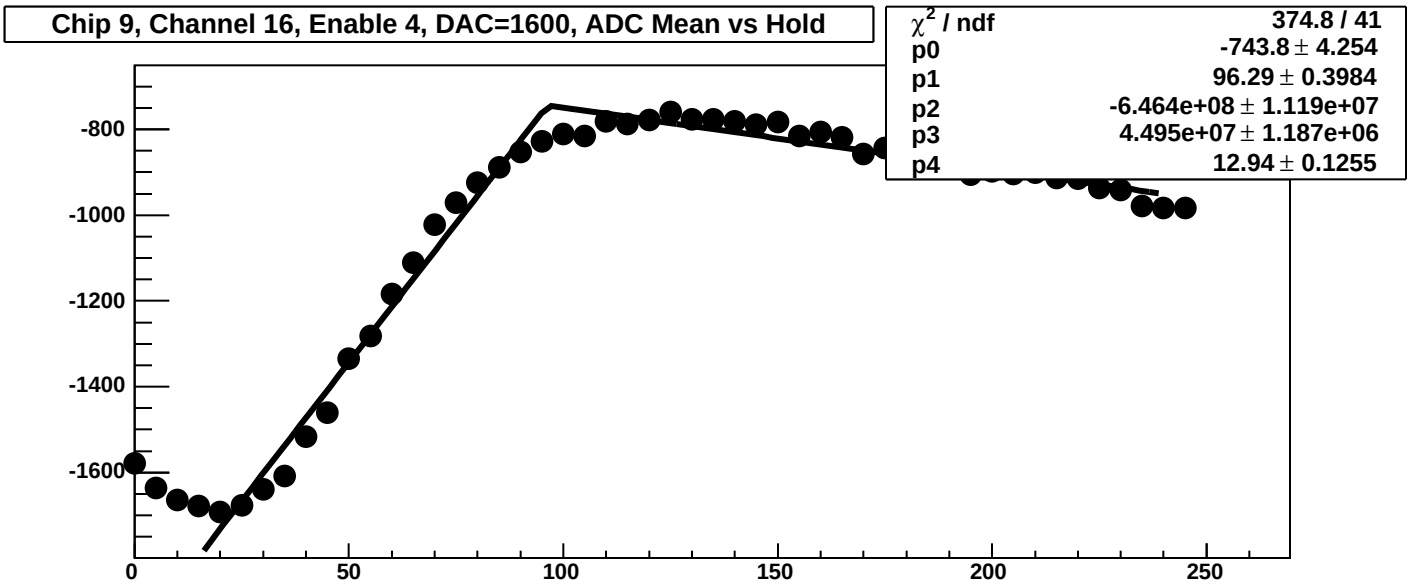


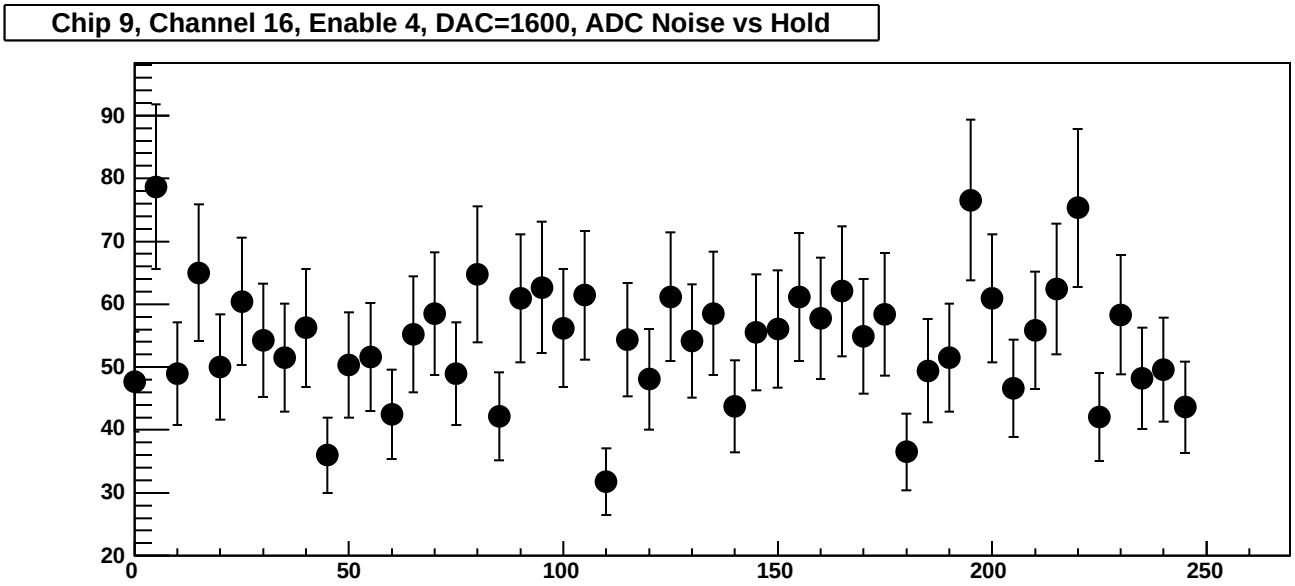
Chip 9, Channel 16, Enable 3, DAC=1600, ADC Noise vs Hold

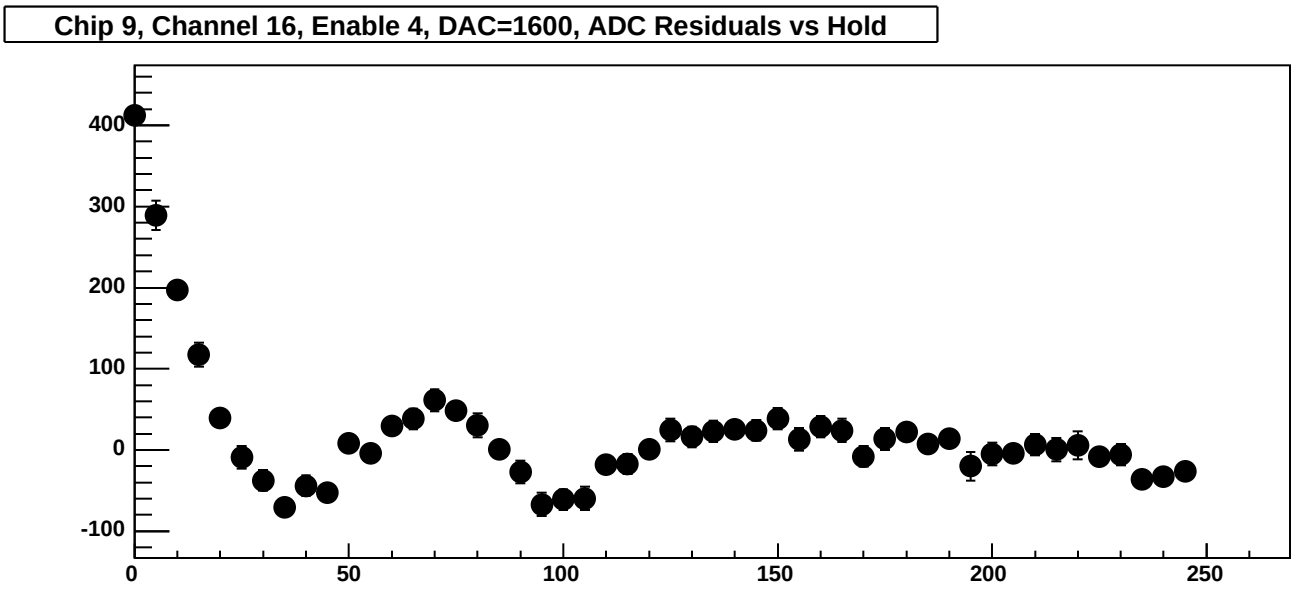


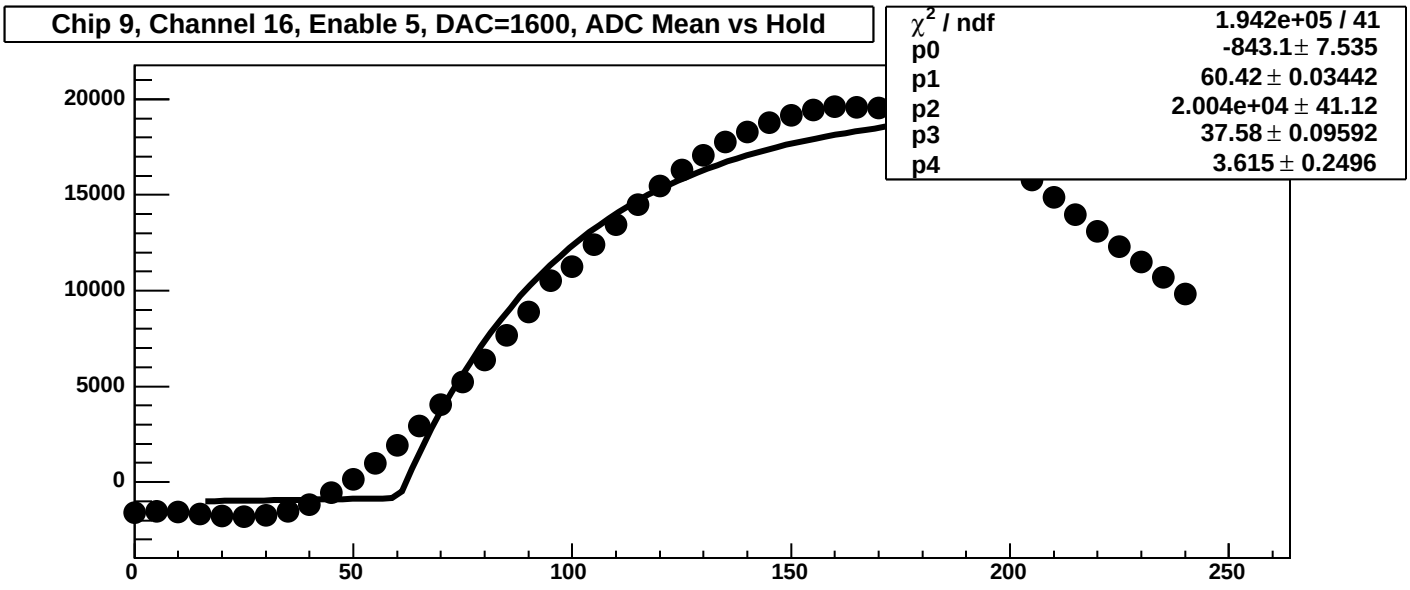
Chip 9, Channel 16, Enable 3, DAC=1600, ADC Residuals vs Hold

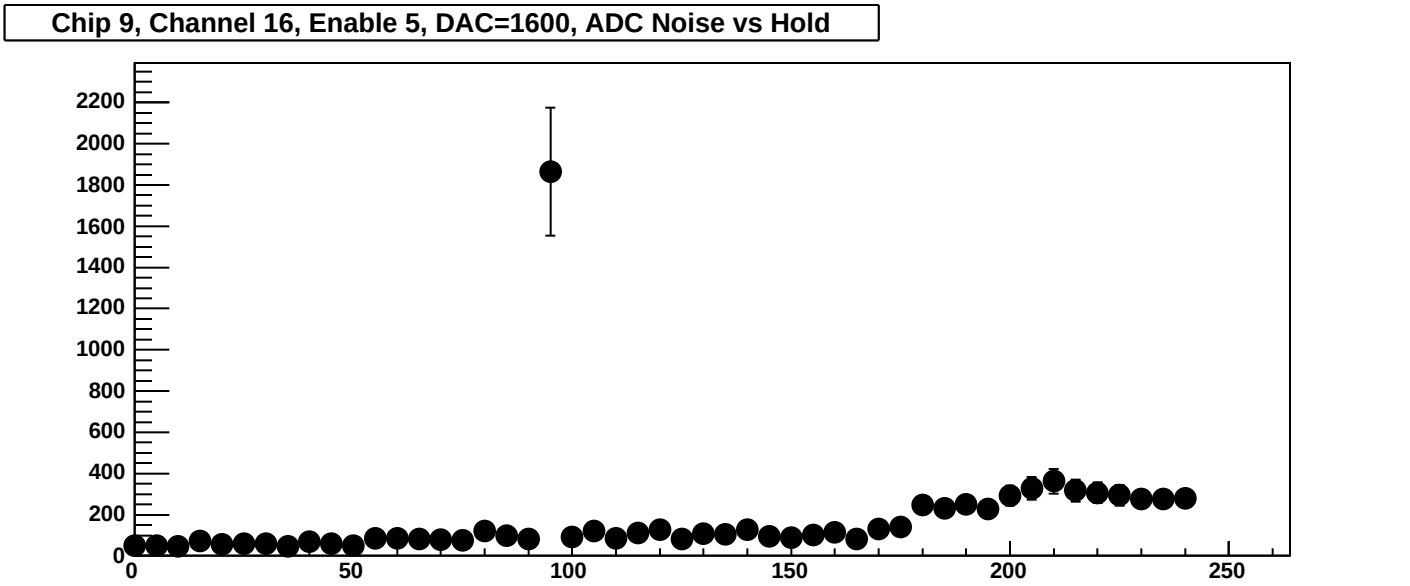


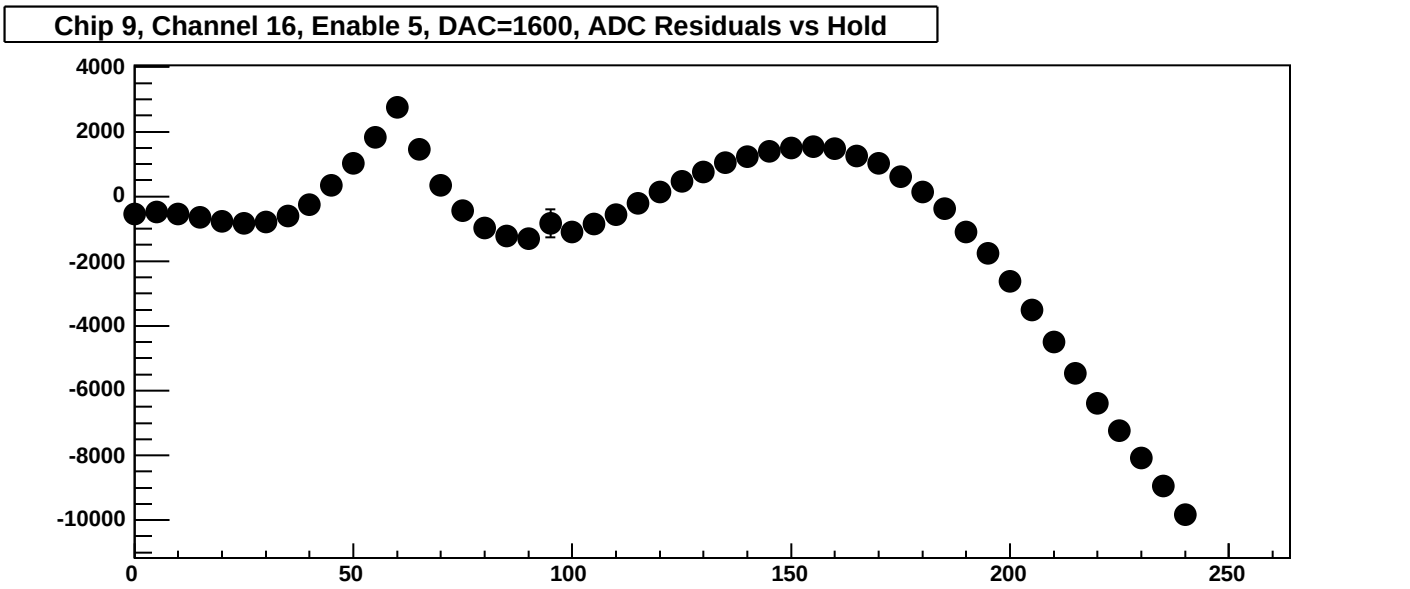

Chip 9, Channel 16, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 374.8 / 41
p0 -743.8 ± 4.254
p1 96.29 ± 0.3984
p2 -6.464e+08 ± 1.119e+07
p3 4.495e+07 ± 1.187e+06
p4 12.94 ± 0.1255
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250

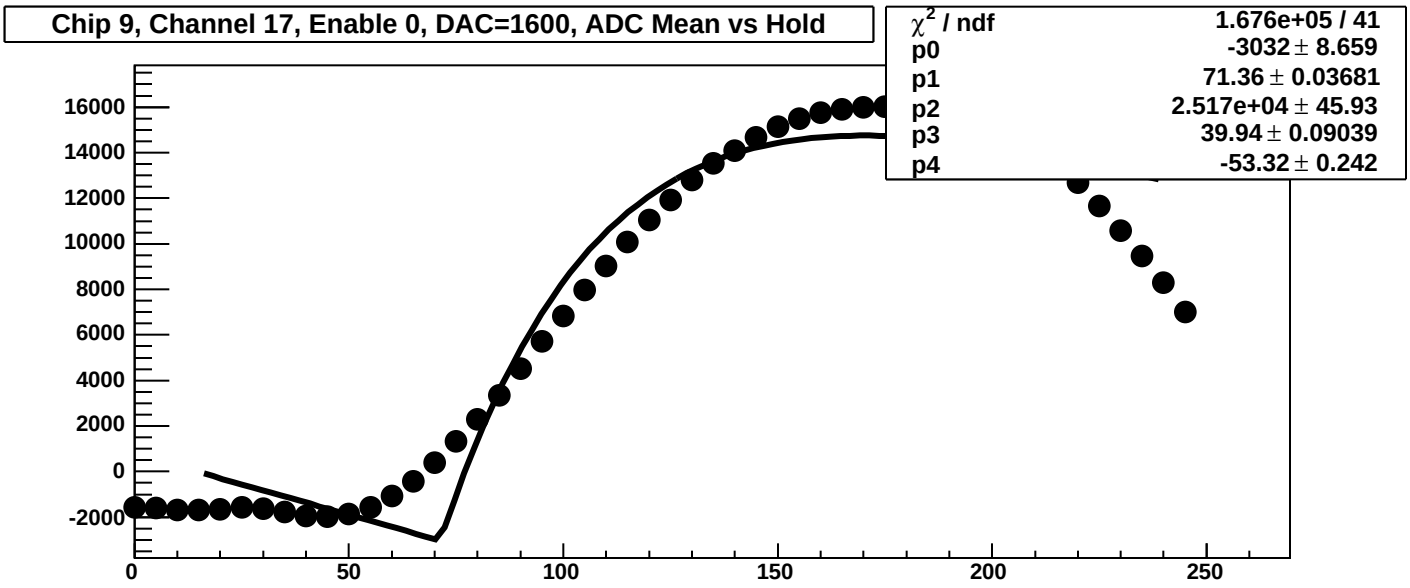

Chip 9, Channel 16, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
20
0
50
100
150
200
250

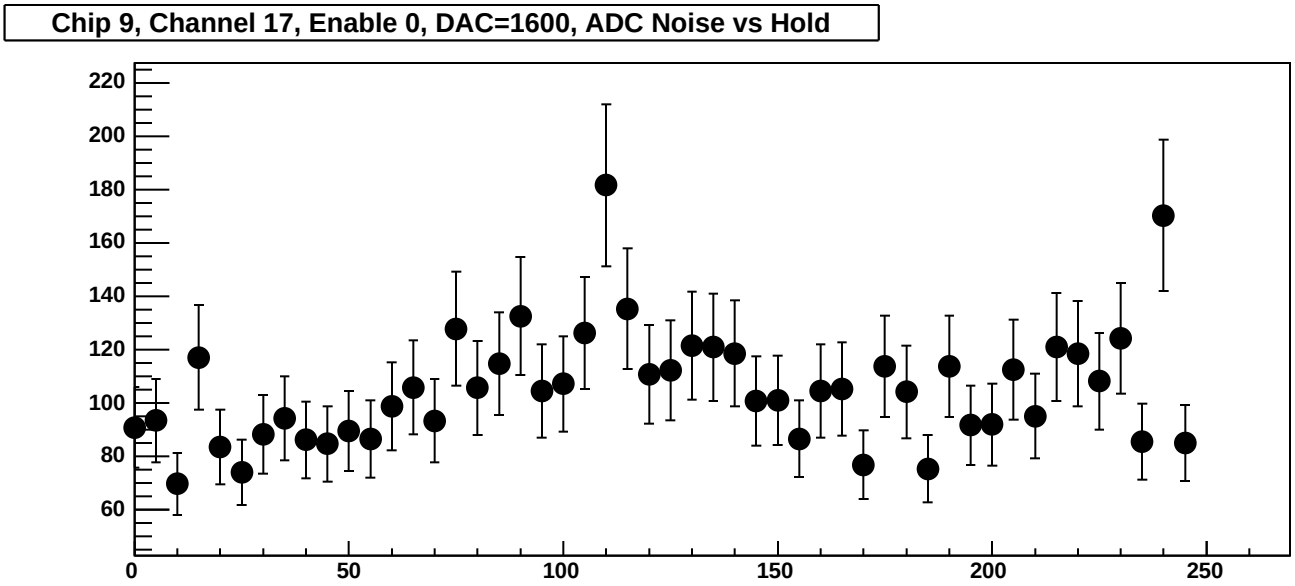

Chip 9, Channel 16, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

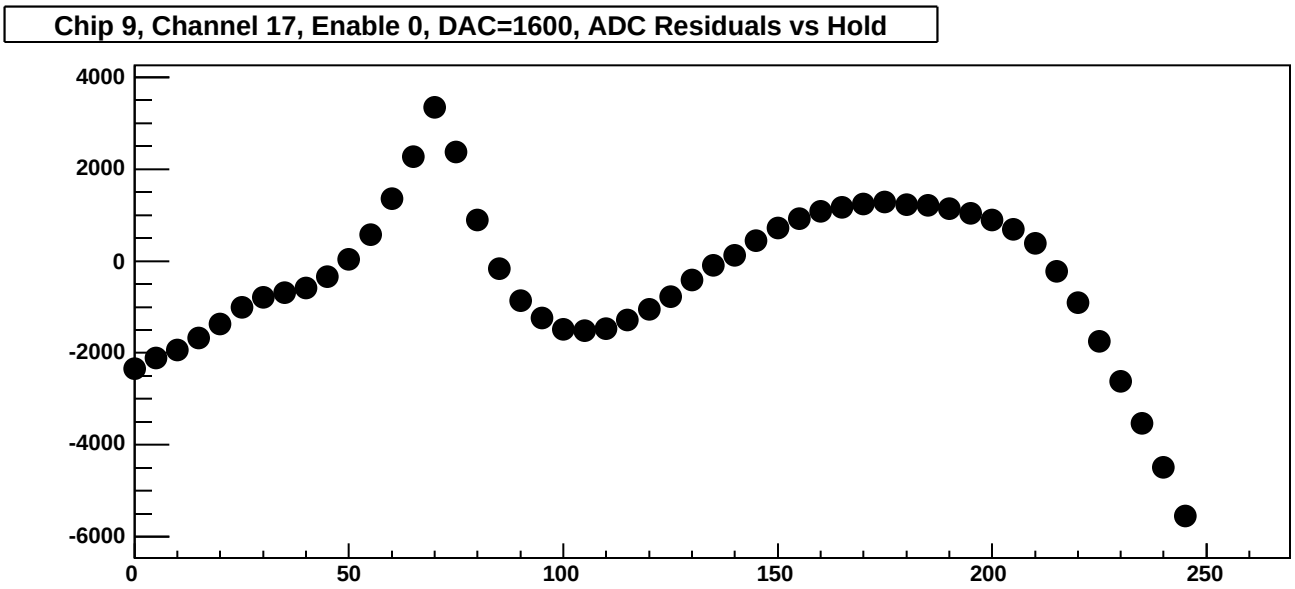

Chip 9, Channel 16, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.942e+05 / 41
p0 -843.1 ± 7.535
p1 60.42 ± 0.03442
p2 2.004e+04 ± 41.12
p3 37.58 ± 0.09592
p4 3.615 ± 0.2496
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 9, Channel 16, Enable 5, DAC=1600, ADC Noise vs Hold
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

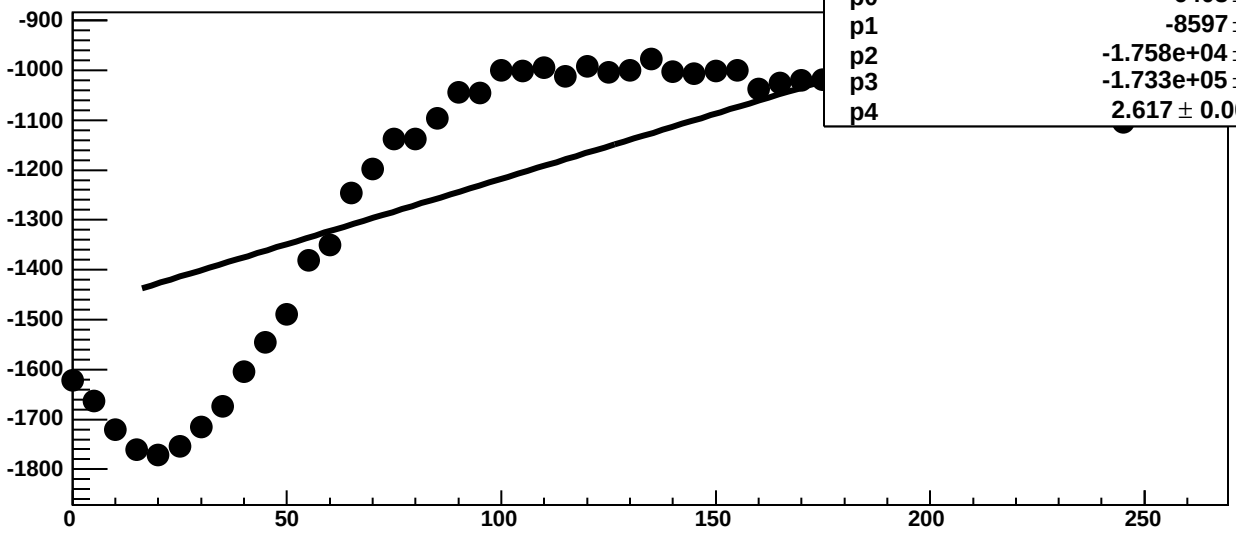

Chip 9, Channel 16, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250


Chip 9, Channel 17, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.676e+05 / 41
p0 -3032 ± 8.659
p1 71.36 ± 0.03681
p2 2.517e+04 ± 45.93
p3 39.94 ± 0.09039
p4 -53.32 ± 0.242
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

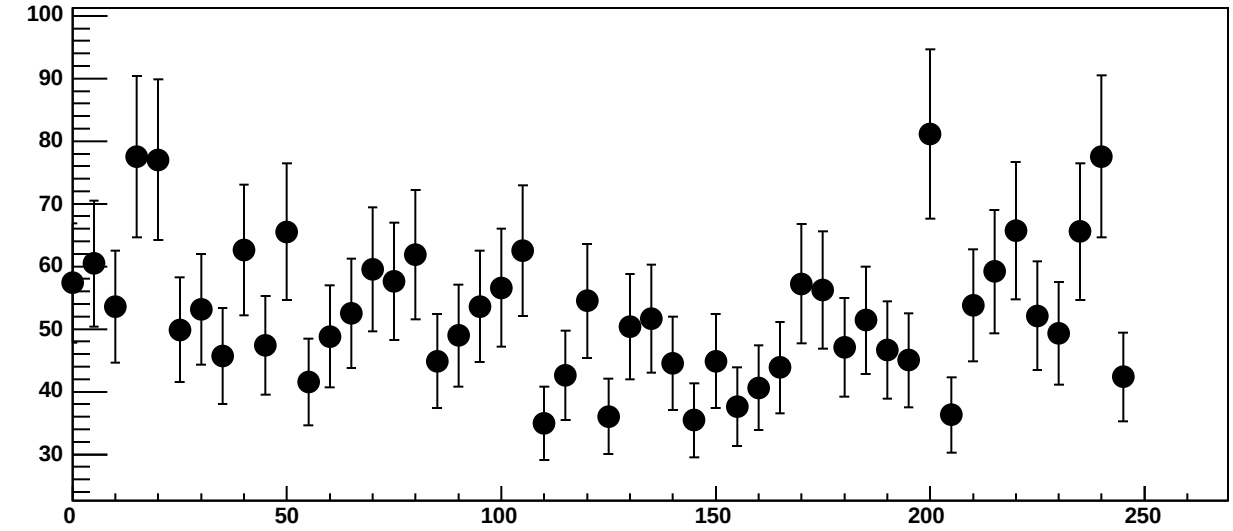

Chip 9, Channel 17, Enable 0, DAC=1600, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
0
50
100
150
200
250


Chip 9, Channel 17, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

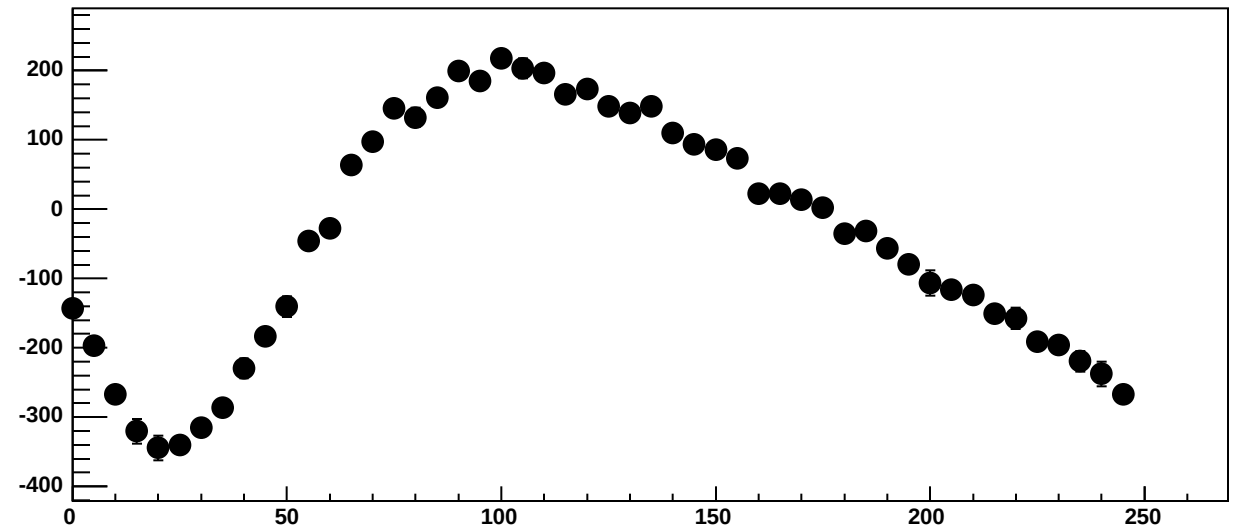
Chip 9, Channel 17, Enable 1, DAC=1600, ADC Mean vs Hold

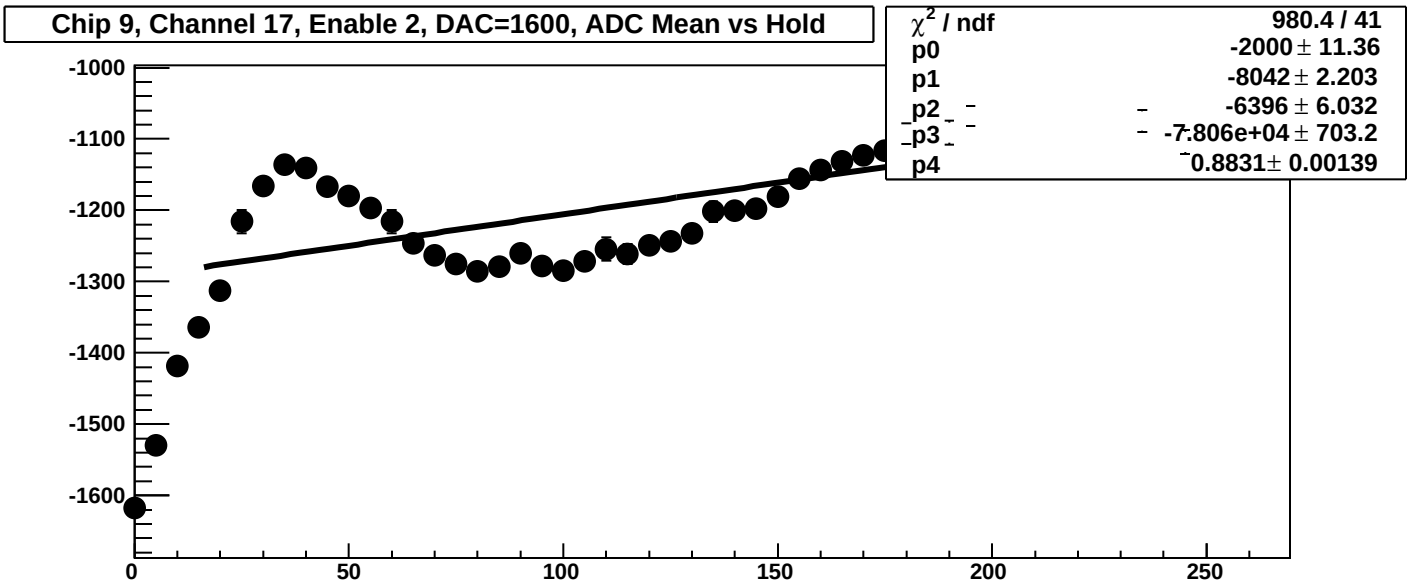


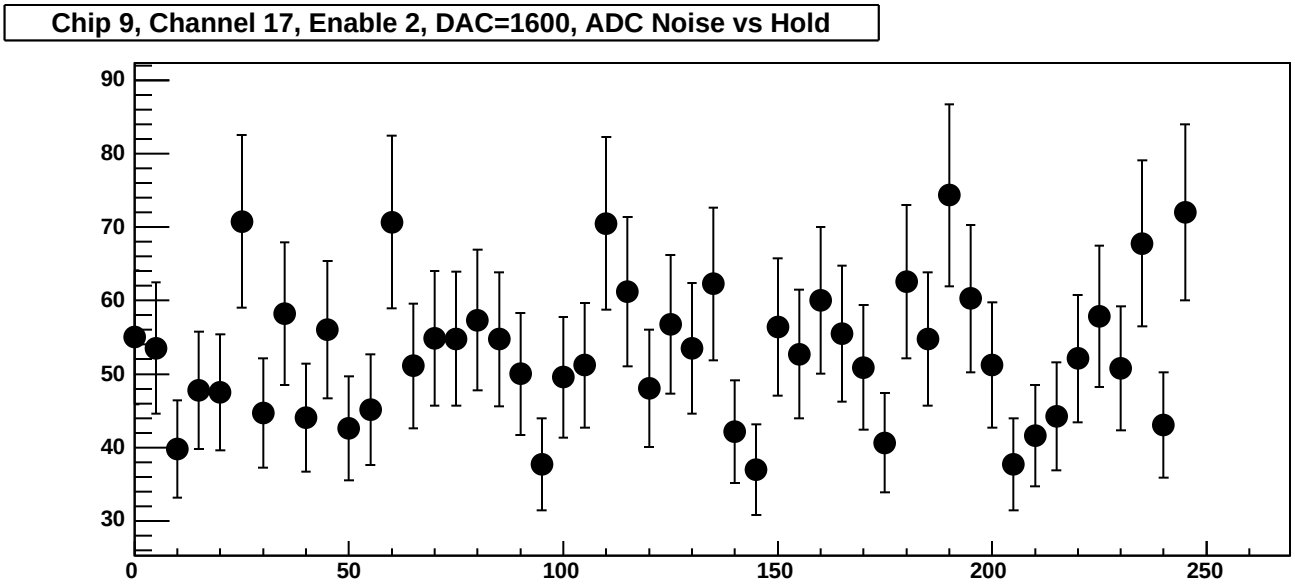
Chip 9, Channel 17, Enable 1, DAC=1600, ADC Noise vs Hold

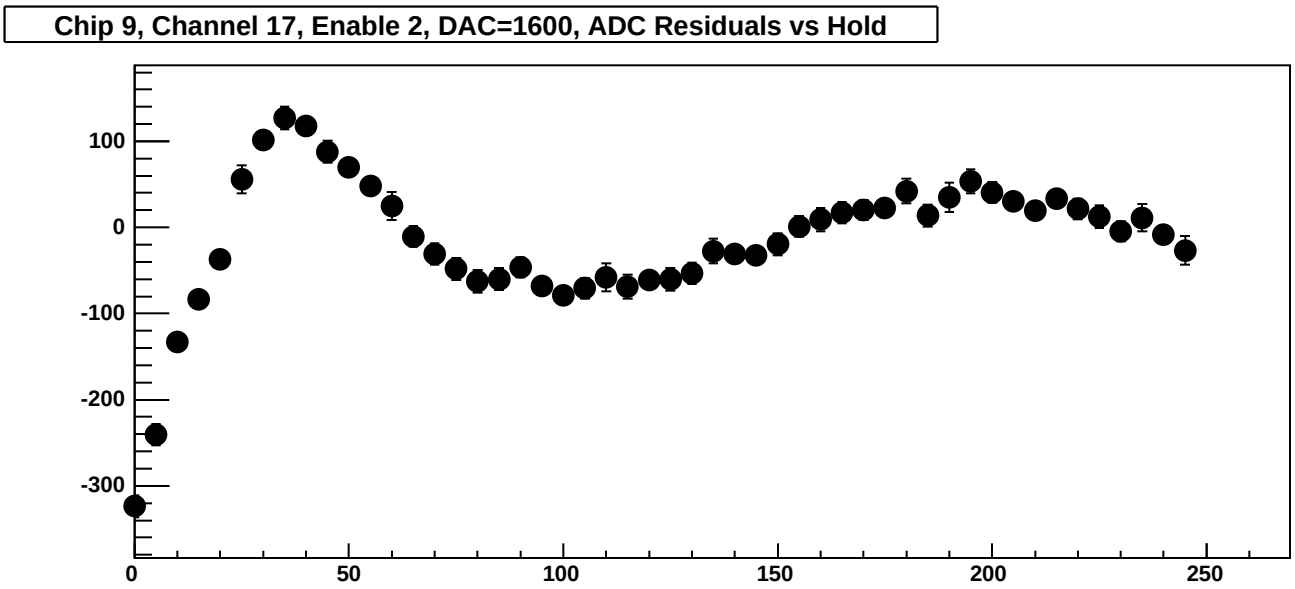


Chip 9, Channel 17, Enable 1, DAC=1600, ADC Residuals vs Hold

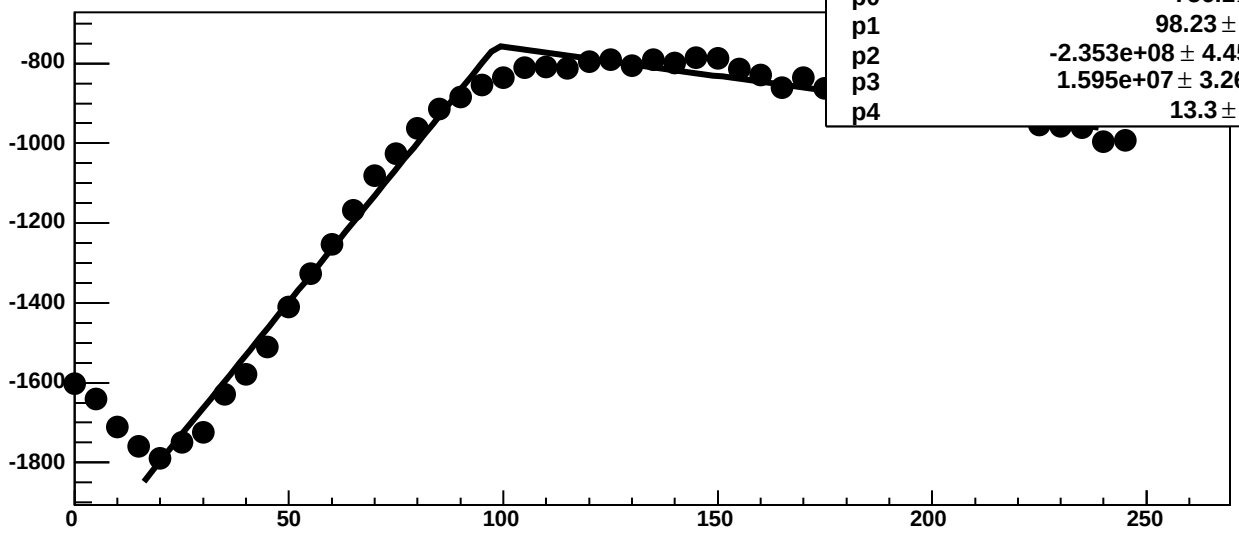



Chip 9, Channel 17, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 980.4 / 41
p0 -2000 ± 11.36
p1 -8042 ± 2.203
p2 -6396 ± 6.032
p3 -7.806e+04 ± 703.2
p4 0.8831 ± 0.00139
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
50
100
150
200
250

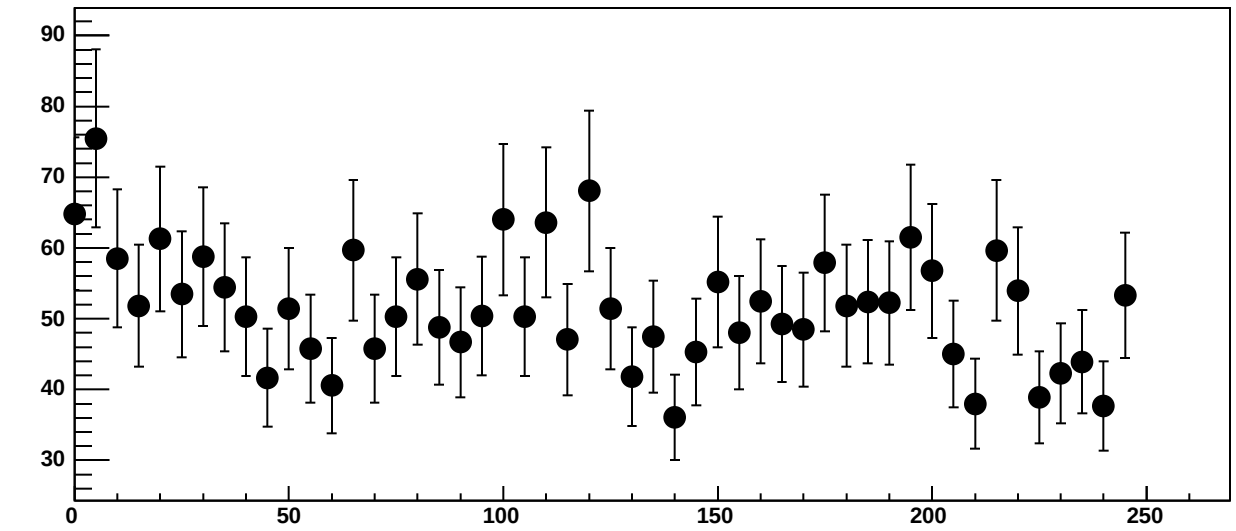

Chip 9, Channel 17, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 9, Channel 17, Enable 2, DAC=1600, ADC Residuals vs Hold
100
0
-100
-200
-300
0
50
100
150
200
250

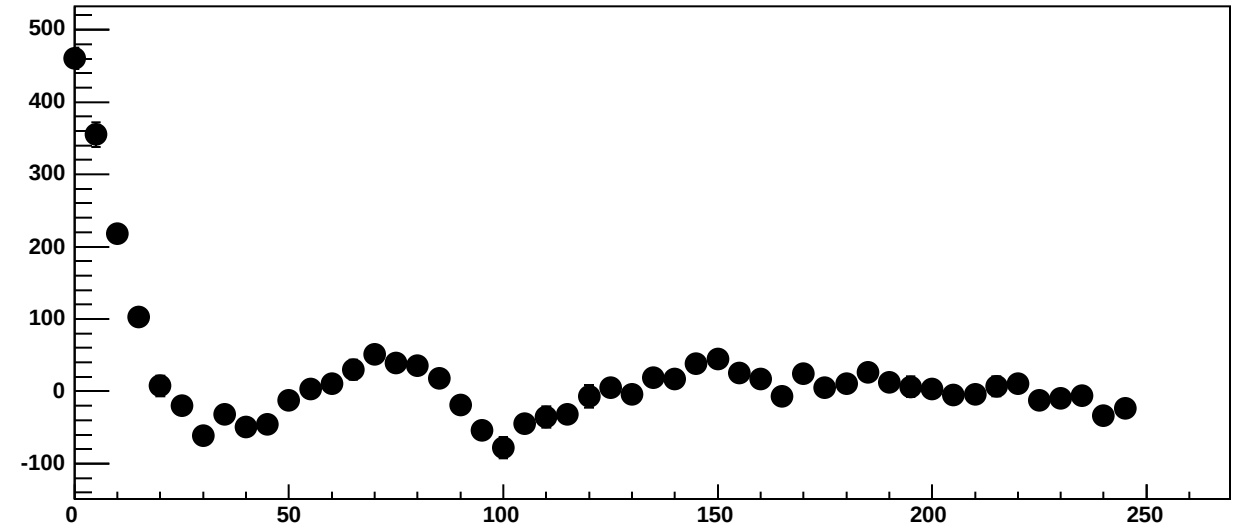
Chip 9, Channel 17, Enable 3, DAC=1600, ADC Mean vs Hold



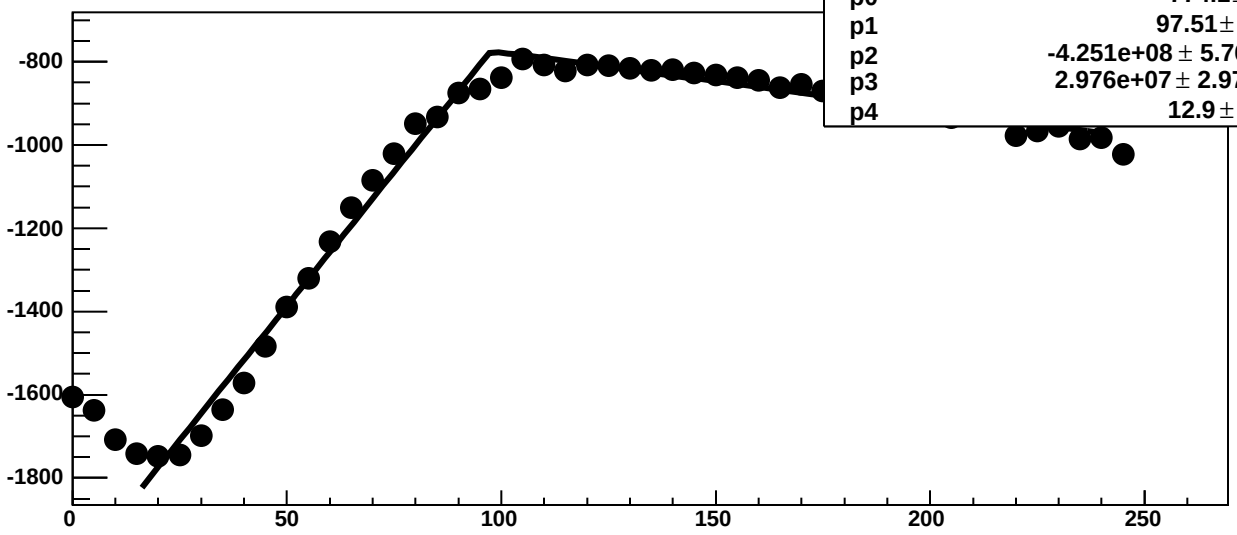
Chip 9, Channel 17, Enable 3, DAC=1600, ADC Noise vs Hold



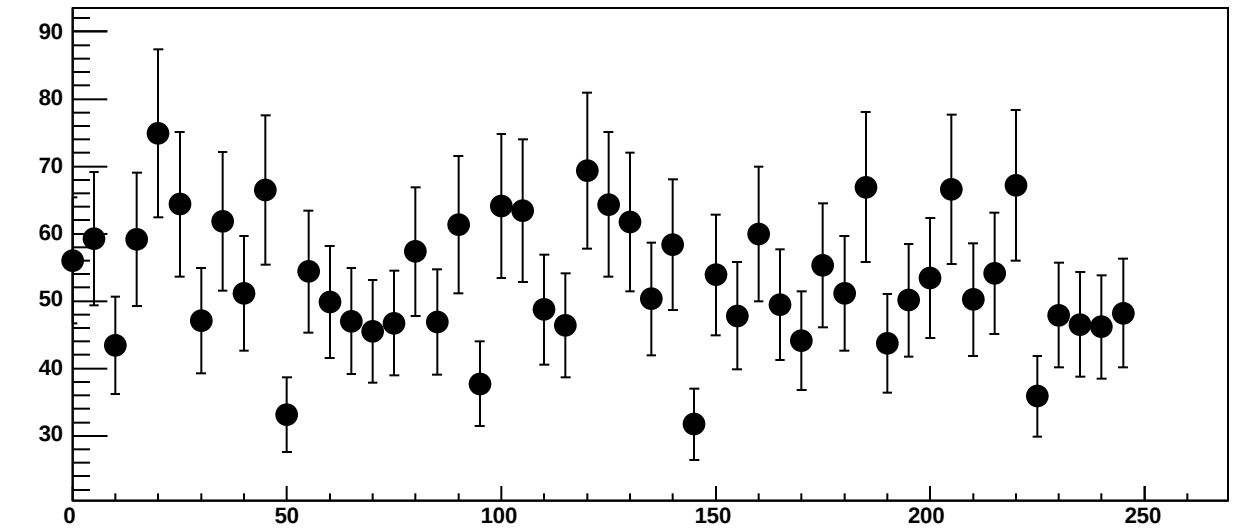
Chip 9, Channel 17, Enable 3, DAC=1600, ADC Residuals vs Hold



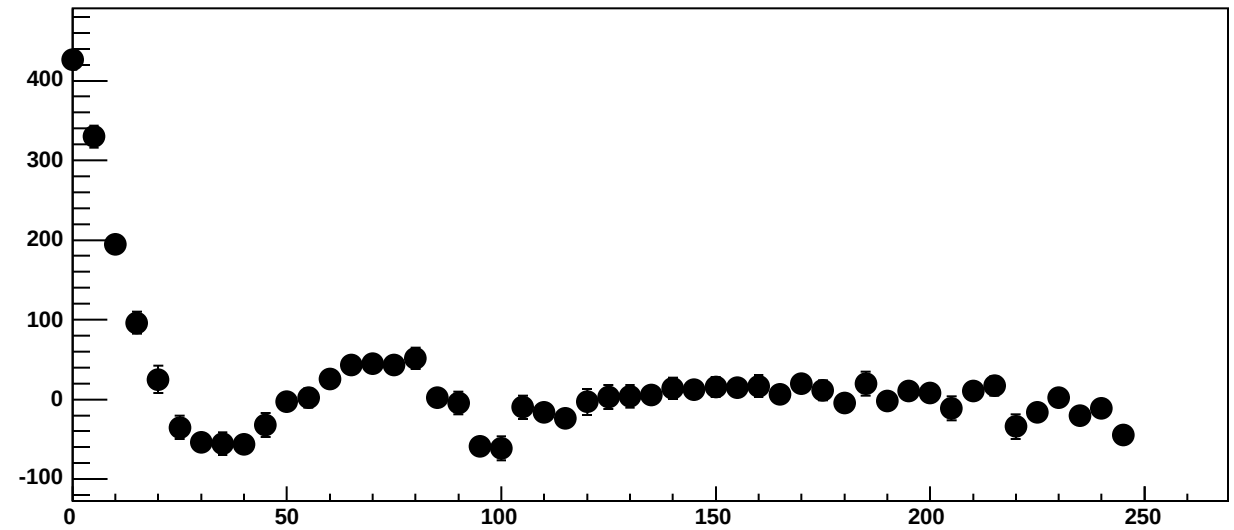
Chip 9, Channel 17, Enable 4, DAC=1600, ADC Mean vs Hold

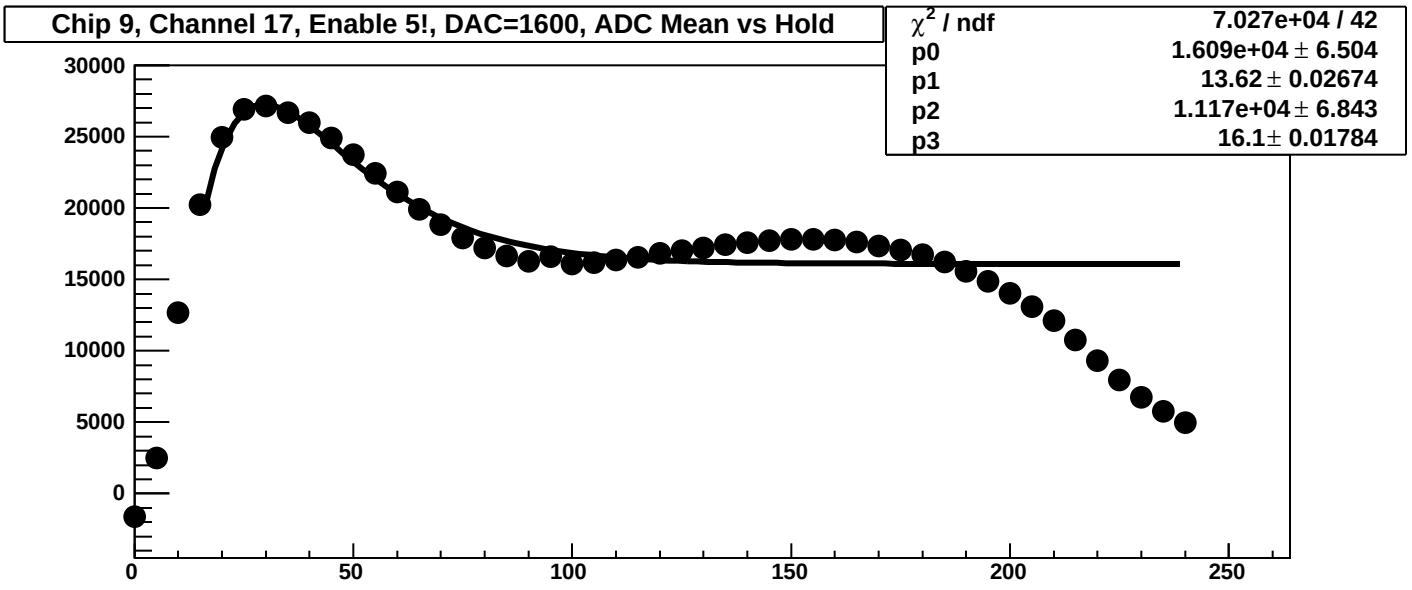


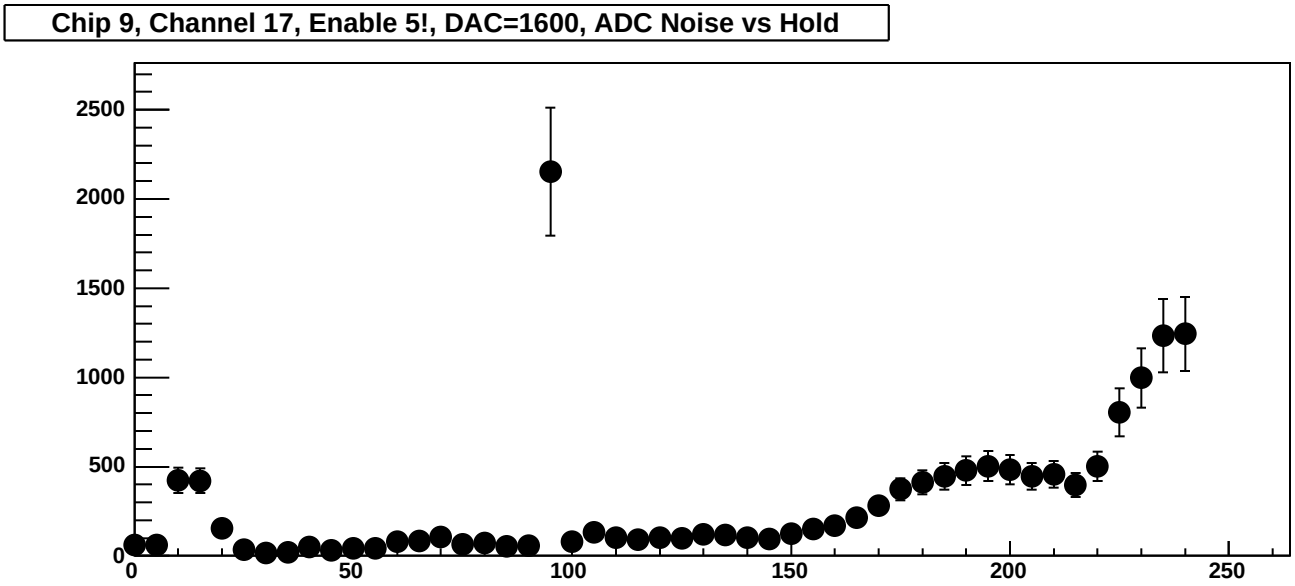
Chip 9, Channel 17, Enable 4, DAC=1600, ADC Noise vs Hold

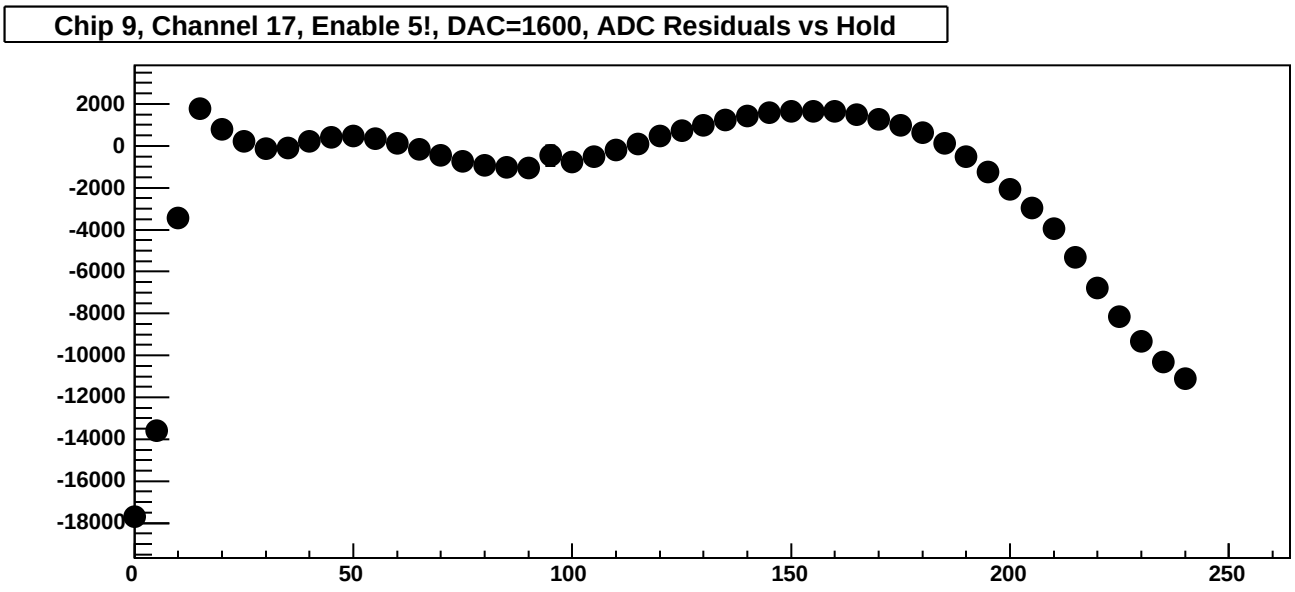


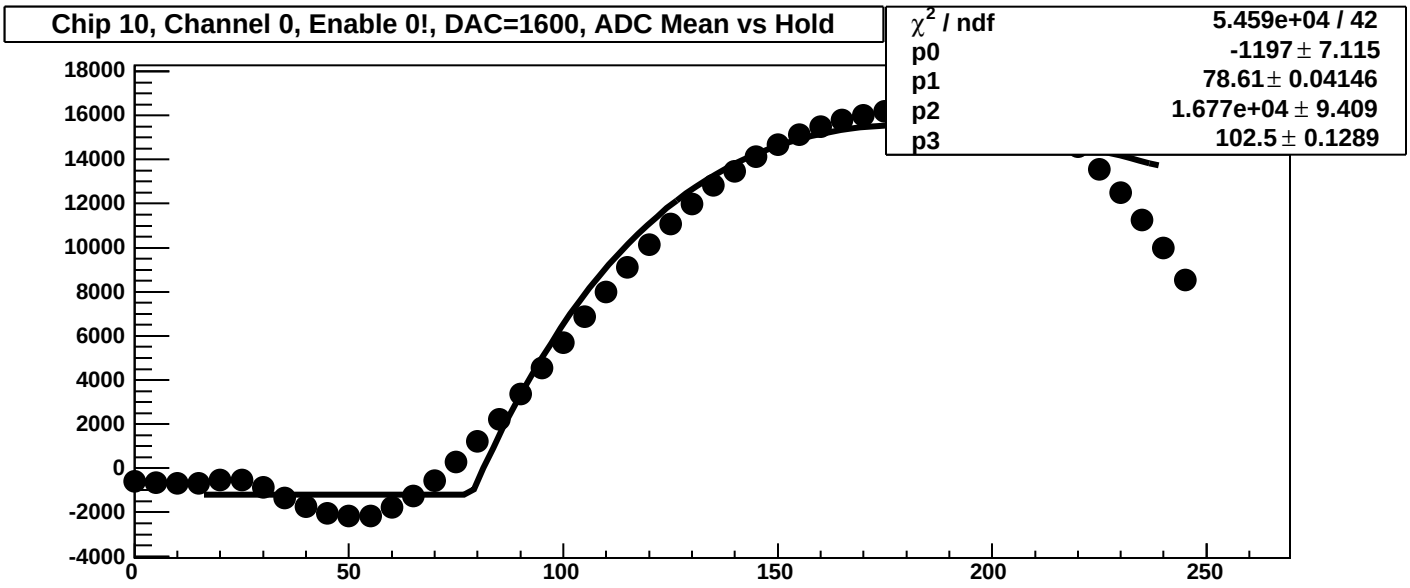
Chip 9, Channel 17, Enable 4, DAC=1600, ADC Residuals vs Hold

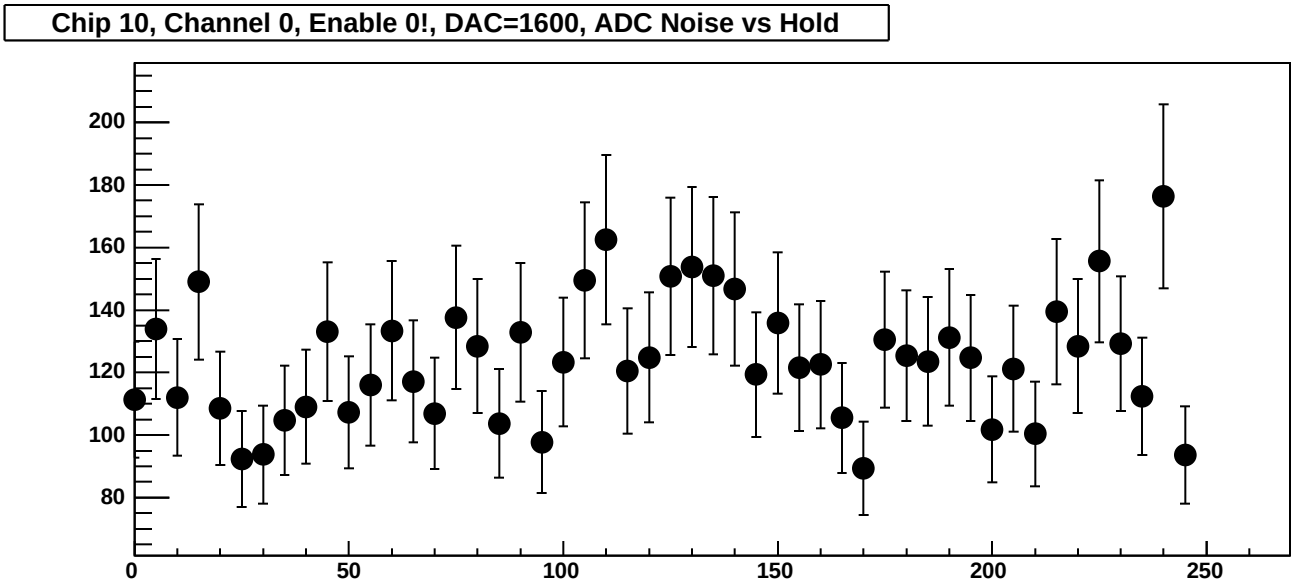


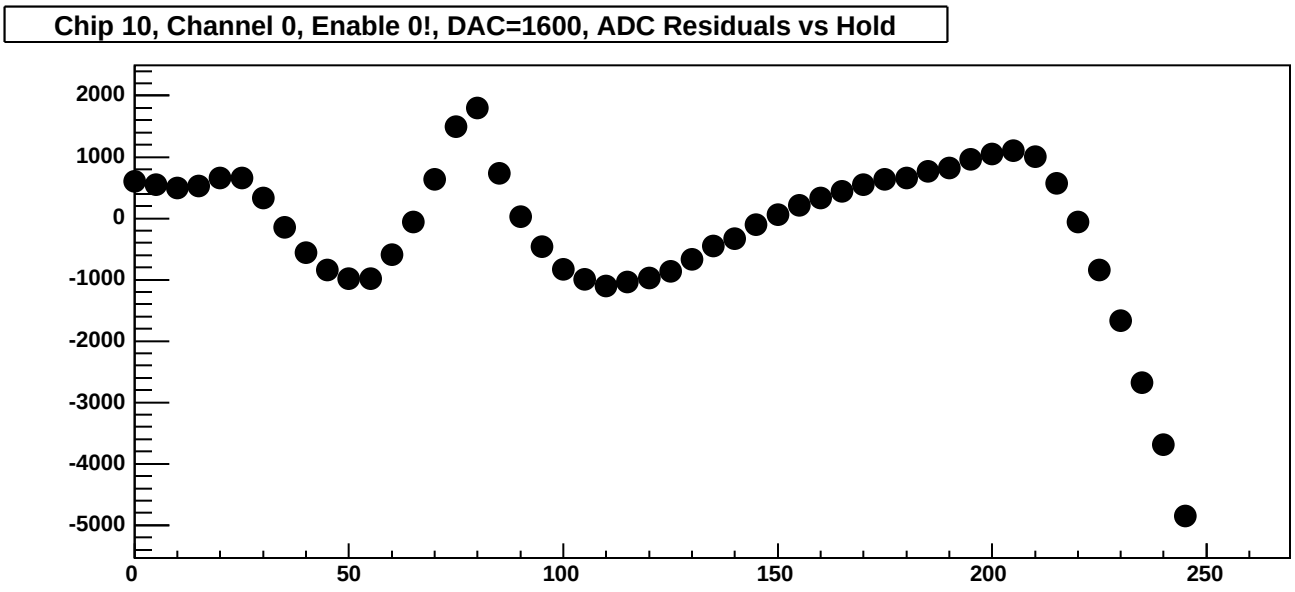

Chip 9, Channel 17, Enable 5!, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.027e+04 / 42
p0 1.609e+04 ± 6.504
p1 13.62 ± 0.02674
p2 1.117e+04 ± 6.843
p3 16.1 ± 0.01784
30000
25000
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 9, Channel 17, Enable 5!, DAC=1600, ADC Noise vs Hold
2500
2000
1500
1000
500
0
0
50
100
150
200
250

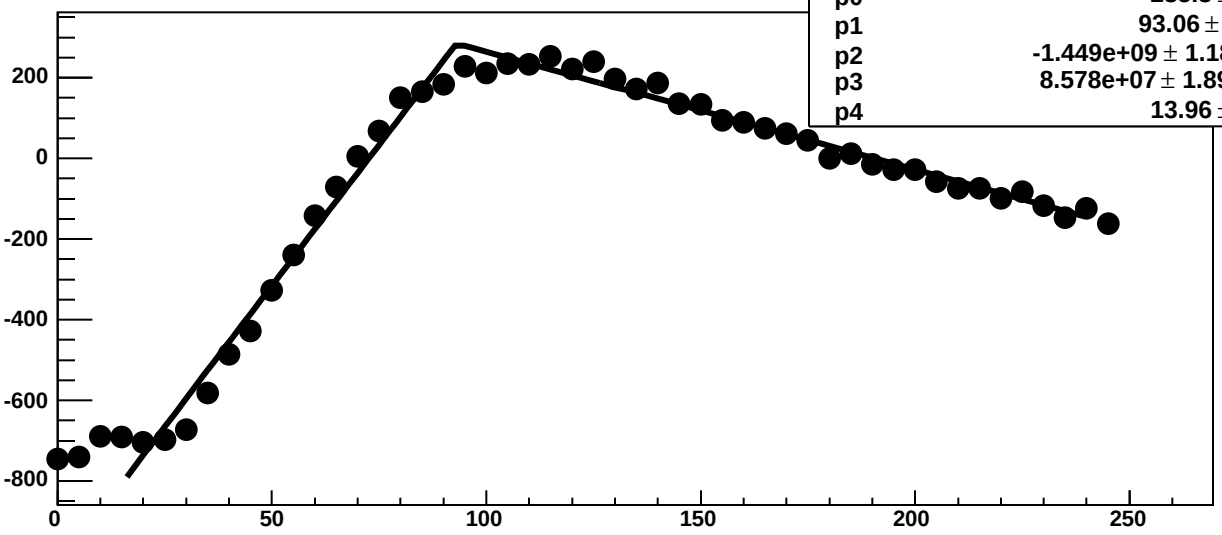

Chip 9, Channel 17, Enable 5!, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
-18000
0
50
100
150
200
250


Chip 10, Channel 0, Enable 0!, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.459e+04 / 42
p0 -1197 ± 7.115
p1 78.61 ± 0.04146
p2 1.677e+04 ± 9.409
p3 102.5 ± 0.1289
18000
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
-4000
0
50
100
150
200
250

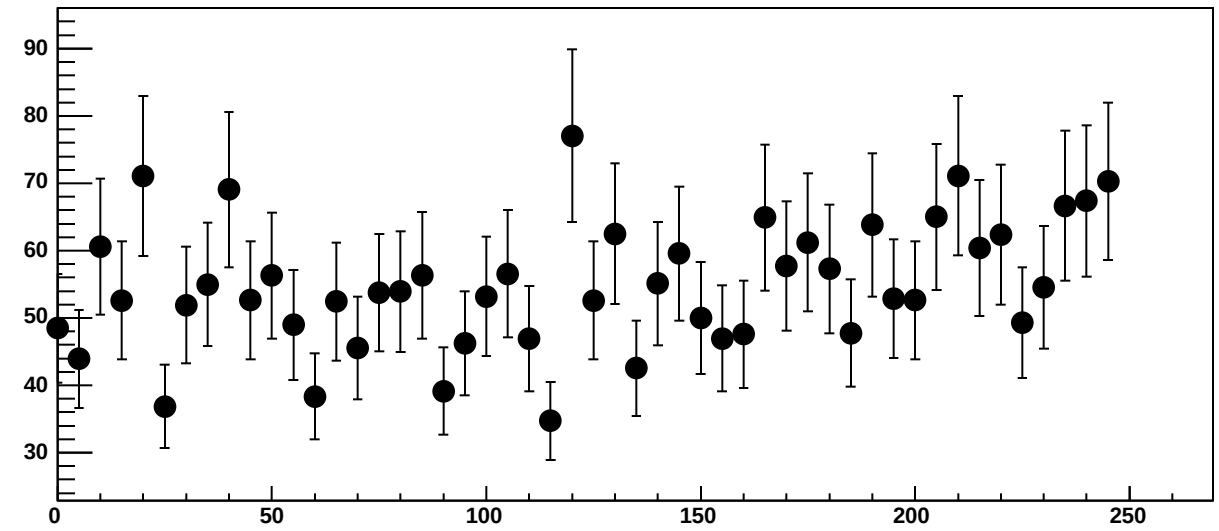

Chip 10, Channel 0, Enable 0!, DAC=1600, ADC Noise vs Hold
200
180
160
140
120
100
80
0
50
100
150
200
250


Chip 10, Channel 0, Enable 0!, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

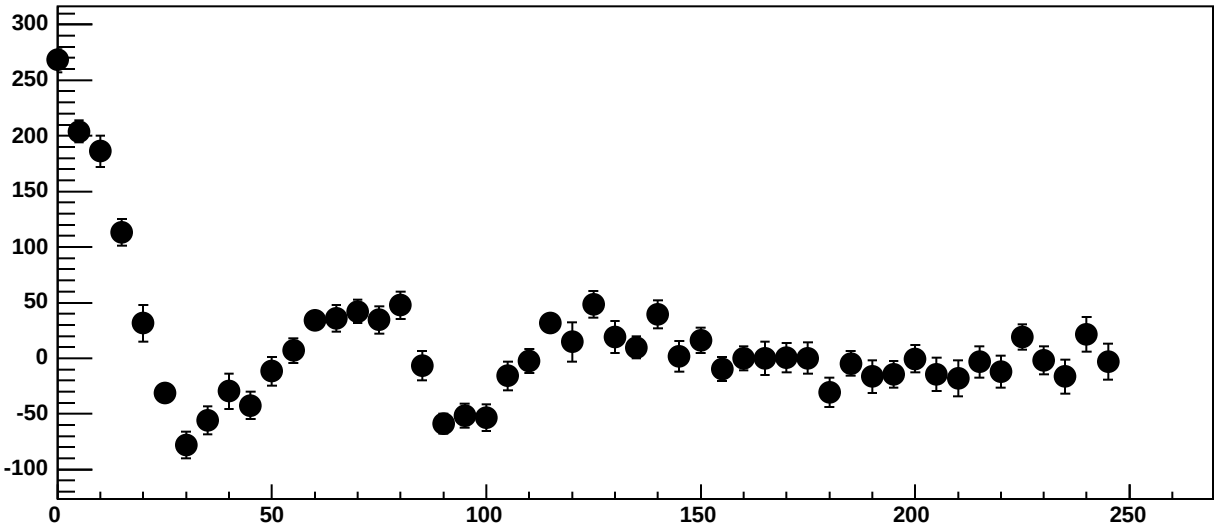
Chip 10, Channel 0, Enable 1, DAC=1600, ADC Mean vs Hold

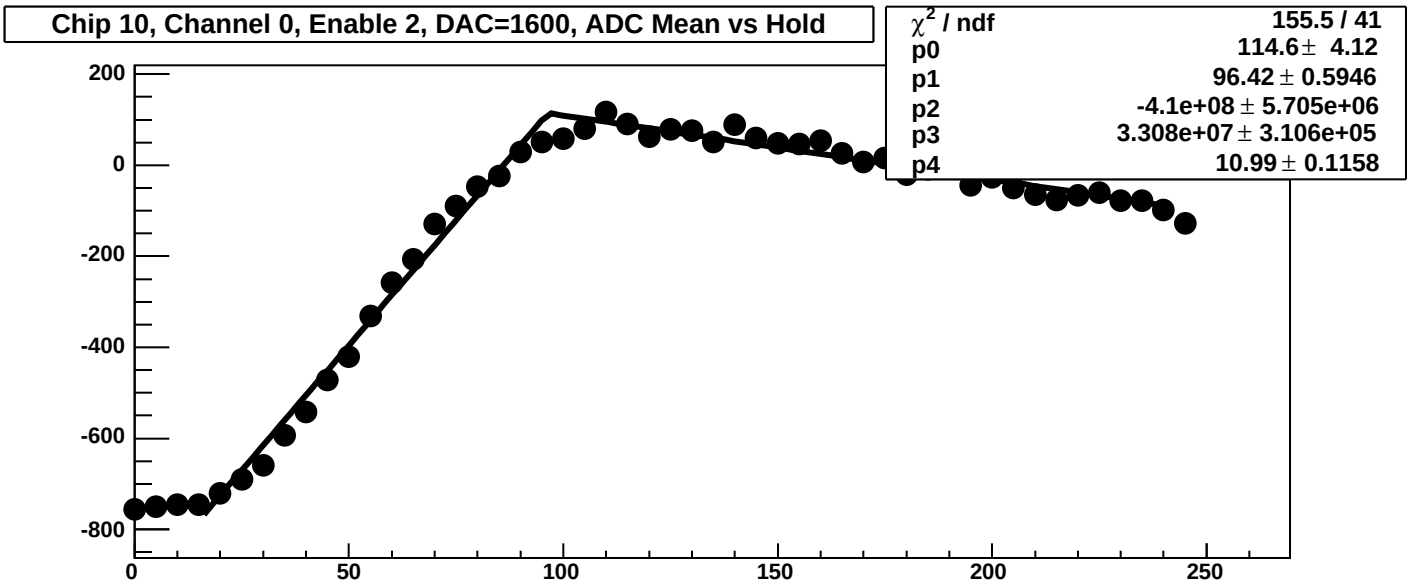


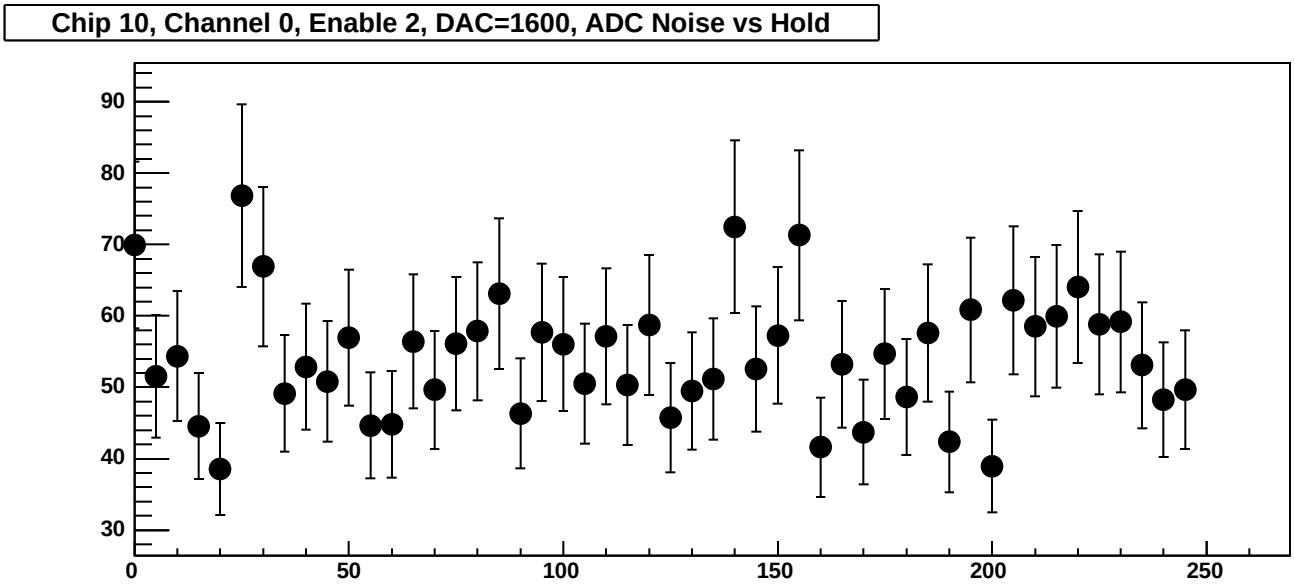
Chip 10, Channel 0, Enable 1, DAC=1600, ADC Noise vs Hold

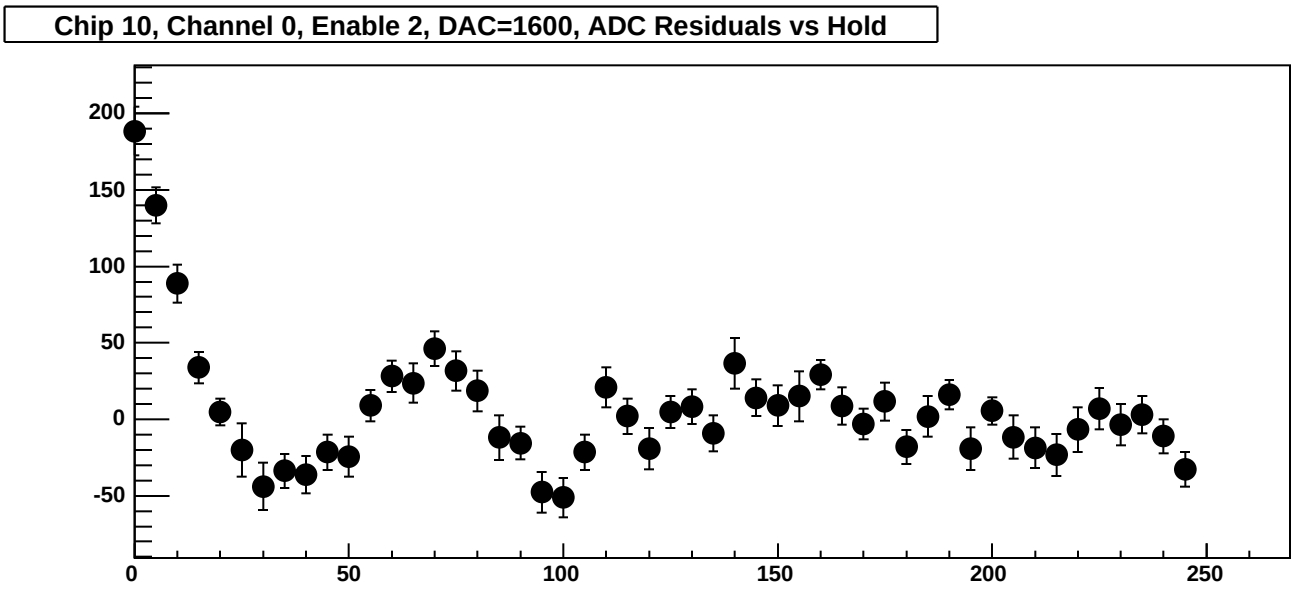


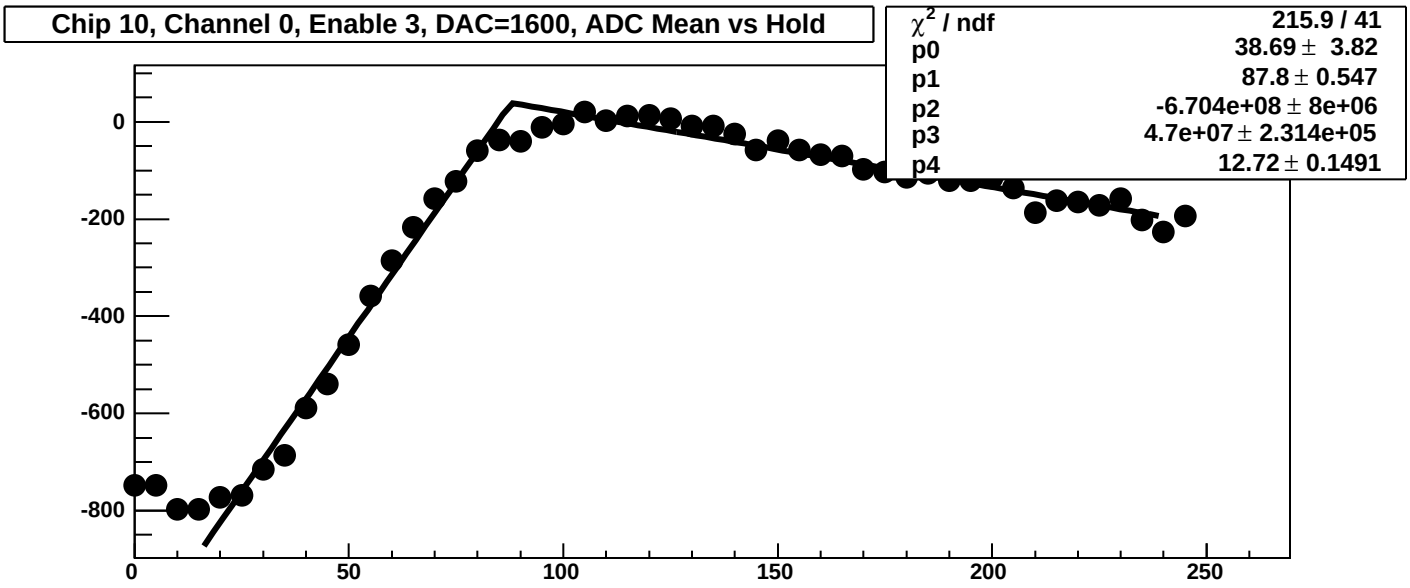
Chip 10, Channel 0, Enable 1, DAC=1600, ADC Residuals vs Hold

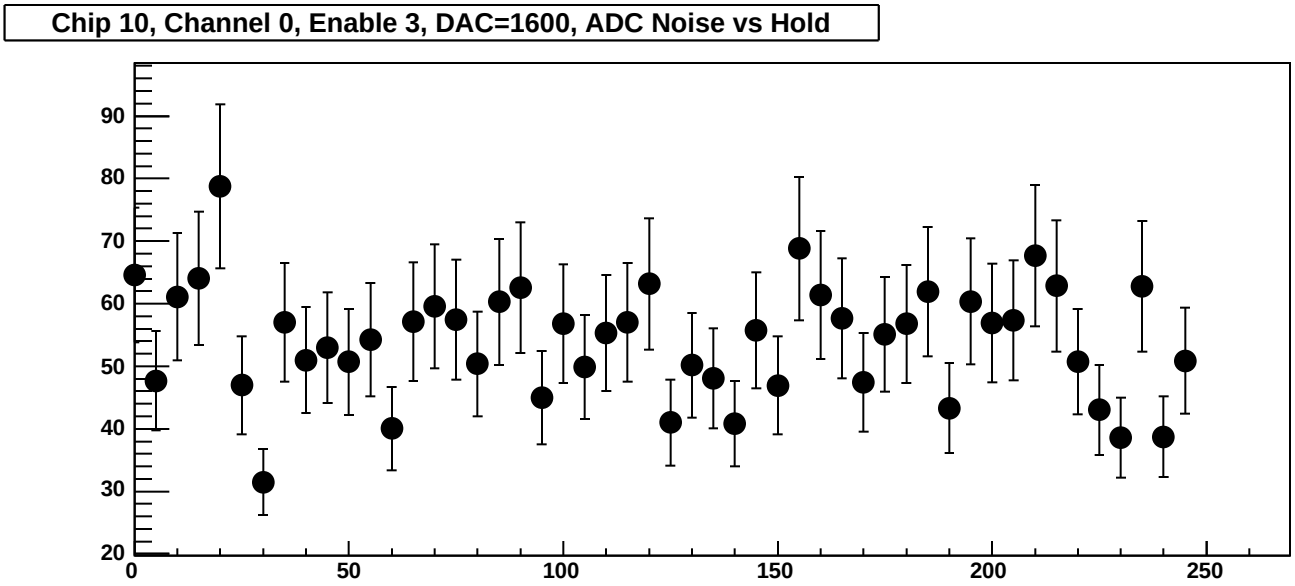


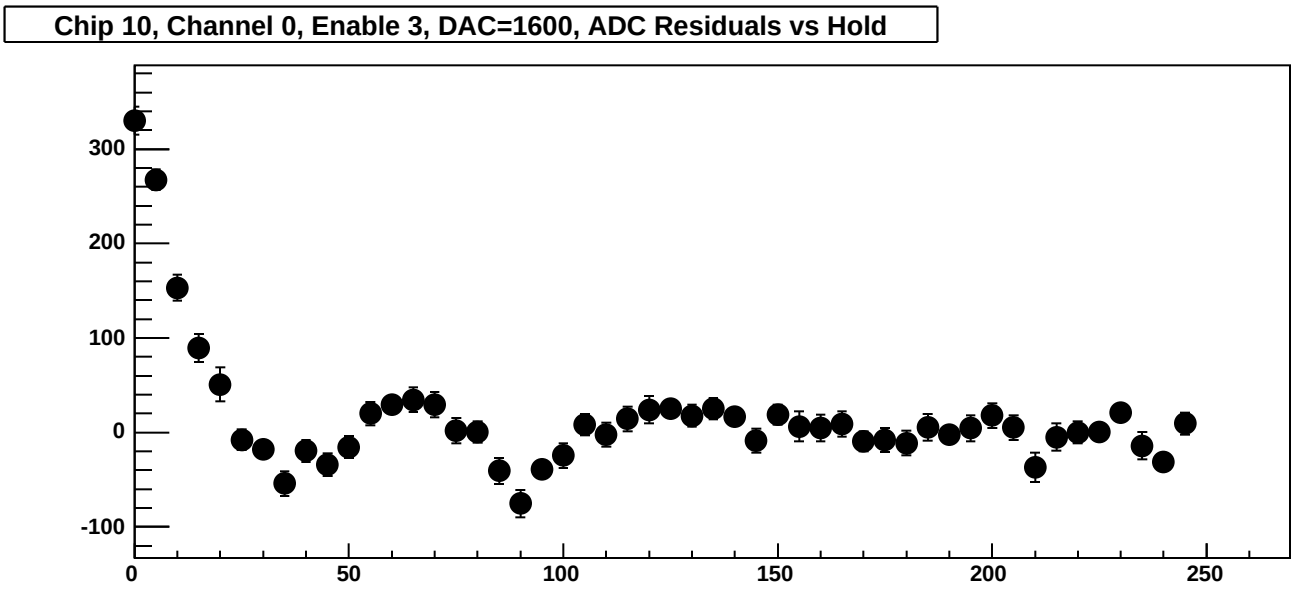

Chip 10, Channel 0, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 155.5 / 41
p0 114.6 ± 4.12
p1 96.42 ± 0.5946
p2 -4.1e+08 ± 5.705e+06
p3 3.308e+07 ± 3.106e+05
p4 10.99 ± 0.1158
200
0
-200
-400
-600
-800
0
50
100
150
200
250


Chip 10, Channel 0, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

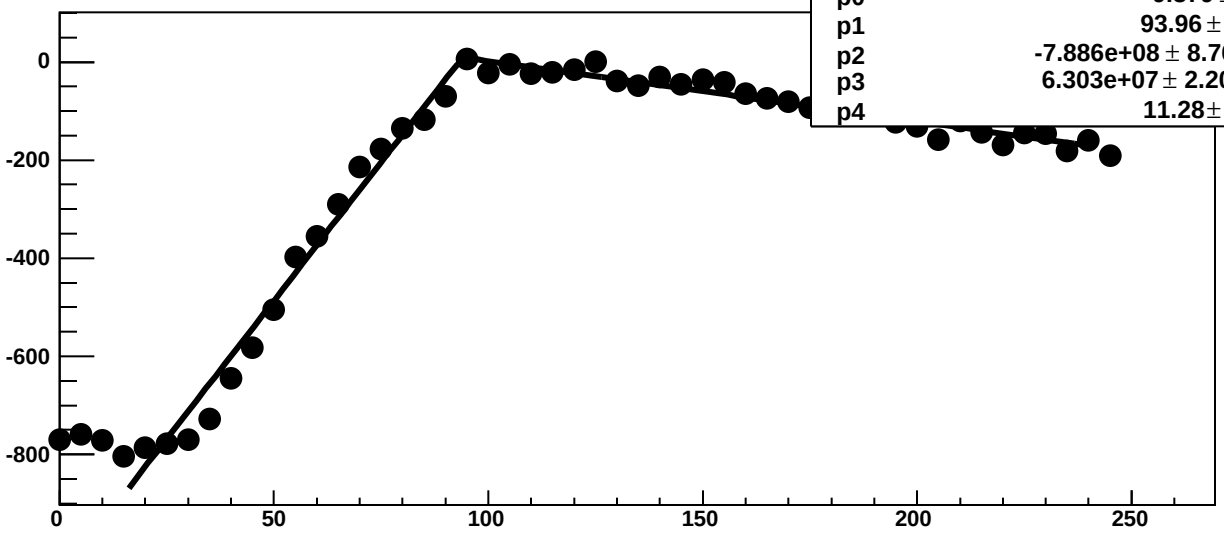

Chip 10, Channel 0, Enable 2, DAC=1600, ADC Residuals vs Hold
200
150
100
50
0
-50
0
50
100
150
200
250


Chip 10, Channel 0, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 215.9 / 41
p0 38.69 ± 3.82
p1 87.8 ± 0.547
p2 -6.704e+08 ± 8e+06
p3 4.7e+07 ± 2.314e+05
p4 12.72 ± 0.1491
0
-200
-400
-600
-800
0
50
100
150
200
250

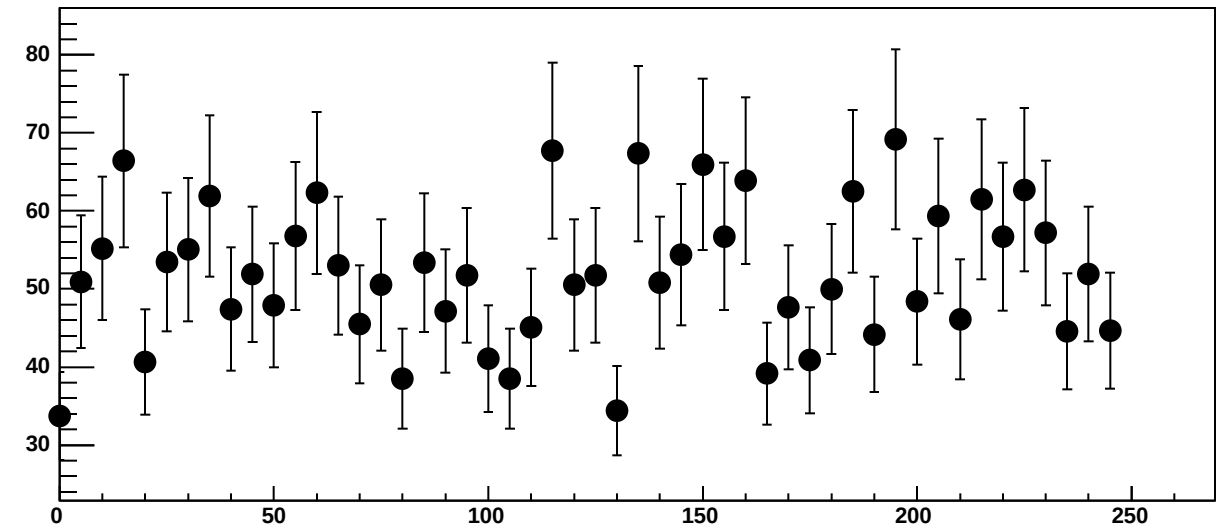

Chip 10, Channel 0, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 10, Channel 0, Enable 3, DAC=1600, ADC Residuals vs Hold
300
200
100
0
-100
0
50
100
150
200
250

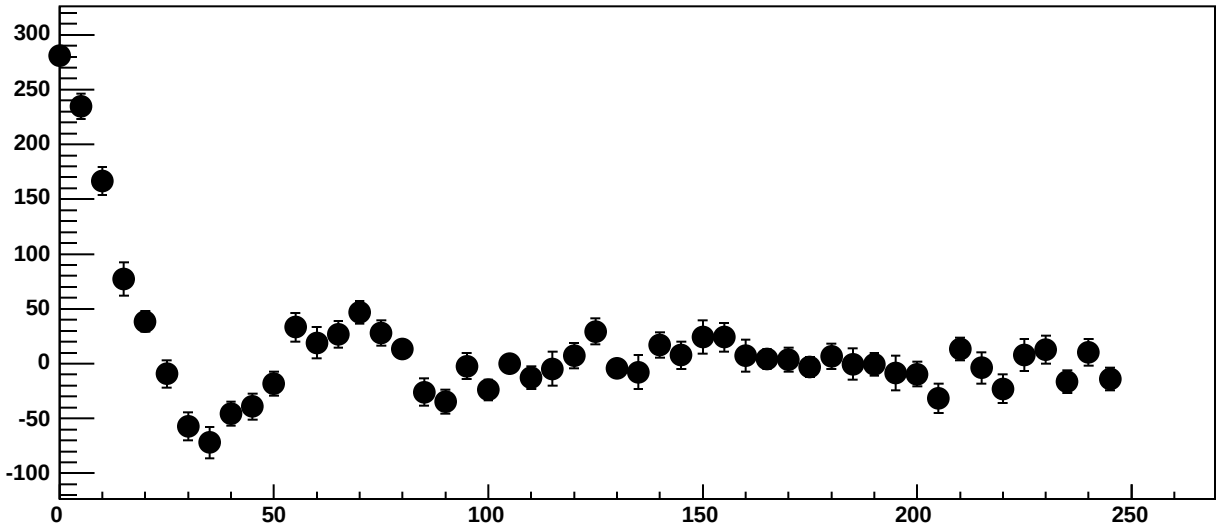
Chip 10, Channel 0, Enable 4, DAC=1600, ADC Mean vs Hold

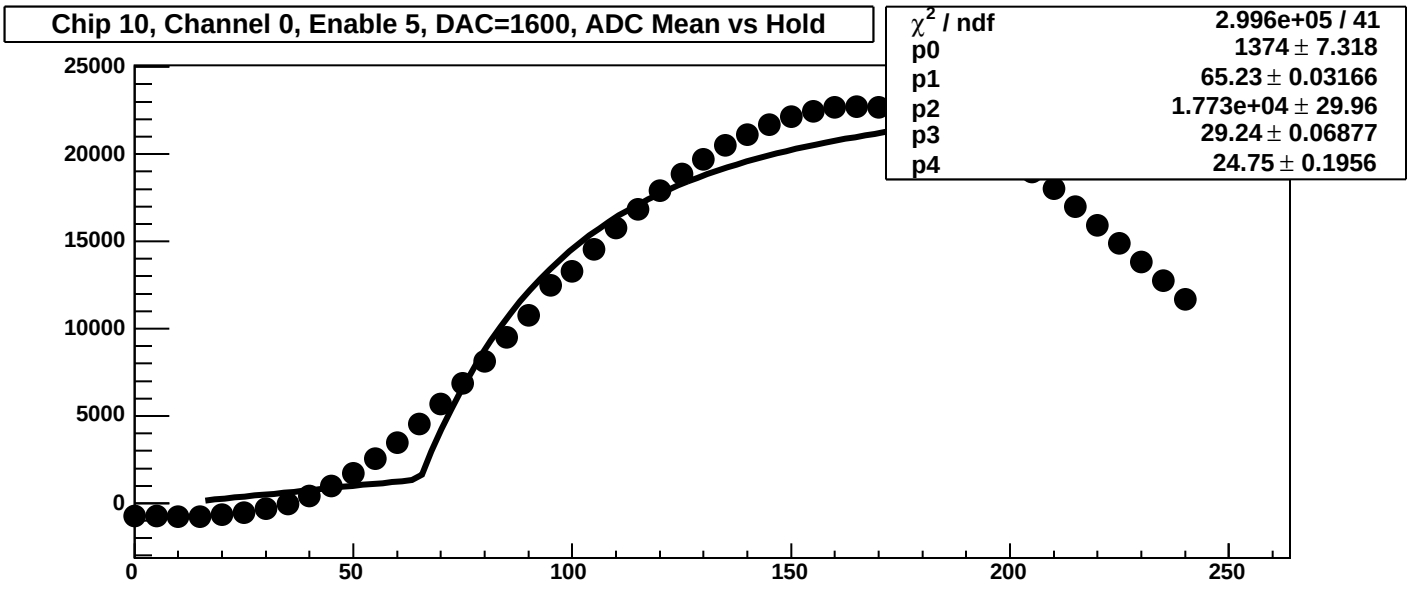


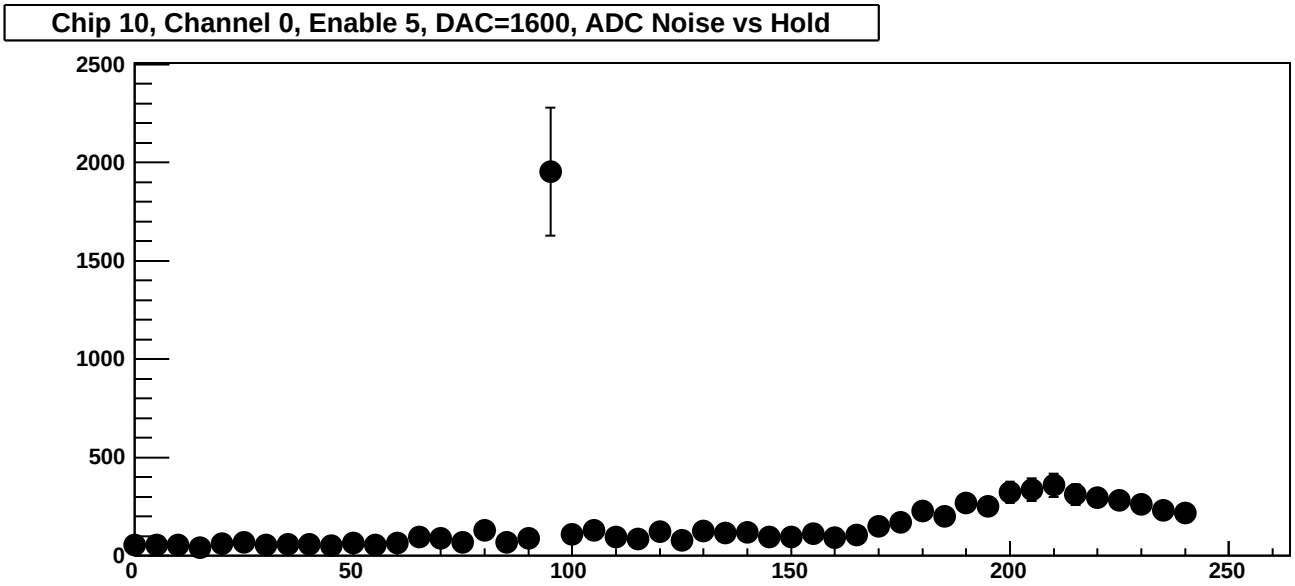
Chip 10, Channel 0, Enable 4, DAC=1600, ADC Noise vs Hold

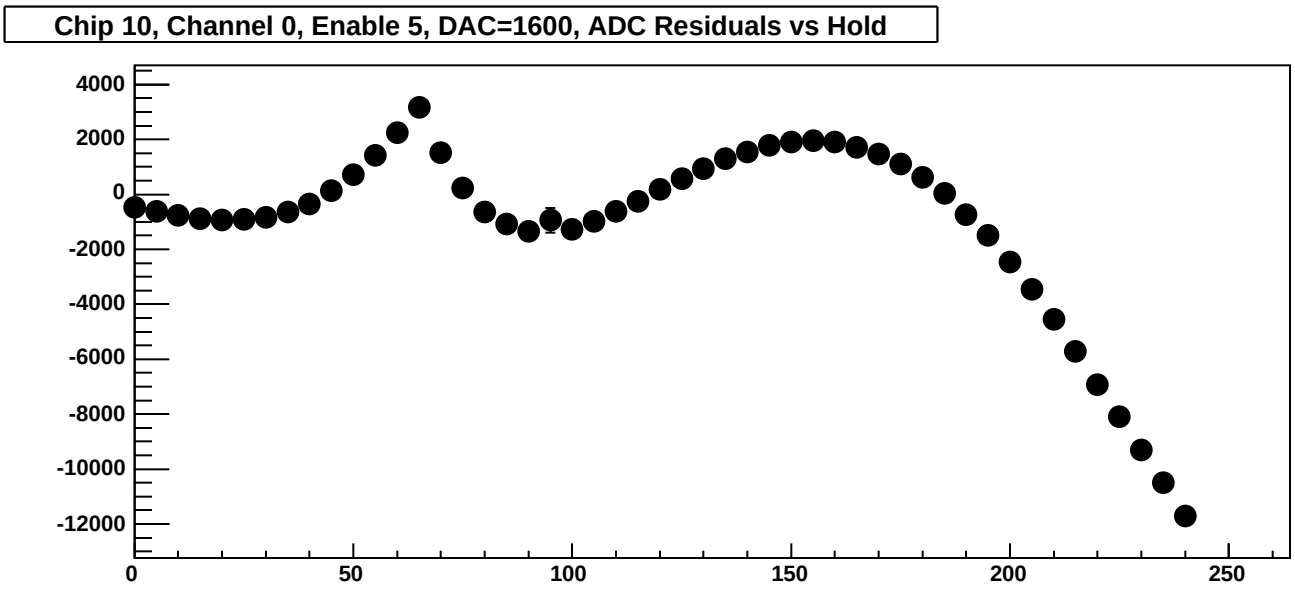


Chip 10, Channel 0, Enable 4, DAC=1600, ADC Residuals vs Hold

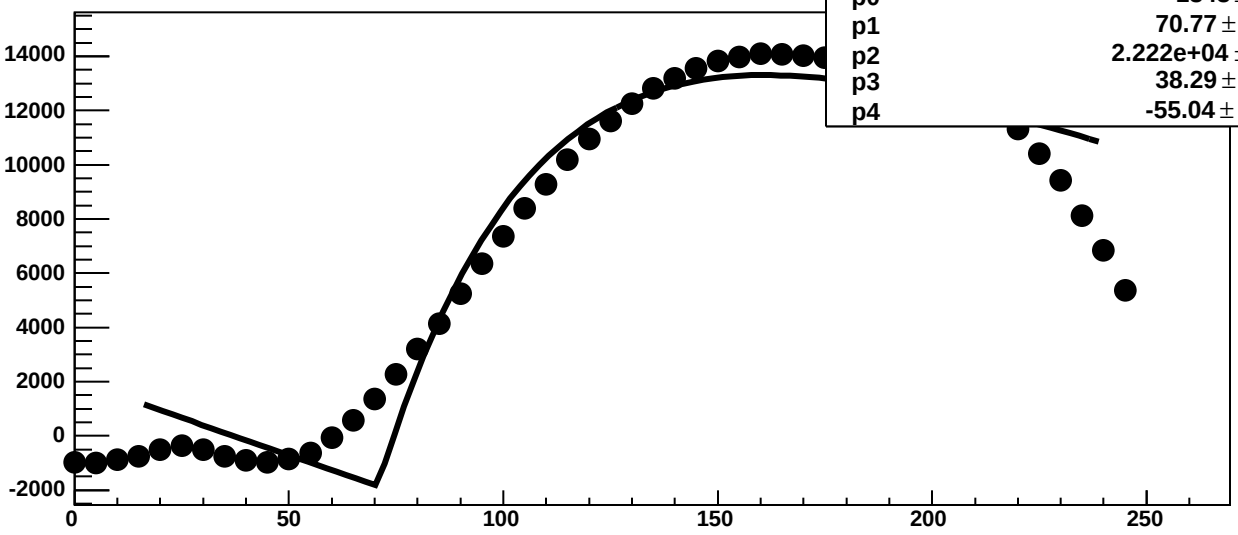



Chip 10, Channel 0, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.996e+05 / 41
p0 1374 ± 7.318
p1 65.23 ± 0.03166
p2 1.773e+04 ± 29.96
p3 29.24 ± 0.06877
p4 24.75 ± 0.1956
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

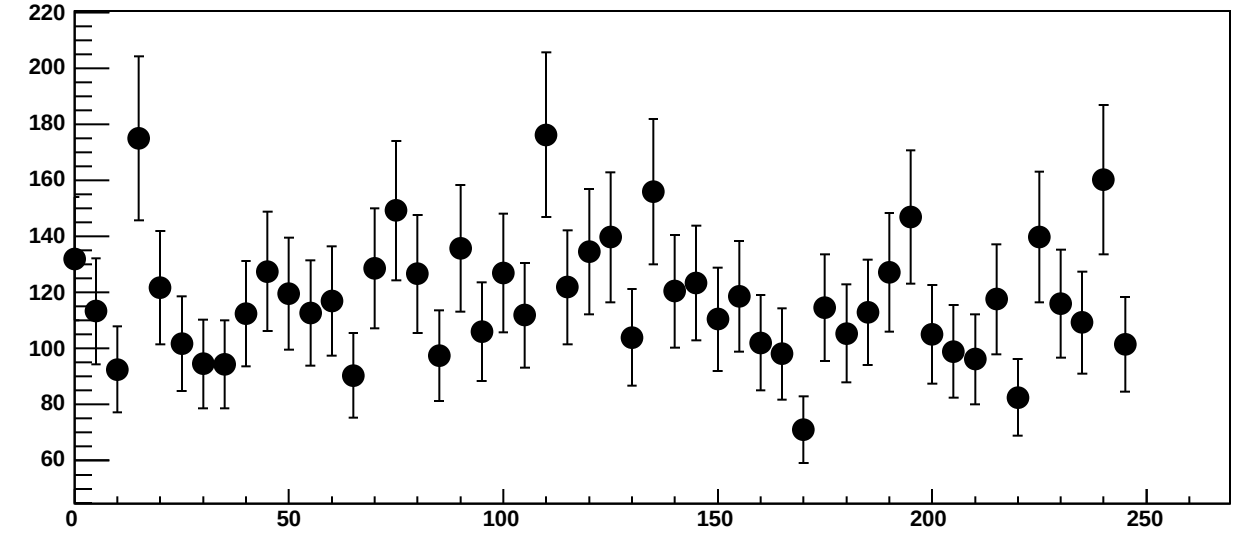

Chip 10, Channel 0, Enable 5, DAC=1600, ADC Noise vs Hold
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 10, Channel 0, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
0
50
100
150
200
250

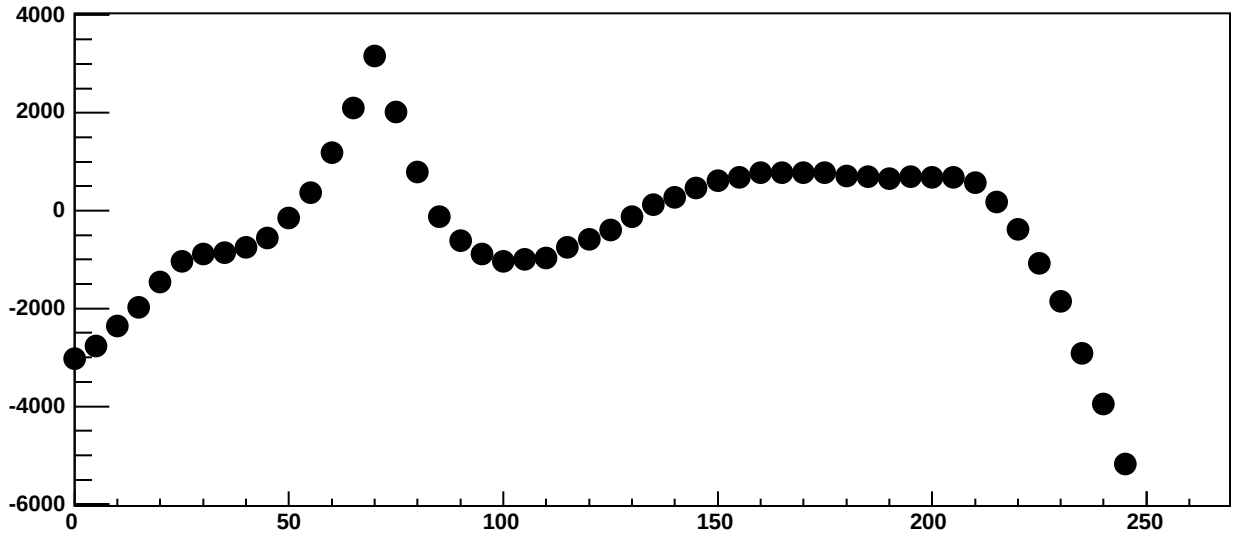
Chip 10, Channel 1, Enable 0, DAC=1600, ADC Mean vs Hold



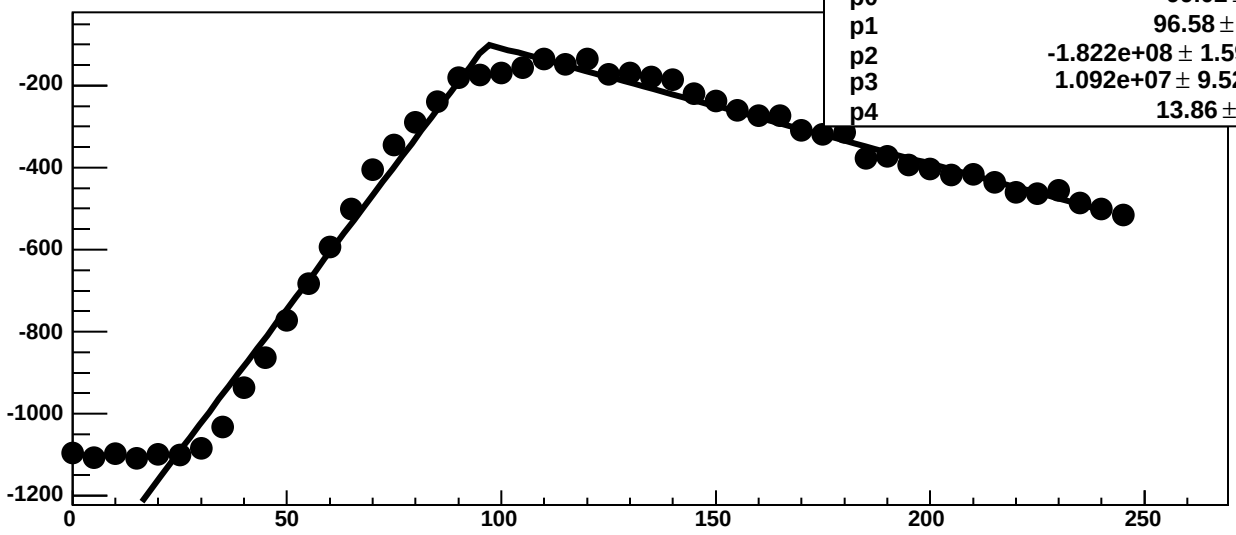
Chip 10, Channel 1, Enable 0, DAC=1600, ADC Noise vs Hold



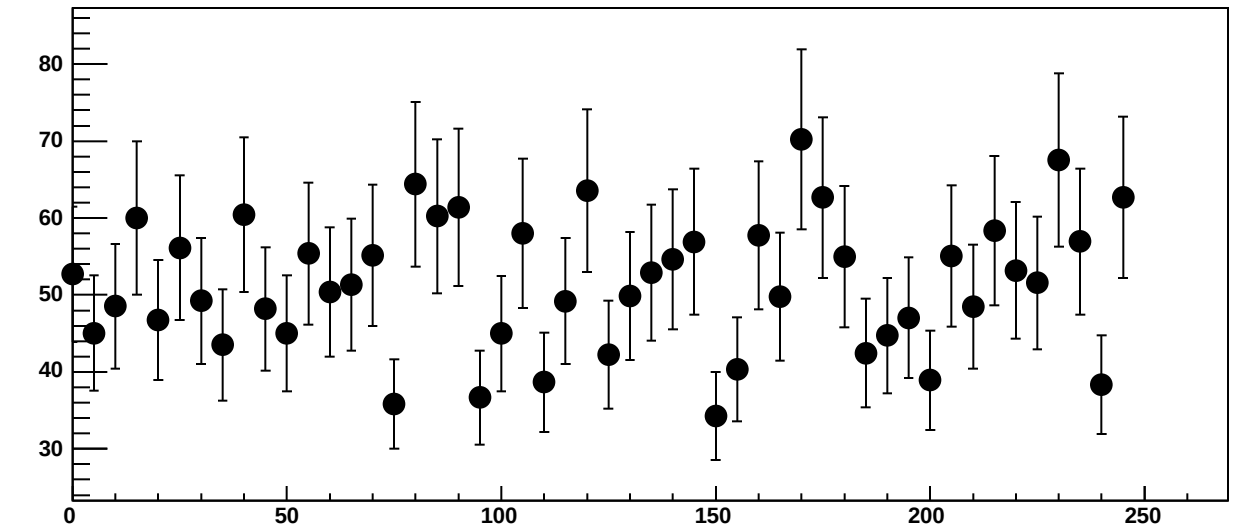
Chip 10, Channel 1, Enable 0, DAC=1600, ADC Residuals vs Hold



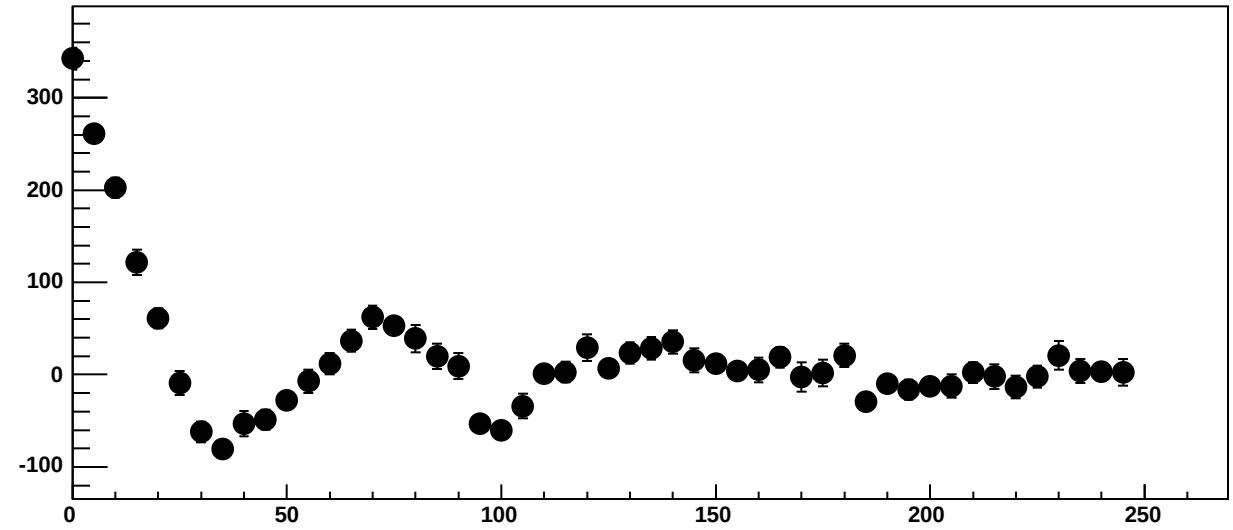
Chip 10, Channel 1, Enable 1, DAC=1600, ADC Mean vs Hold



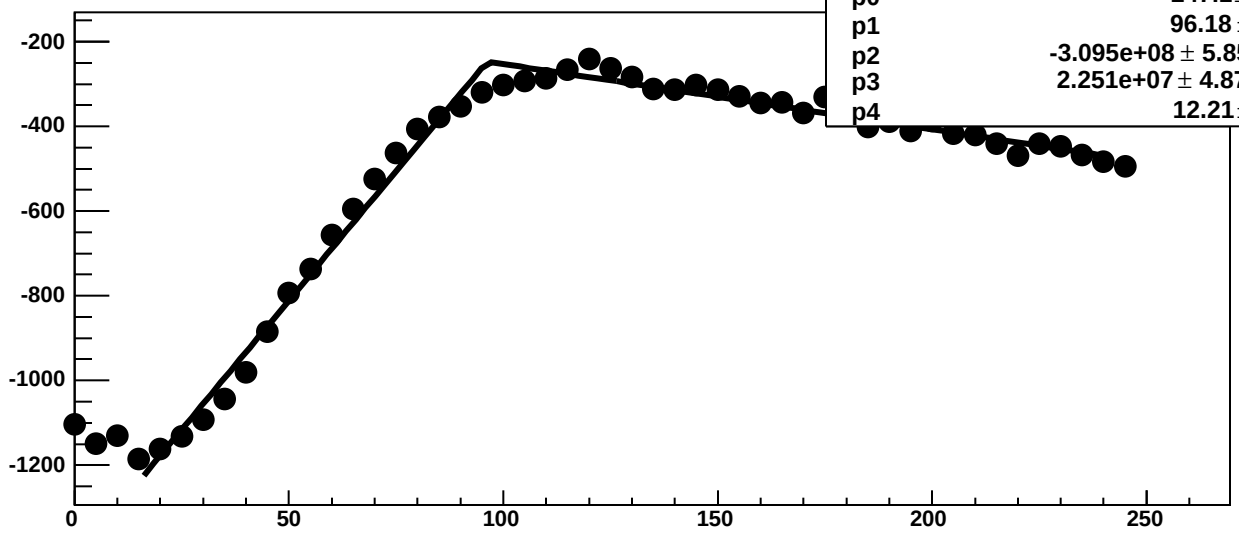
Chip 10, Channel 1, Enable 1, DAC=1600, ADC Noise vs Hold



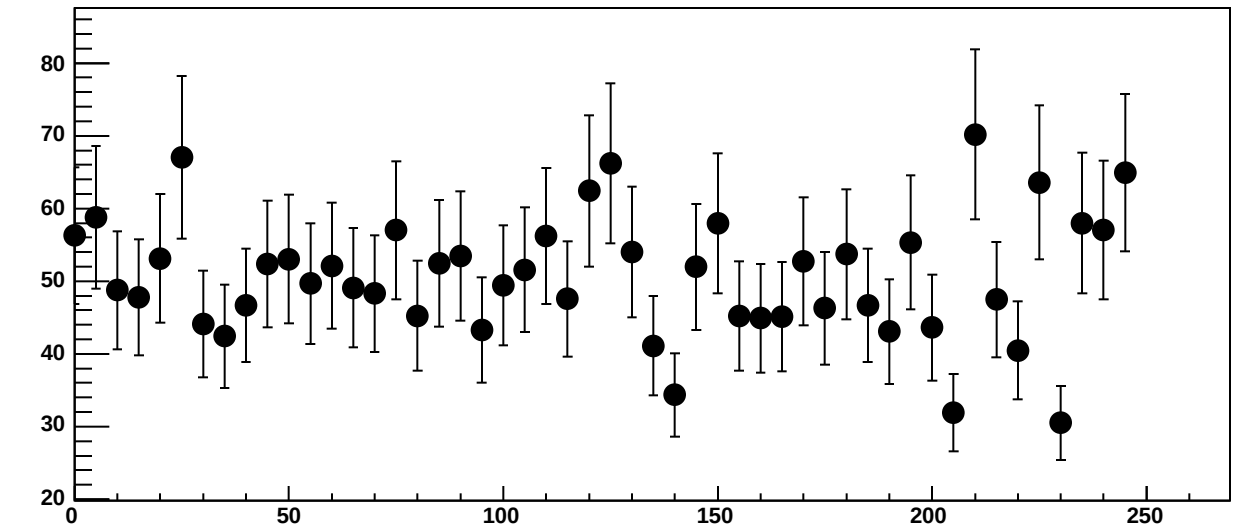
Chip 10, Channel 1, Enable 1, DAC=1600, ADC Residuals vs Hold



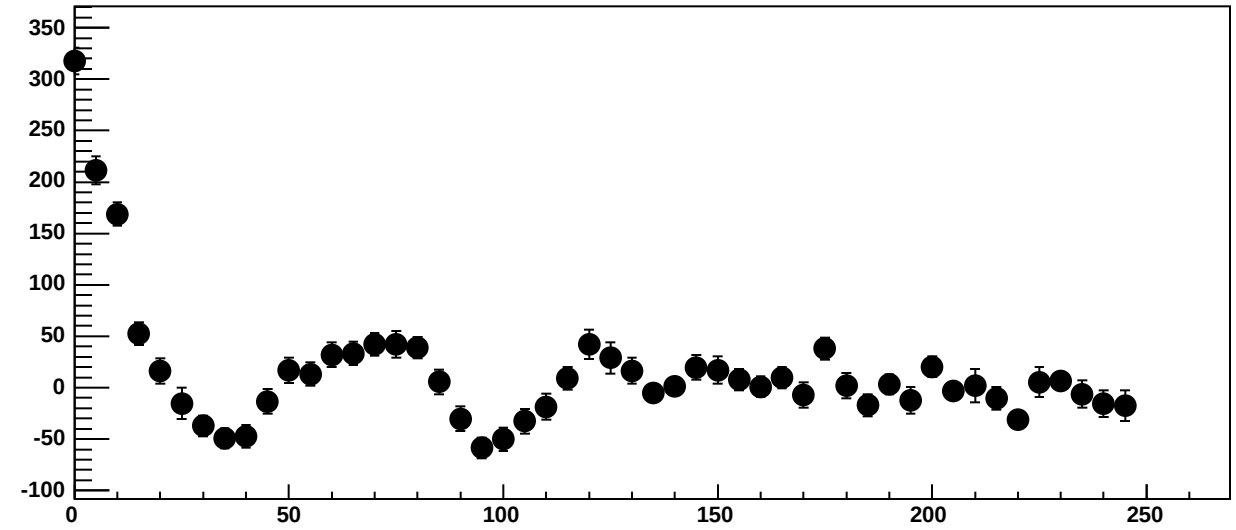
Chip 10, Channel 1, Enable 2, DAC=1600, ADC Mean vs Hold



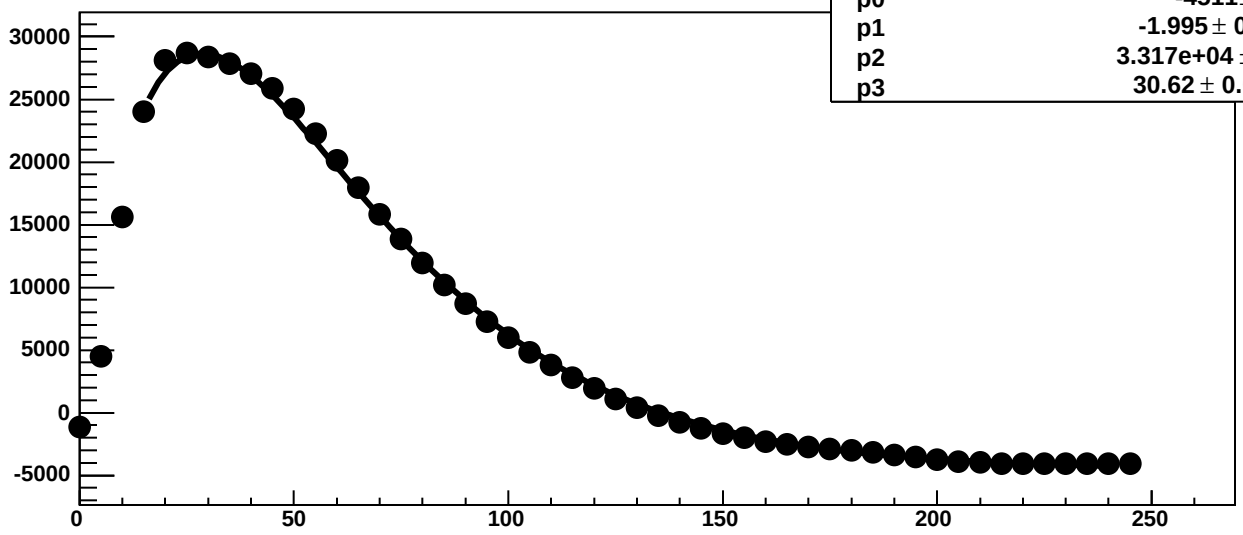
Chip 10, Channel 1, Enable 2, DAC=1600, ADC Noise vs Hold



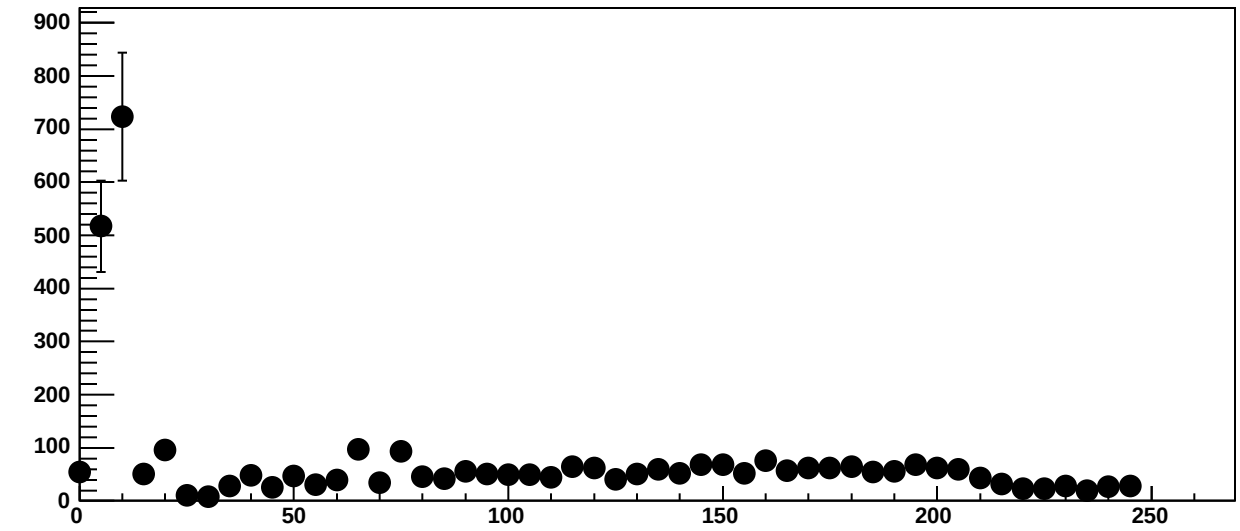
Chip 10, Channel 1, Enable 2, DAC=1600, ADC Residuals vs Hold



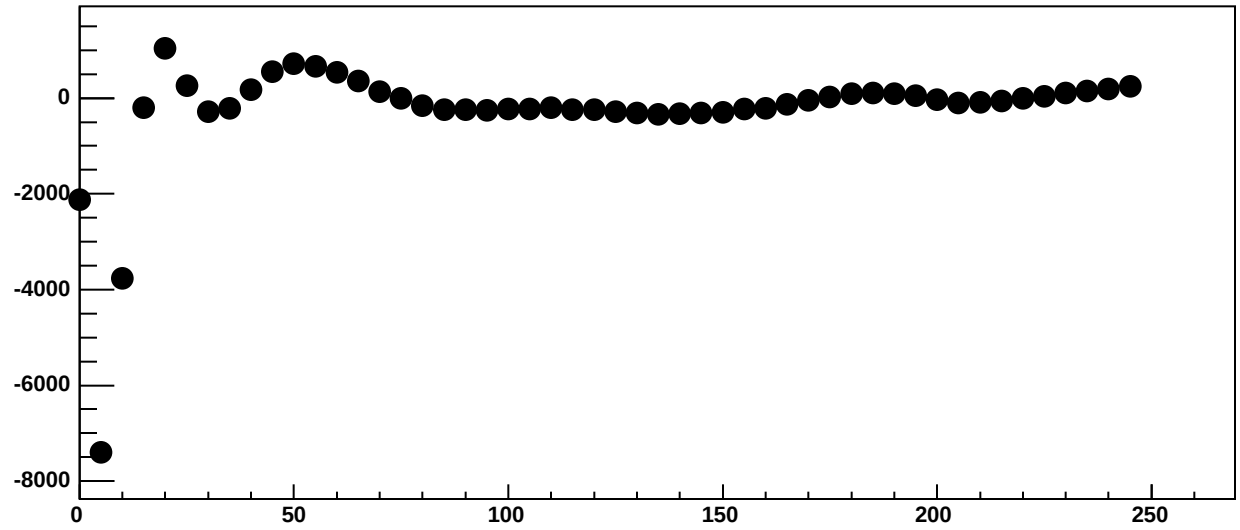
Chip 10, Channel 1, Enable 3!, DAC=1600, ADC Mean vs Hold

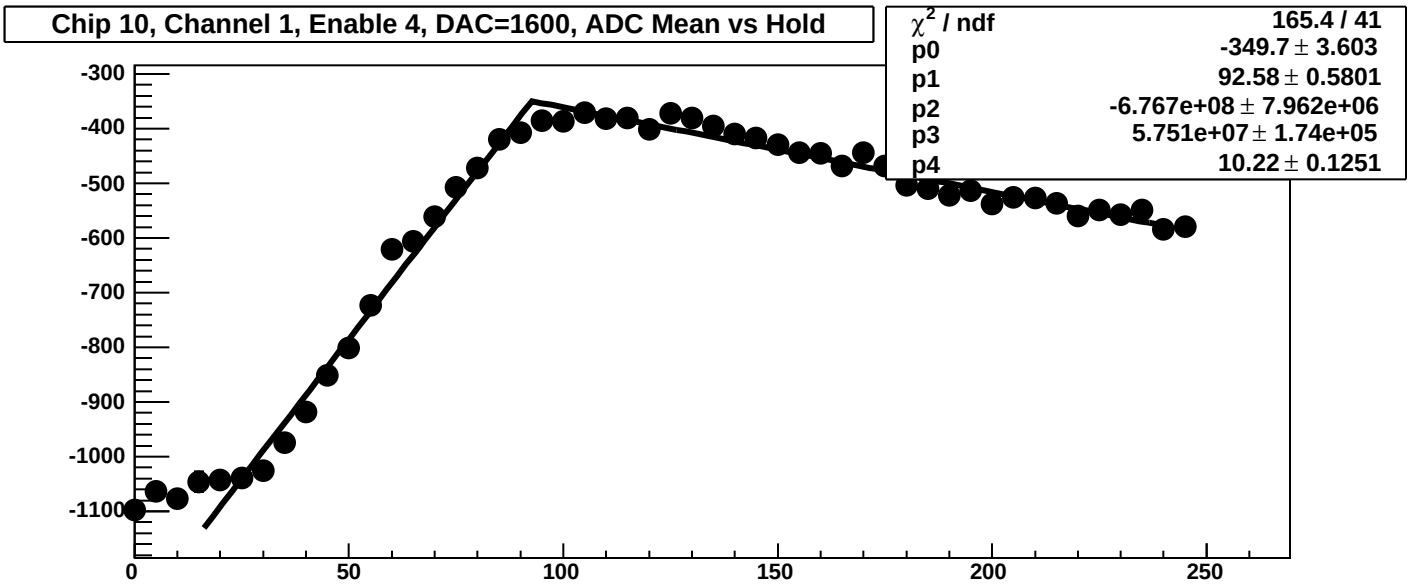


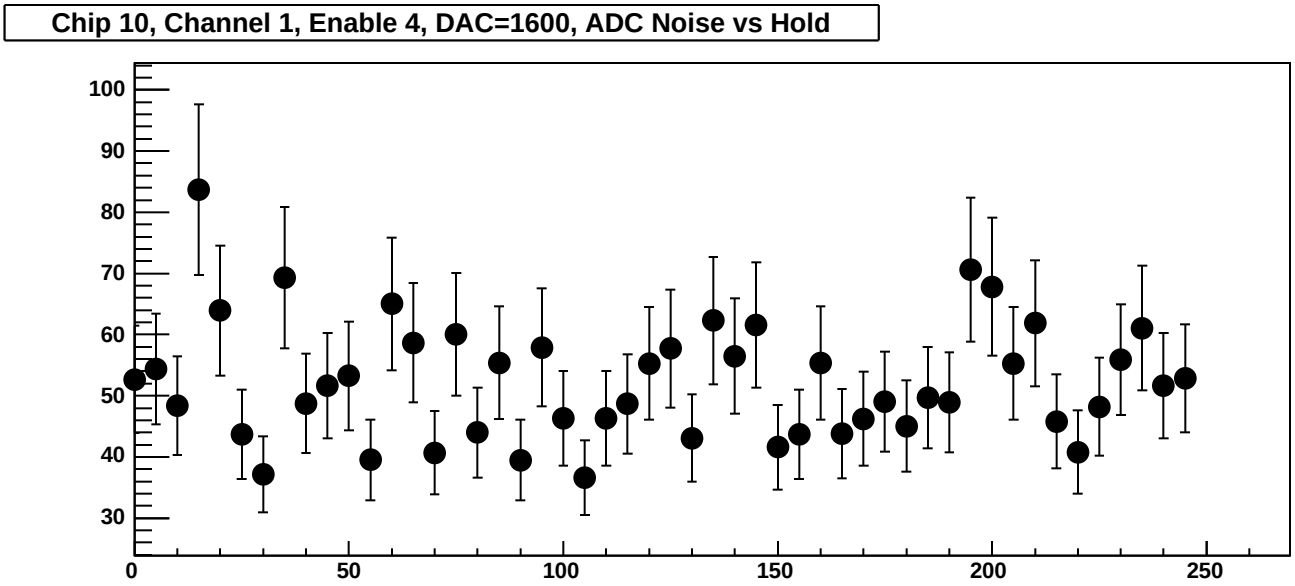
Chip 10, Channel 1, Enable 3!, DAC=1600, ADC Noise vs Hold

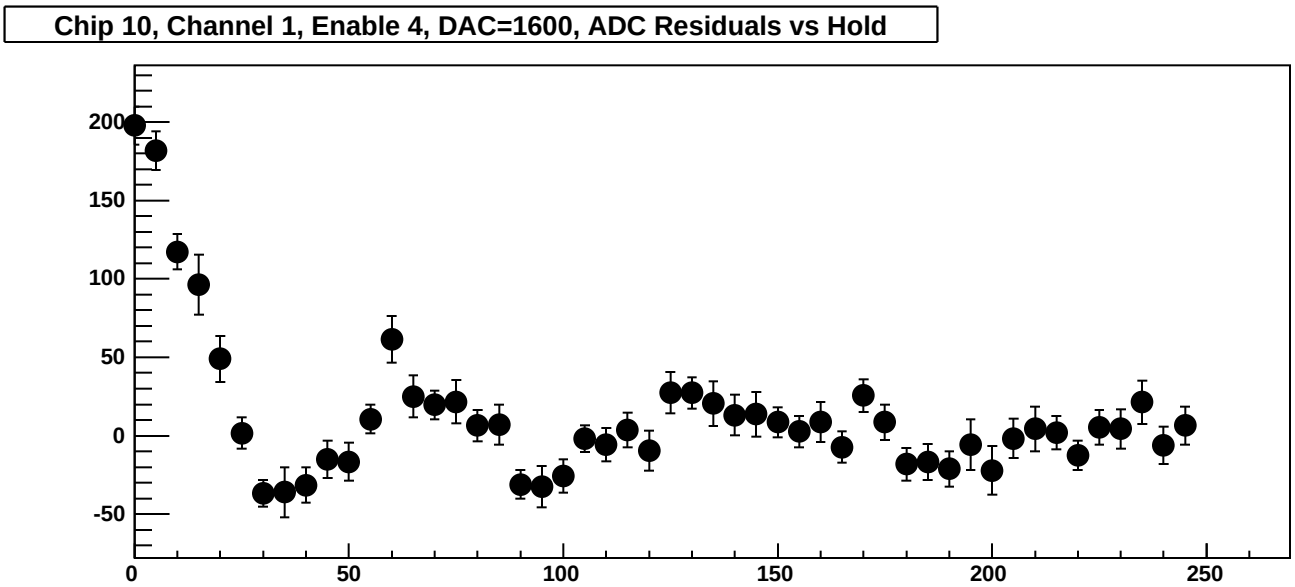


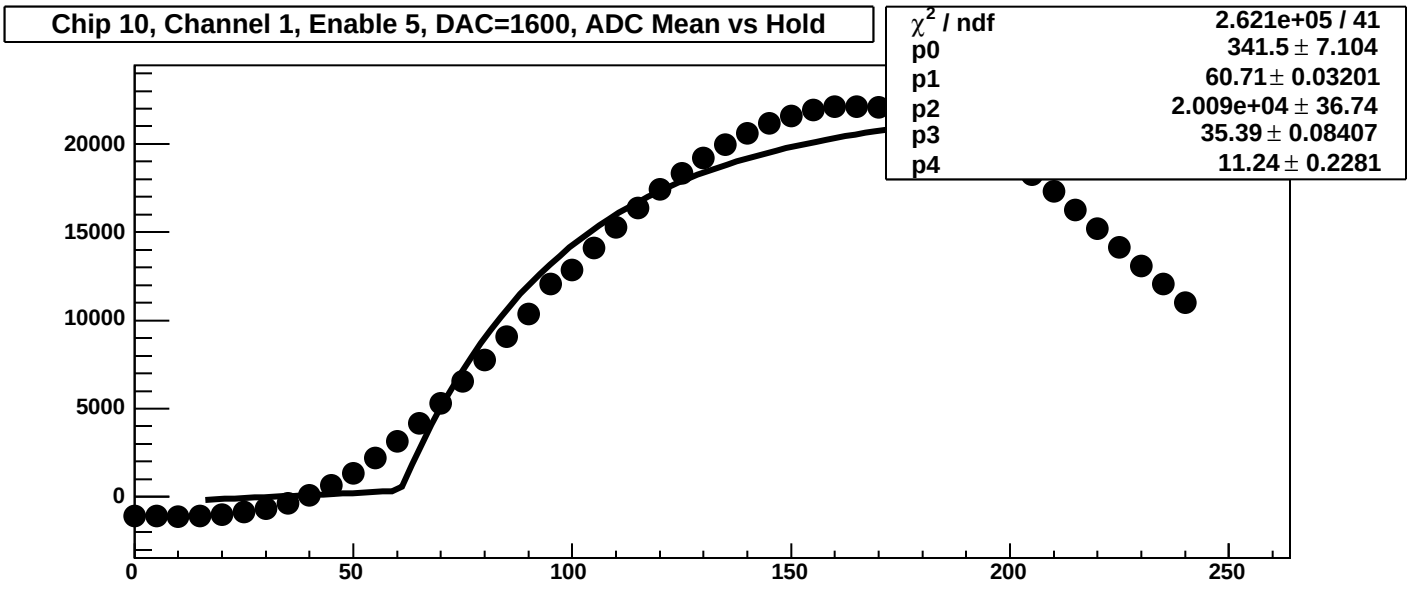
Chip 10, Channel 1, Enable 3!, DAC=1600, ADC Residuals vs Hold

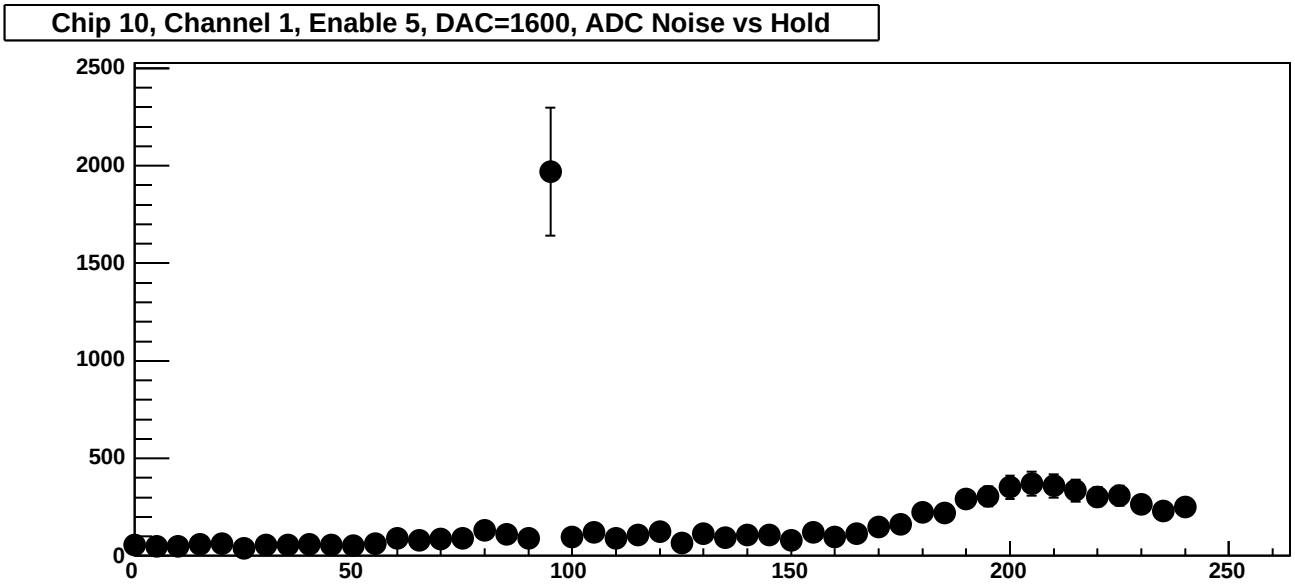


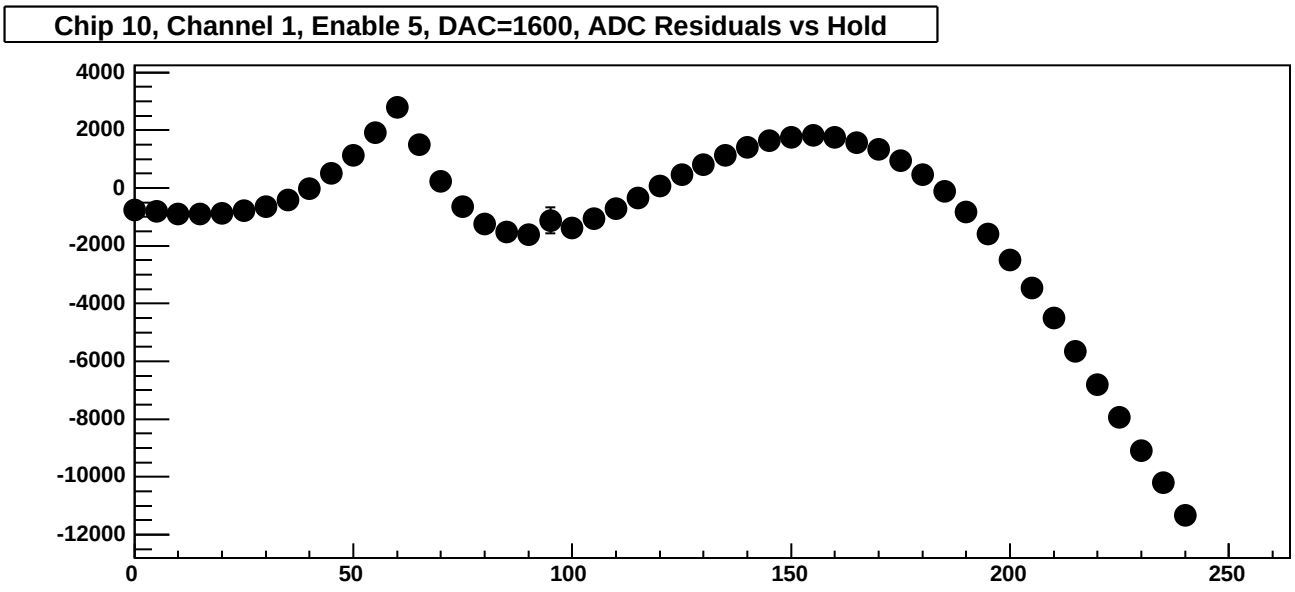

Chip 10, Channel 1, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 165.4 / 41
p0 -349.7 ± 3.603
p1 92.58 ± 0.5801
p2 -6.767e+08 ± 7.962e+06
p3 5.751e+07 ± 1.74e+05
p4 10.22 ± 0.1251
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
50
100
150
200
250

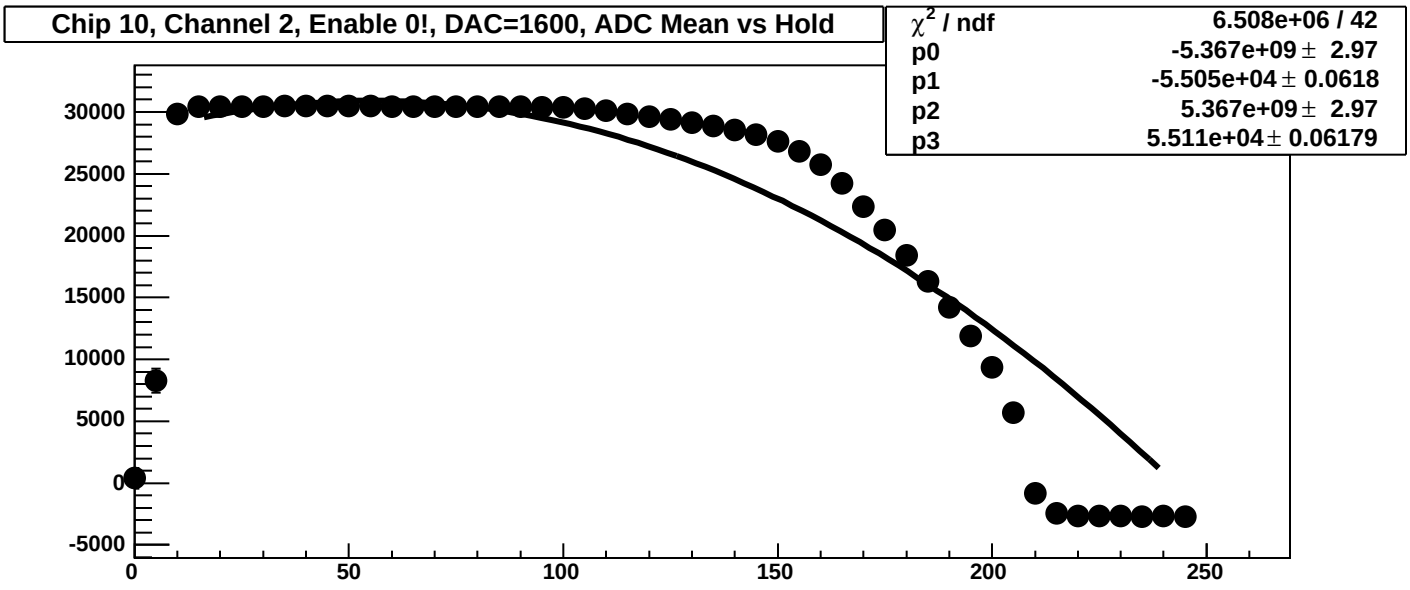

Chip 10, Channel 1, Enable 4, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250

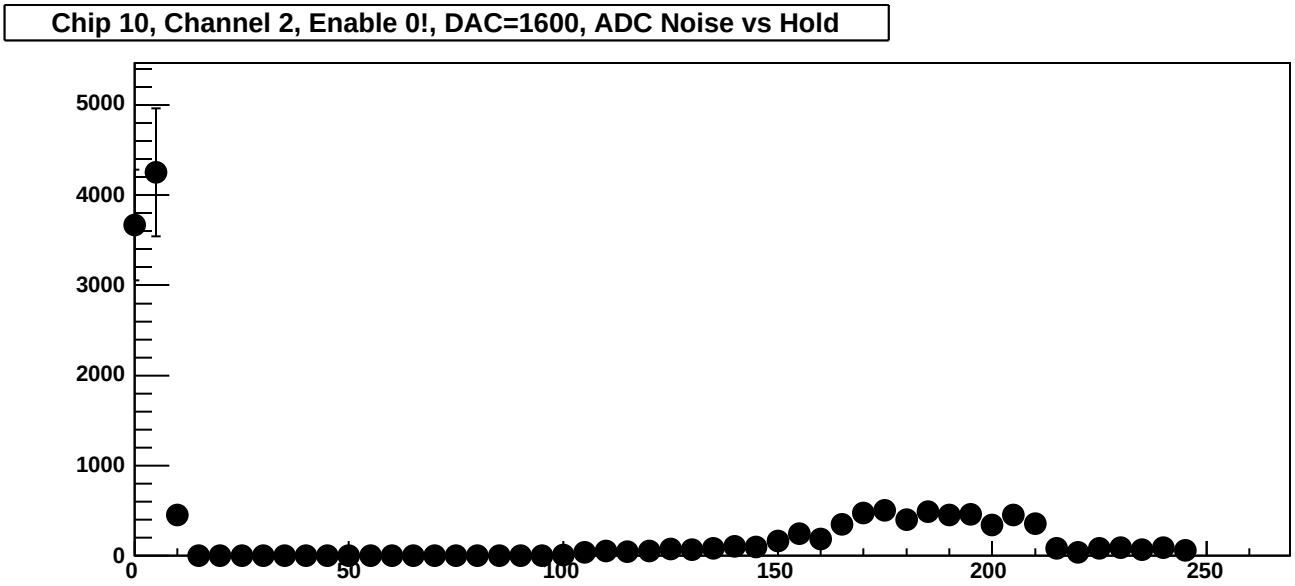

Chip 10, Channel 1, Enable 4, DAC=1600, ADC Residuals vs Hold
200
150
100
50
0
-50
0
50
100
150
200
250

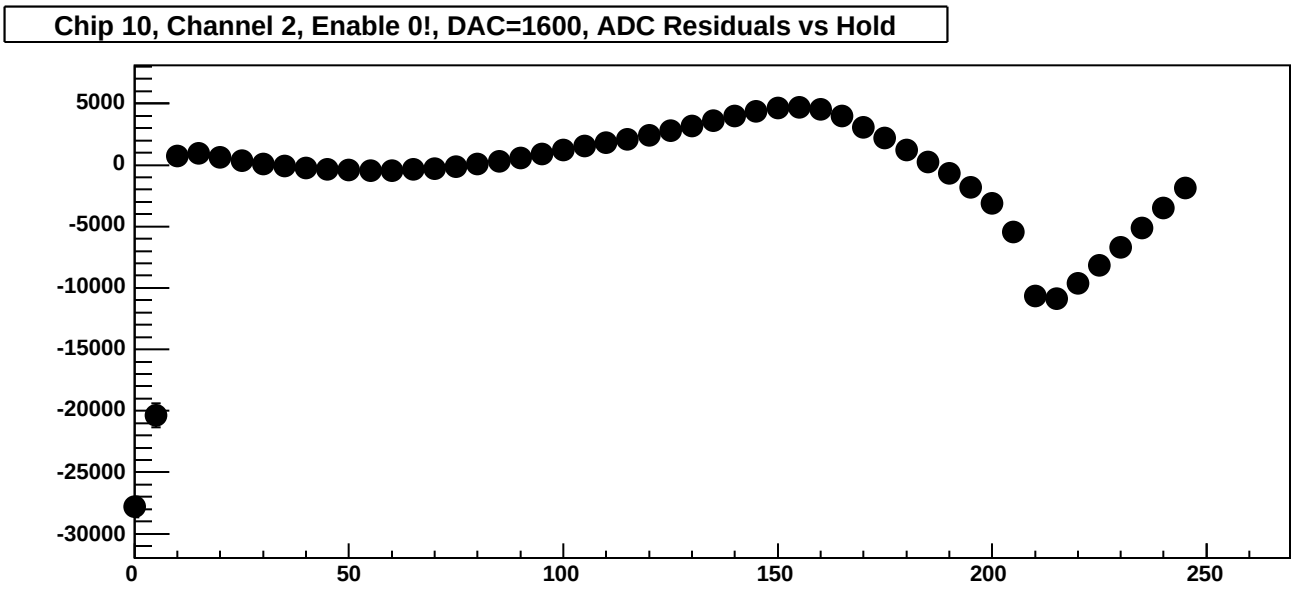

Chip 10, Channel 1, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.621e+05 / 41
p0 341.5 ± 7.104
p1 60.71 ± 0.03201
p2 2.009e+04 ± 36.74
p3 35.39 ± 0.08407
p4 11.24 ± 0.2281
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 10, Channel 1, Enable 5, DAC=1600, ADC Noise vs Hold
2500
2000
1500
1000
500
0
0
50
100
150
200
250

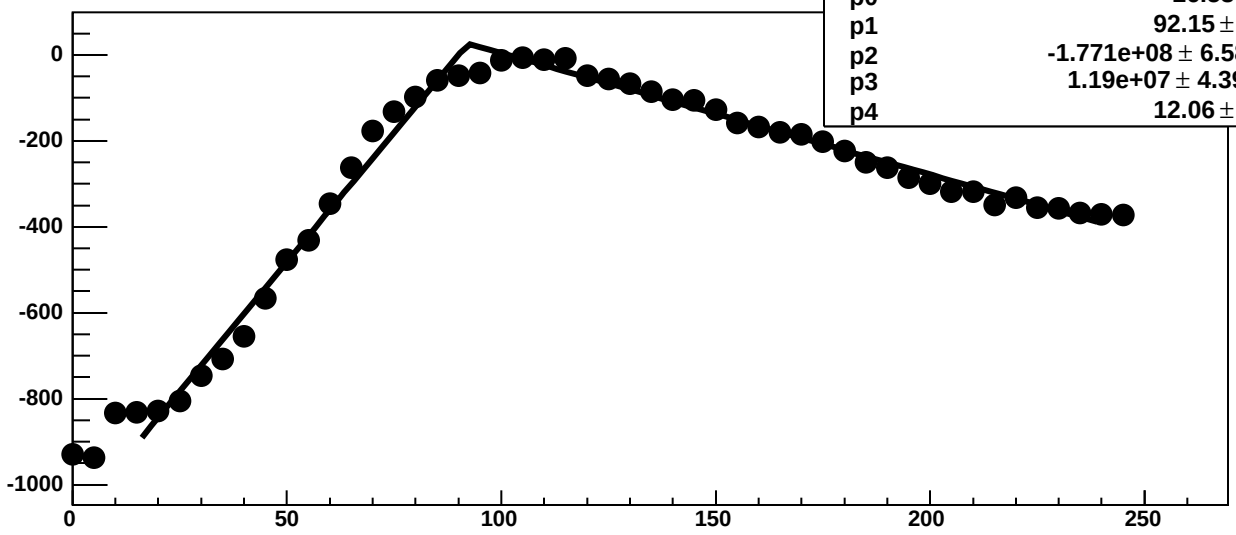

Chip 10, Channel 1, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
0
50
100
150
200
250


Chip 10, Channel 2, Enable 0!, DAC=1600, ADC Mean vs Hold
χ² / ndf 6.508e+06 / 42
p0 -5.367e+09 ± 2.97
p1 -5.505e+04 ± 0.0618
p2 5.367e+09 ± 2.97
p3 5.511e+04 ± 0.06179
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

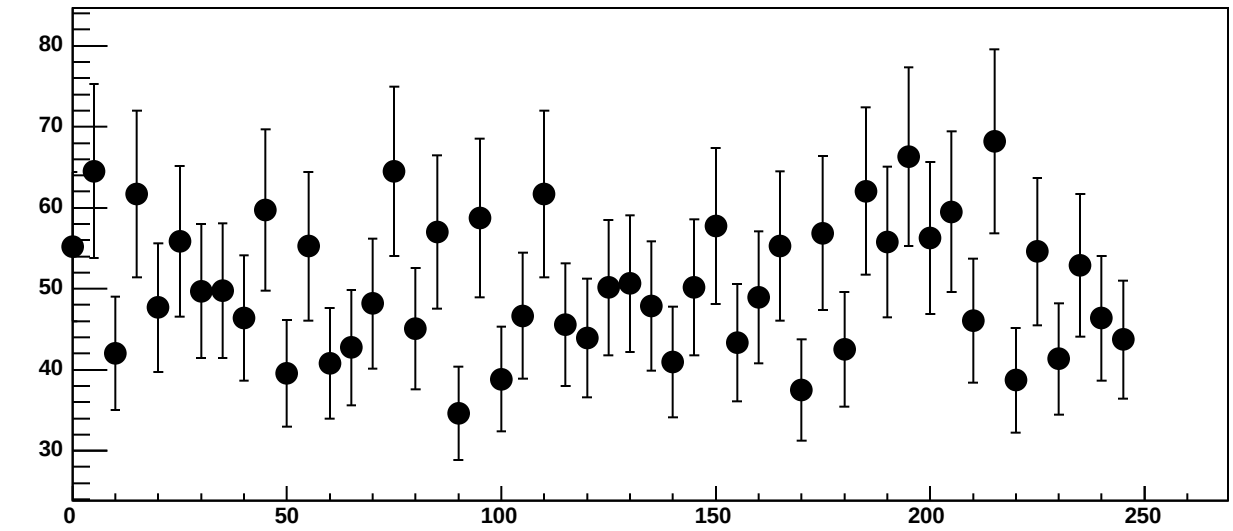

Chip 10, Channel 2, Enable 0!, DAC=1600, ADC Noise vs Hold
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 10, Channel 2, Enable 0!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
0
50
100
150
200
250

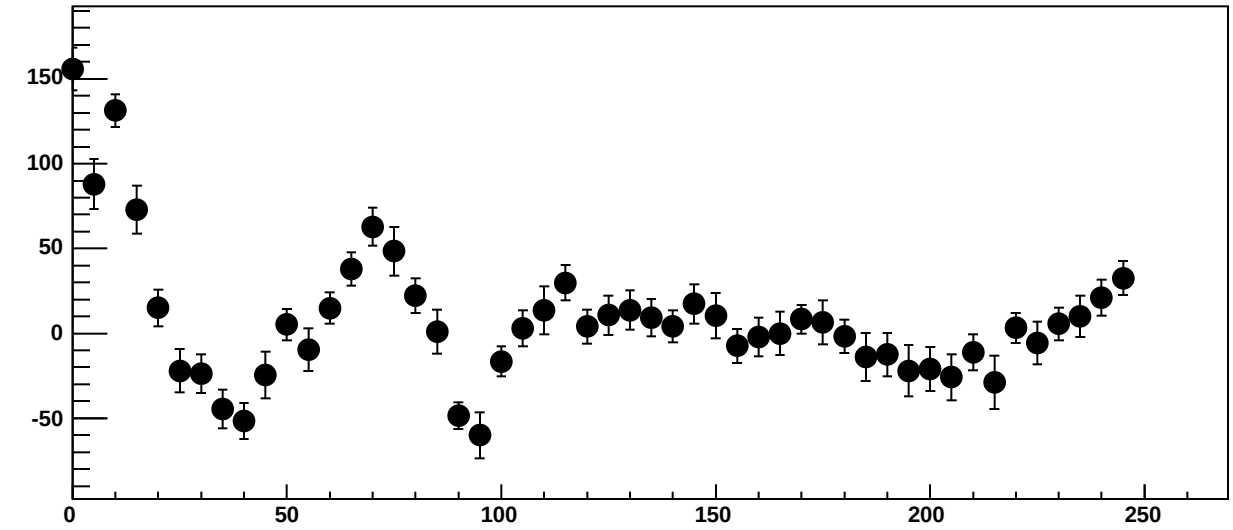
Chip 10, Channel 2, Enable 1, DAC=1600, ADC Mean vs Hold

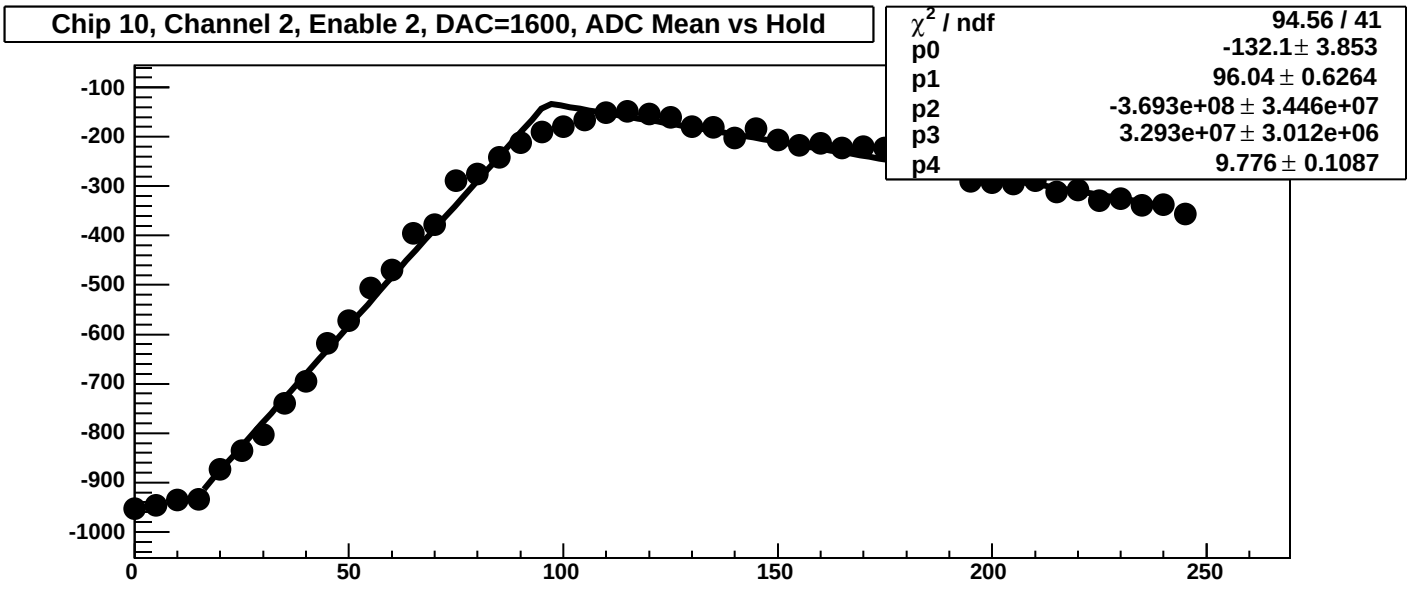


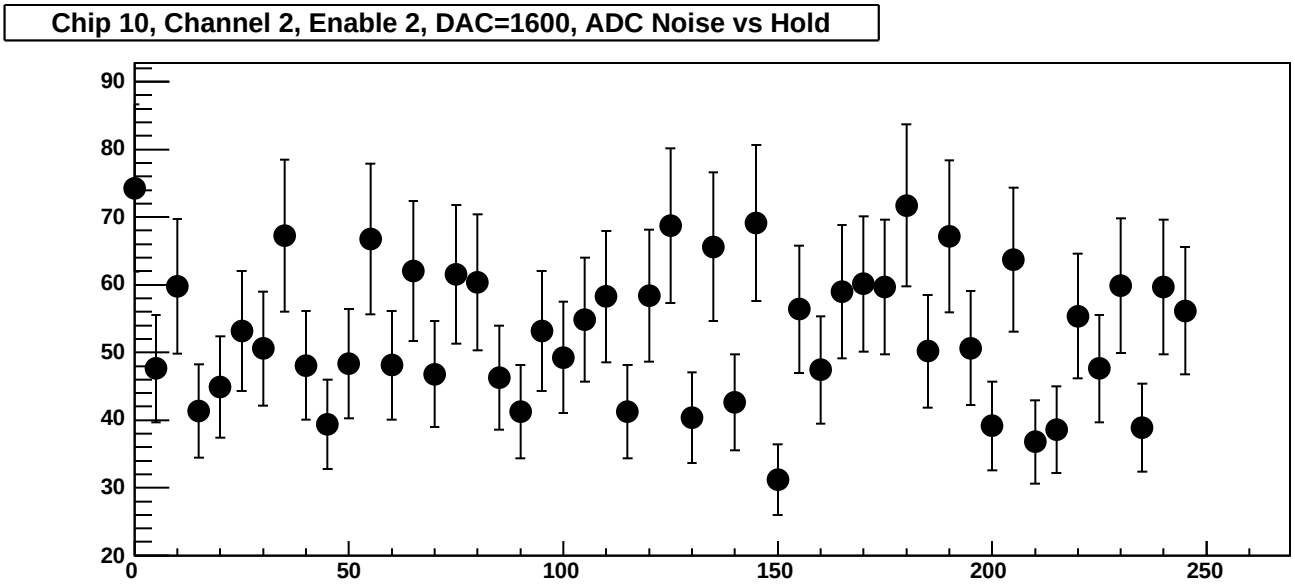
Chip 10, Channel 2, Enable 1, DAC=1600, ADC Noise vs Hold

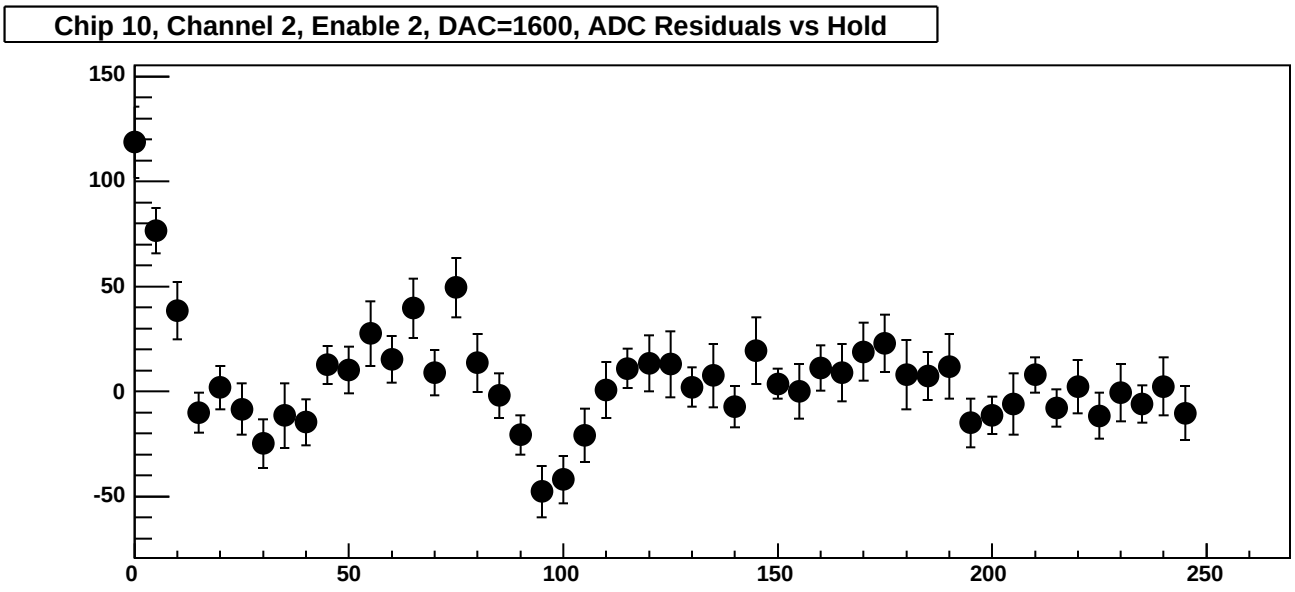


Chip 10, Channel 2, Enable 1, DAC=1600, ADC Residuals vs Hold

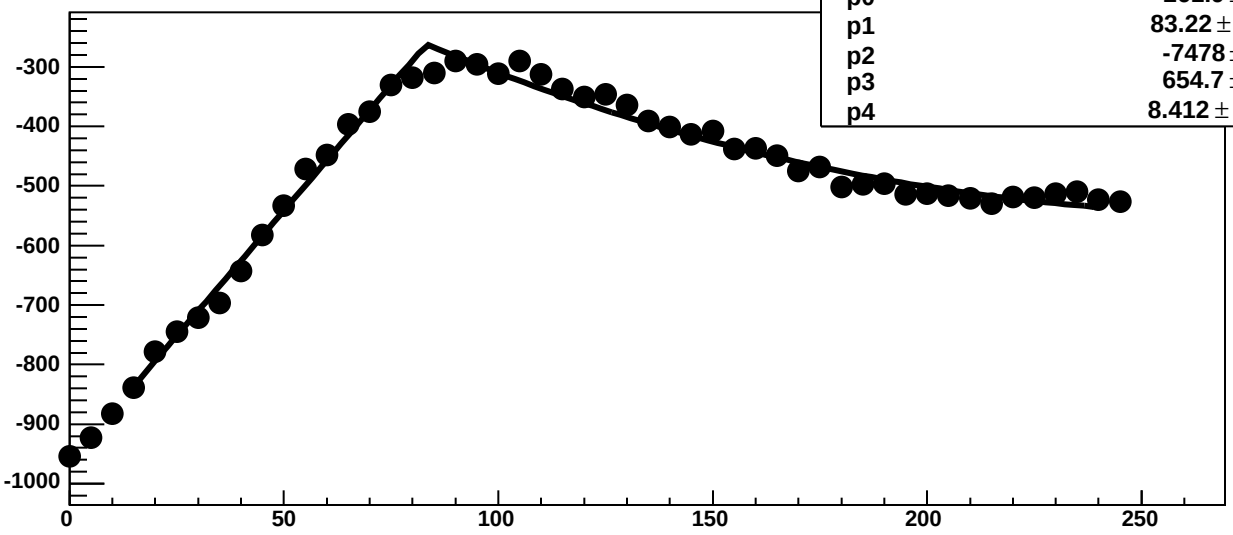



Chip 10, Channel 2, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 94.56 / 41
p0 -132.1 ± 3.853
p1 96.04 ± 0.6264
p2 -3.693e+08 ± 3.446e+07
p3 3.293e+07 ± 3.012e+06
p4 9.776 ± 0.1087
-100
-200
-300
-400
-500
-600
-700
-800
-900
-1000
0
50
100
150
200
250

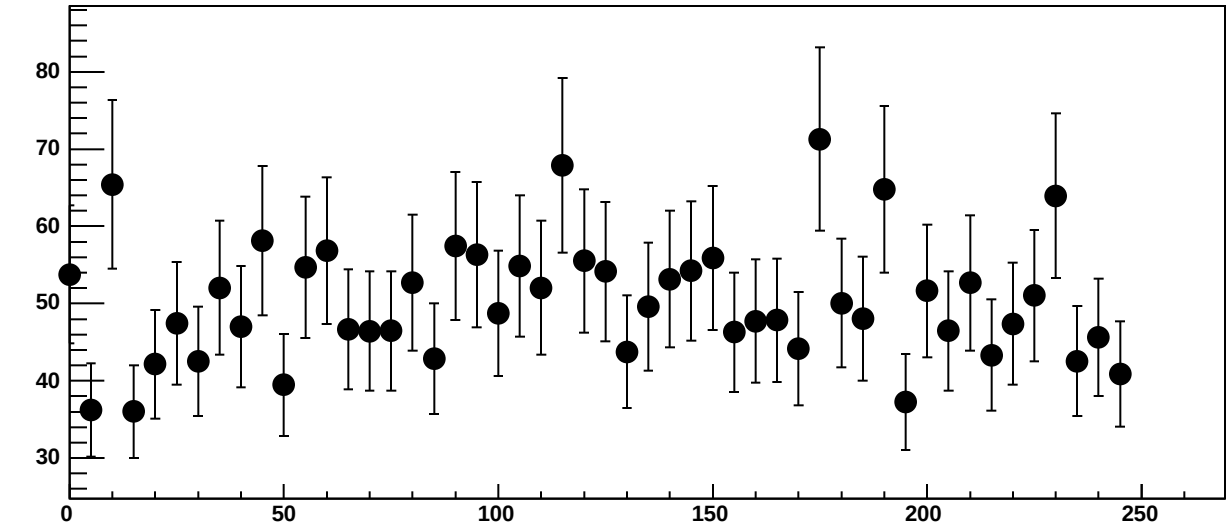

Chip 10, Channel 2, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 10, Channel 2, Enable 2, DAC=1600, ADC Residuals vs Hold
150
100
50
0
-50
0
50
100
150
200
250

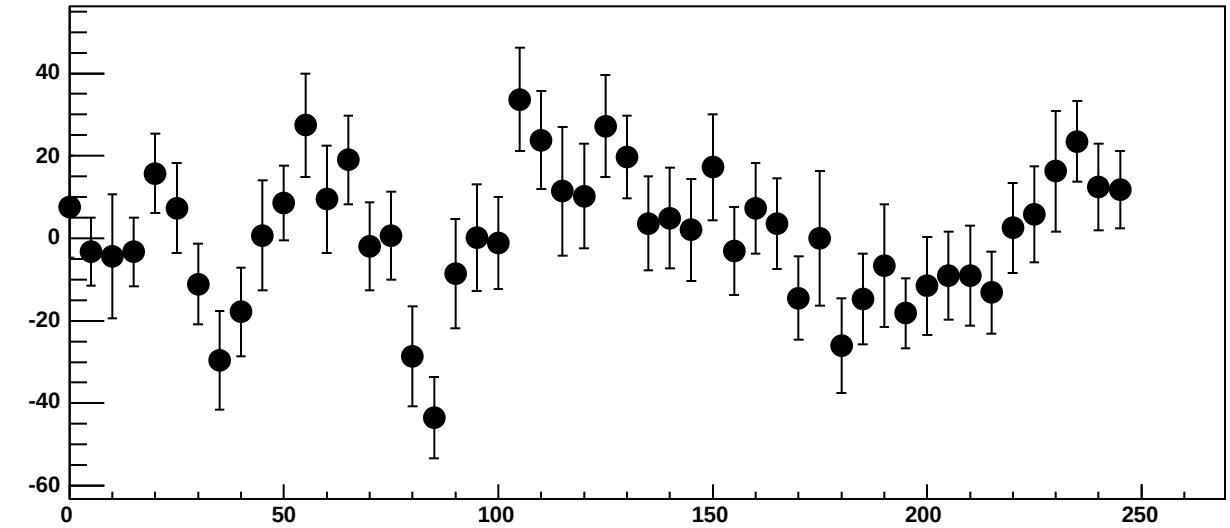
Chip 10, Channel 2, Enable 3, DAC=1600, ADC Mean vs Hold



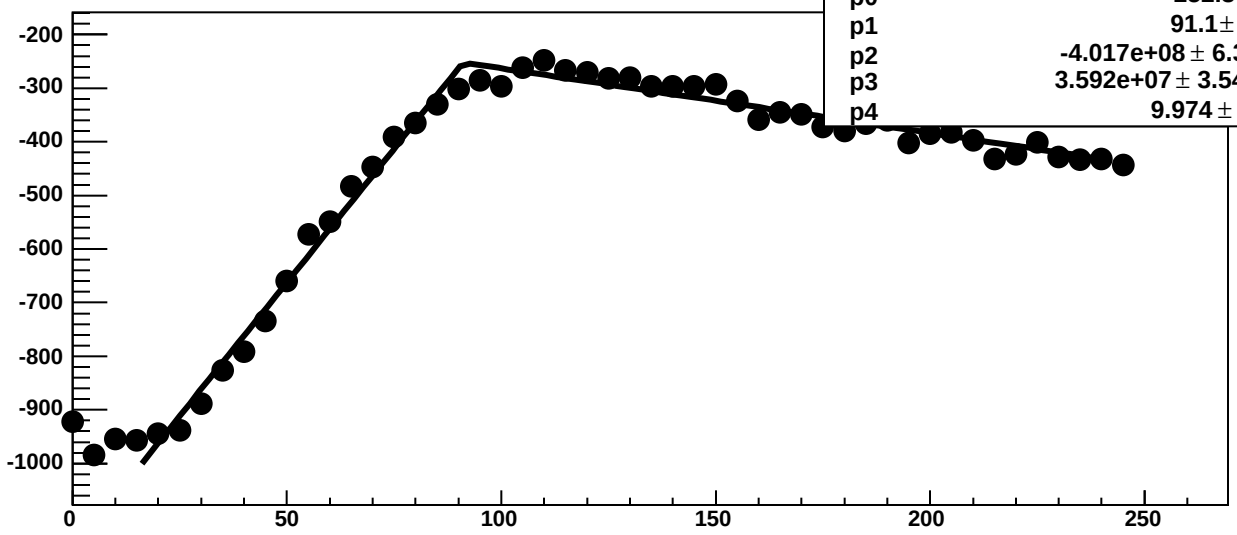
Chip 10, Channel 2, Enable 3, DAC=1600, ADC Noise vs Hold



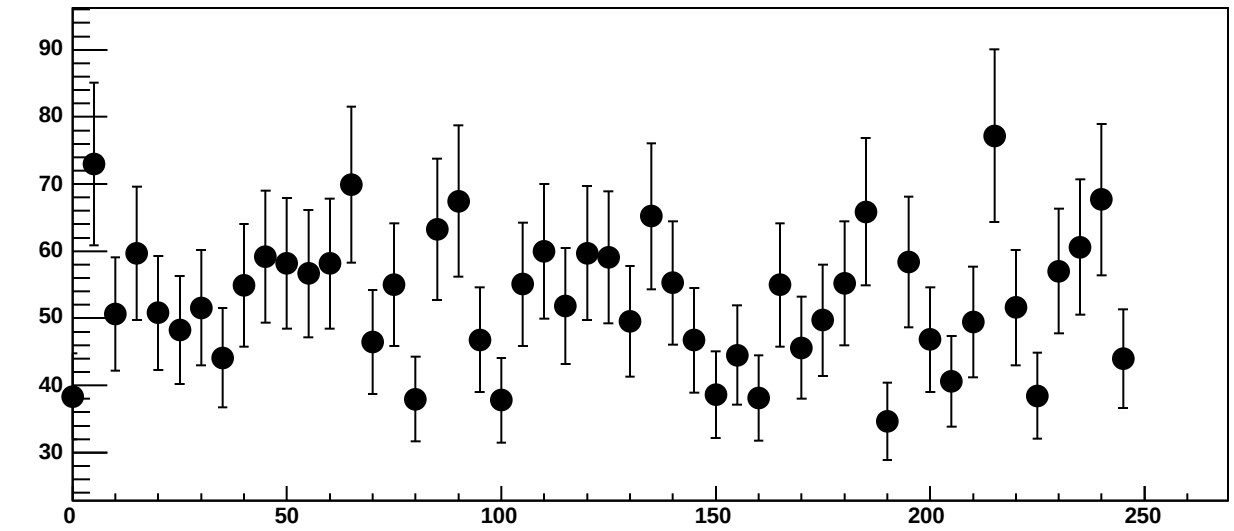
Chip 10, Channel 2, Enable 3, DAC=1600, ADC Residuals vs Hold



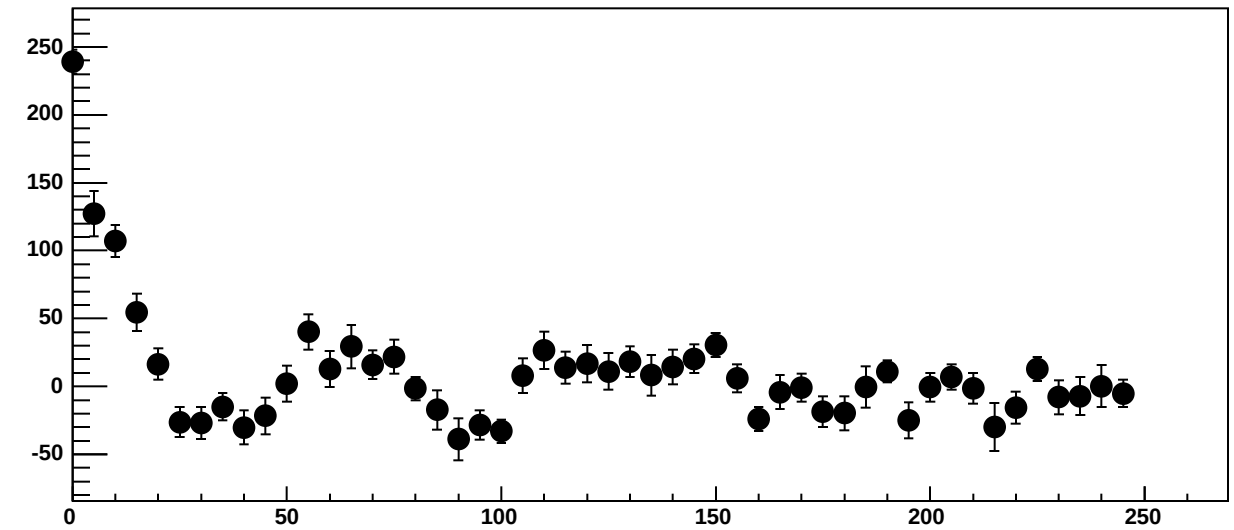
Chip 10, Channel 2, Enable 4, DAC=1600, ADC Mean vs Hold

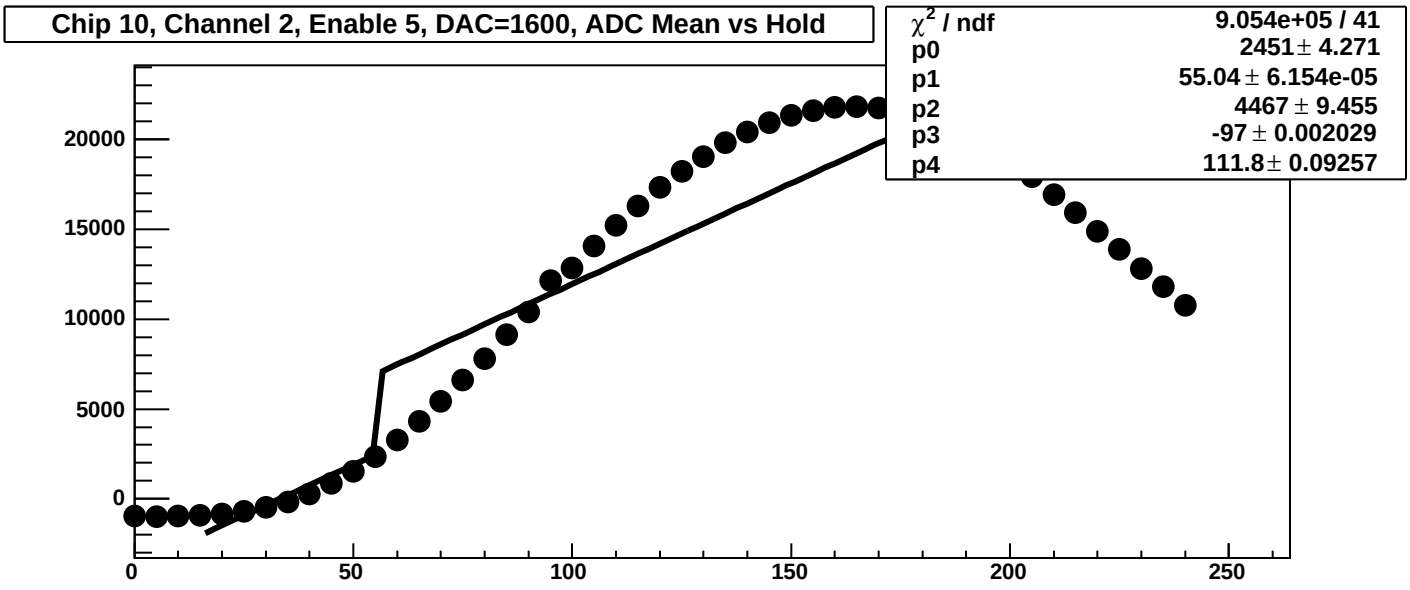


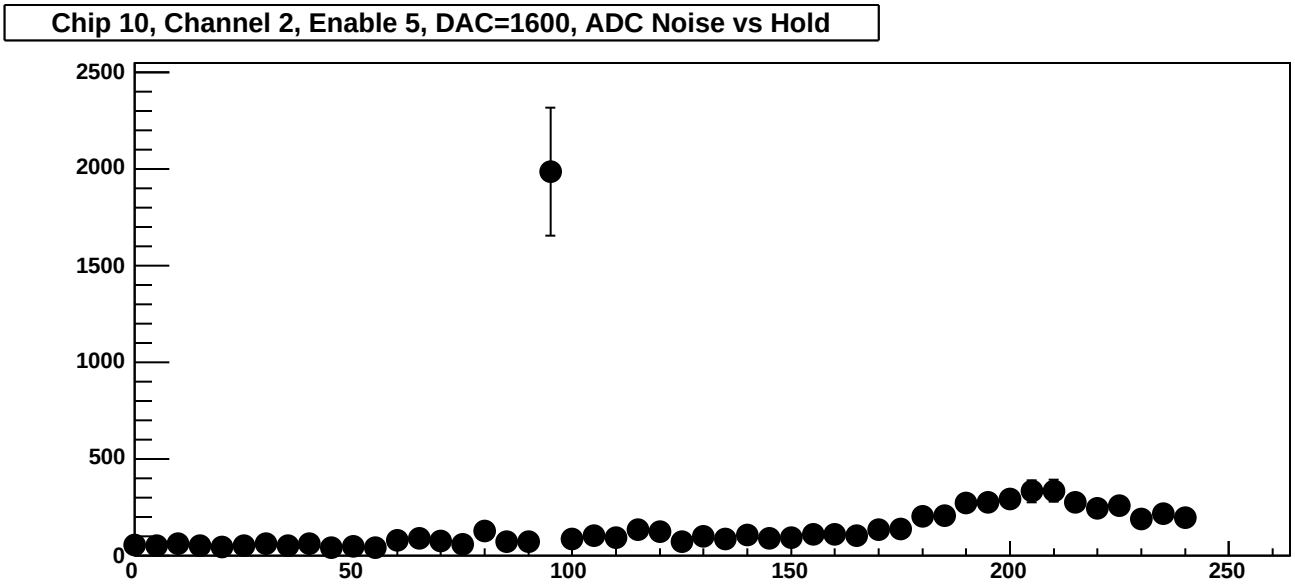
Chip 10, Channel 2, Enable 4, DAC=1600, ADC Noise vs Hold

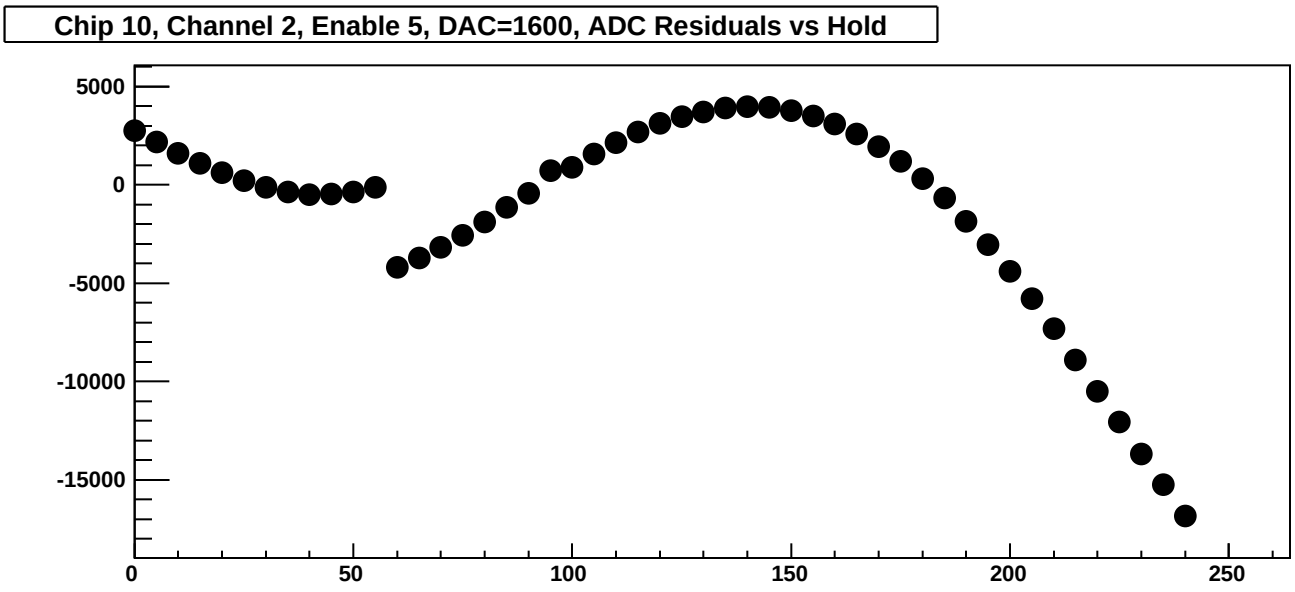


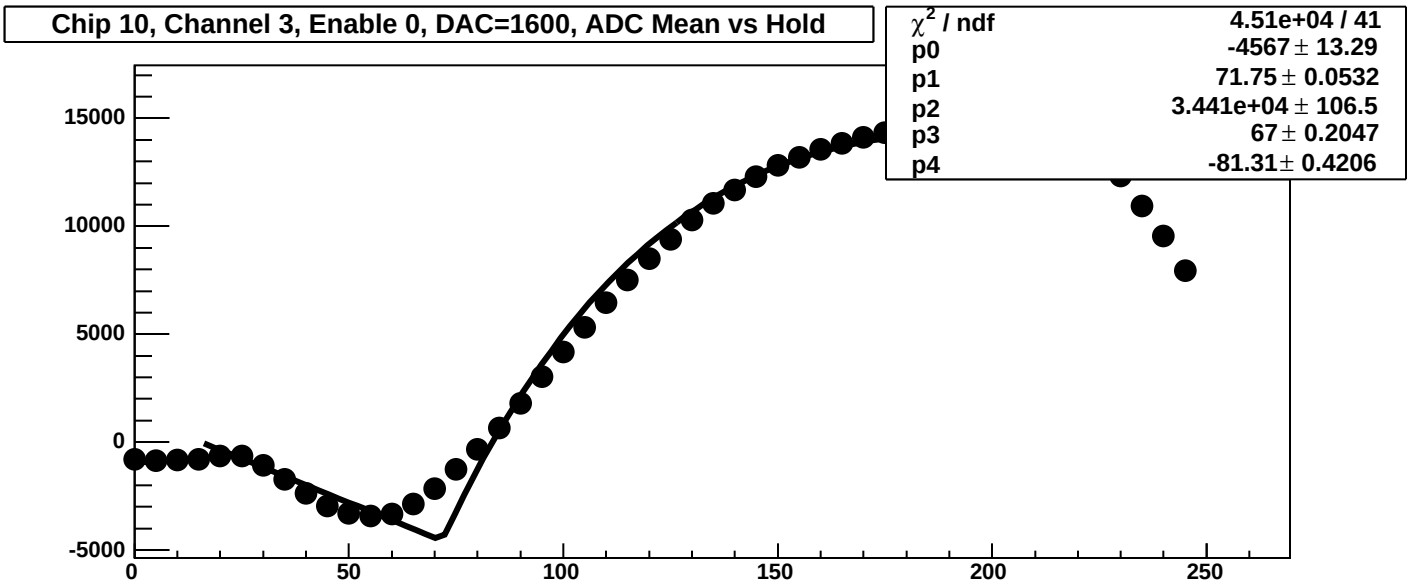
Chip 10, Channel 2, Enable 4, DAC=1600, ADC Residuals vs Hold

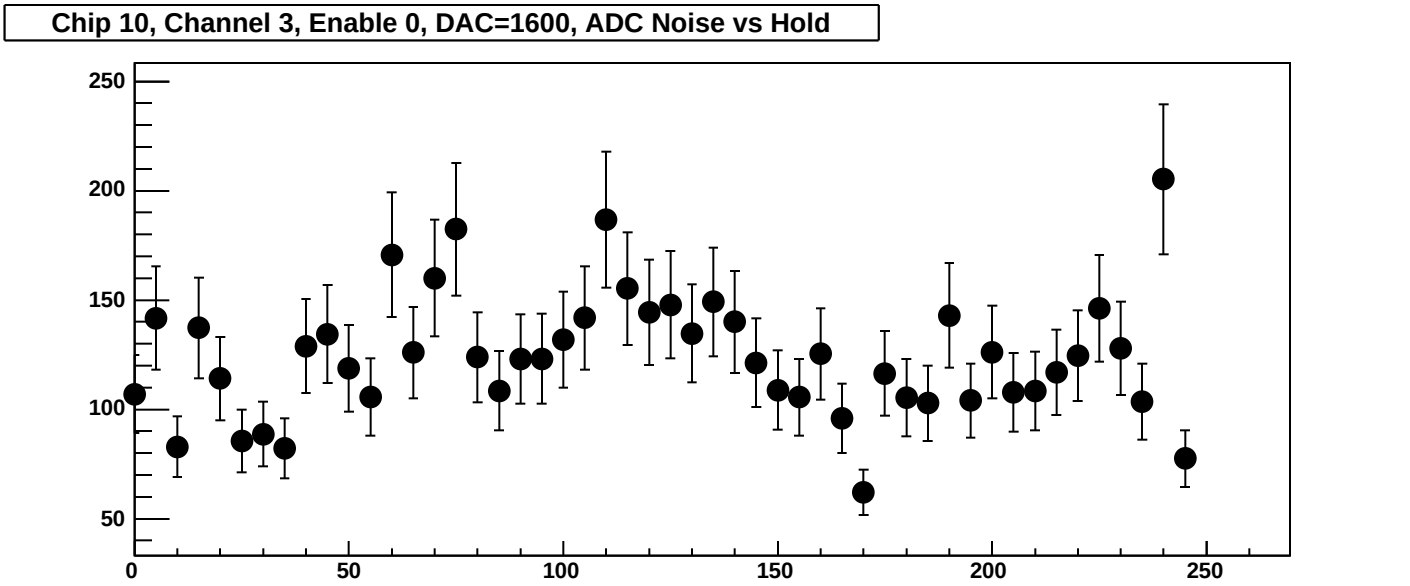


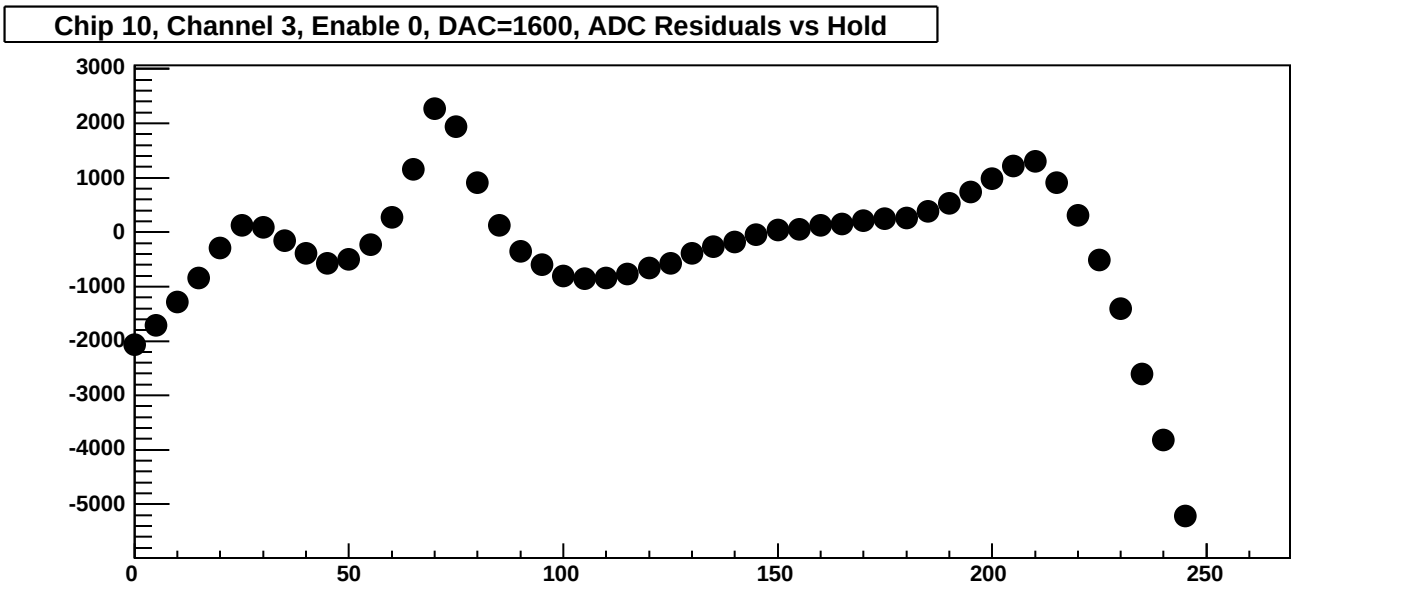

Chip 10, Channel 2, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 9.054e+05 / 41
p0 2451 ± 4.271
p1 55.04 ± 6.154e-05
p2 4467 ± 9.455
p3 -97 ± 0.002029
p4 111.8 ± 0.09257
20000
15000
10000
5000
0
0
50
100
150
200
250

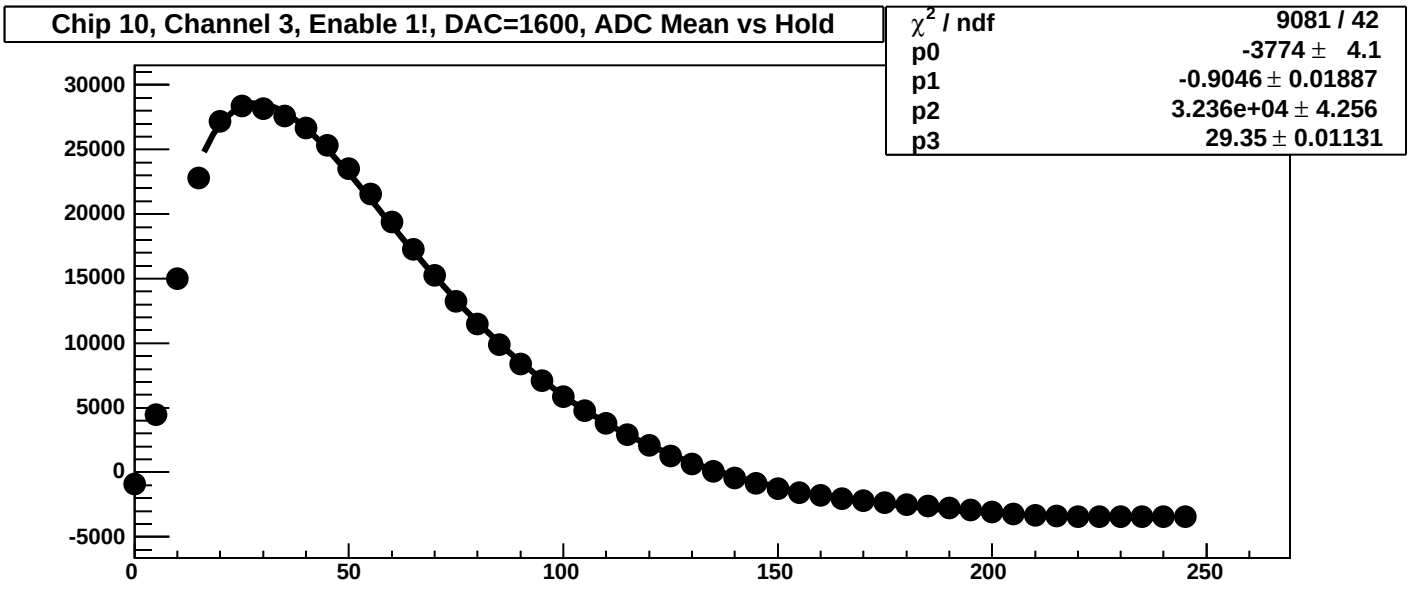

Chip 10, Channel 2, Enable 5, DAC=1600, ADC Noise vs Hold
2500
2000
1500
1000
500
0
0
50
100
150
200
250

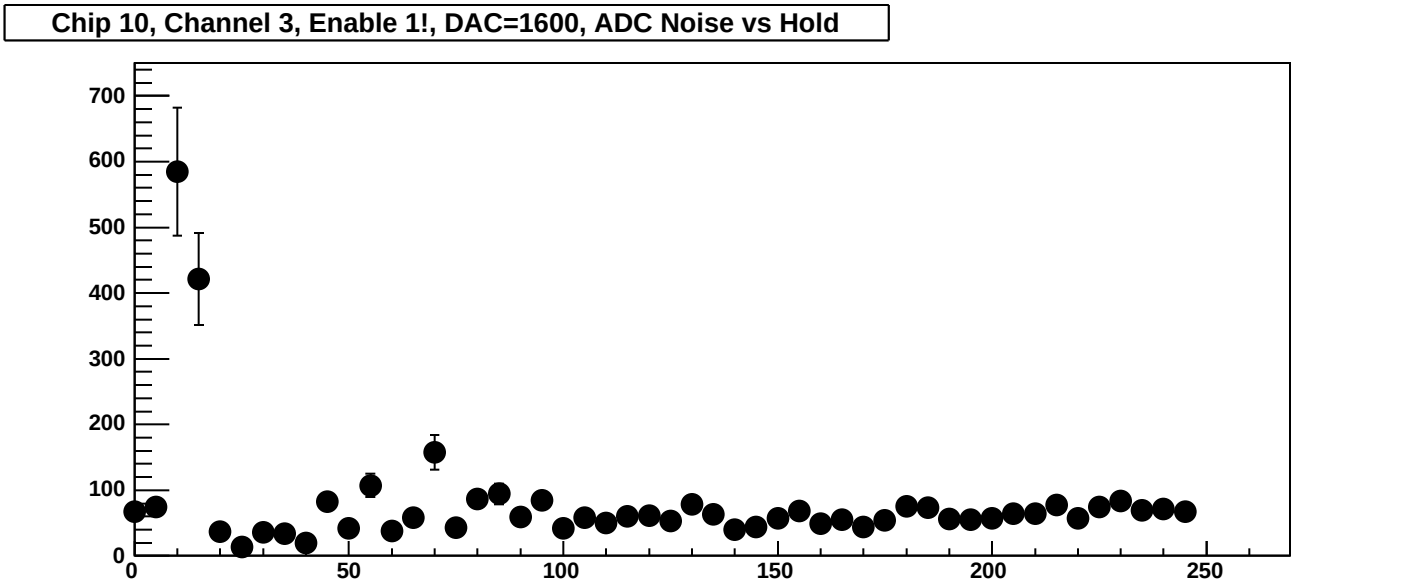

Chip 10, Channel 2, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
0
50
100
150
200
250

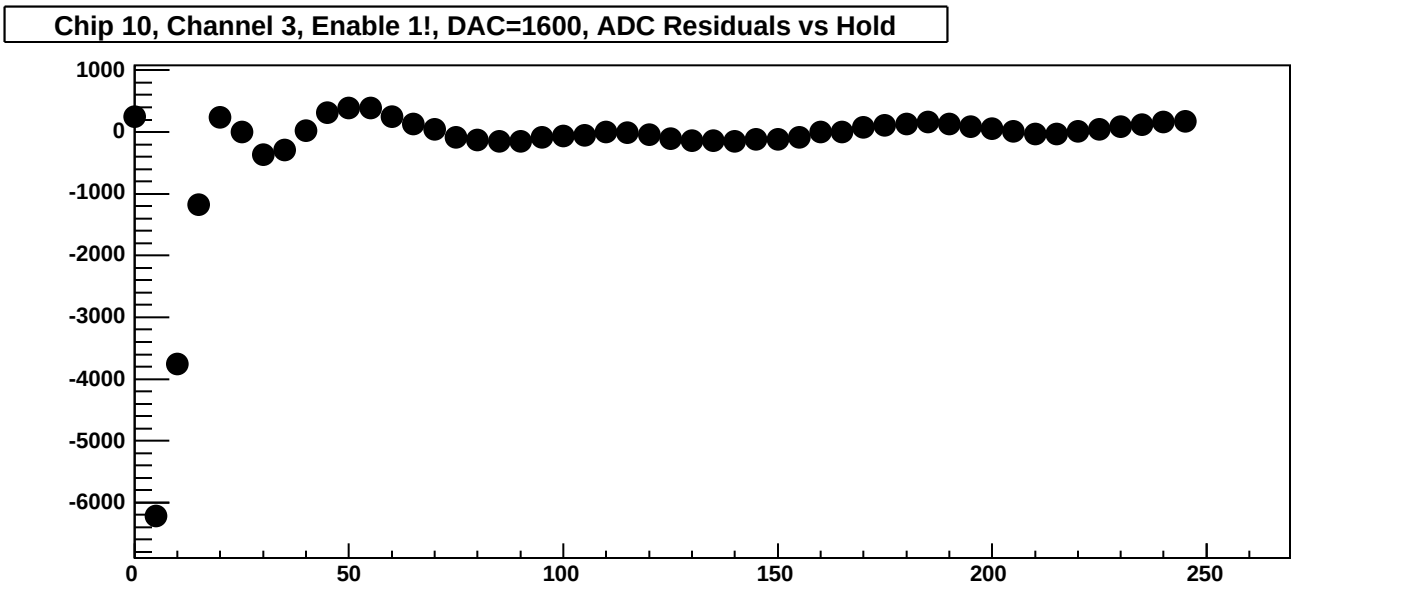

Chip 10, Channel 3, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.51e+04 / 41
p0 -4567 ± 13.29
p1 71.75 ± 0.0532
p2 3.441e+04 ± 106.5
p3 67 ± 0.2047
p4 -81.31 ± 0.4206
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 10, Channel 3, Enable 0, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
50
100
150
200
250

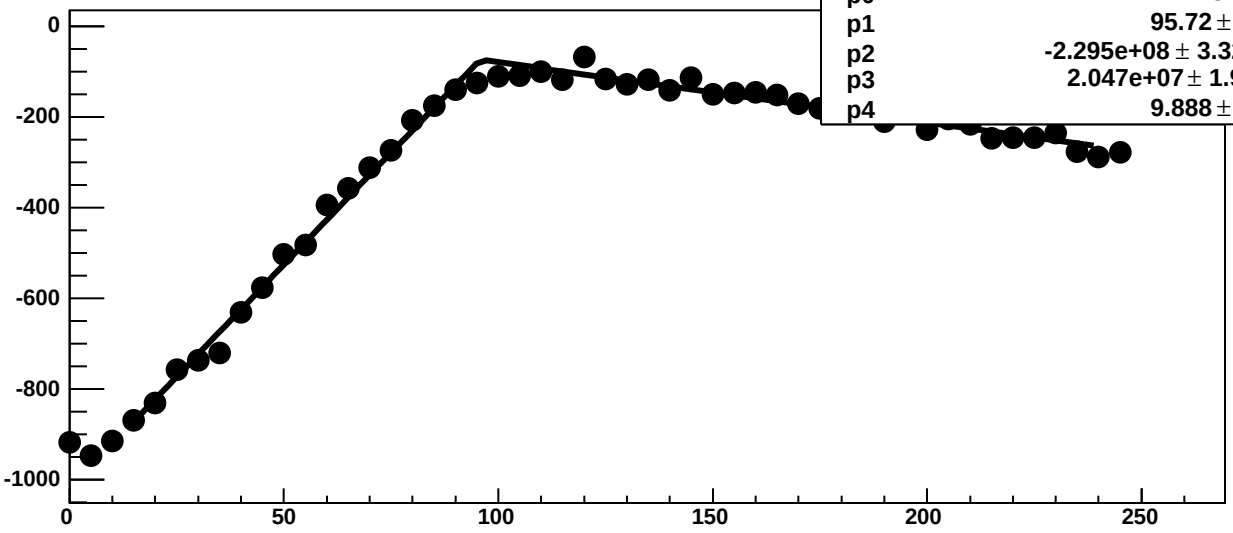

Chip 10, Channel 3, Enable 0, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250


Chip 10, Channel 3, Enable 1!, DAC=1600, ADC Mean vs Hold
χ² / ndf 9081 / 42
p0 -3774 ± 4.1
p1 -0.9046 ± 0.01887
p2 3.236e+04 ± 4.256
p3 29.35 ± 0.01131
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

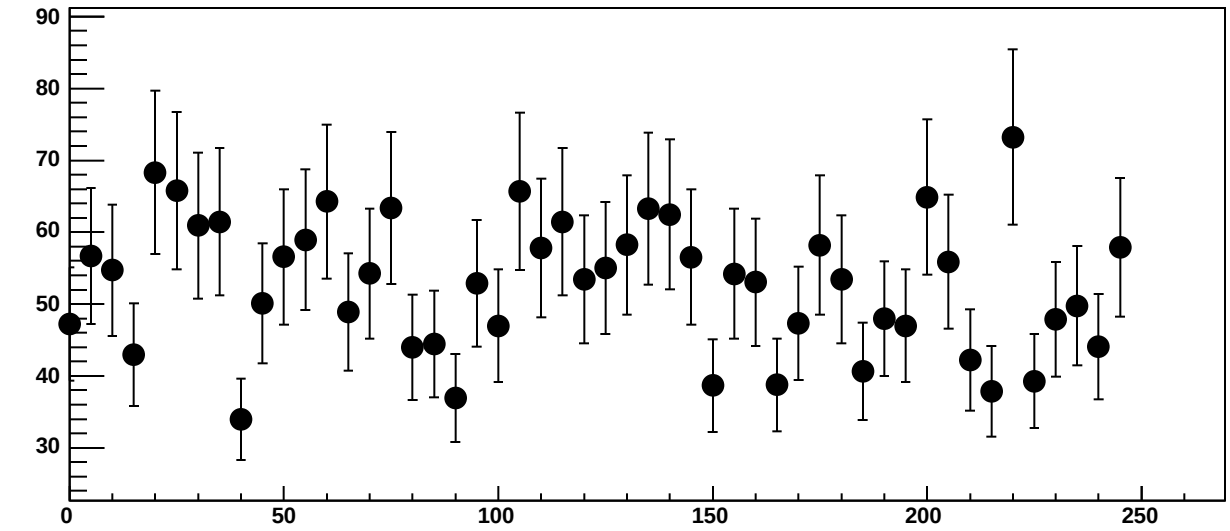

Chip 10, Channel 3, Enable 1!, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 10, Channel 3, Enable 1!, DAC=1600, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
50
100
150
200
250

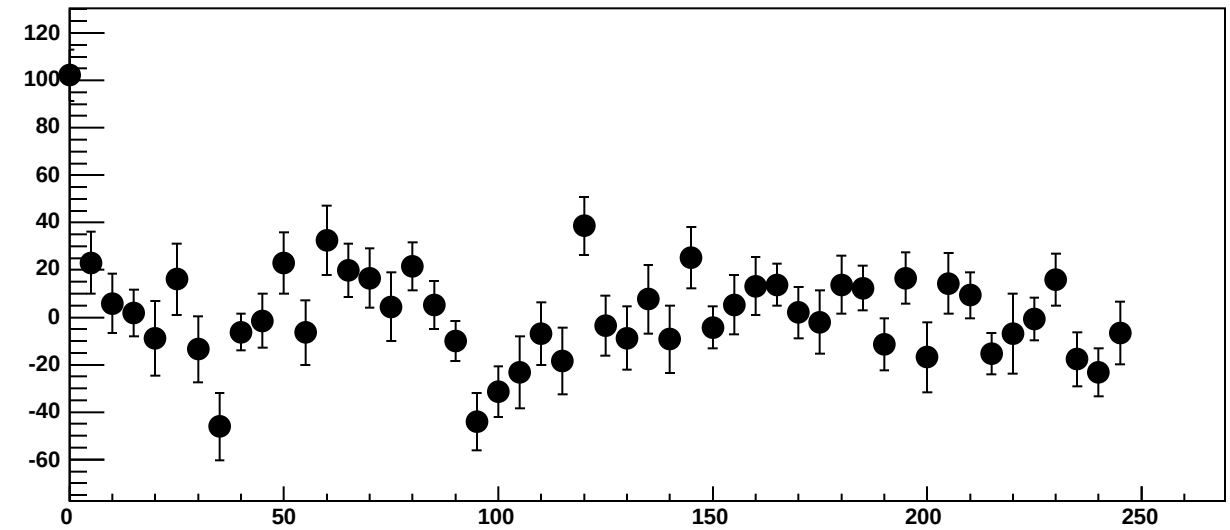
Chip 10, Channel 3, Enable 2, DAC=1600, ADC Mean vs Hold

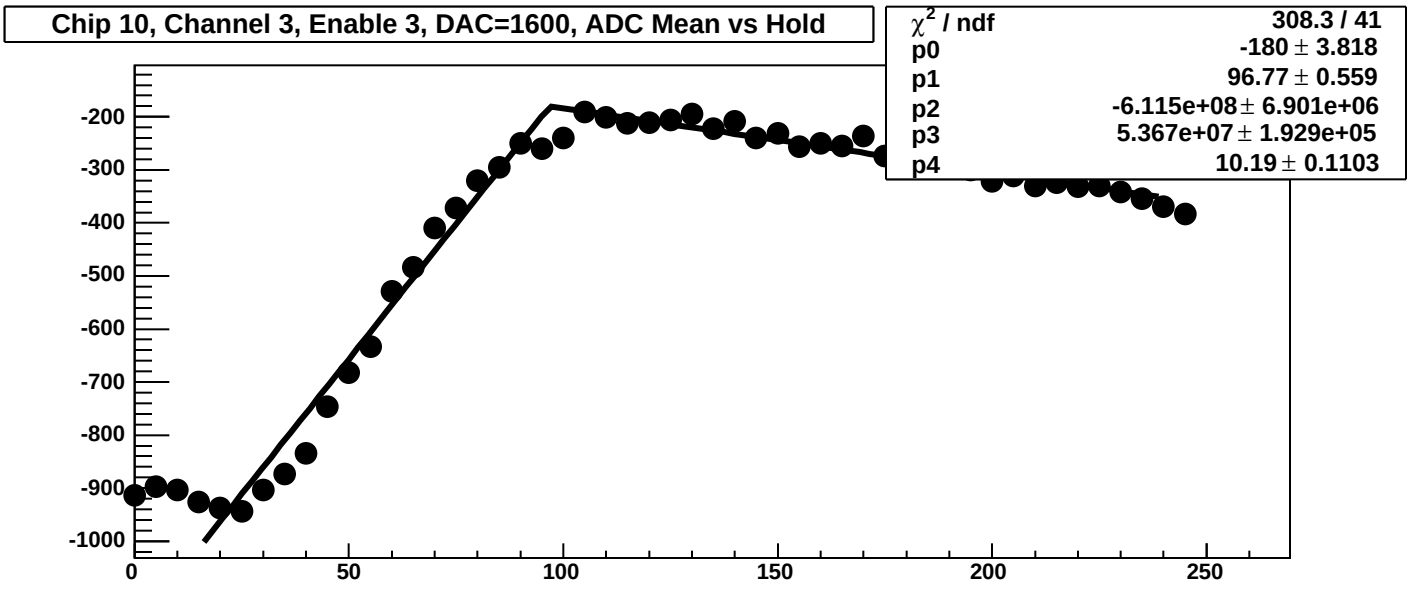


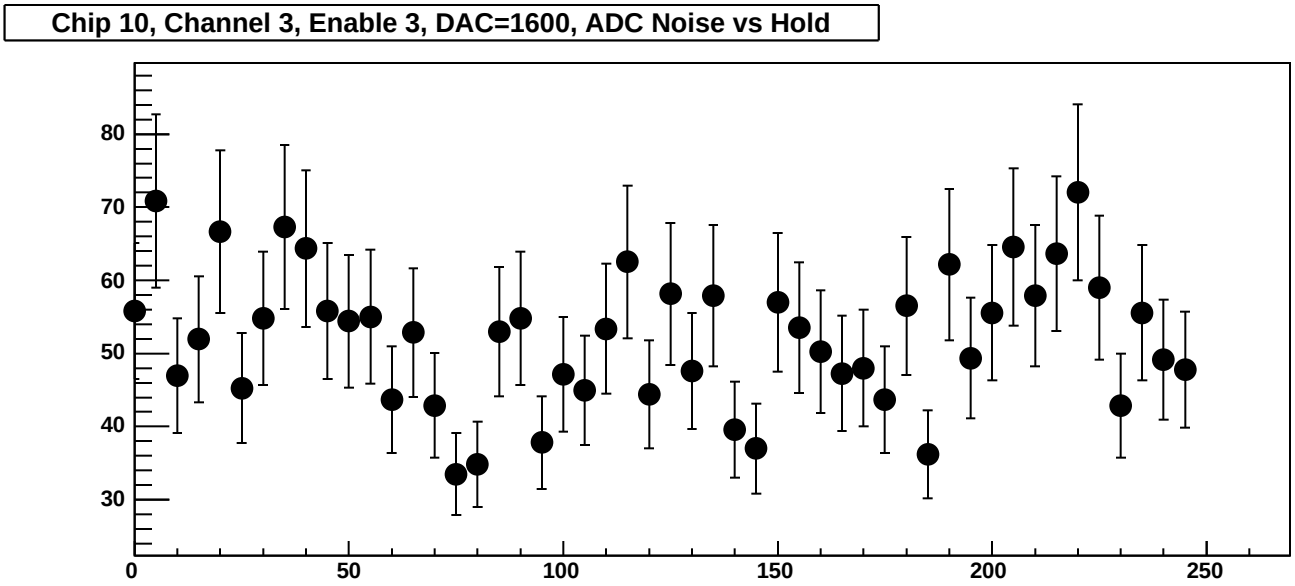
Chip 10, Channel 3, Enable 2, DAC=1600, ADC Noise vs Hold

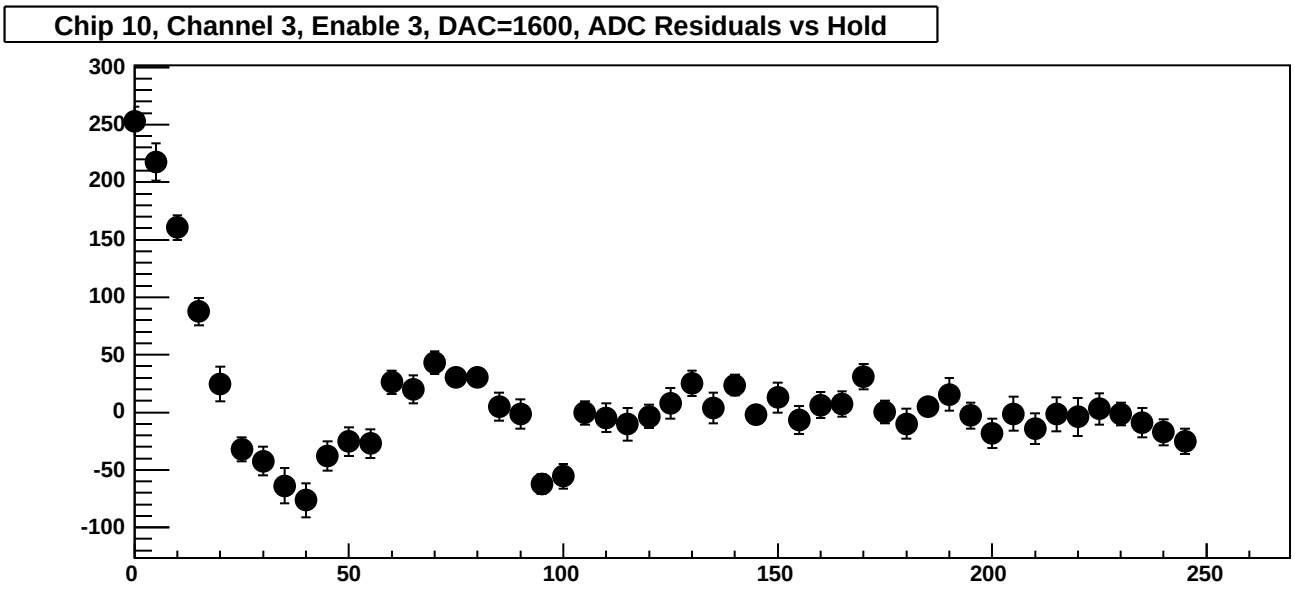


Chip 10, Channel 3, Enable 2, DAC=1600, ADC Residuals vs Hold

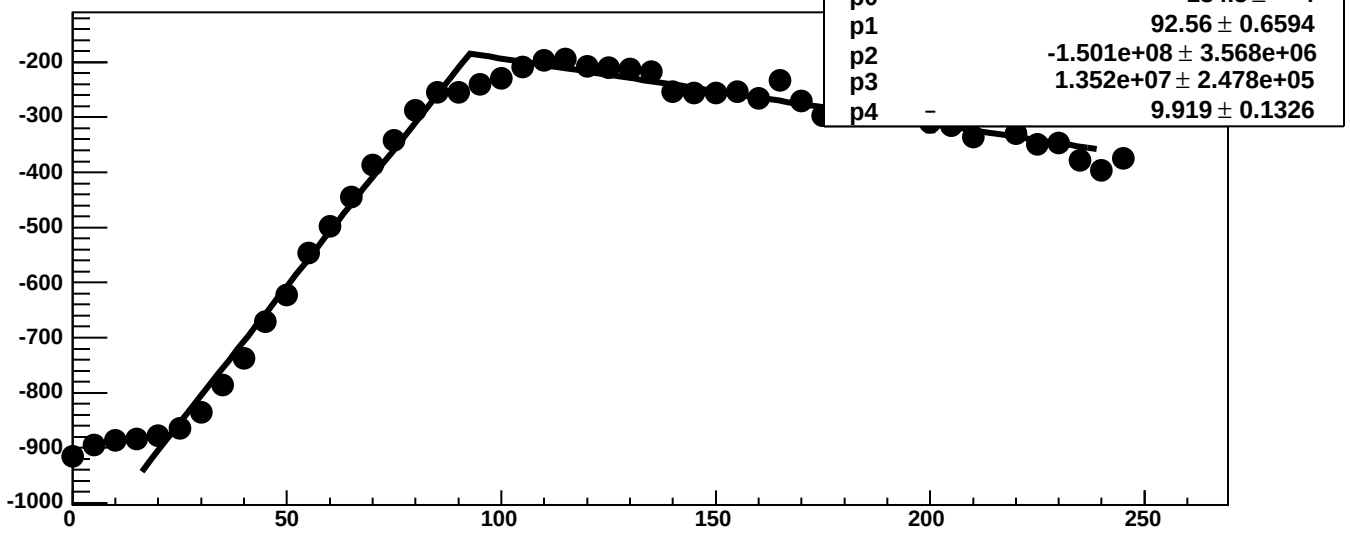



Chip 10, Channel 3, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 308.3 / 41
p0 -180 ± 3.818
p1 96.77 ± 0.559
p2 -6.115e+08 ± 6.901e+06
p3 5.367e+07 ± 1.929e+05
p4 10.19 ± 0.1103
-200
-300
-400
-500
-600
-700
-800
-900
-1000
0
50
100
150
200
250

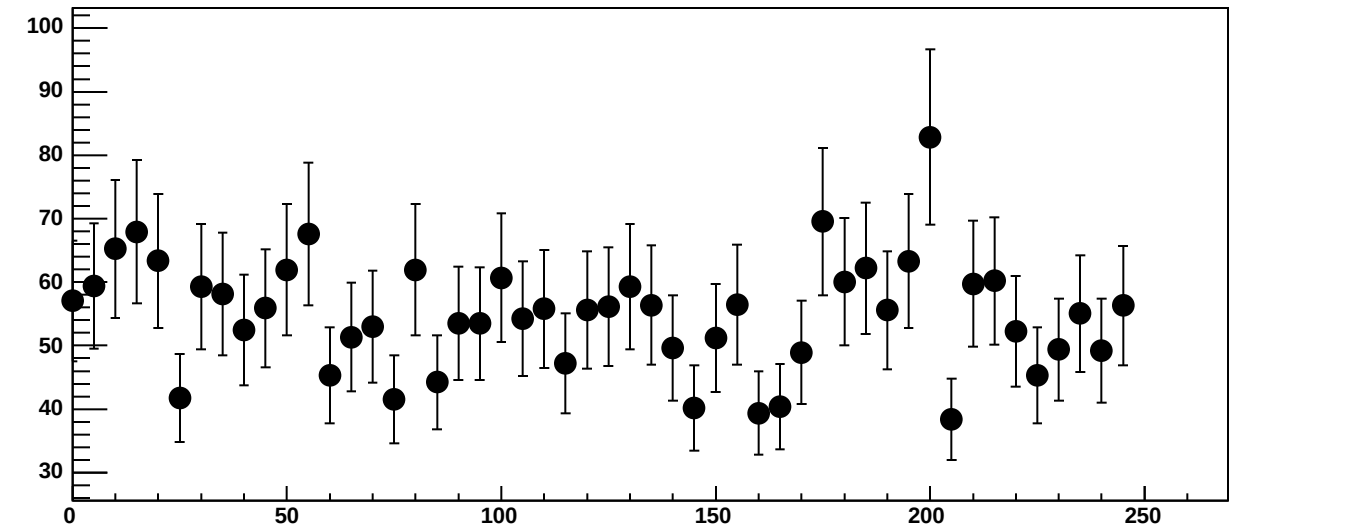

Chip 10, Channel 3, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 10, Channel 3, Enable 3, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

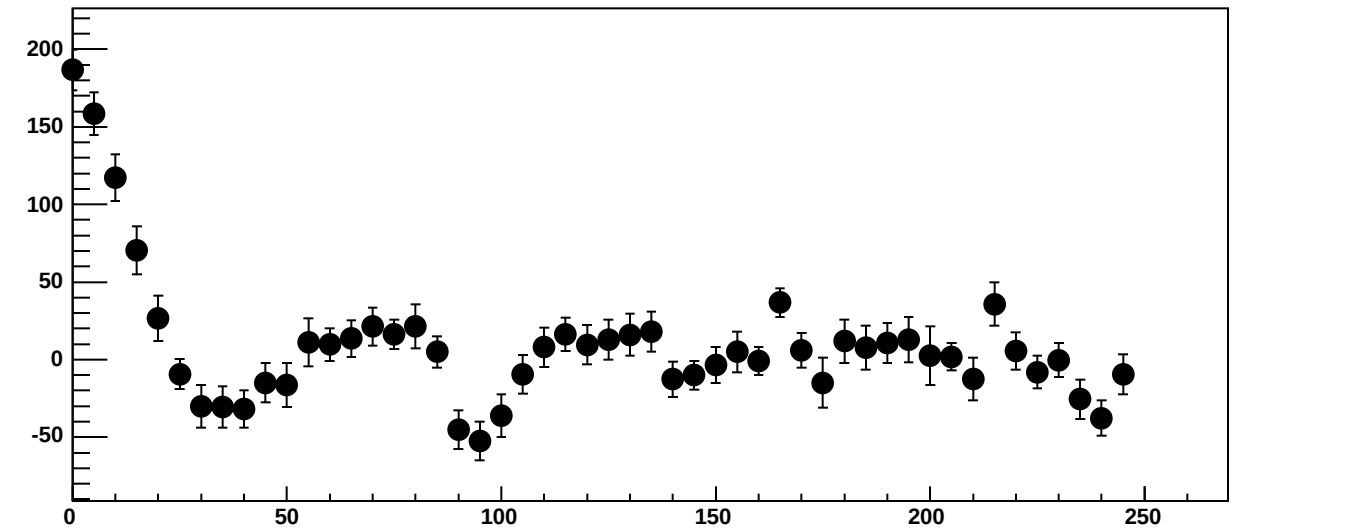
Chip 10, Channel 3, Enable 4, DAC=1600, ADC Mean vs Hold



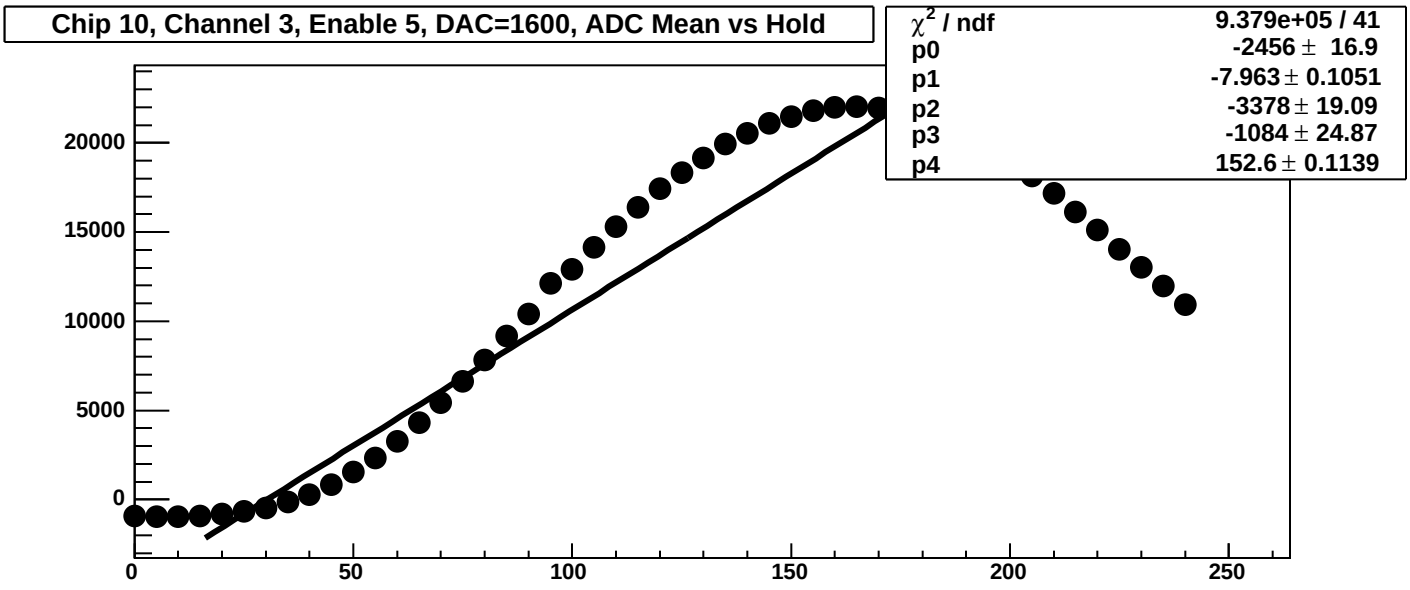
Chip 10, Channel 3, Enable 4, DAC=1600, ADC Noise vs Hold



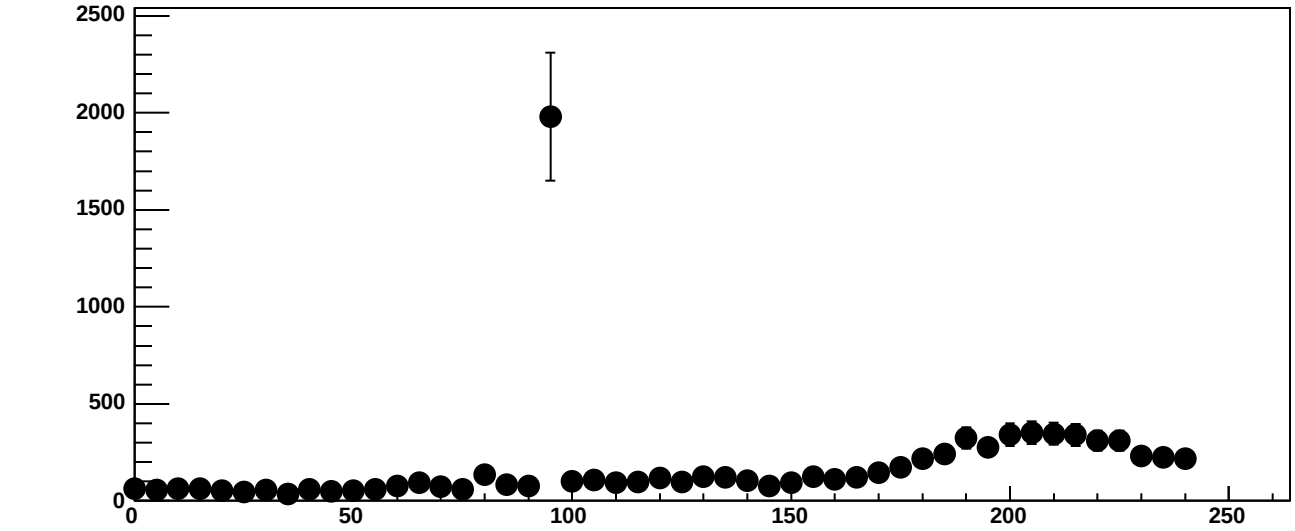
Chip 10, Channel 3, Enable 4, DAC=1600, ADC Residuals vs Hold



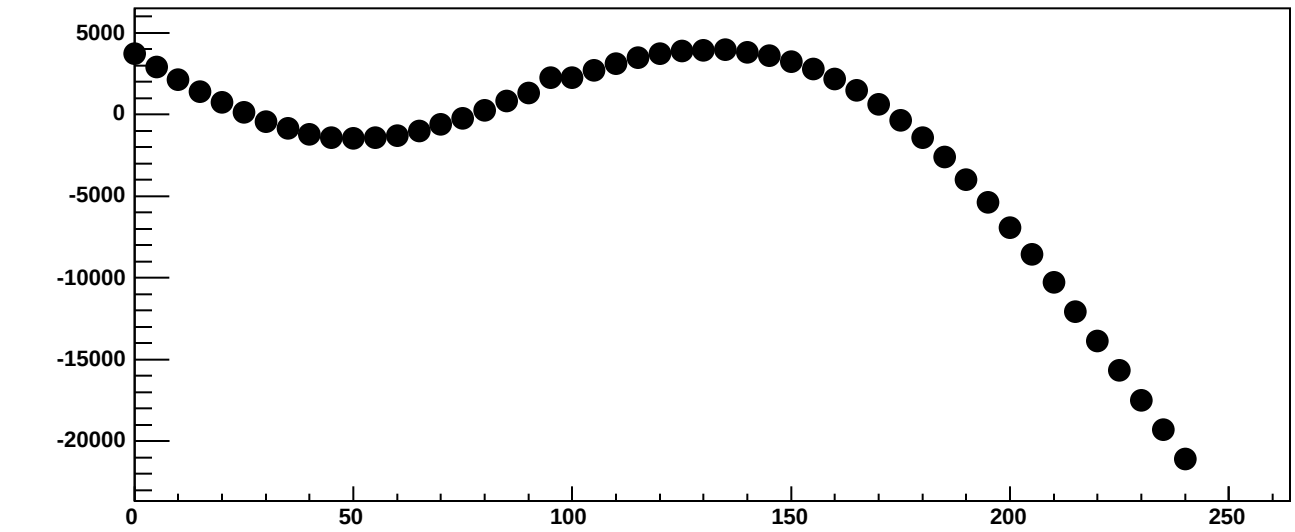
Chip 10, Channel 3, Enable 5, DAC=1600, ADC Mean vs Hold

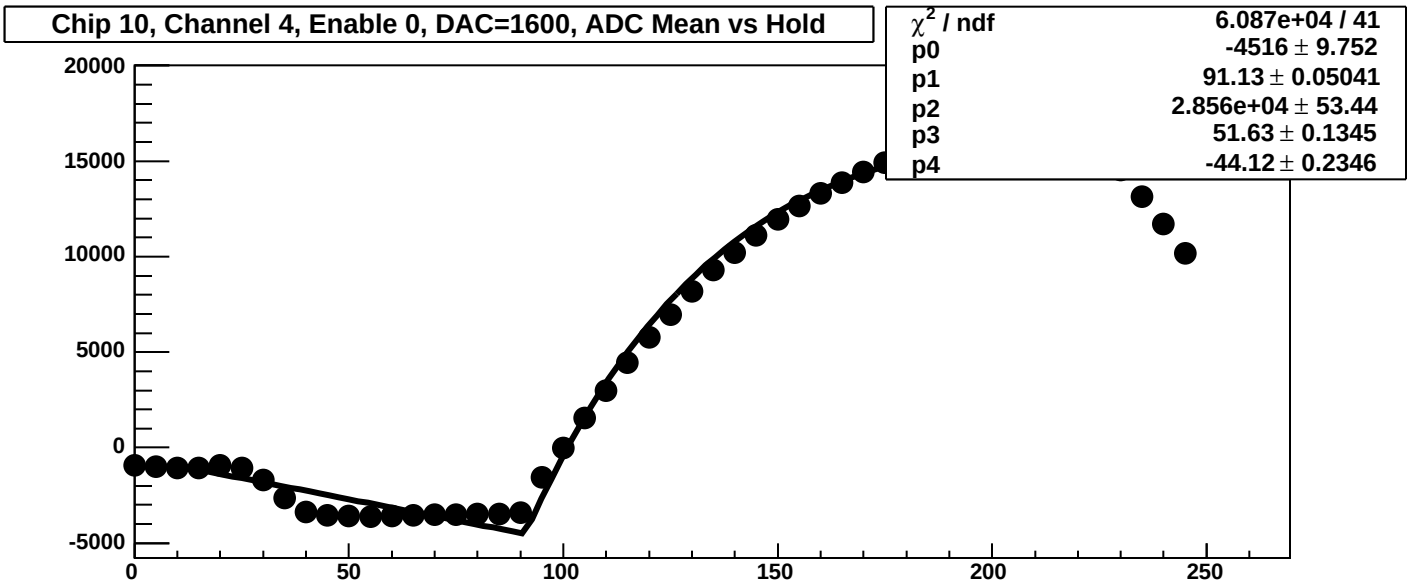


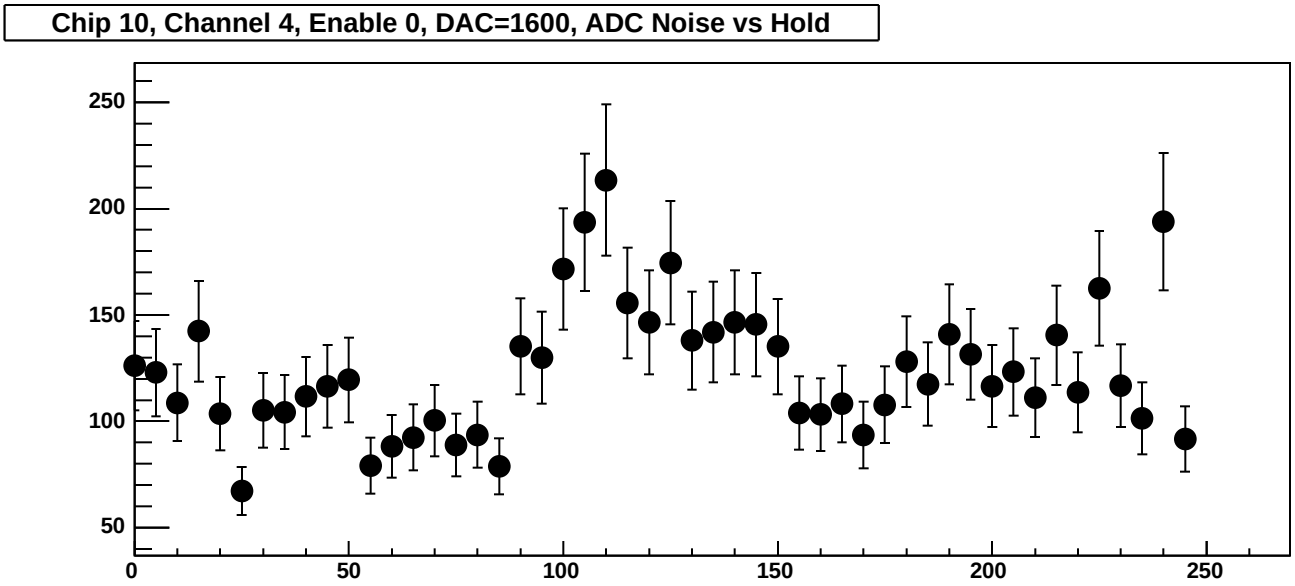
Chip 10, Channel 3, Enable 5, DAC=1600, ADC Noise vs Hold

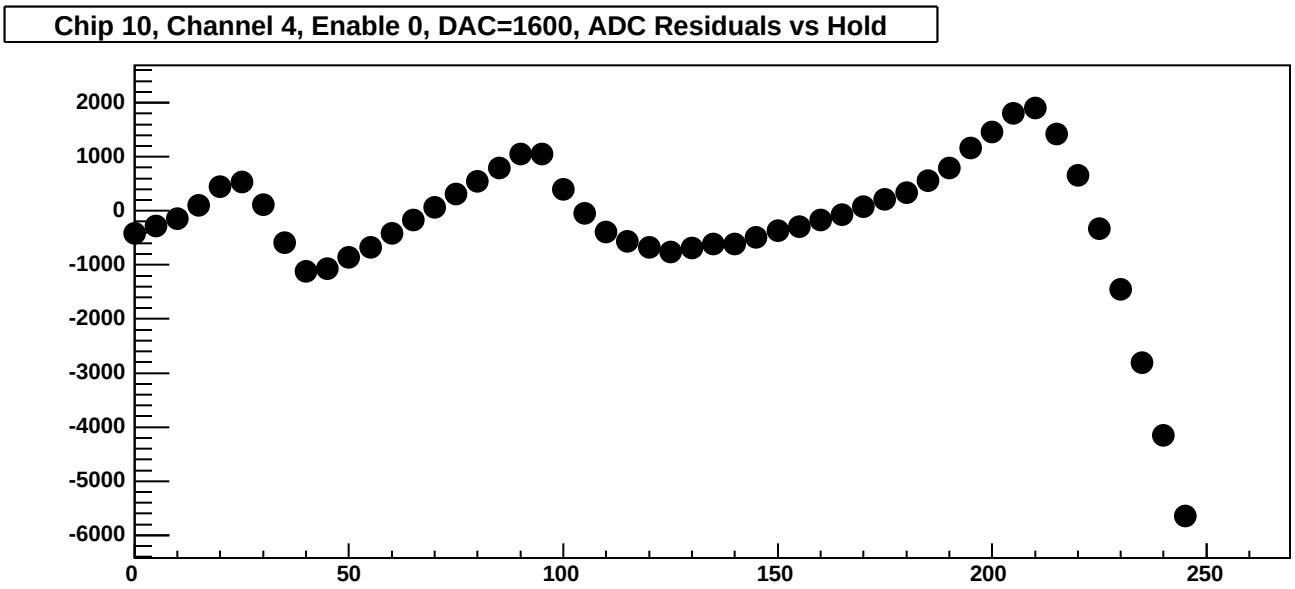


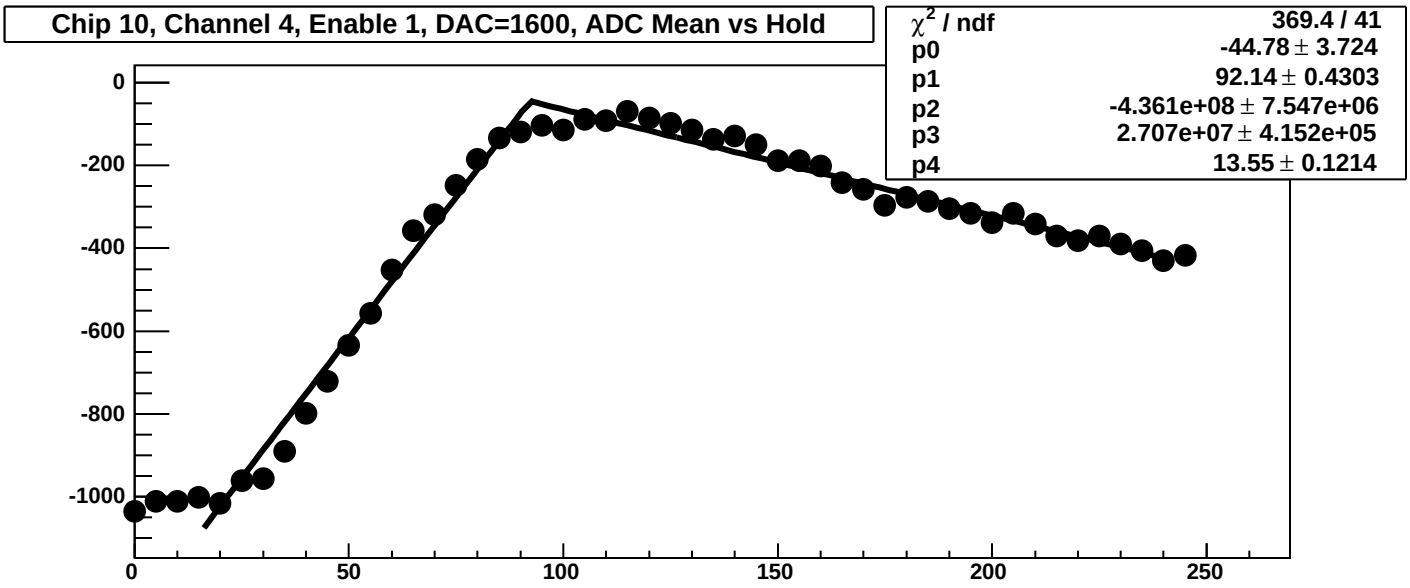
Chip 10, Channel 3, Enable 5, DAC=1600, ADC Residuals vs Hold

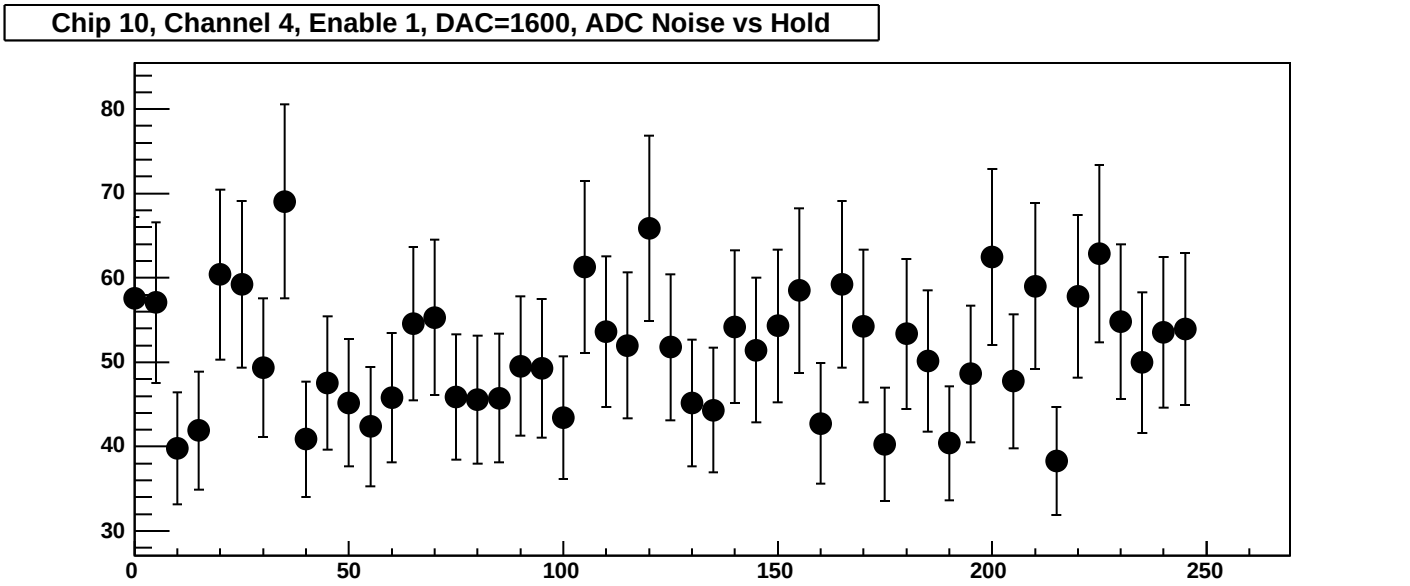


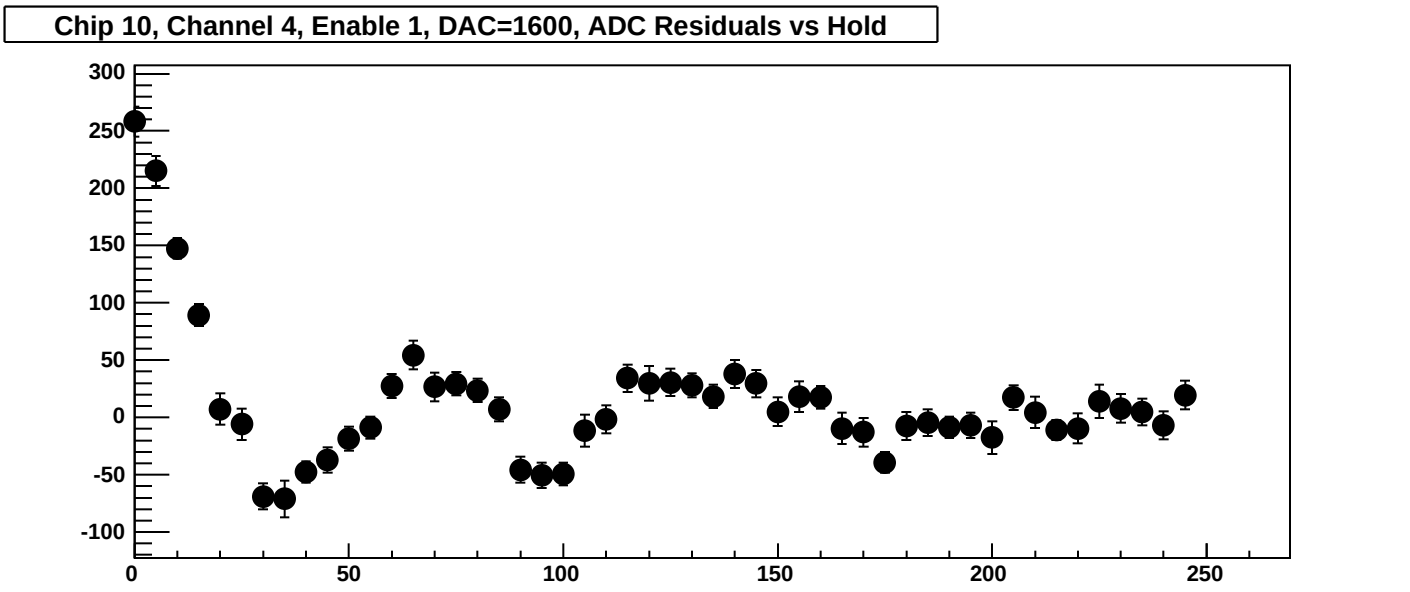

Chip 10, Channel 4, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 6.087e+04 / 41
p0 -4516 ± 9.752
p1 91.13 ± 0.05041
p2 2.856e+04 ± 53.44
p3 51.63 ± 0.1345
p4 -44.12 ± 0.2346
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 10, Channel 4, Enable 0, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
50
100
150
200
250

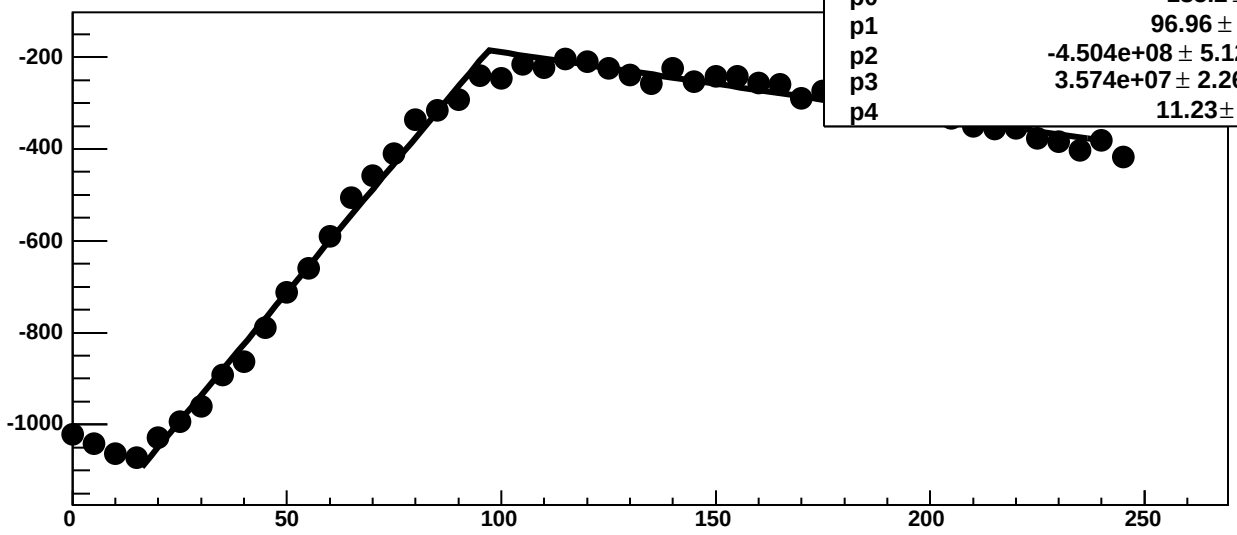

Chip 10, Channel 4, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
50
100
150
200
250


Chip 10, Channel 4, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 369.4 / 41
p0 -44.78 ± 3.724
p1 92.14 ± 0.4303
p2 -4.361e+08 ± 7.547e+06
p3 2.707e+07 ± 4.152e+05
p4 13.55 ± 0.1214
0
-200
-400
-600
-800
-1000
0
50
100
150
200
250

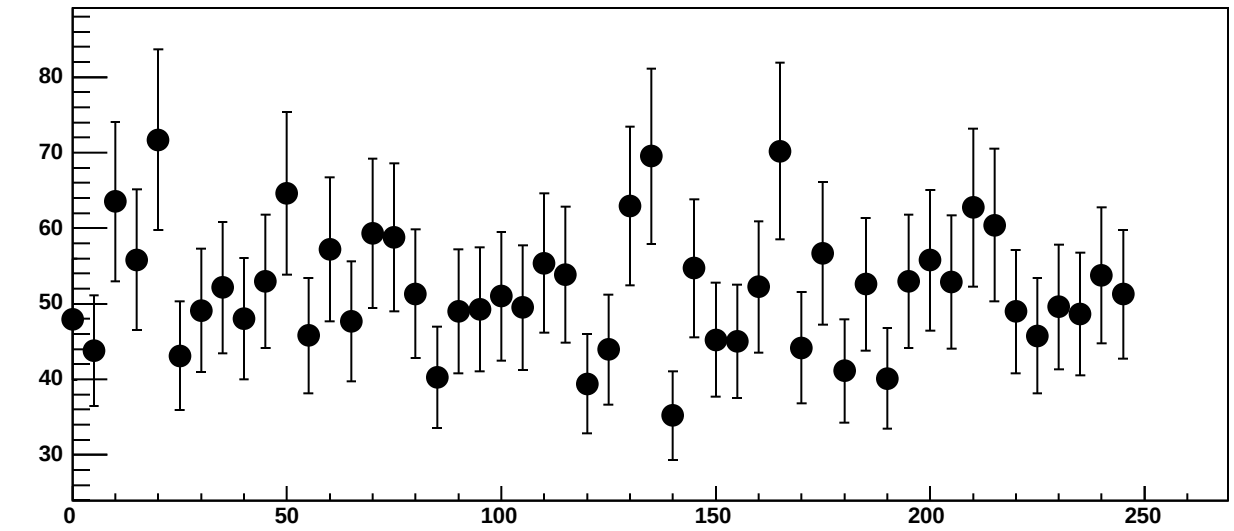

Chip 10, Channel 4, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 10, Channel 4, Enable 1, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

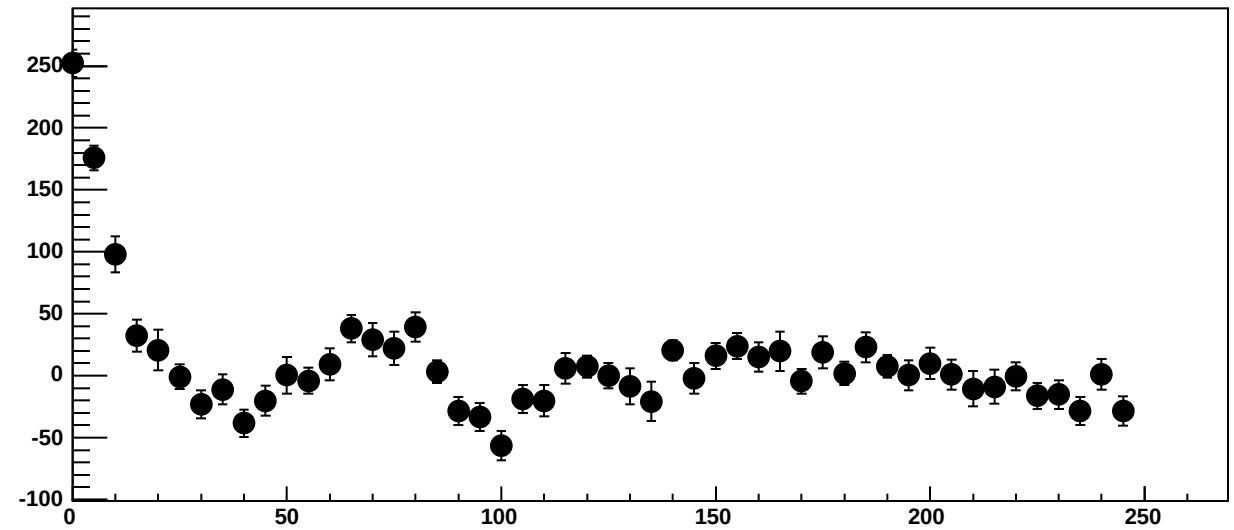
Chip 10, Channel 4, Enable 2, DAC=1600, ADC Mean vs Hold

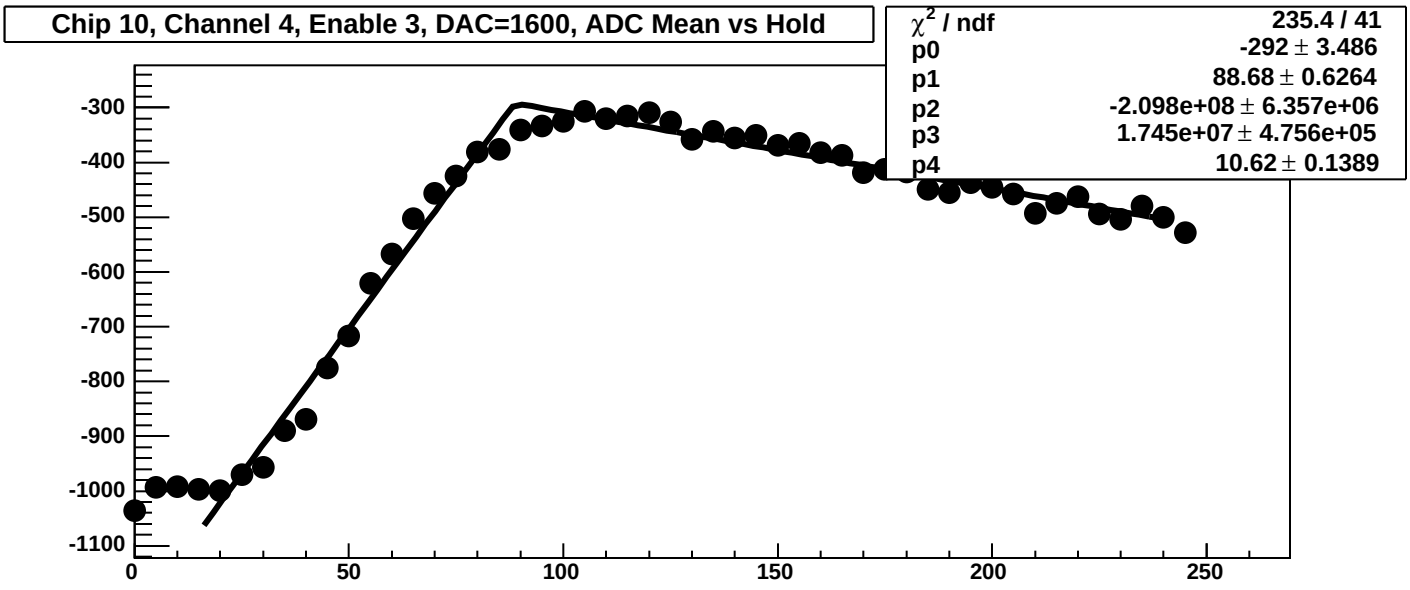


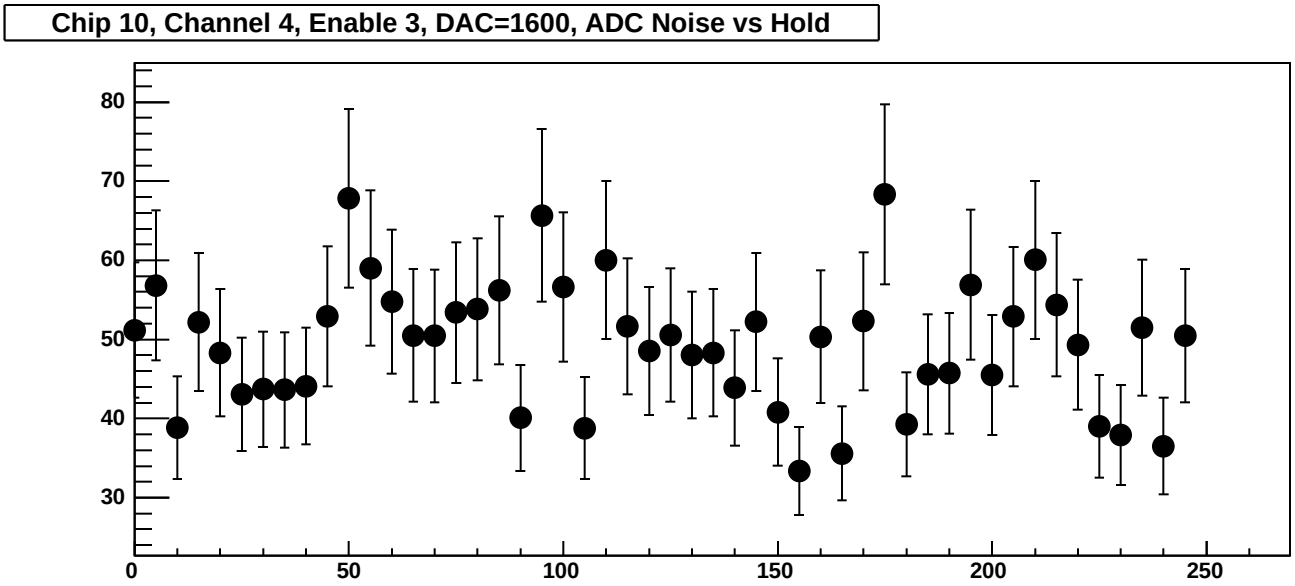
Chip 10, Channel 4, Enable 2, DAC=1600, ADC Noise vs Hold

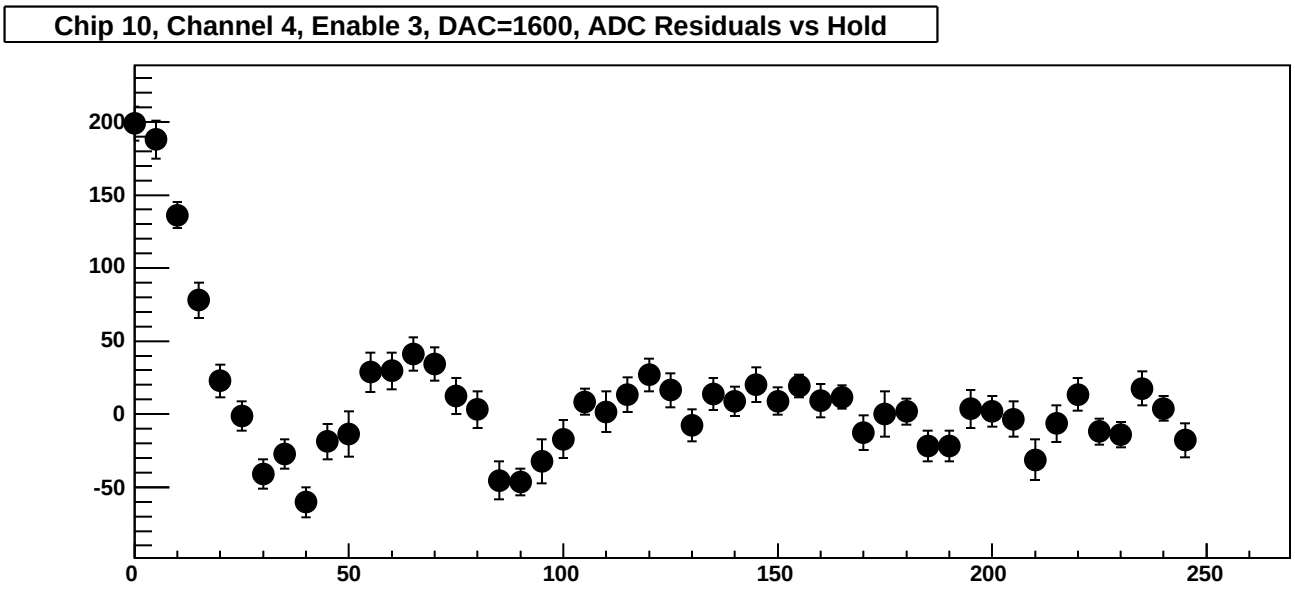


Chip 10, Channel 4, Enable 2, DAC=1600, ADC Residuals vs Hold



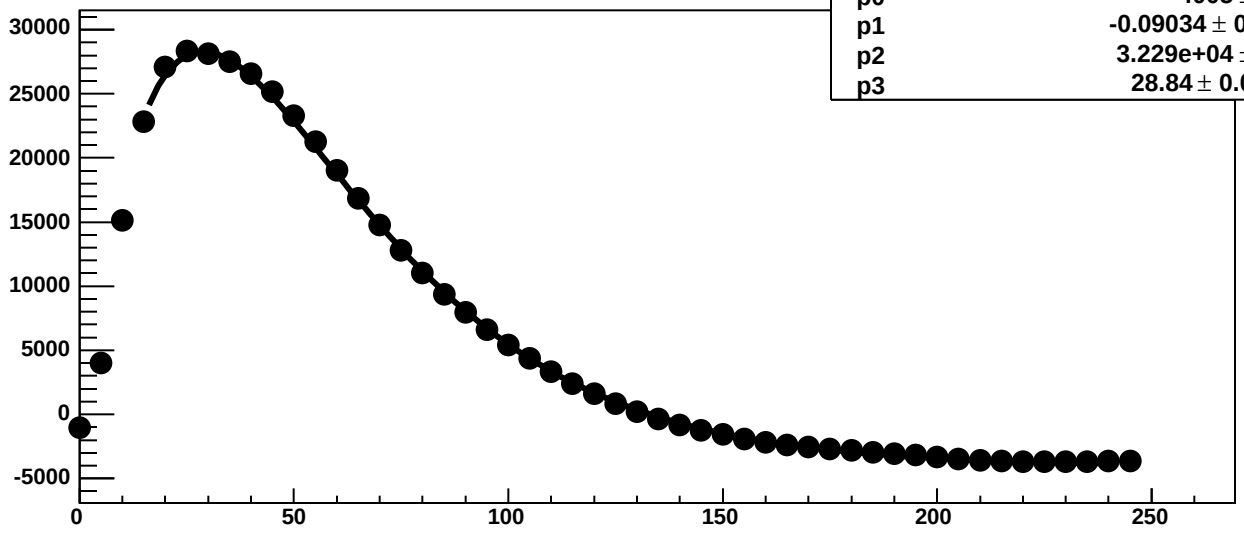

Chip 10, Channel 4, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 235.4 / 41
p0 -292 ± 3.486
p1 88.68 ± 0.6264
p2 -2.098e+08 ± 6.357e+06
p3 1.745e+07 ± 4.756e+05
p4 10.62 ± 0.1389
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
50
100
150
200
250


Chip 10, Channel 4, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

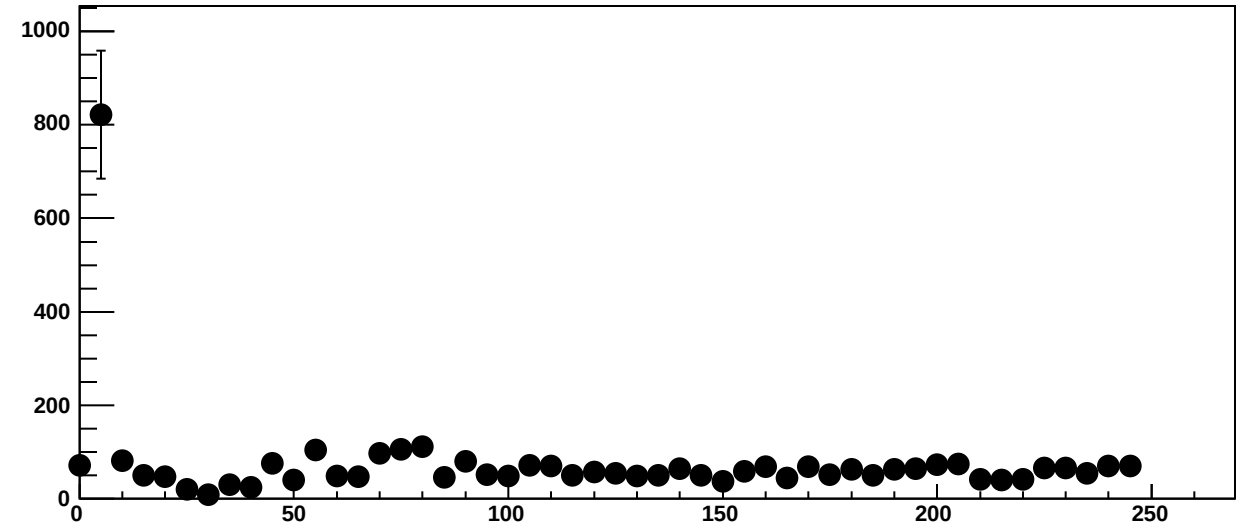

Chip 10, Channel 4, Enable 3, DAC=1600, ADC Residuals vs Hold
200
150
100
50
0
-50
0
50
100
150
200
250

Chip 10, Channel 4, Enable 4!, DAC=1600, ADC Mean vs Hold

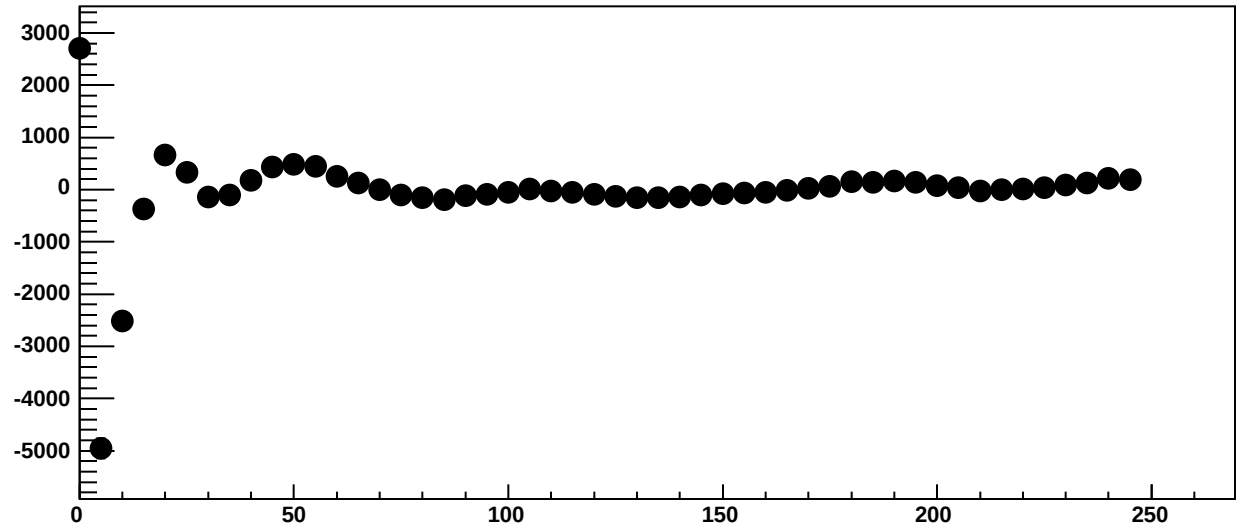
χ^2 / ndf	2.103e+04 / 42
p0	-4005 ± 3.462
p1	-0.09034 ± 0.01349
p2	3.229e+04 ± 3.724
p3	28.84 ± 0.008713

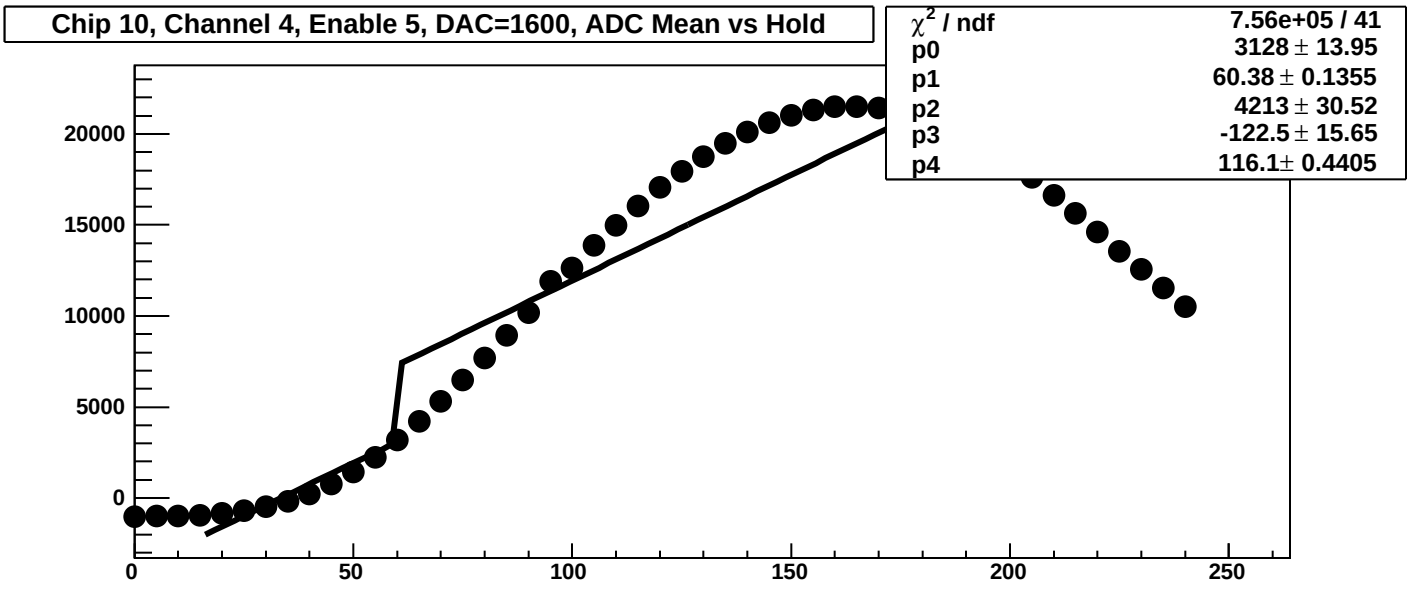


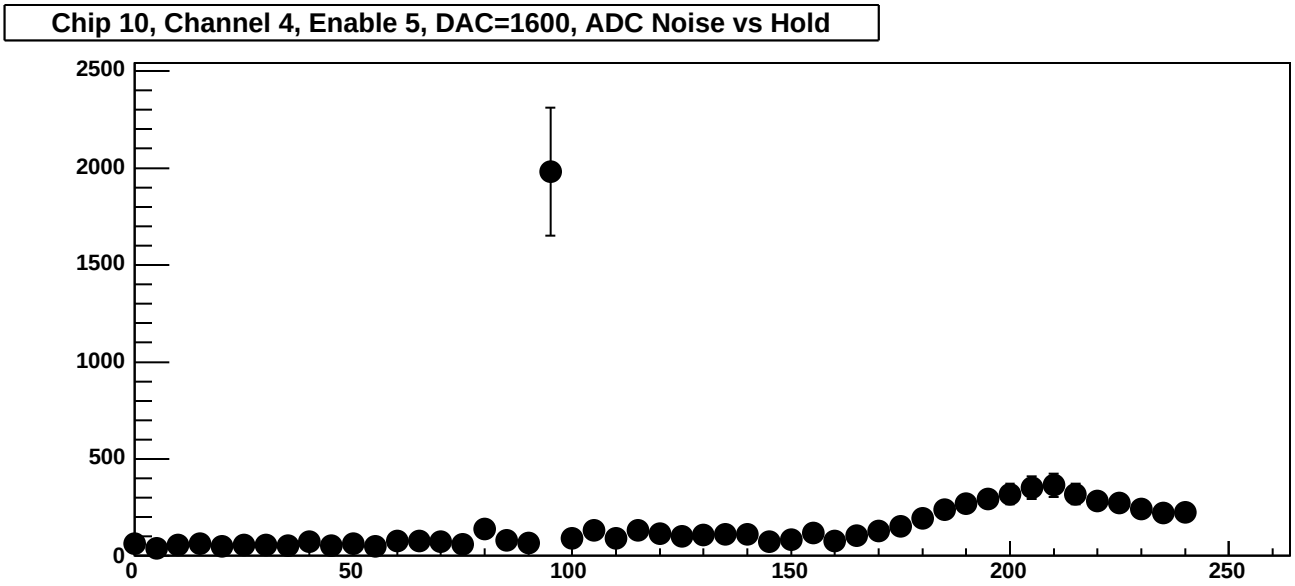
Chip 10, Channel 4, Enable 4!, DAC=1600, ADC Noise vs Hold

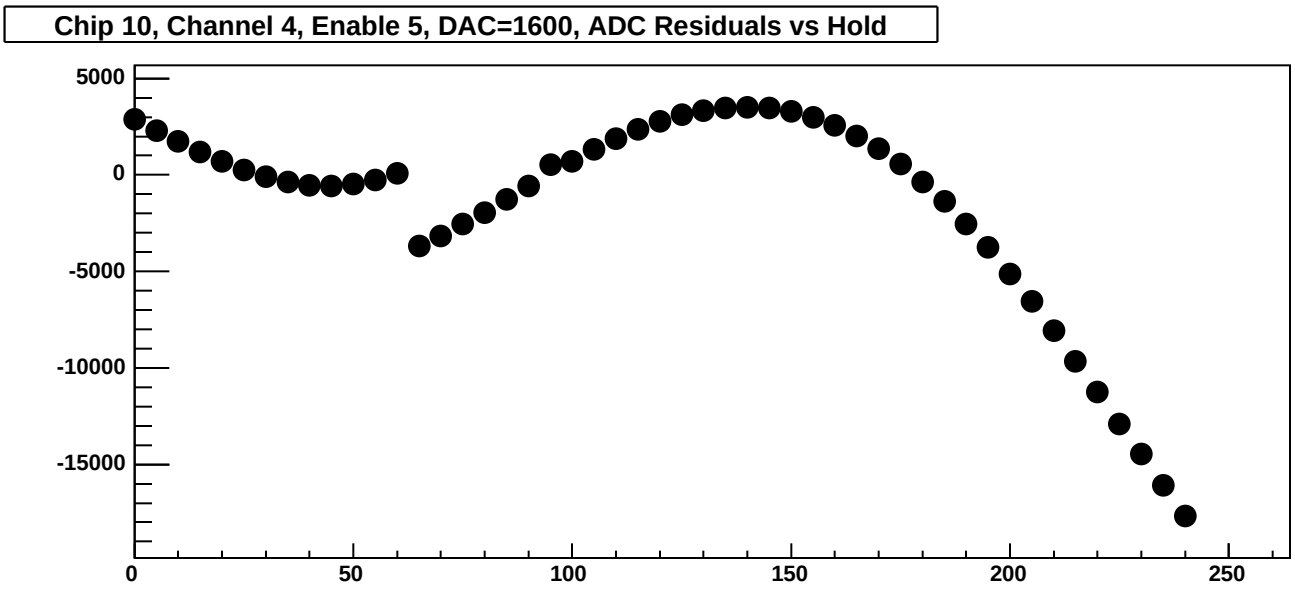


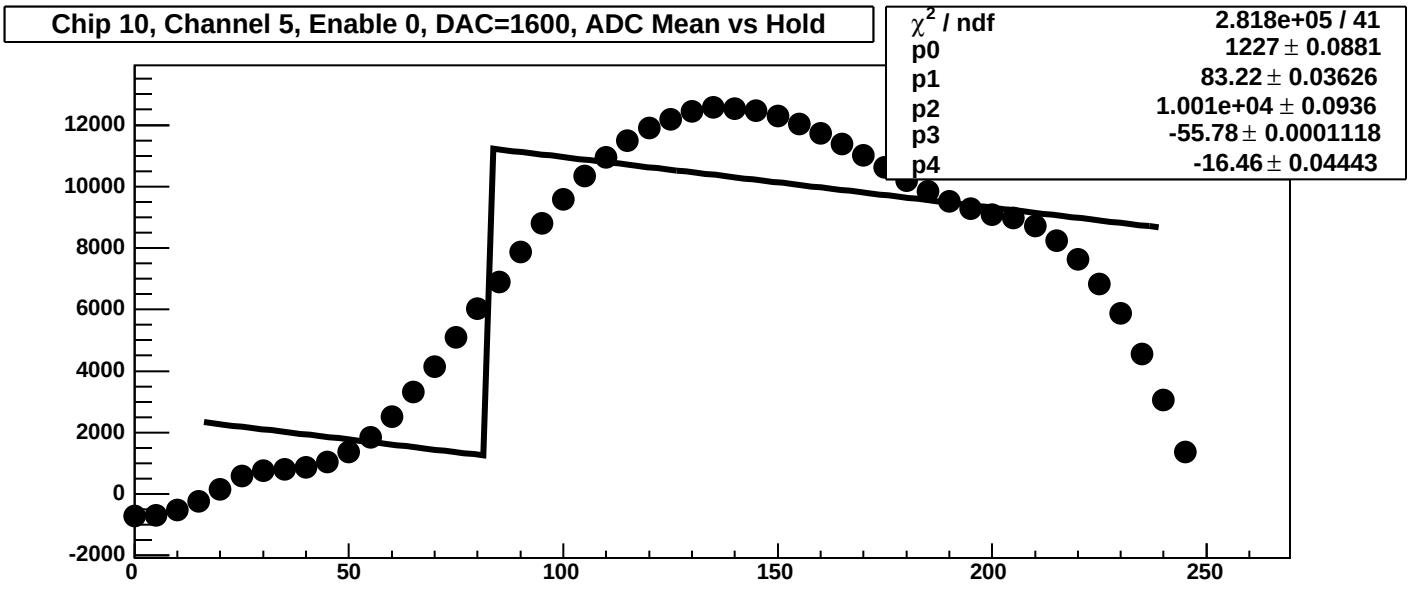
Chip 10, Channel 4, Enable 4!, DAC=1600, ADC Residuals vs Hold

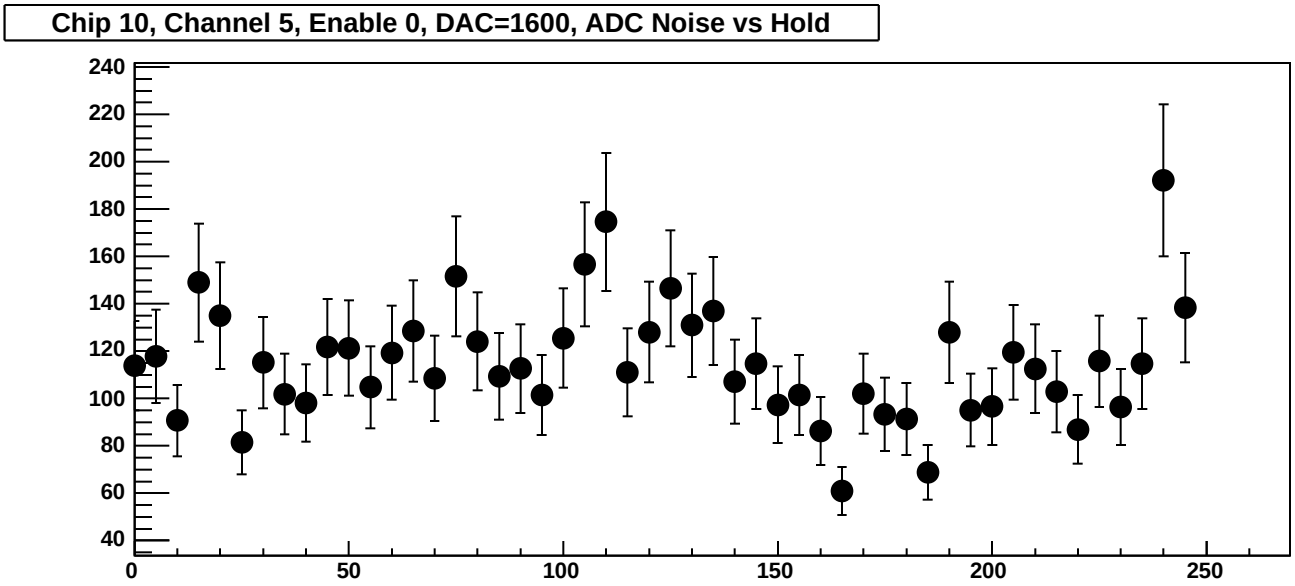


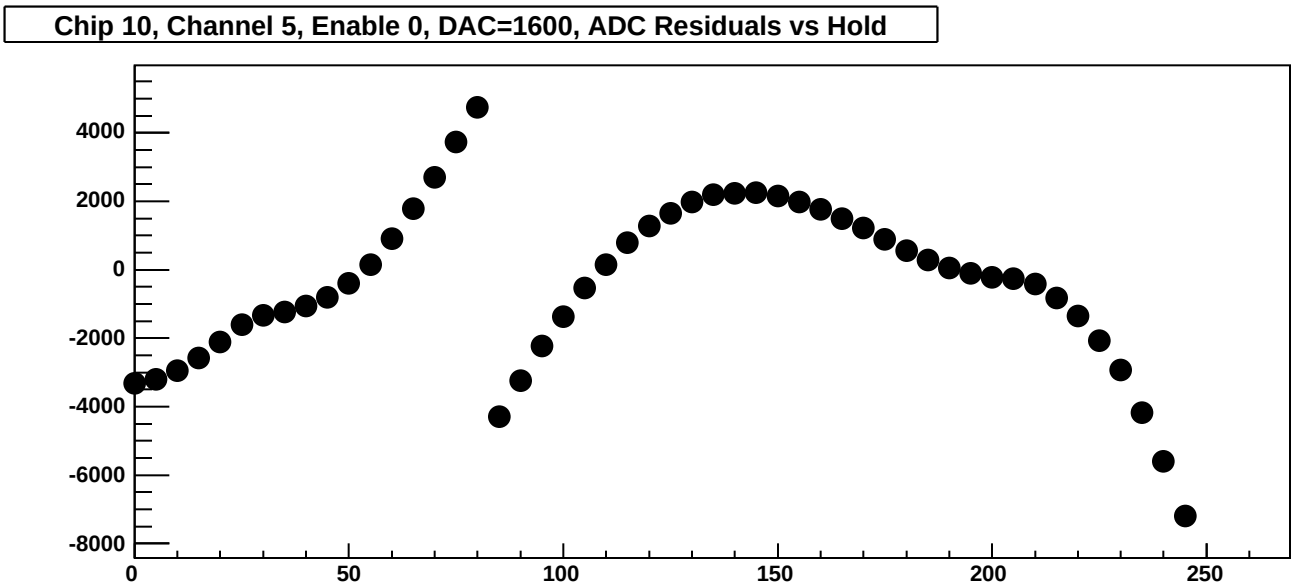

Chip 10, Channel 4, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.56e+05 / 41
p0 3128 ± 13.95
p1 60.38 ± 0.1355
p2 4213 ± 30.52
p3 -122.5 ± 15.65
p4 116.1 ± 0.4405
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 10, Channel 4, Enable 5, DAC=1600, ADC Noise vs Hold
2500
2000
1500
1000
500
0
0
50
100
150
200
250

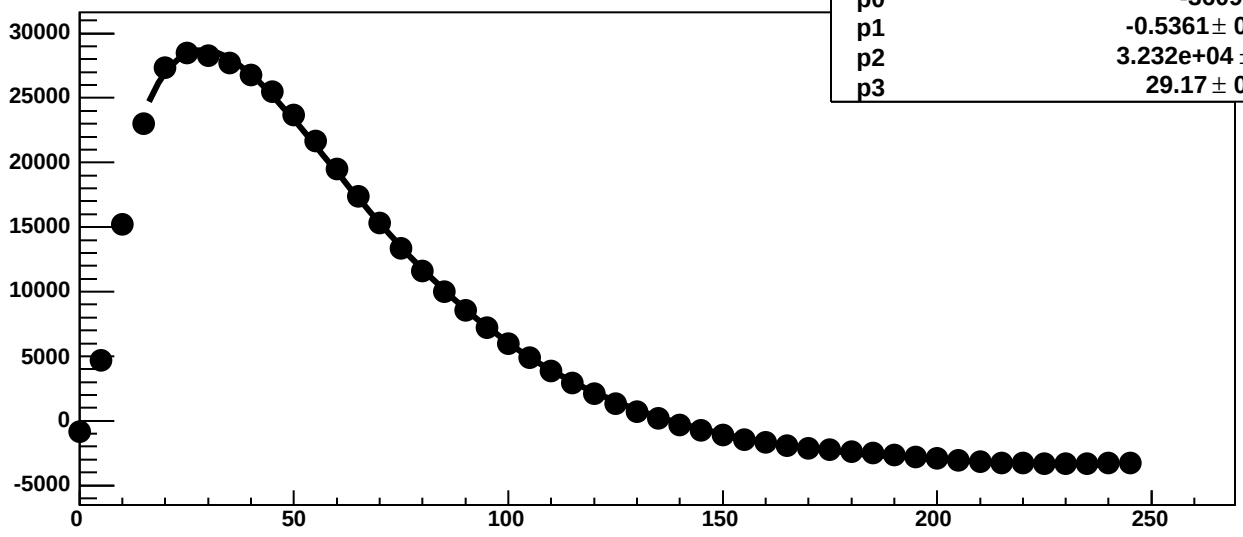

Chip 10, Channel 4, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
0
50
100
150
200
250


Chip 10, Channel 5, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.818e+05 / 41
p0 1227 ± 0.0881
p1 83.22 ± 0.03626
p2 1.001e+04 ± 0.0936
p3 -55.78 ± 0.0001118
p4 -16.46 ± 0.04443
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

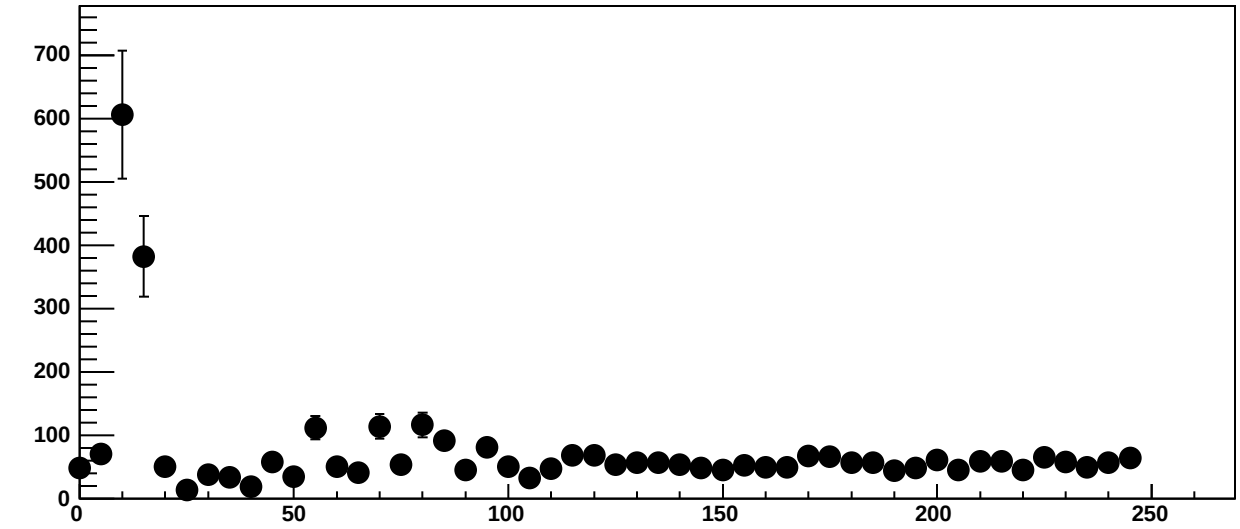

Chip 10, Channel 5, Enable 0, DAC=1600, ADC Noise vs Hold
240
220
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250


Chip 10, Channel 5, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250

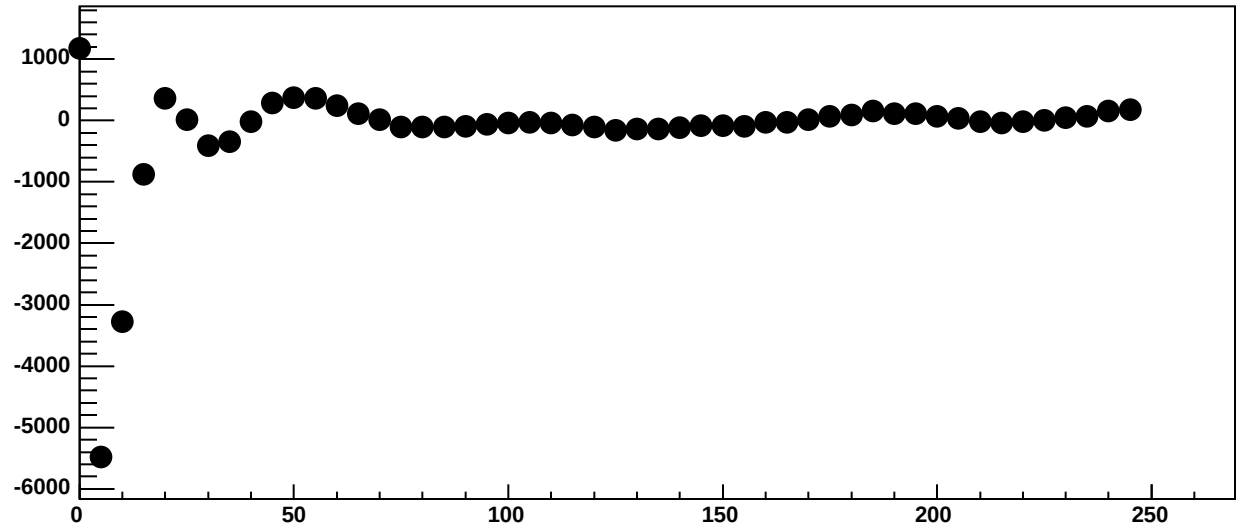
Chip 10, Channel 5, Enable 1!, DAC=1600, ADC Mean vs Hold



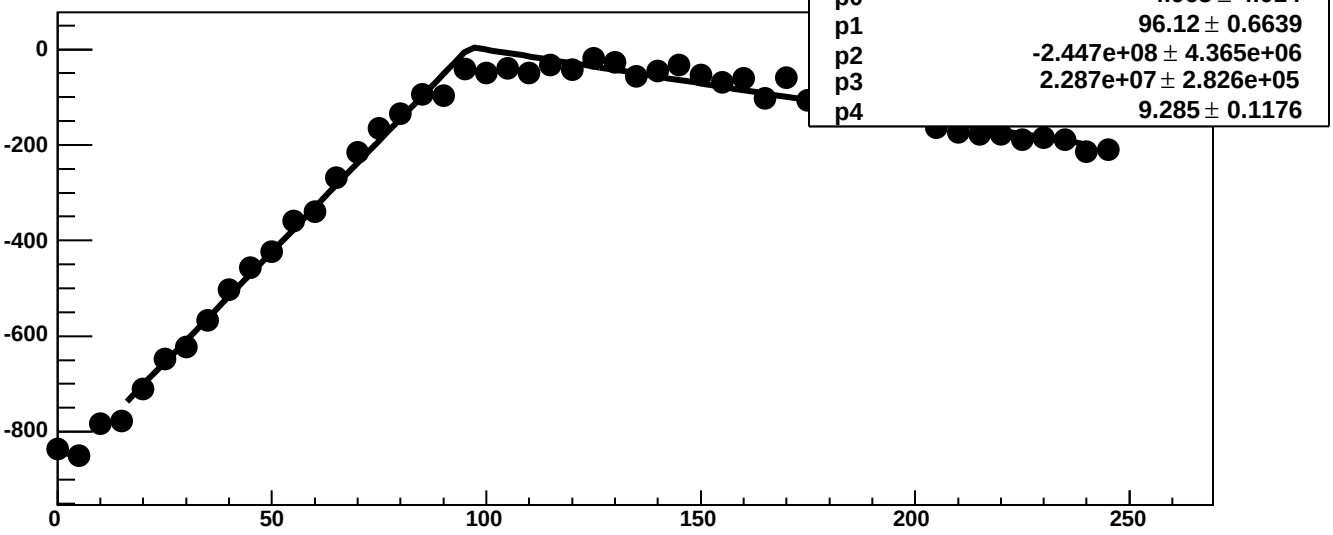
Chip 10, Channel 5, Enable 1!, DAC=1600, ADC Noise vs Hold



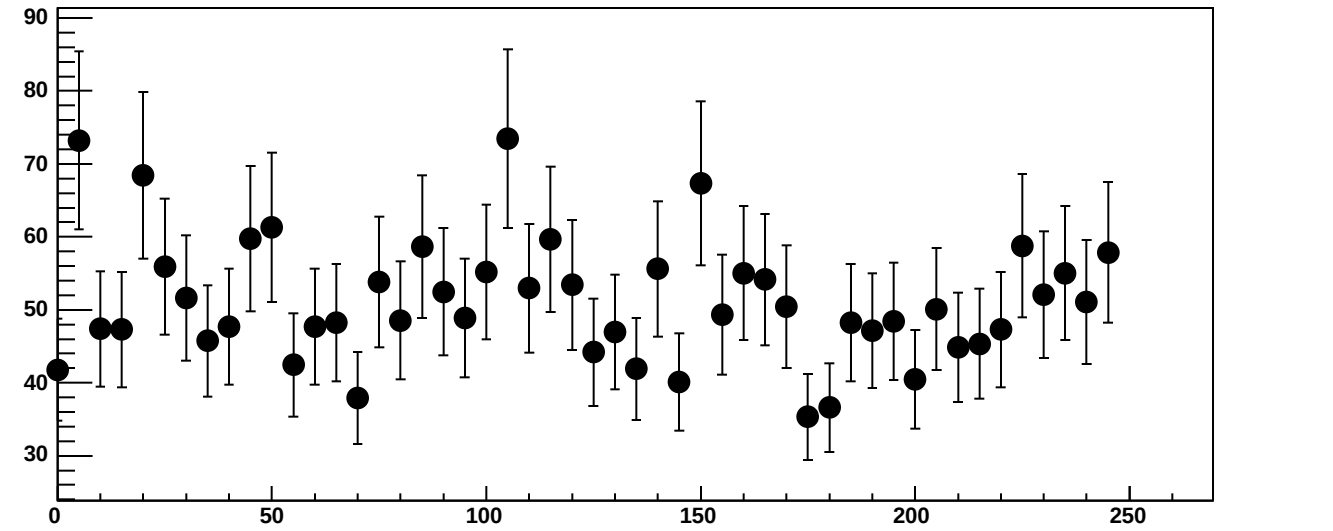
Chip 10, Channel 5, Enable 1!, DAC=1600, ADC Residuals vs Hold



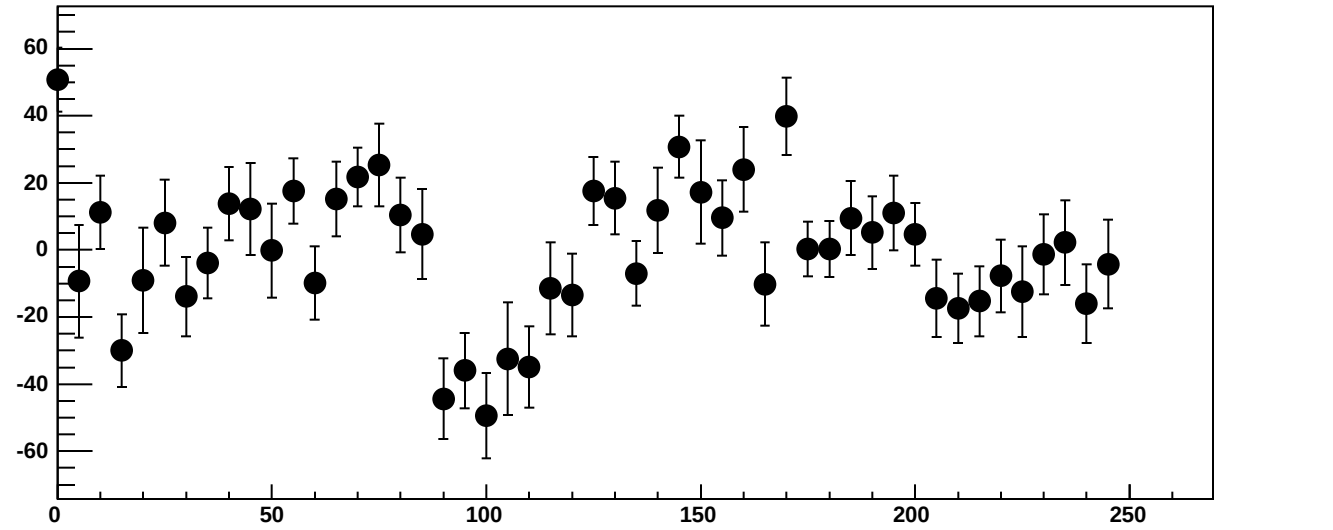
Chip 10, Channel 5, Enable 2, DAC=1600, ADC Mean vs Hold

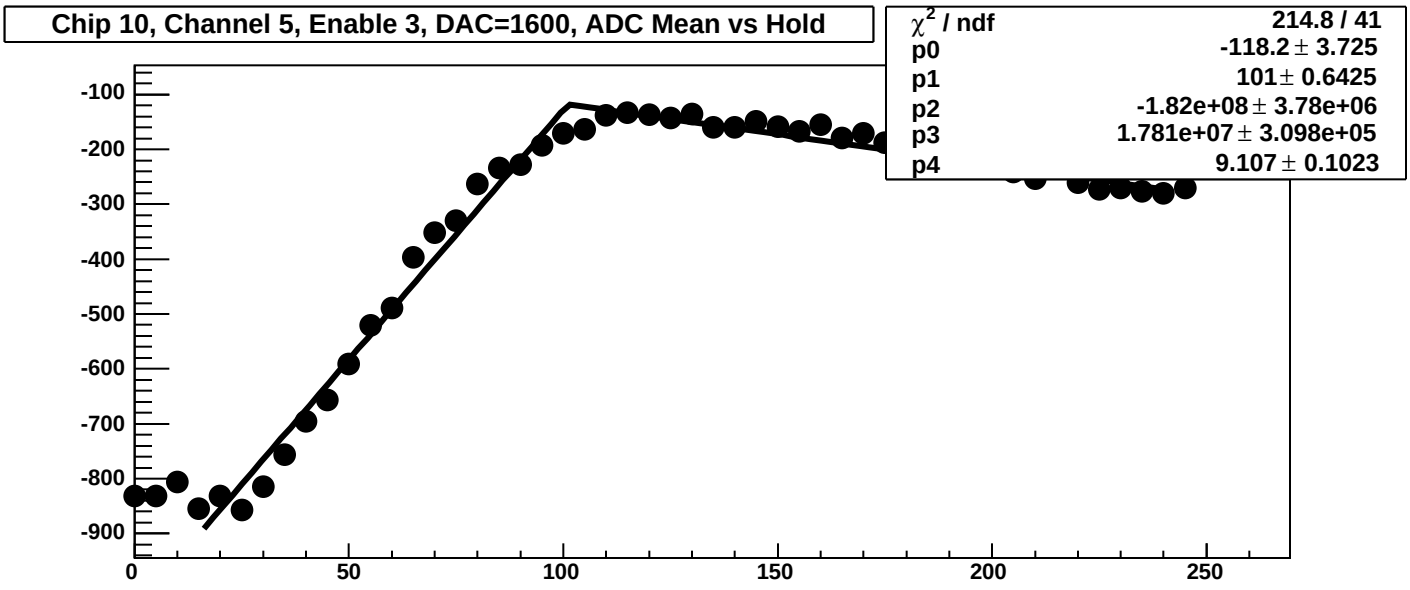


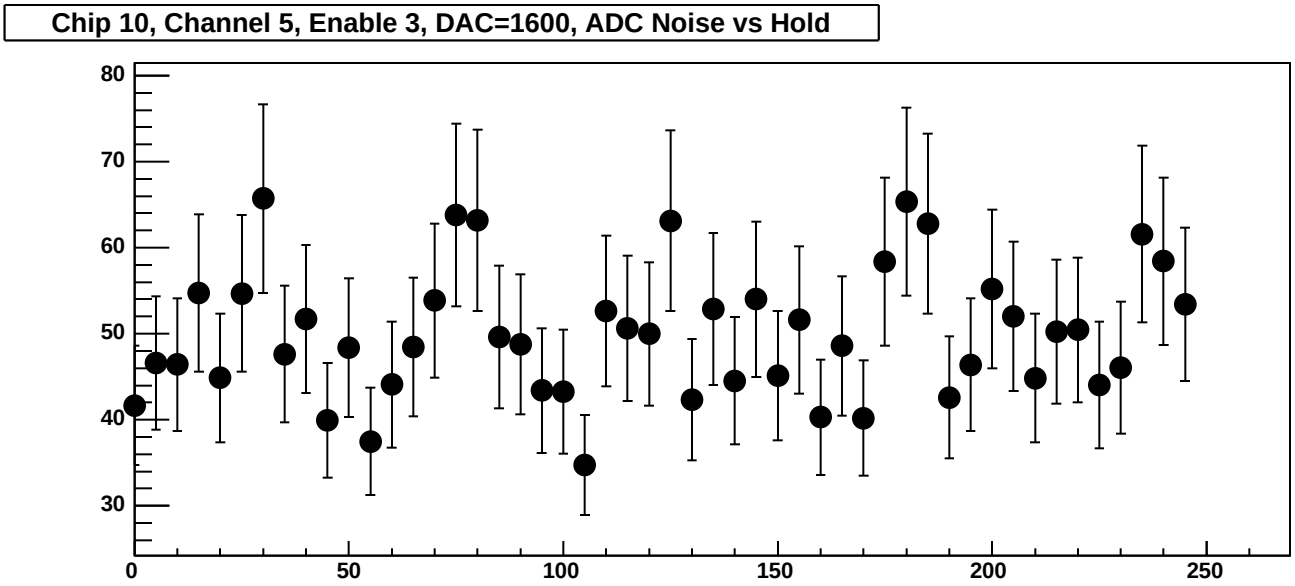
Chip 10, Channel 5, Enable 2, DAC=1600, ADC Noise vs Hold

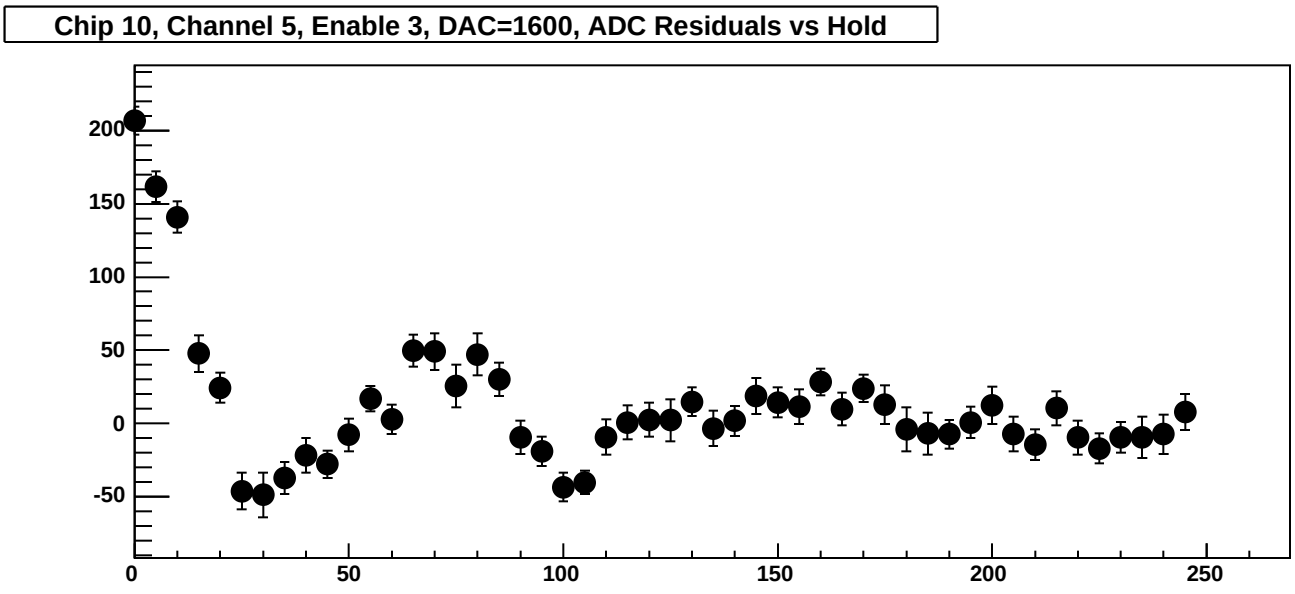


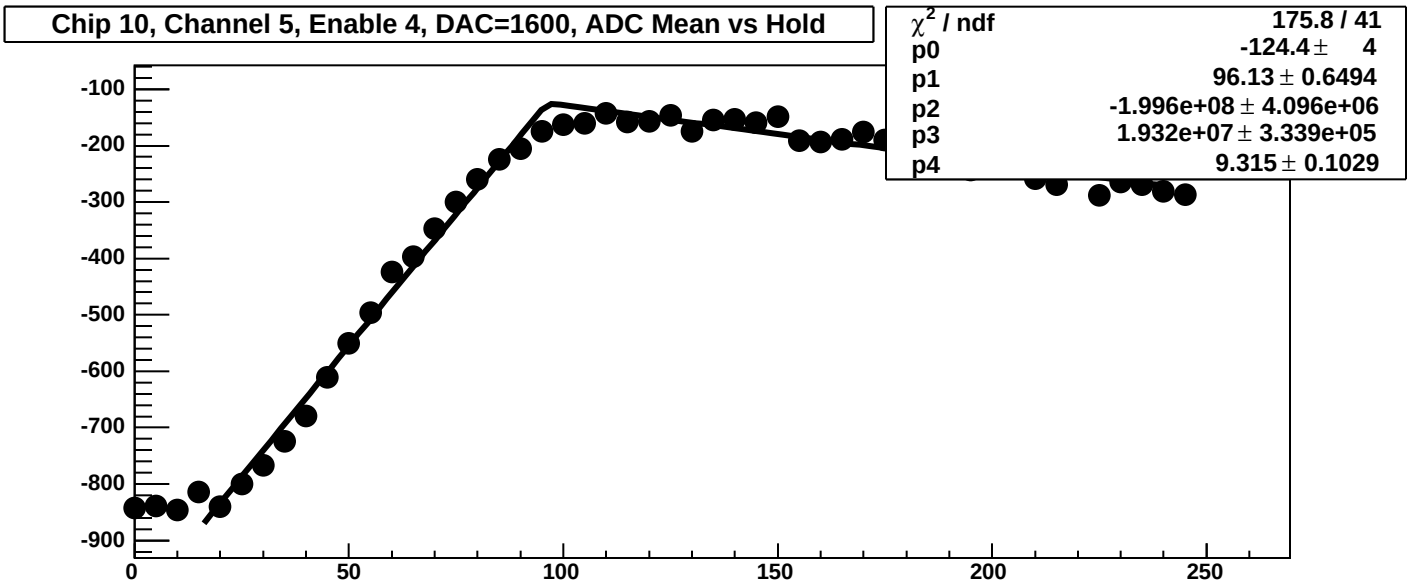
Chip 10, Channel 5, Enable 2, DAC=1600, ADC Residuals vs Hold

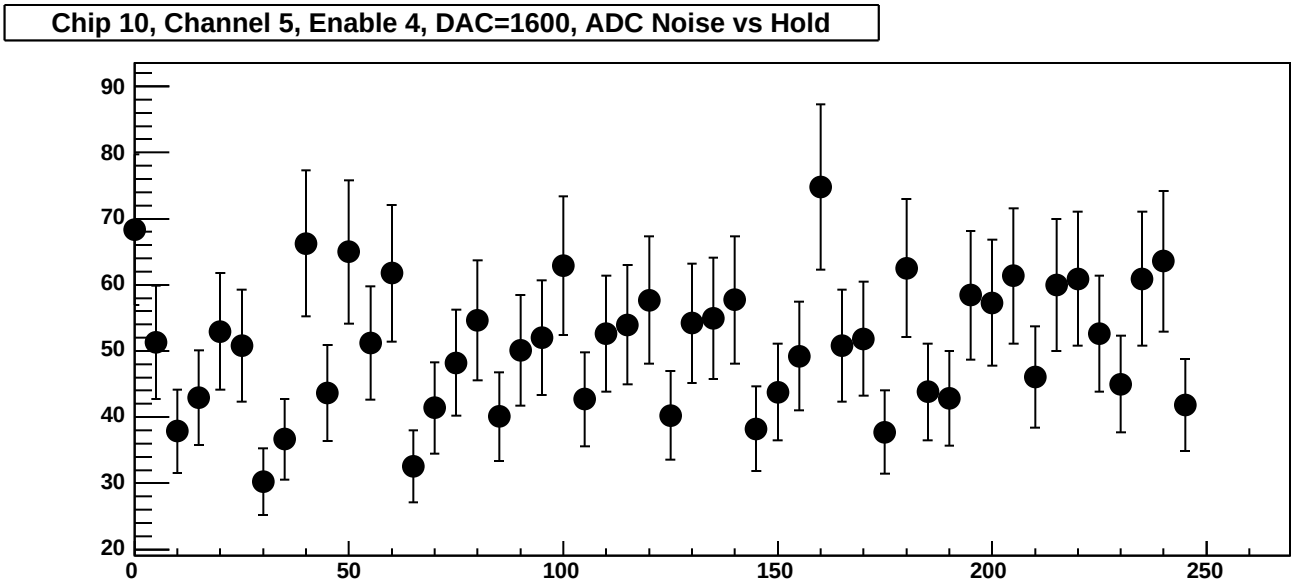


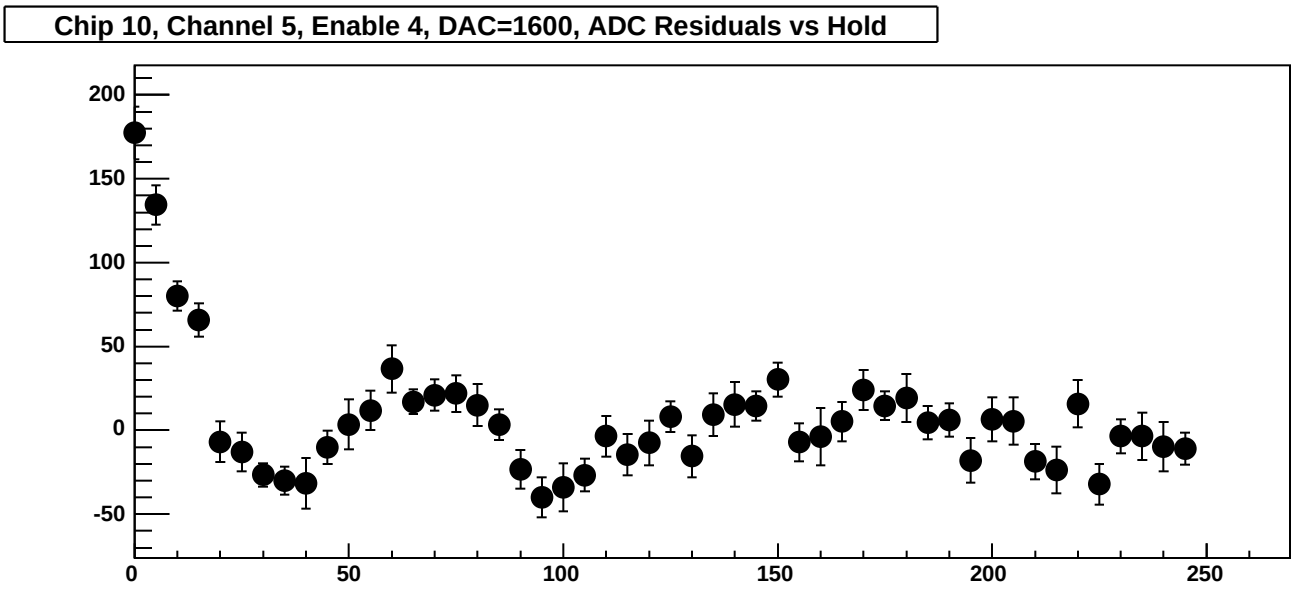

Chip 10, Channel 5, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 214.8 / 41
p0 -118.2 ± 3.725
p1 101 ± 0.6425
p2 -1.82e+08 ± 3.78e+06
p3 1.781e+07 ± 3.098e+05
p4 9.107 ± 0.1023
-100
-200
-300
-400
-500
-600
-700
-800
-900
0
50
100
150
200
250

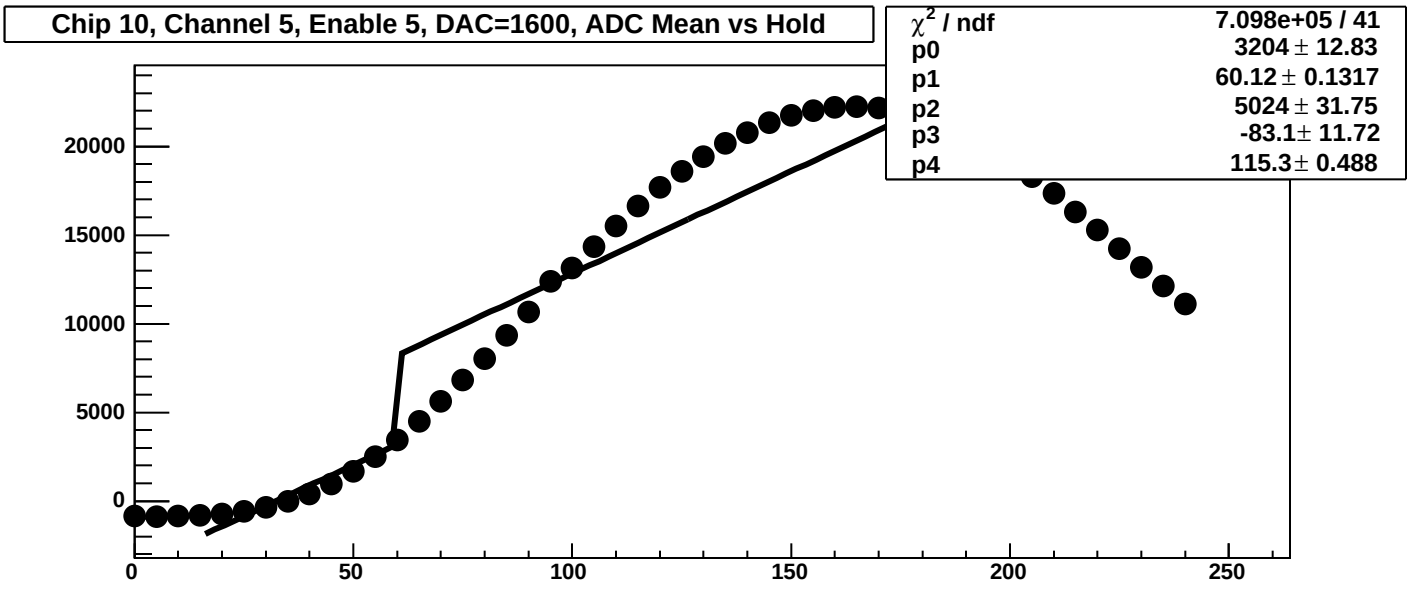

Chip 10, Channel 5, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

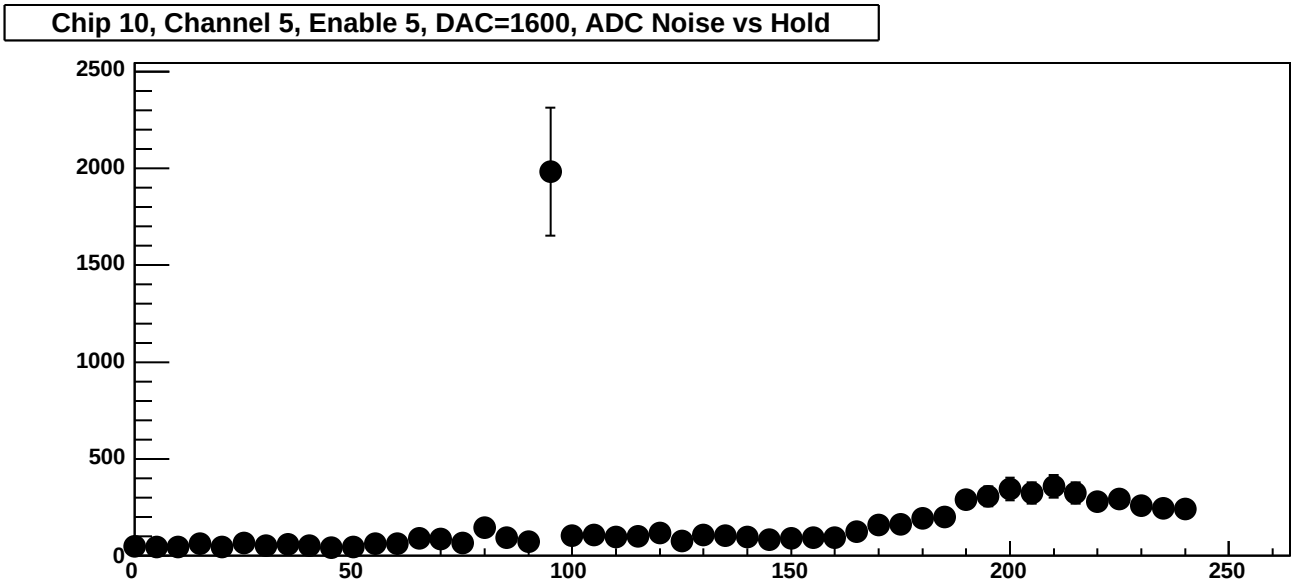

Chip 10, Channel 5, Enable 3, DAC=1600, ADC Residuals vs Hold
200
150
100
50
0
-50
0
50
100
150
200
250

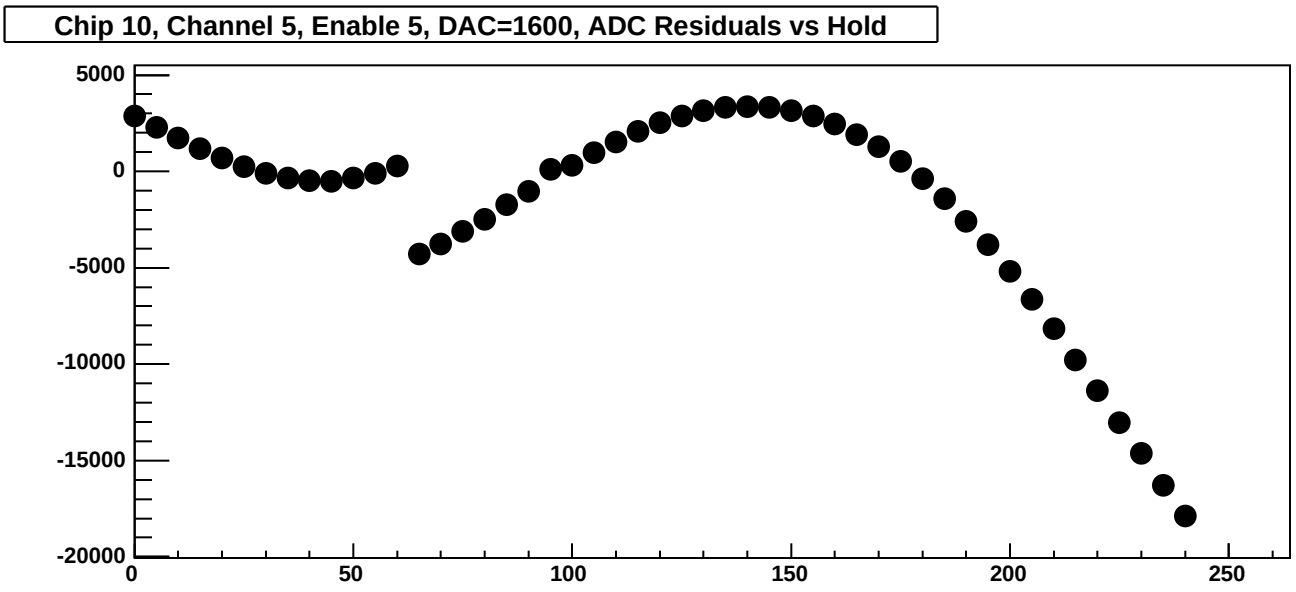

Chip 10, Channel 5, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 175.8 / 41
p0 -124.4 ± 4
p1 96.13 ± 0.6494
p2 -1.996e+08 ± 4.096e+06
p3 1.932e+07 ± 3.339e+05
p4 9.315 ± 0.1029
-100
-200
-300
-400
-500
-600
-700
-800
-900
0
50
100
150
200
250


Chip 10, Channel 5, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
20
0
50
100
150
200
250

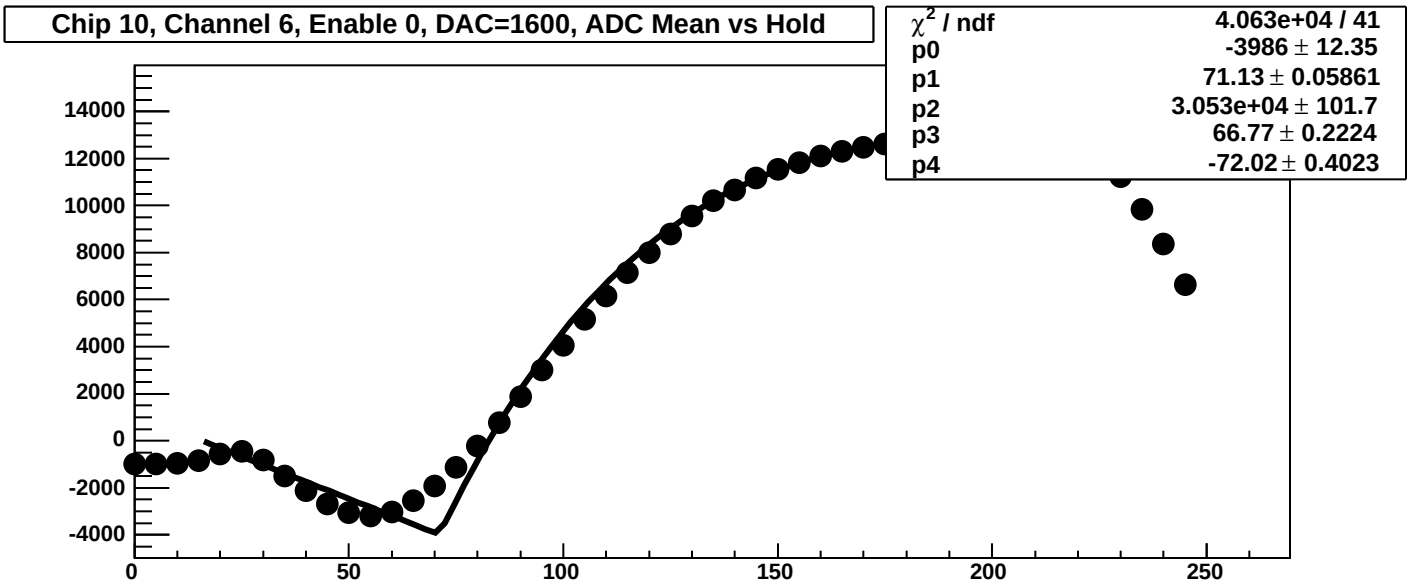

Chip 10, Channel 5, Enable 4, DAC=1600, ADC Residuals vs Hold
200
150
100
50
0
-50
0
50
100
150
200
250


Chip 10, Channel 5, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.098e+05 / 41
p0 3204 ± 12.83
p1 60.12 ± 0.1317
p2 5024 ± 31.75
p3 -83.1 ± 11.72
p4 115.3 ± 0.488
20000
15000
10000
5000
0
0
50
100
150
200
250

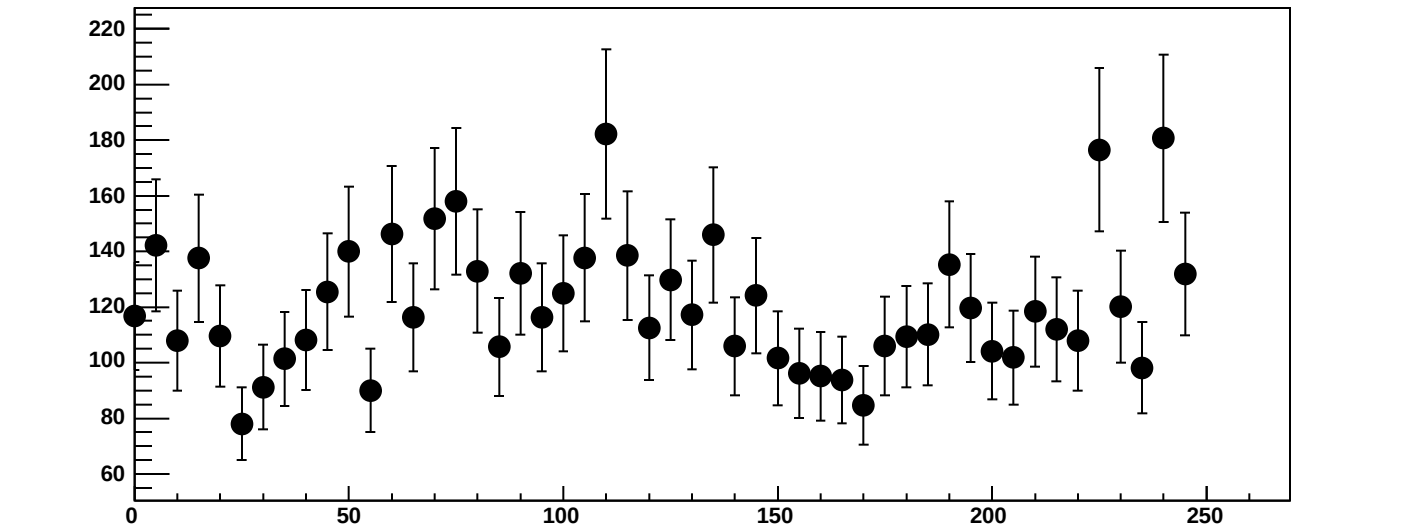

Chip 10, Channel 5, Enable 5, DAC=1600, ADC Noise vs Hold
2500
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 10, Channel 5, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

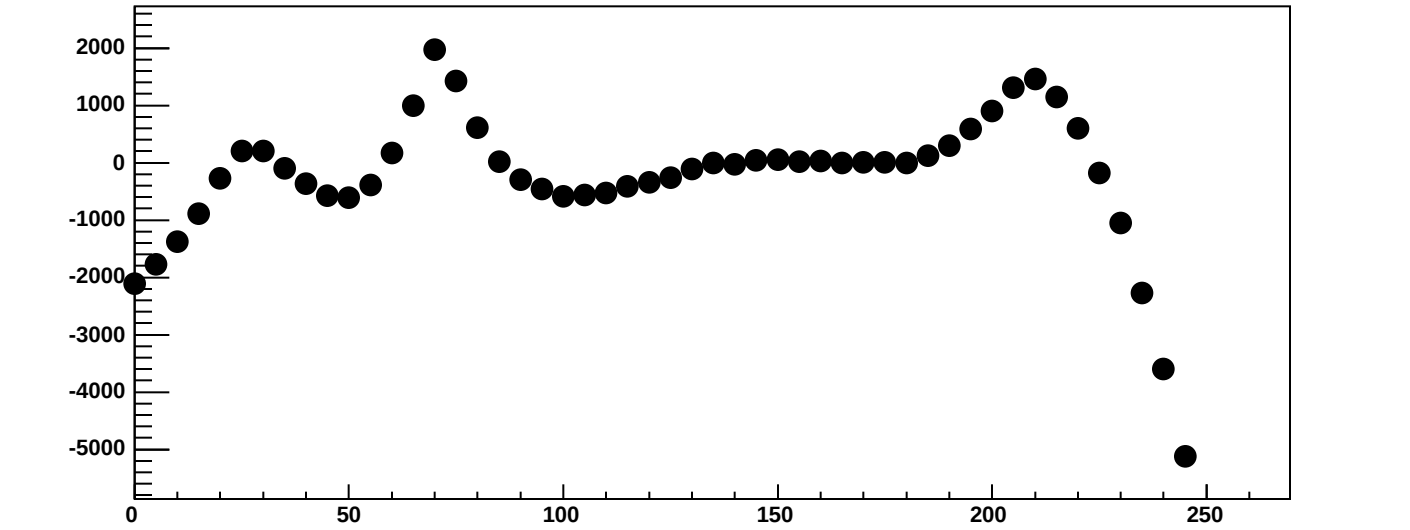
Chip 10, Channel 6, Enable 0, DAC=1600, ADC Mean vs Hold

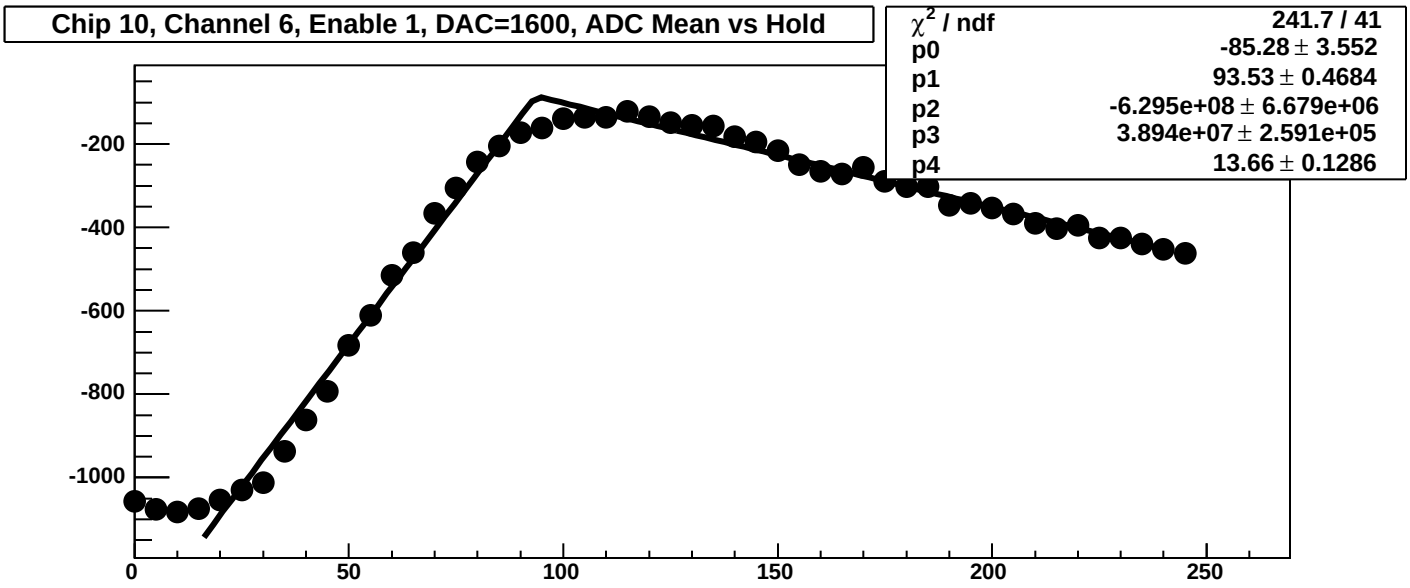


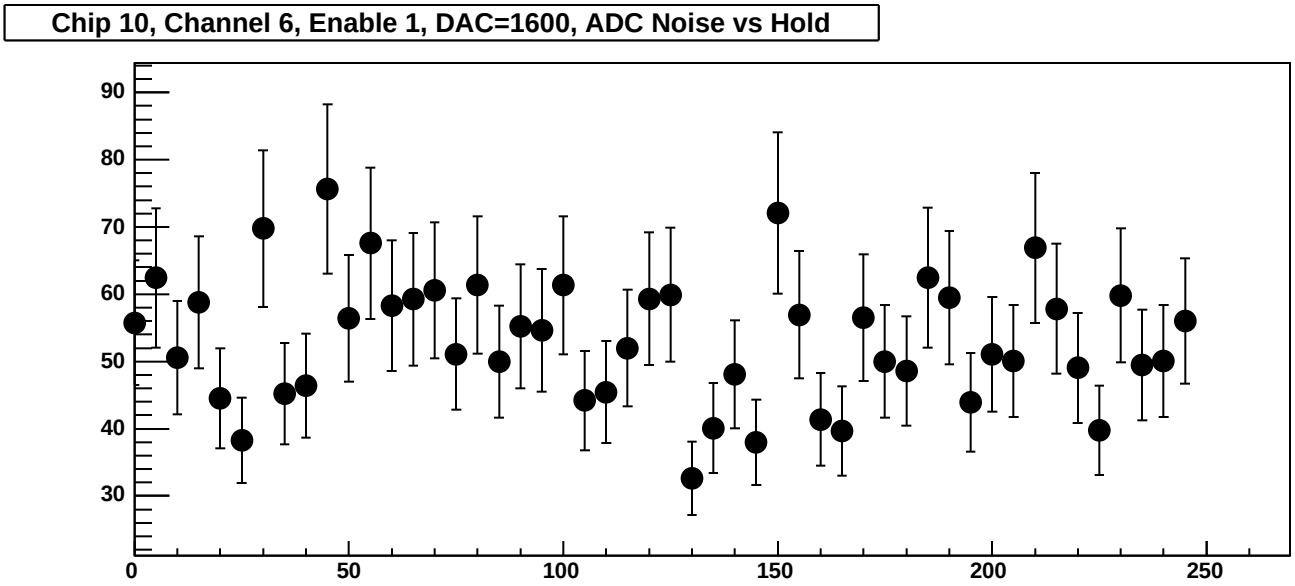
Chip 10, Channel 6, Enable 0, DAC=1600, ADC Noise vs Hold

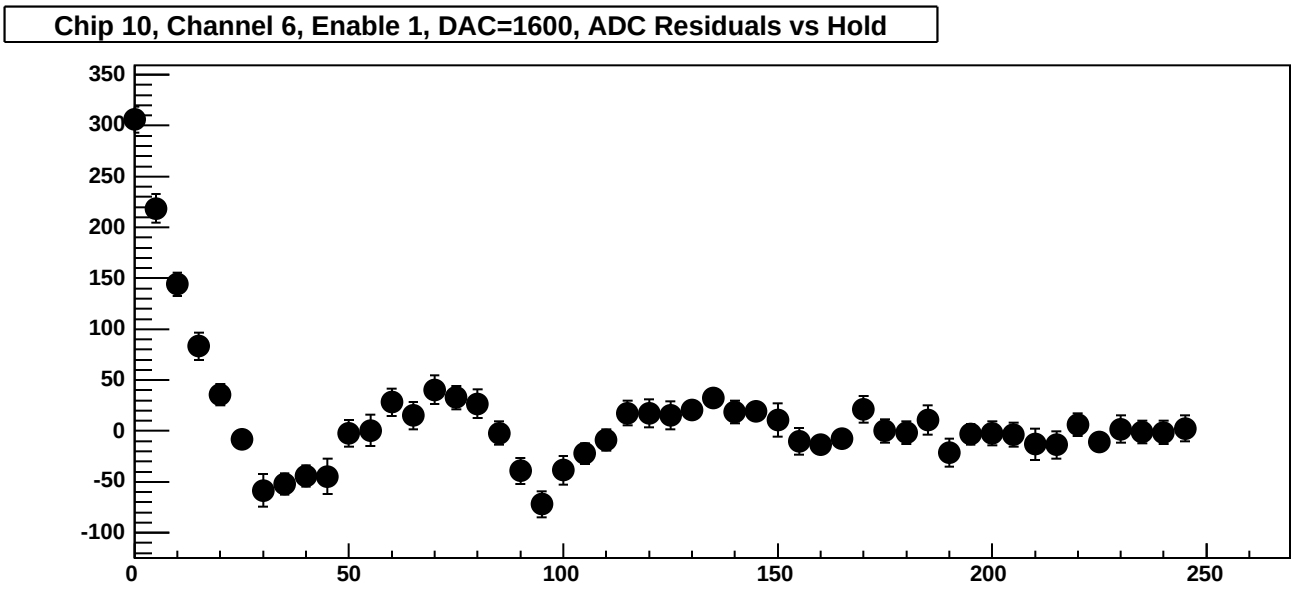


Chip 10, Channel 6, Enable 0, DAC=1600, ADC Residuals vs Hold

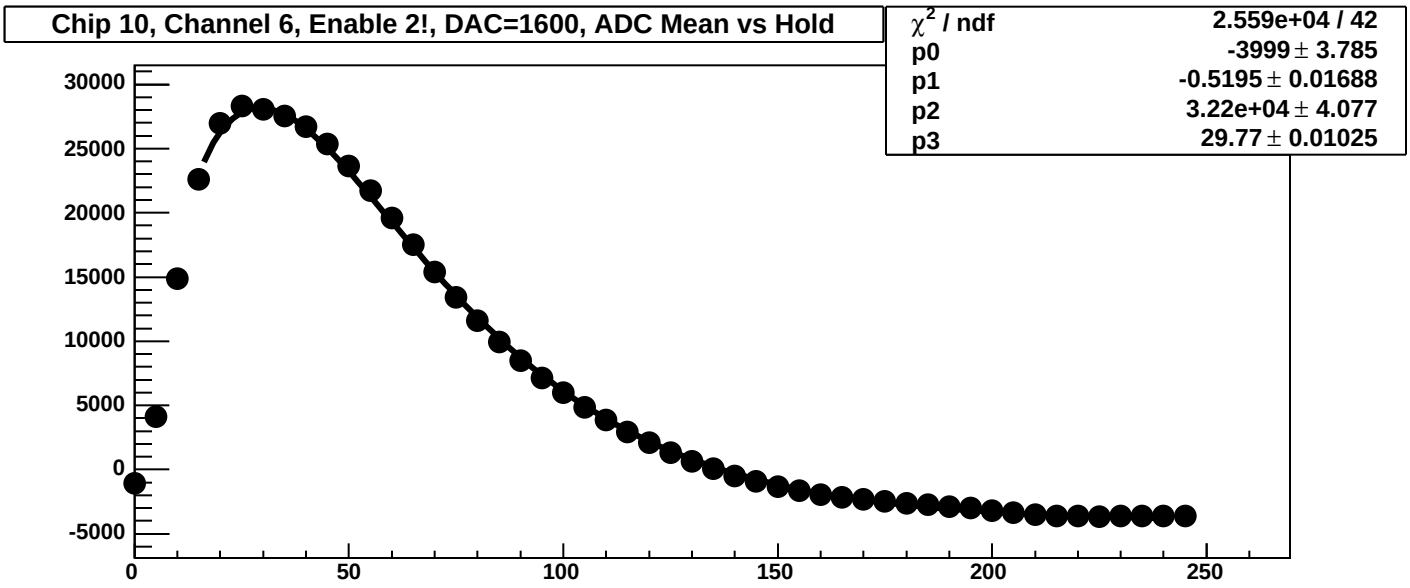



Chip 10, Channel 6, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 241.7 / 41
p0 -85.28 ± 3.552
p1 93.53 ± 0.4684
p2 -6.295e+08 ± 6.679e+06
p3 3.894e+07 ± 2.591e+05
p4 13.66 ± 0.1286
-200
-400
-600
-800
-1000
0
50
100
150
200
250

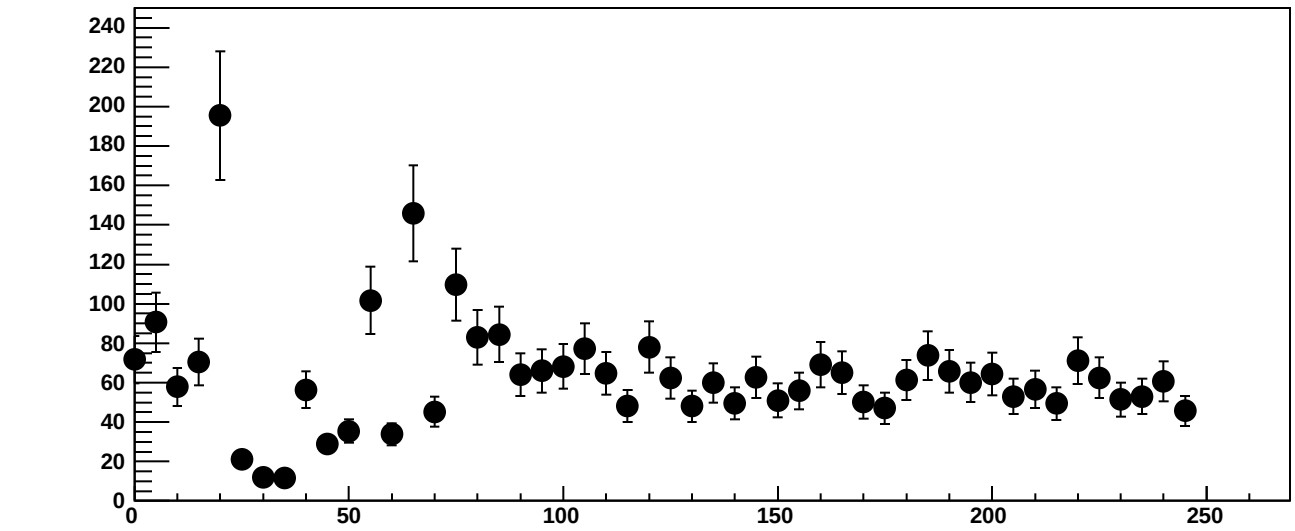

Chip 10, Channel 6, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 10, Channel 6, Enable 1, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

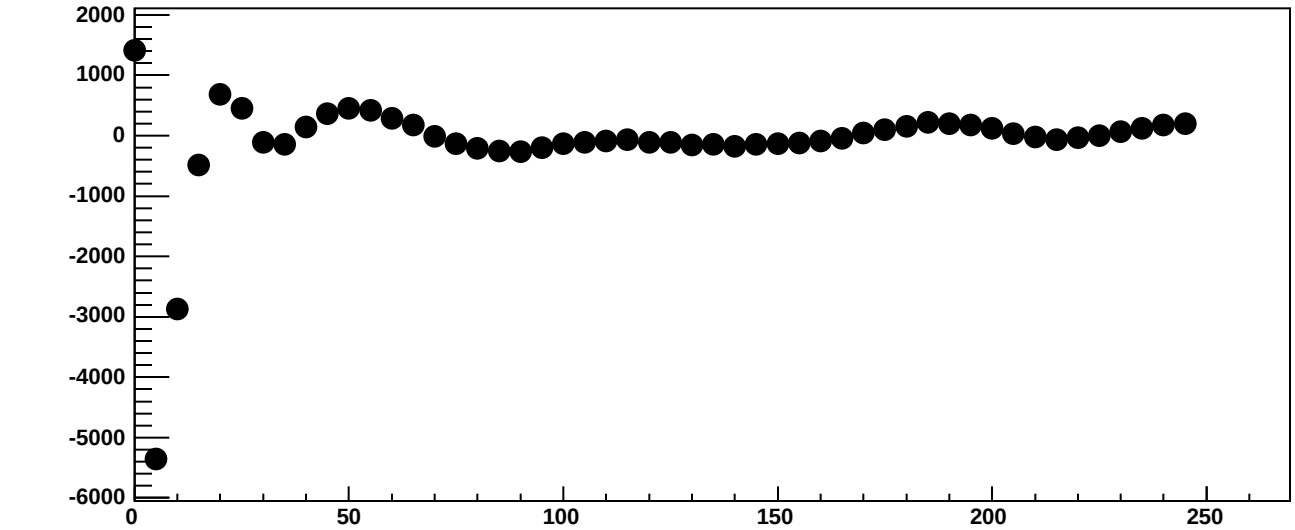
Chip 10, Channel 6, Enable 2!, DAC=1600, ADC Mean vs Hold



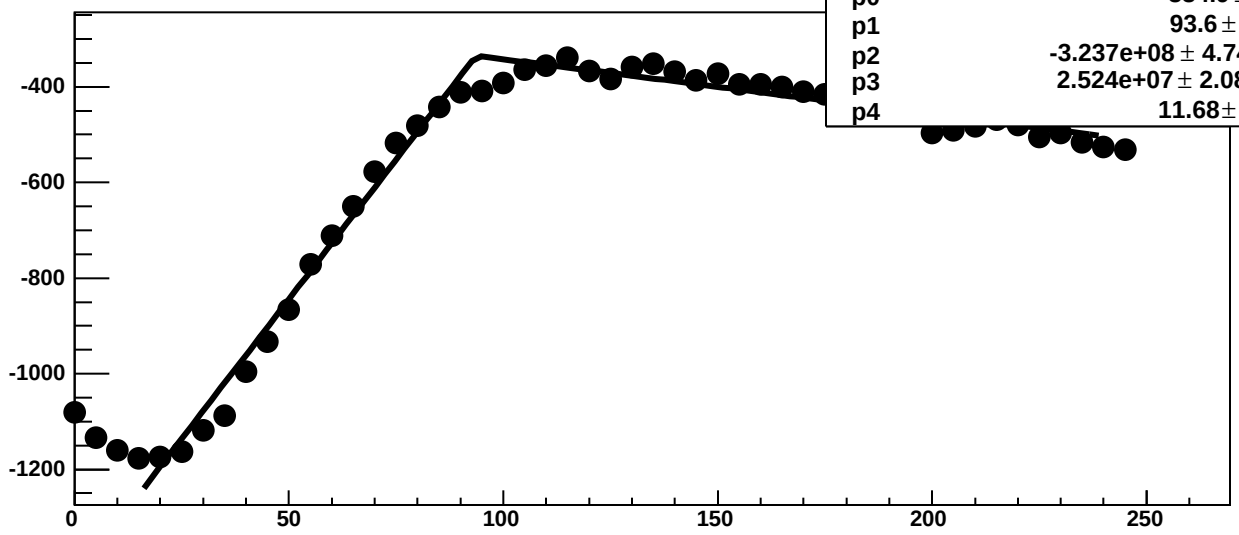
Chip 10, Channel 6, Enable 2!, DAC=1600, ADC Noise vs Hold



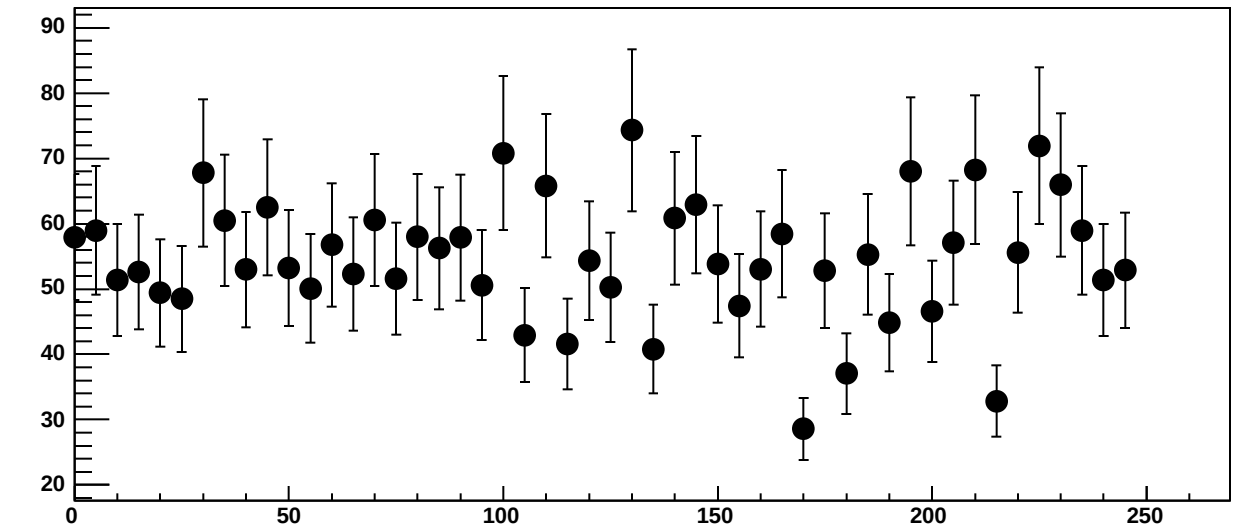
Chip 10, Channel 6, Enable 2!, DAC=1600, ADC Residuals vs Hold



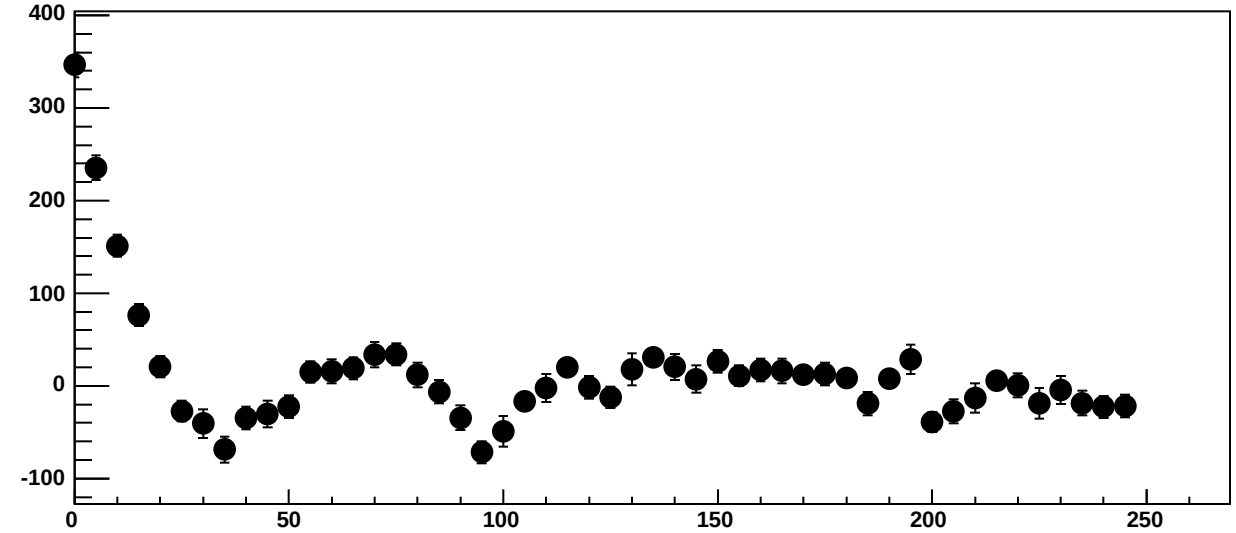
Chip 10, Channel 6, Enable 3, DAC=1600, ADC Mean vs Hold



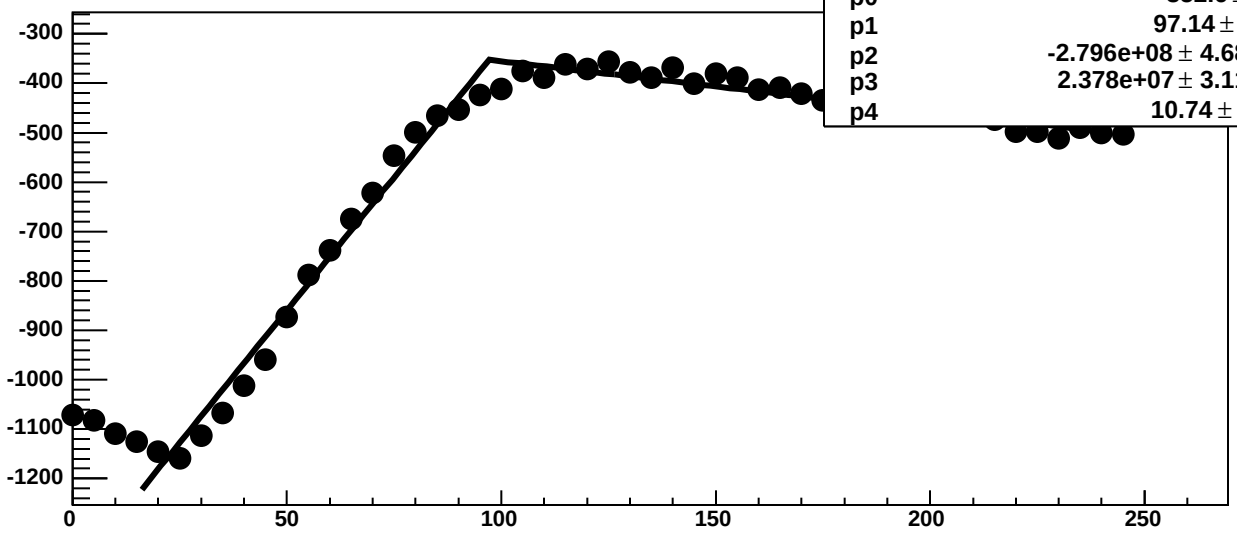
Chip 10, Channel 6, Enable 3, DAC=1600, ADC Noise vs Hold



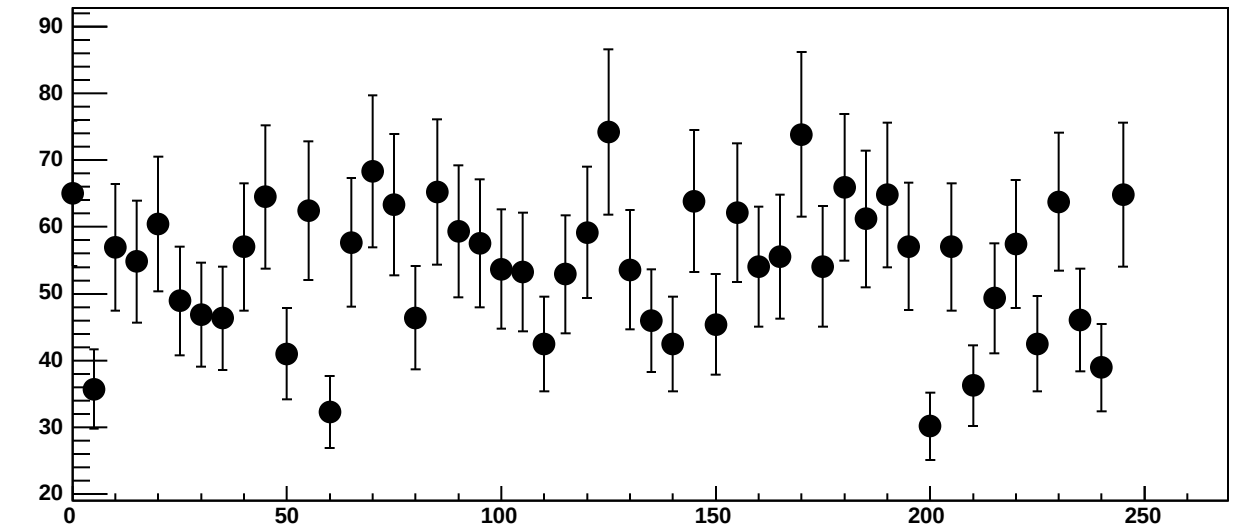
Chip 10, Channel 6, Enable 3, DAC=1600, ADC Residuals vs Hold



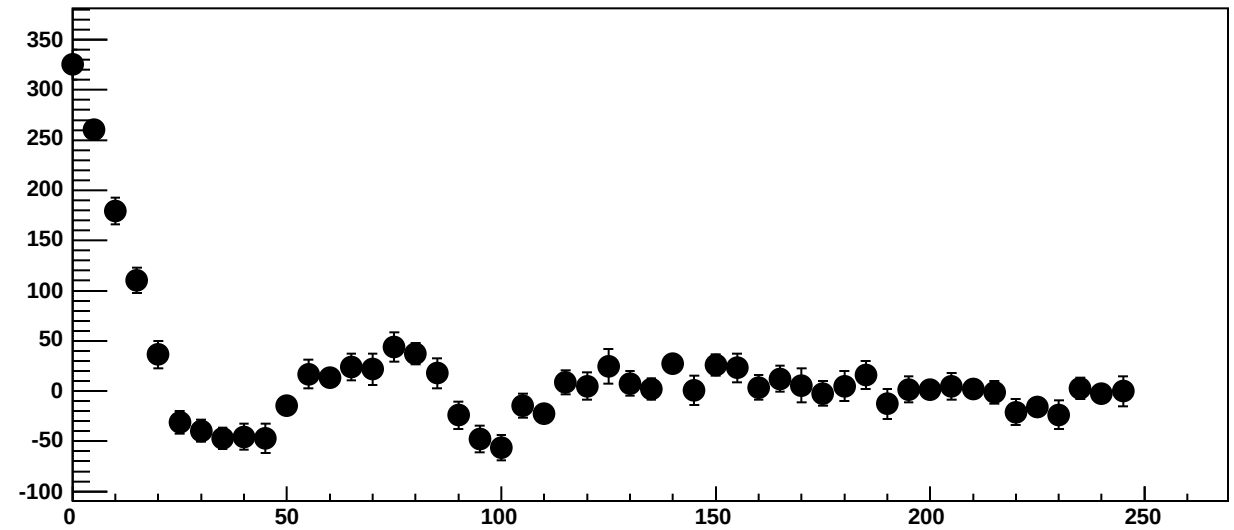
Chip 10, Channel 6, Enable 4, DAC=1600, ADC Mean vs Hold

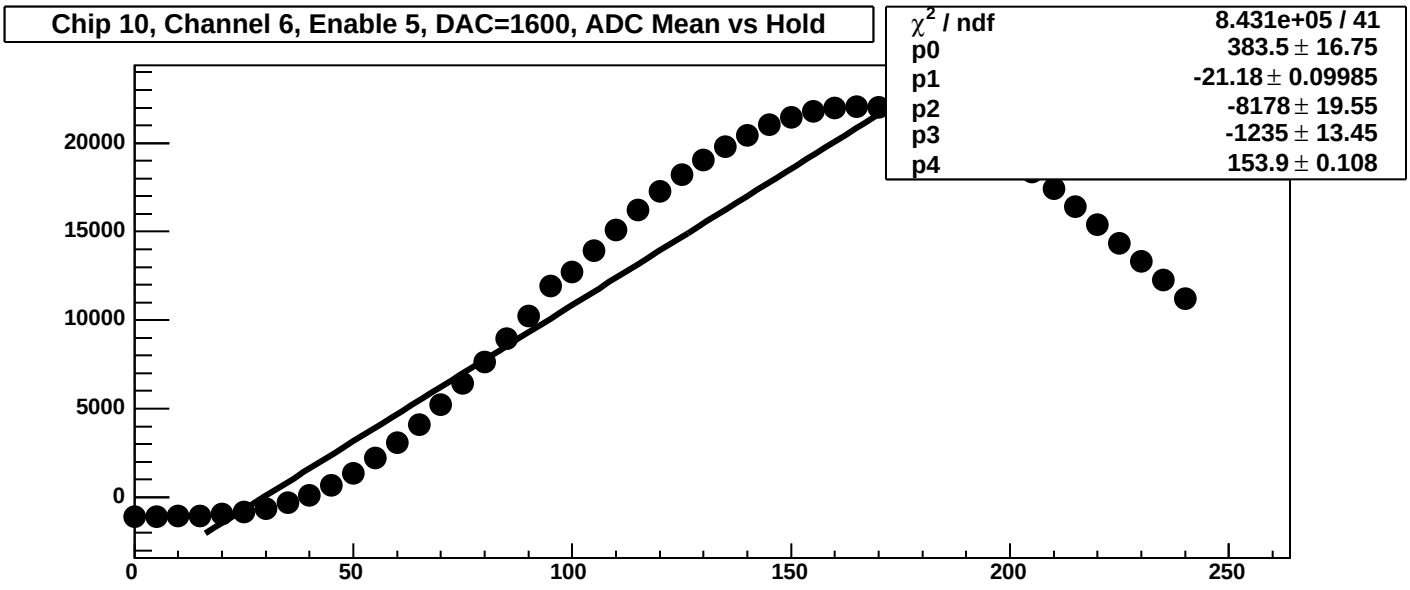


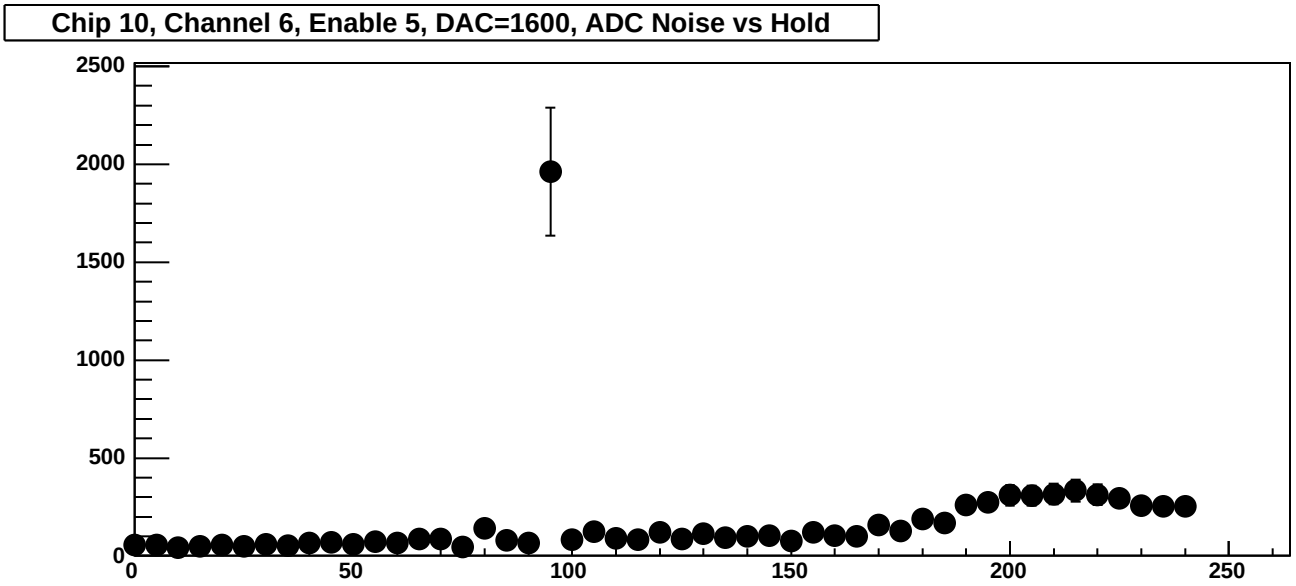
Chip 10, Channel 6, Enable 4, DAC=1600, ADC Noise vs Hold

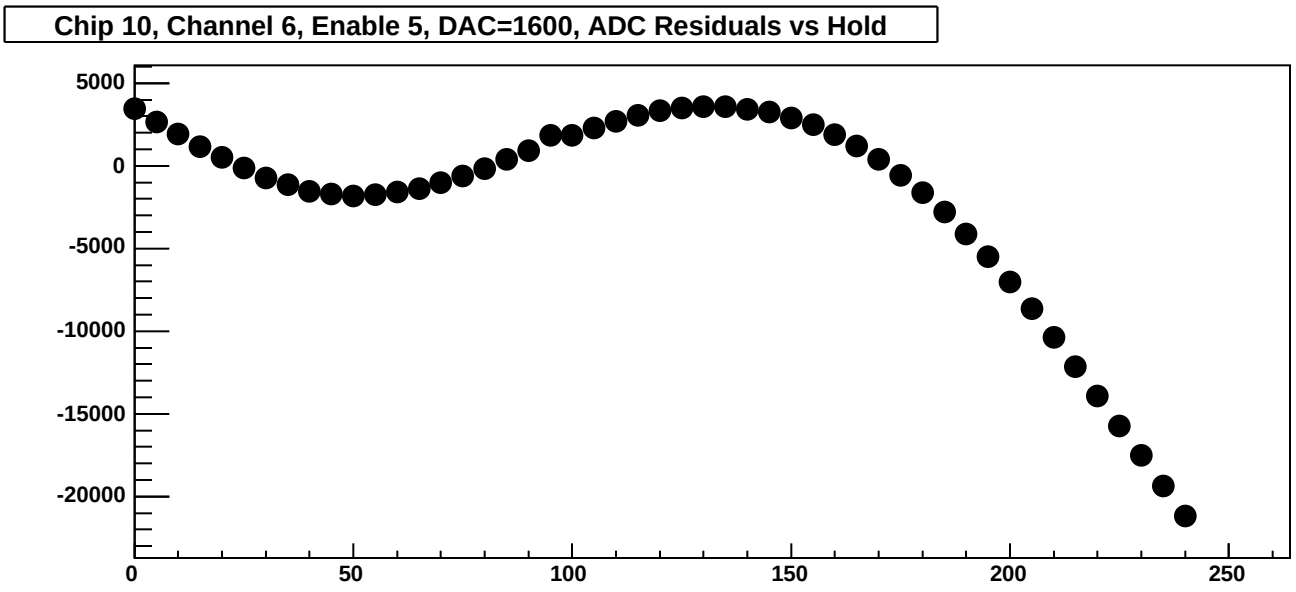


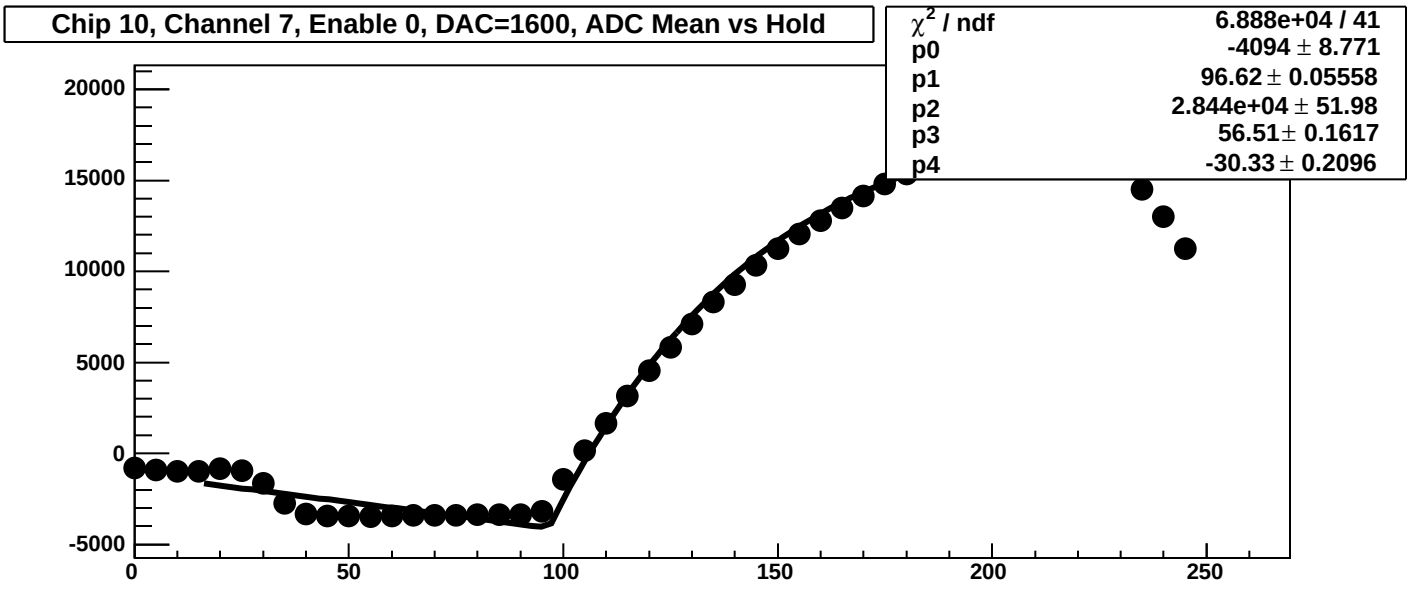
Chip 10, Channel 6, Enable 4, DAC=1600, ADC Residuals vs Hold

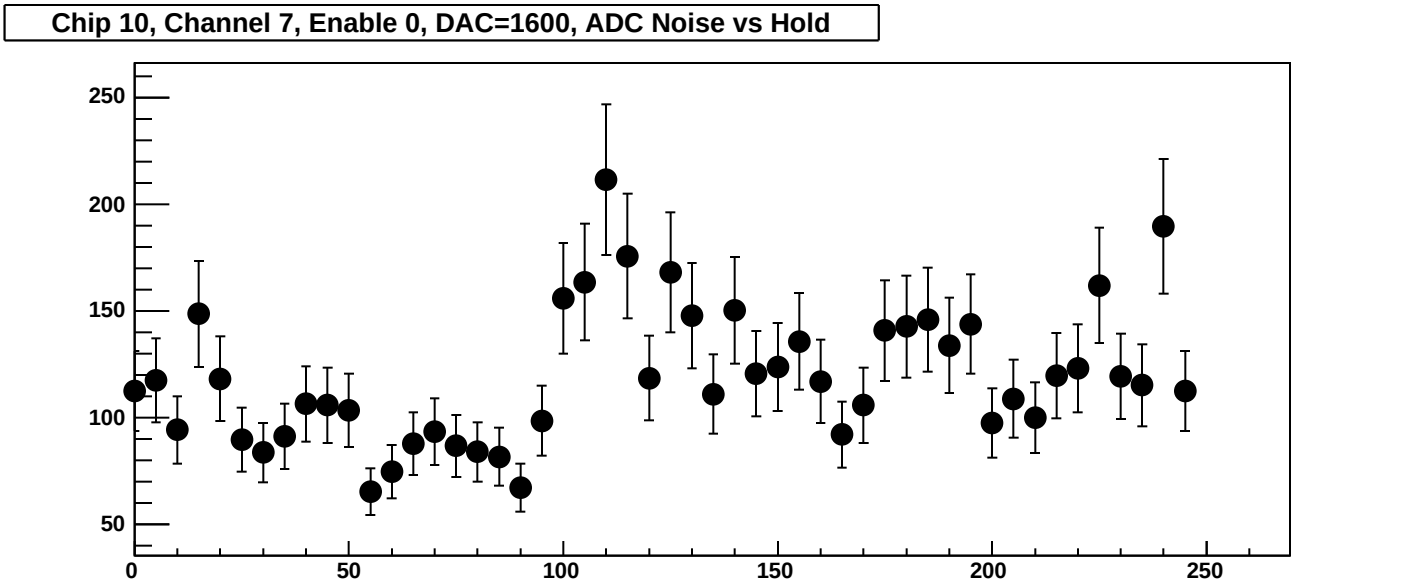


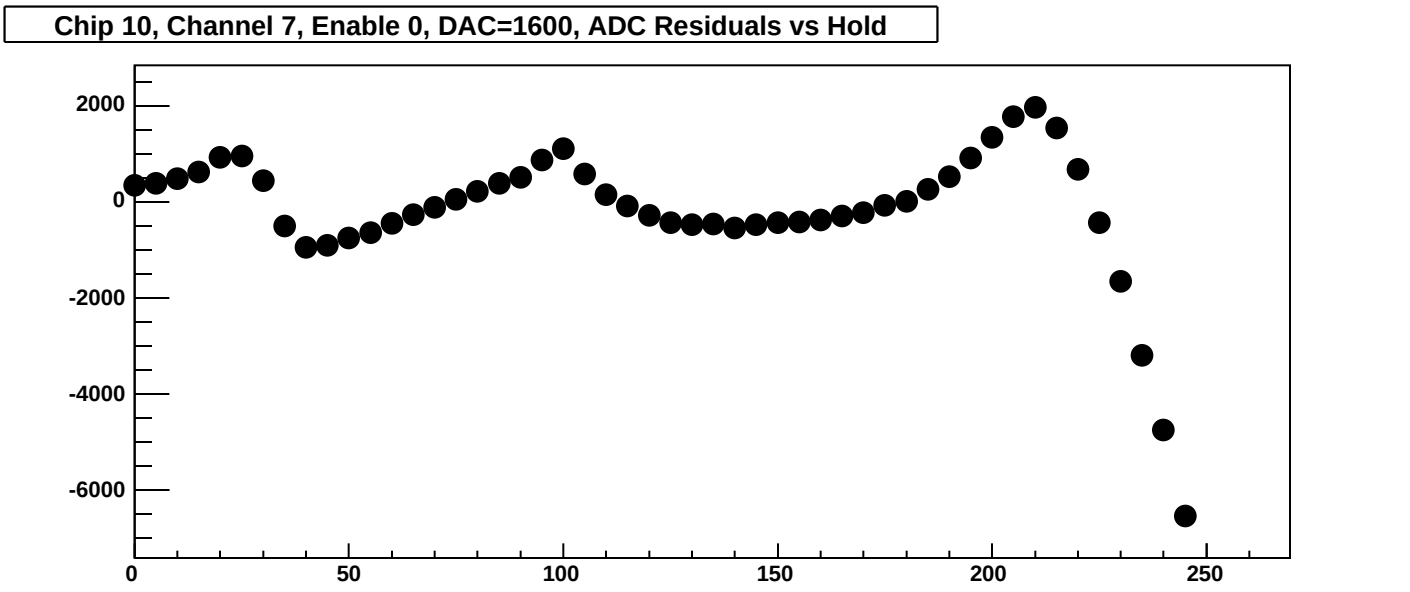

Chip 10, Channel 6, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 8.431e+05 / 41
p0 383.5 ± 16.75
p1 -21.18 ± 0.09985
p2 -8178 ± 19.55
p3 -1235 ± 13.45
p4 153.9 ± 0.108
20000
15000
10000
5000
0
0
50
100
150
200
250

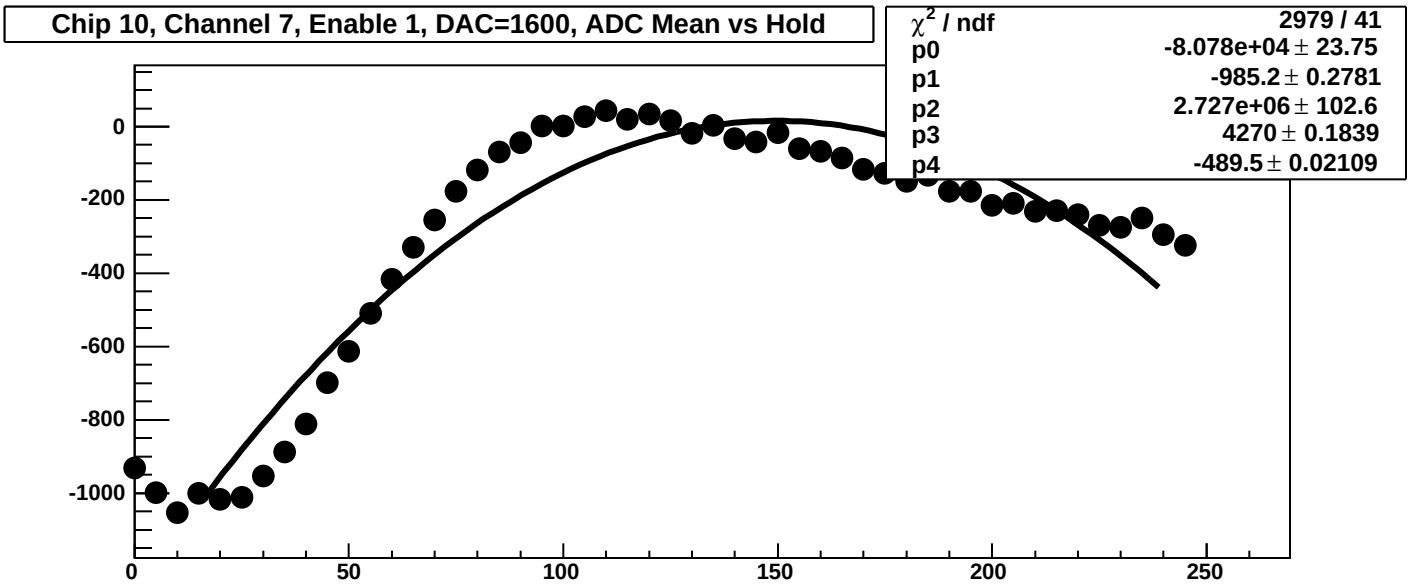

Chip 10, Channel 6, Enable 5, DAC=1600, ADC Noise vs Hold
2500
2000
1500
1000
500
0
0
50
100
150
200
250

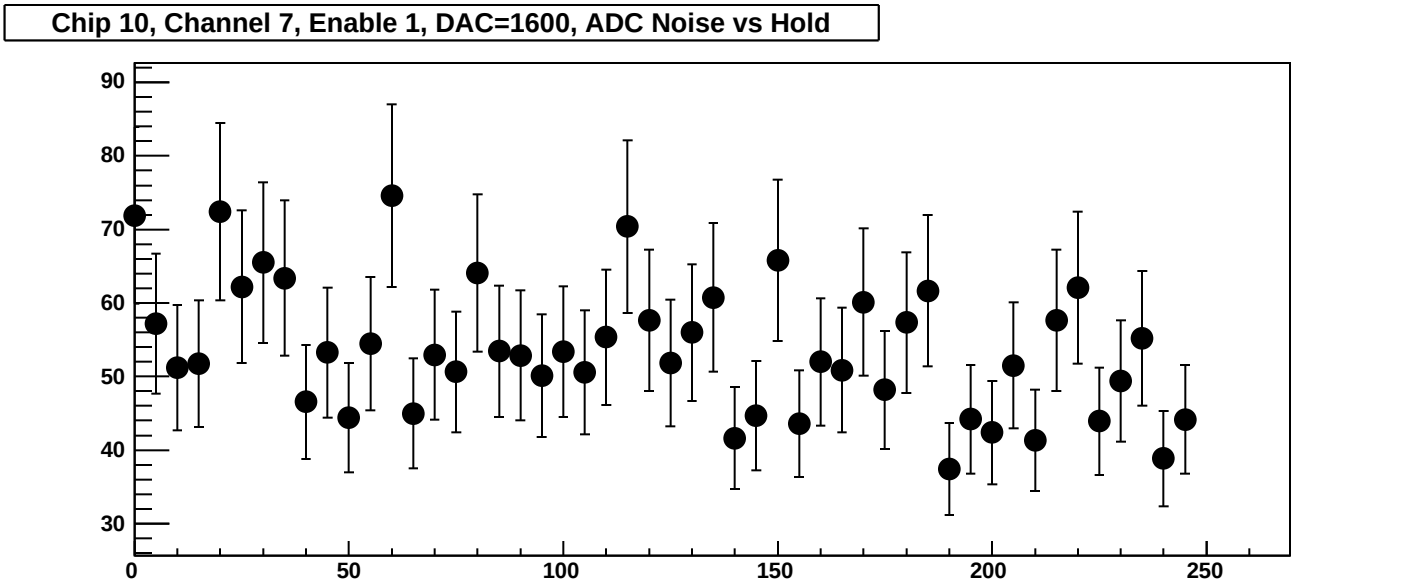

Chip 10, Channel 6, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

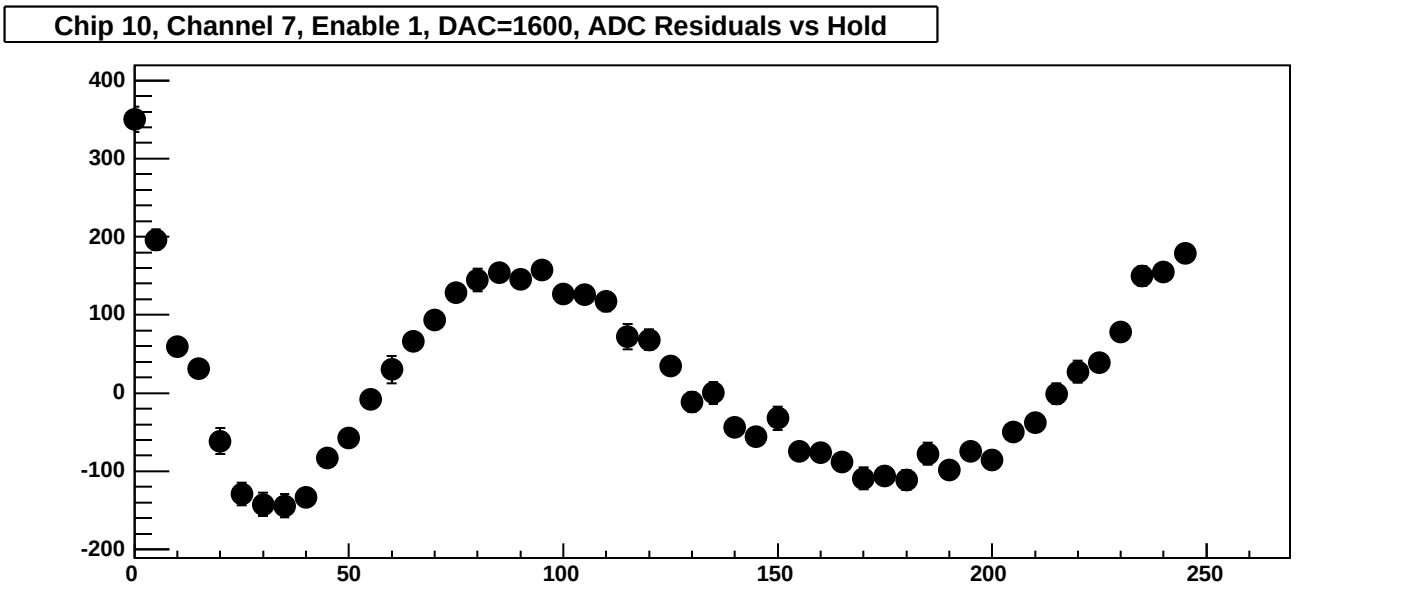

Chip 10, Channel 7, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 6.888e+04 / 41
p0 -4094 ± 8.771
p1 96.62 ± 0.05558
p2 2.844e+04 ± 51.98
p3 56.51 ± 0.1617
p4 -30.33 ± 0.2096
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

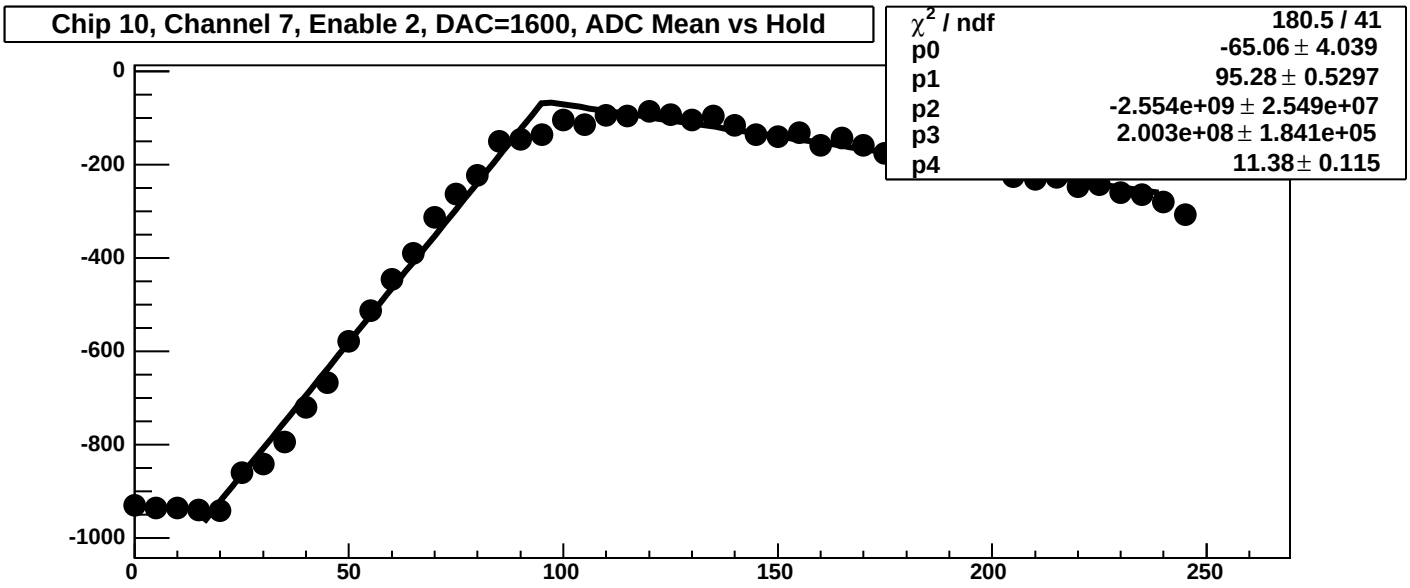

Chip 10, Channel 7, Enable 0, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
50
100
150
200
250

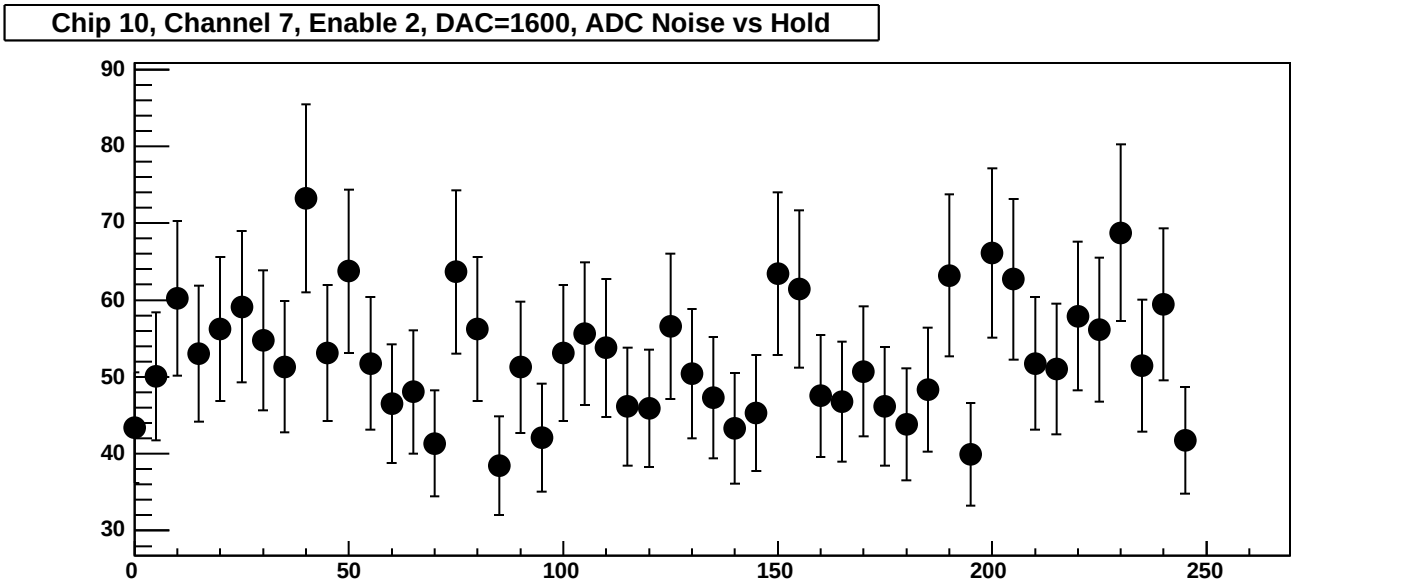

Chip 10, Channel 7, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

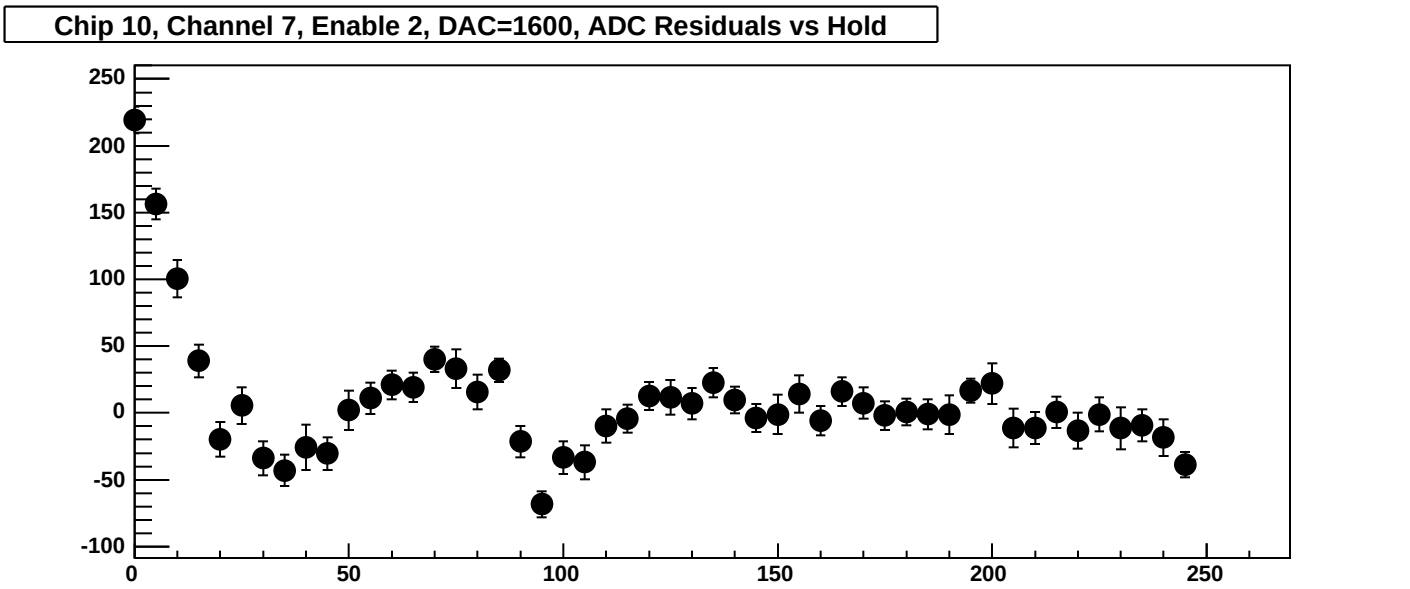

Chip 10, Channel 7, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 2979 / 41
p0 -8.078e+04 ± 23.75
p1 -985.2 ± 0.2781
p2 2.727e+06 ± 102.6
p3 4270 ± 0.1839
p4 -489.5 ± 0.02109
0
-200
-400
-600
-800
-1000
0
50
100
150
200
250

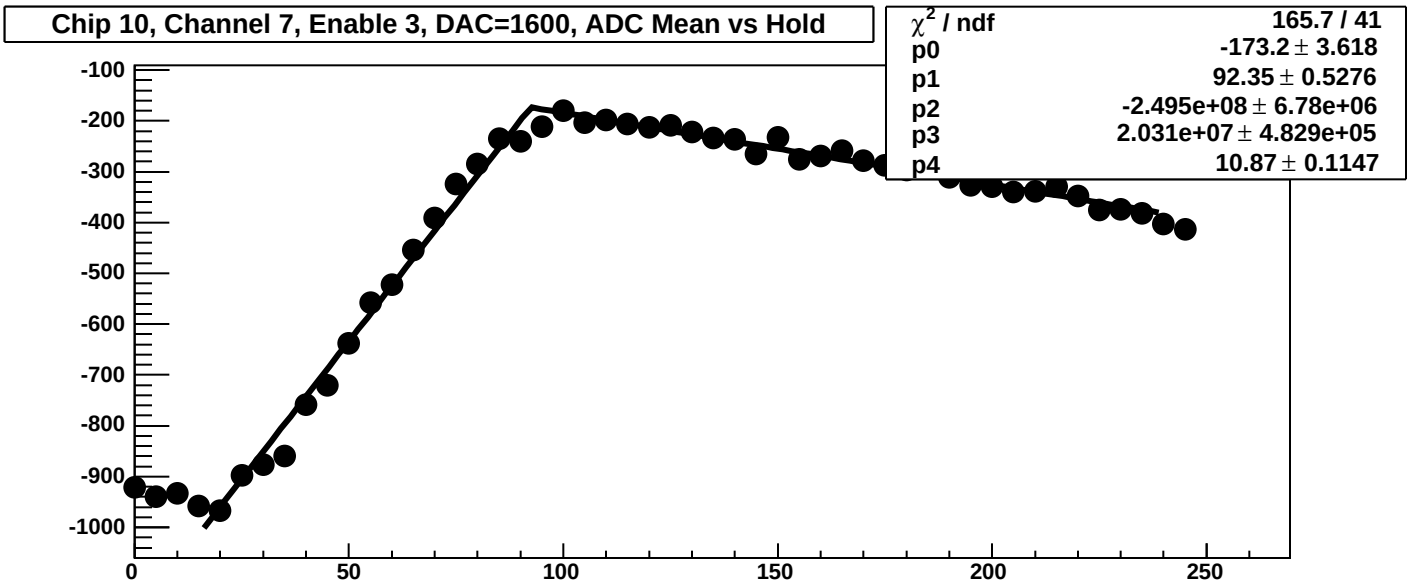

Chip 10, Channel 7, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

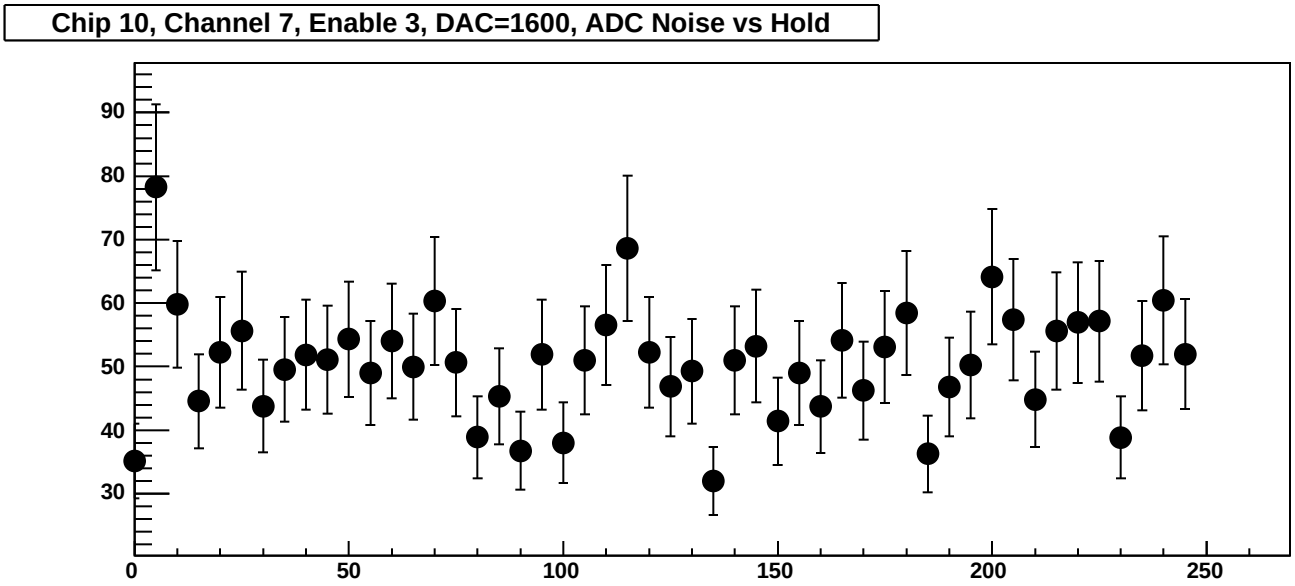

Chip 10, Channel 7, Enable 1, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
-200
0
50
100
150
200
250

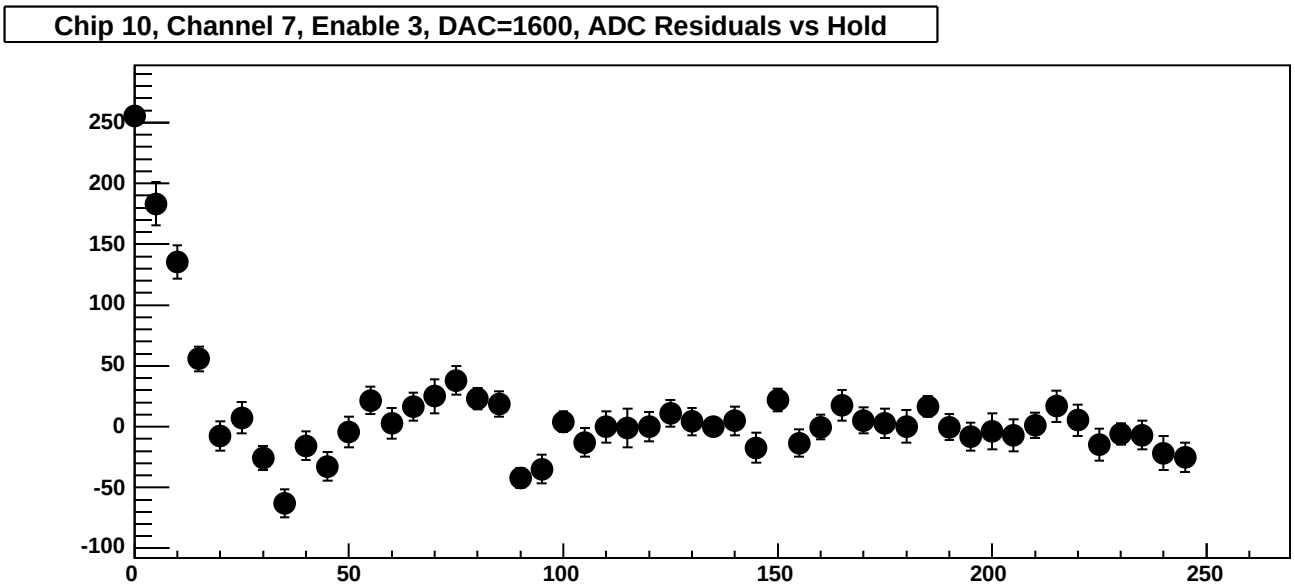

Chip 10, Channel 7, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 180.5 / 41
p0 -65.06 ± 4.039
p1 95.28 ± 0.5297
p2 -2.554e+09 ± 2.549e+07
p3 2.003e+08 ± 1.841e+05
p4 11.38 ± 0.115
0
-200
-400
-600
-800
-1000
0
50
100
150
200
250

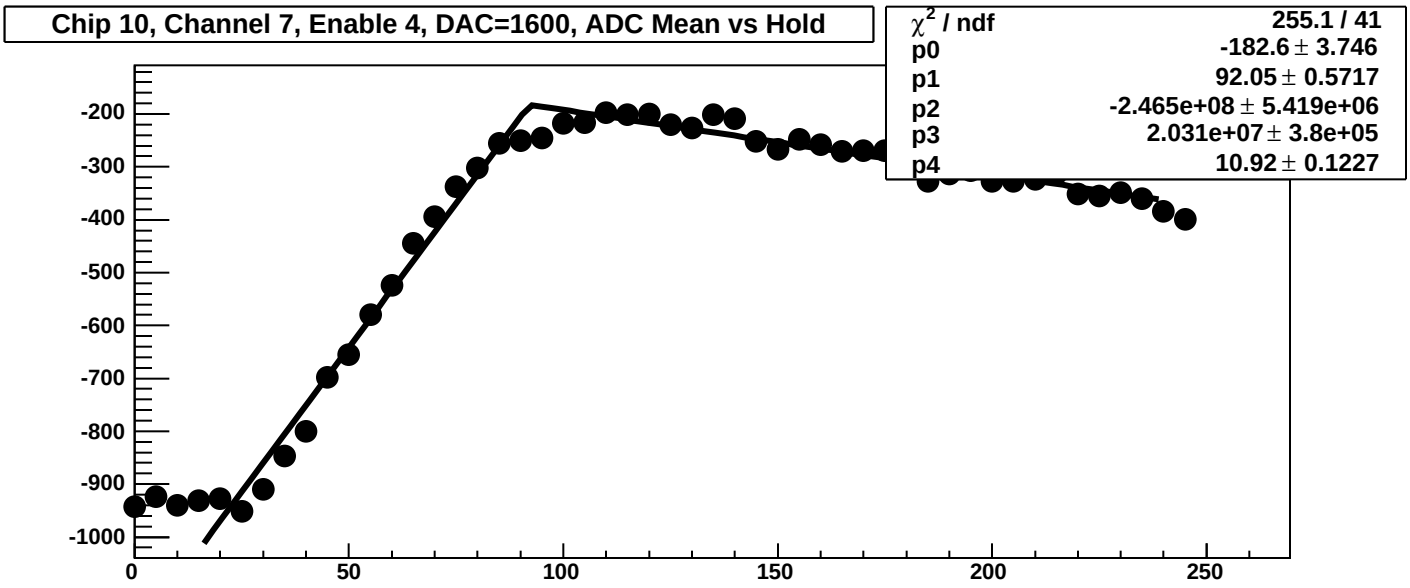

Chip 10, Channel 7, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

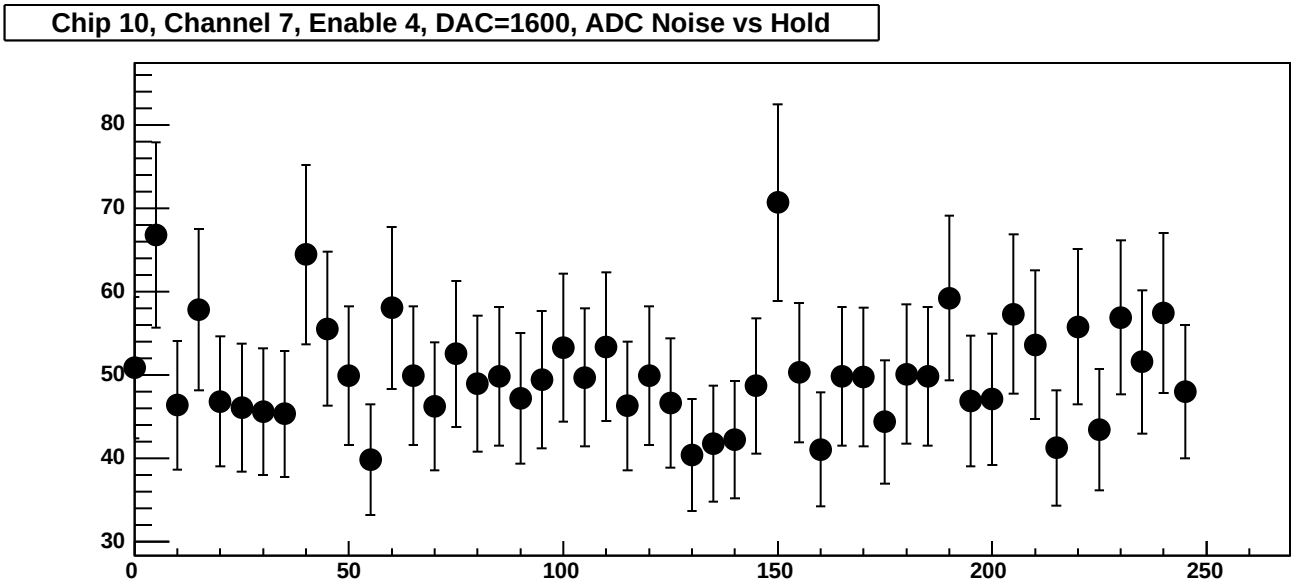

Chip 10, Channel 7, Enable 2, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

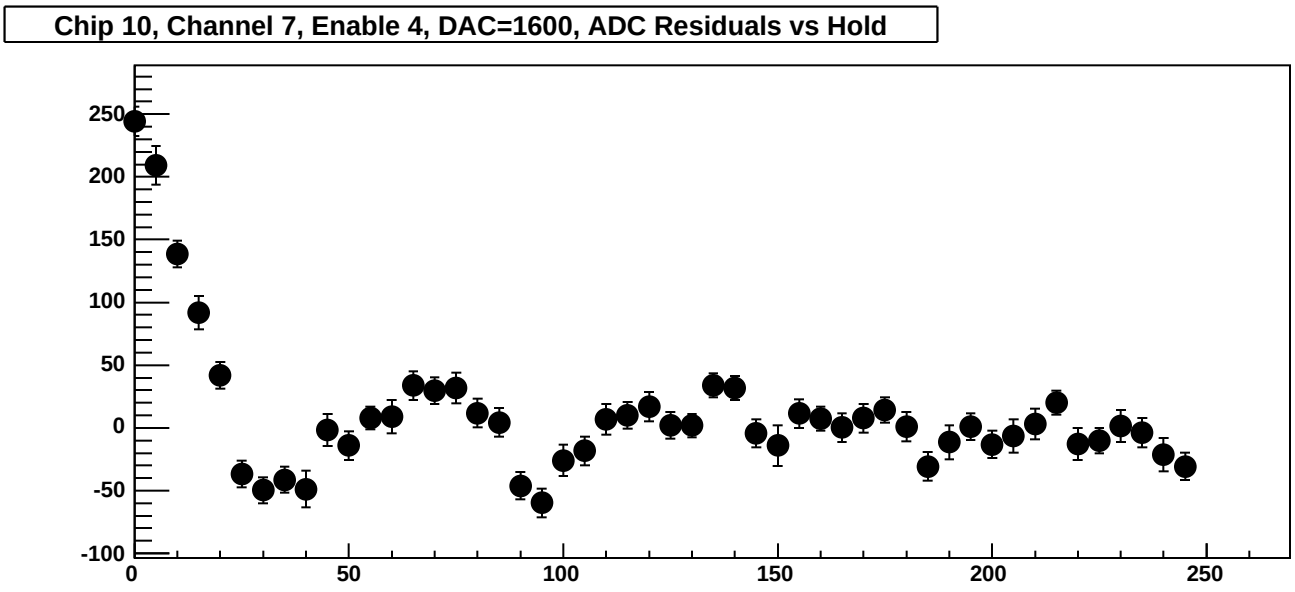

Chip 10, Channel 7, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 165.7 / 41
p0 -173.2 ± 3.618
p1 92.35 ± 0.5276
p2 -2.495e+08 ± 6.78e+06
p3 2.031e+07 ± 4.829e+05
p4 10.87 ± 0.1147
-100
-200
-300
-400
-500
-600
-700
-800
-900
-1000
0
50
100
150
200
250


Chip 10, Channel 7, Enable 3, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

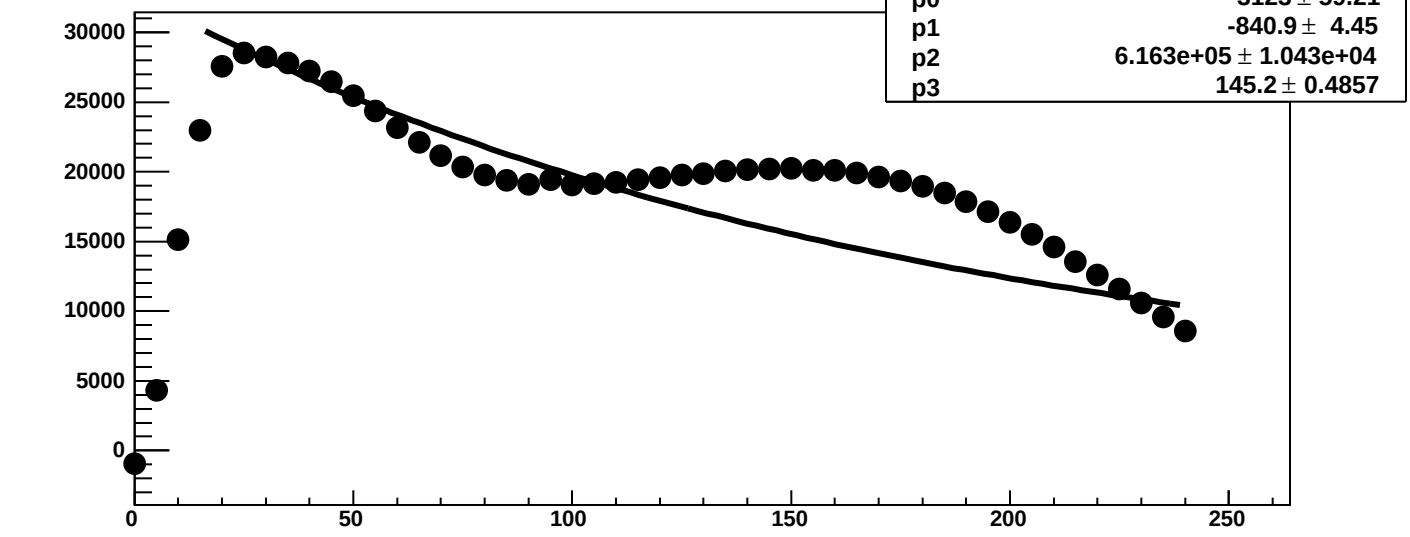

Chip 10, Channel 7, Enable 3, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250


Chip 10, Channel 7, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 255.1 / 41
p0 -182.6 ± 3.746
p1 92.05 ± 0.5717
p2 -2.465e+08 ± 5.419e+06
p3 2.031e+07 ± 3.8e+05
p4 10.92 ± 0.1227
-200
-300
-400
-500
-600
-700
-800
-900
-1000
0
50
100
150
200
250

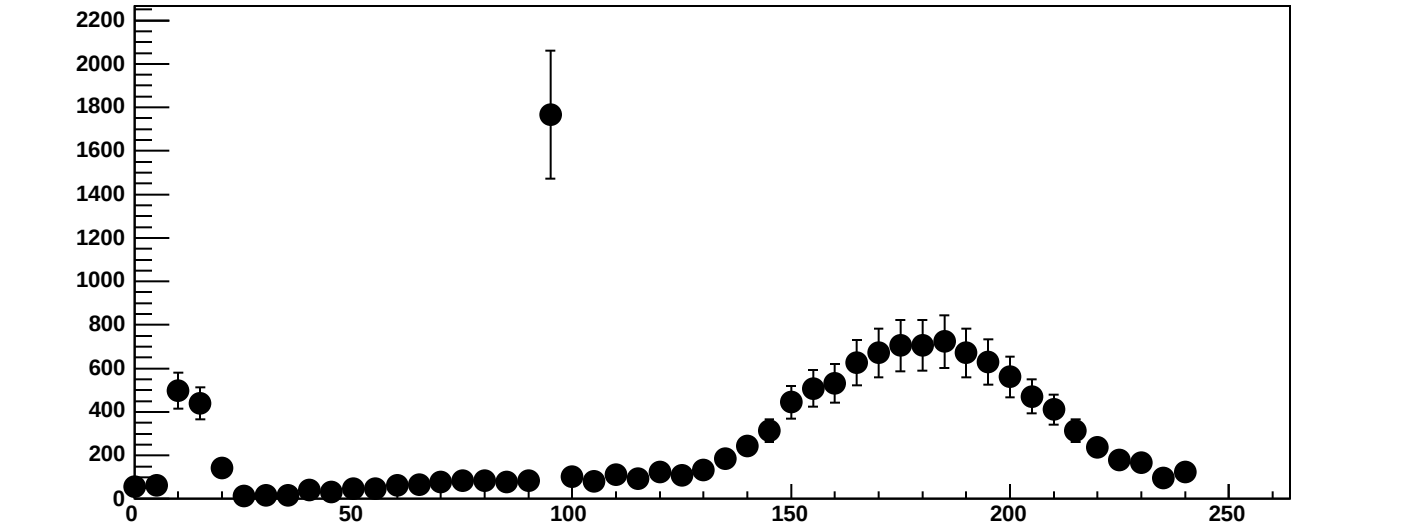

Chip 10, Channel 7, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 10, Channel 7, Enable 4, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

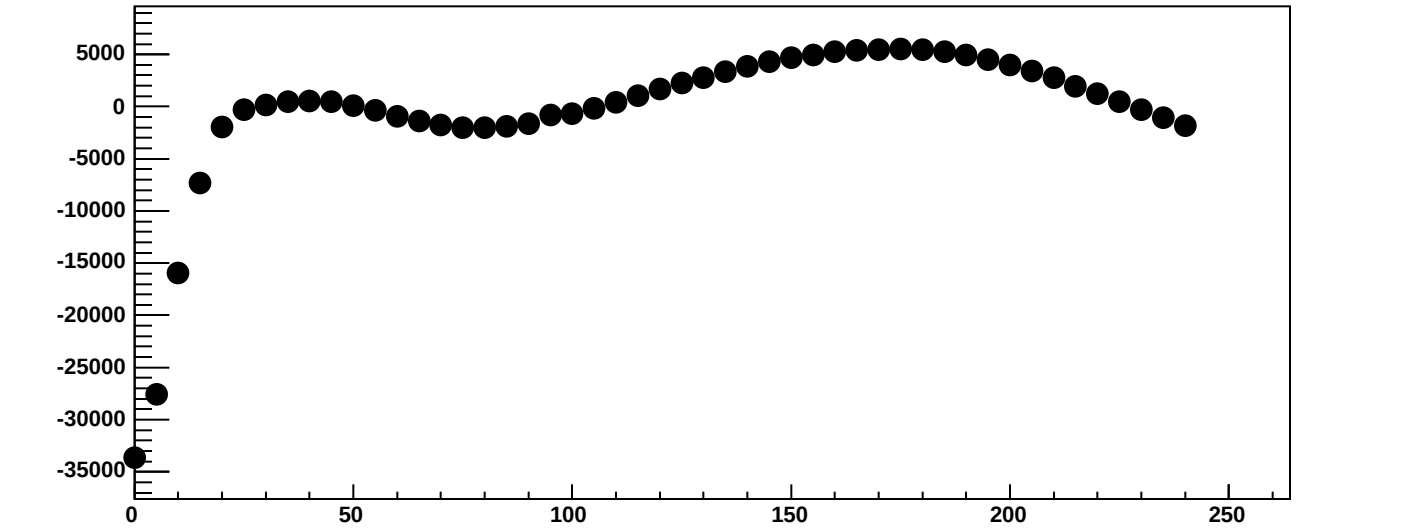
Chip 10, Channel 7, Enable 5!, DAC=1600, ADC Mean vs Hold



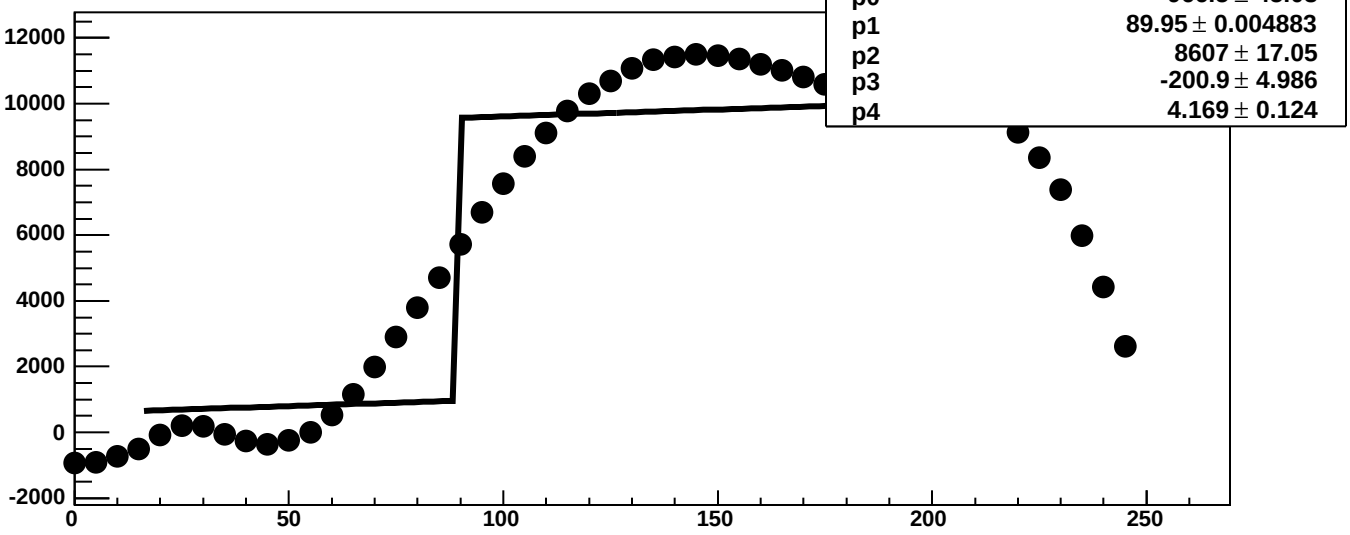
Chip 10, Channel 7, Enable 5!, DAC=1600, ADC Noise vs Hold



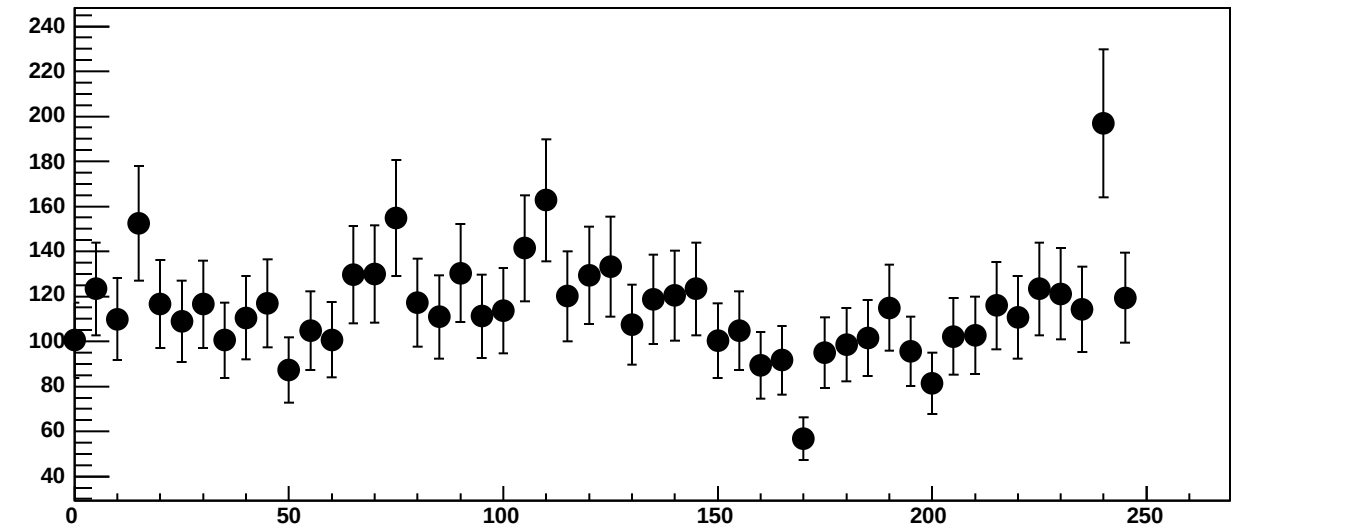
Chip 10, Channel 7, Enable 5!, DAC=1600, ADC Residuals vs Hold



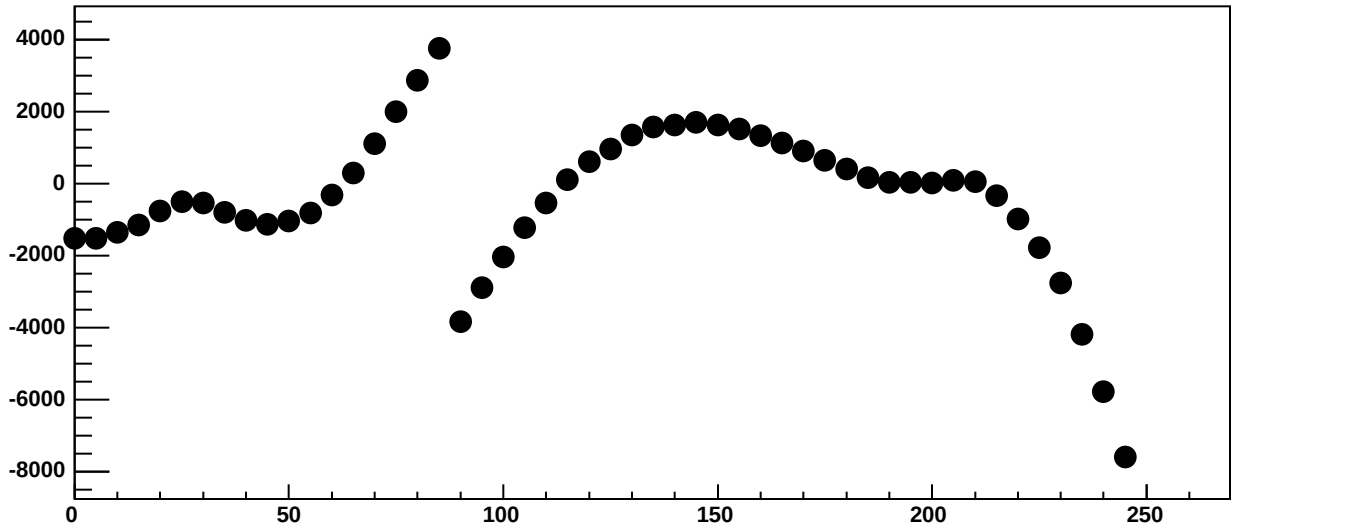
Chip 10, Channel 8, Enable 0, DAC=1600, ADC Mean vs Hold

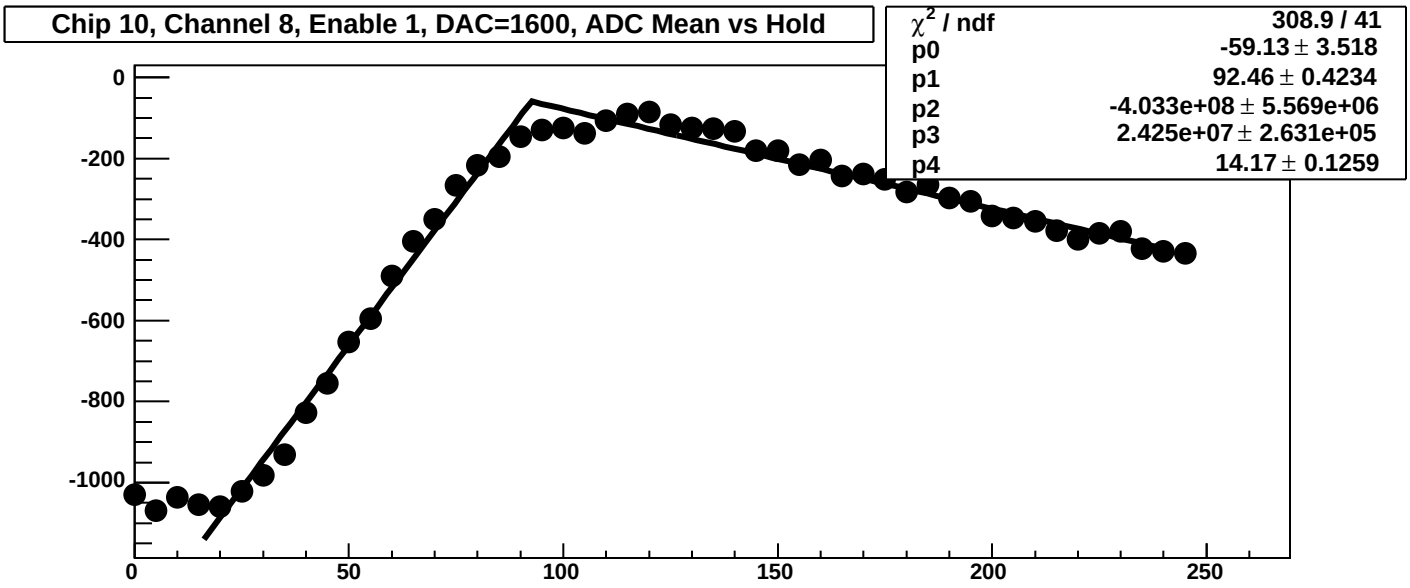


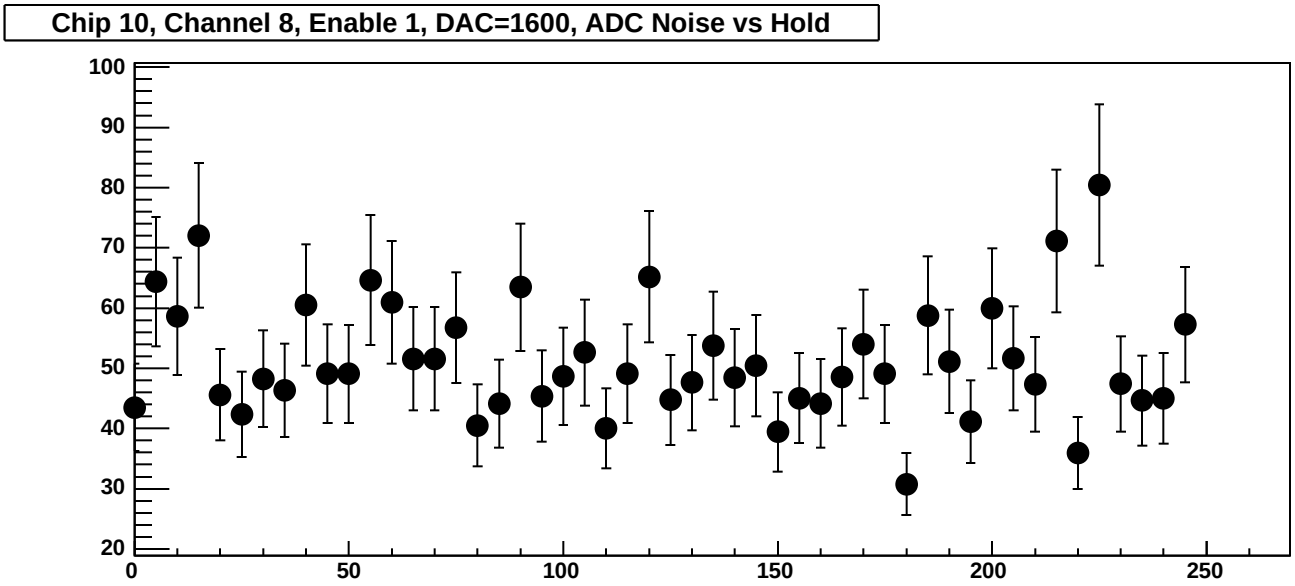
Chip 10, Channel 8, Enable 0, DAC=1600, ADC Noise vs Hold

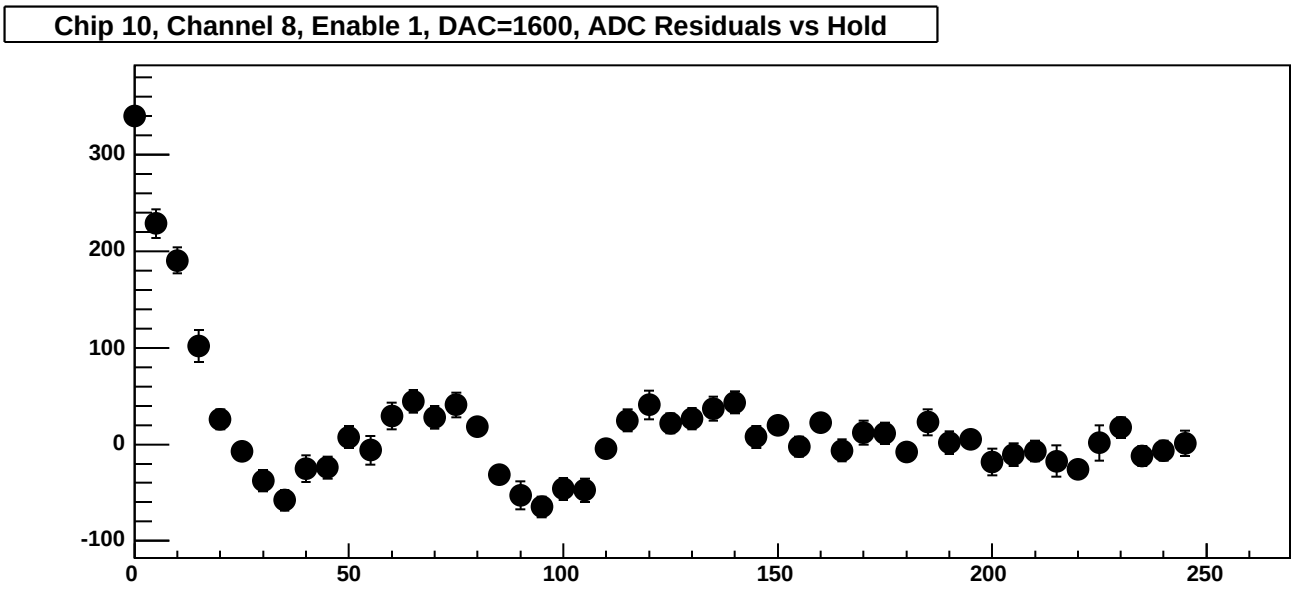


Chip 10, Channel 8, Enable 0, DAC=1600, ADC Residuals vs Hold

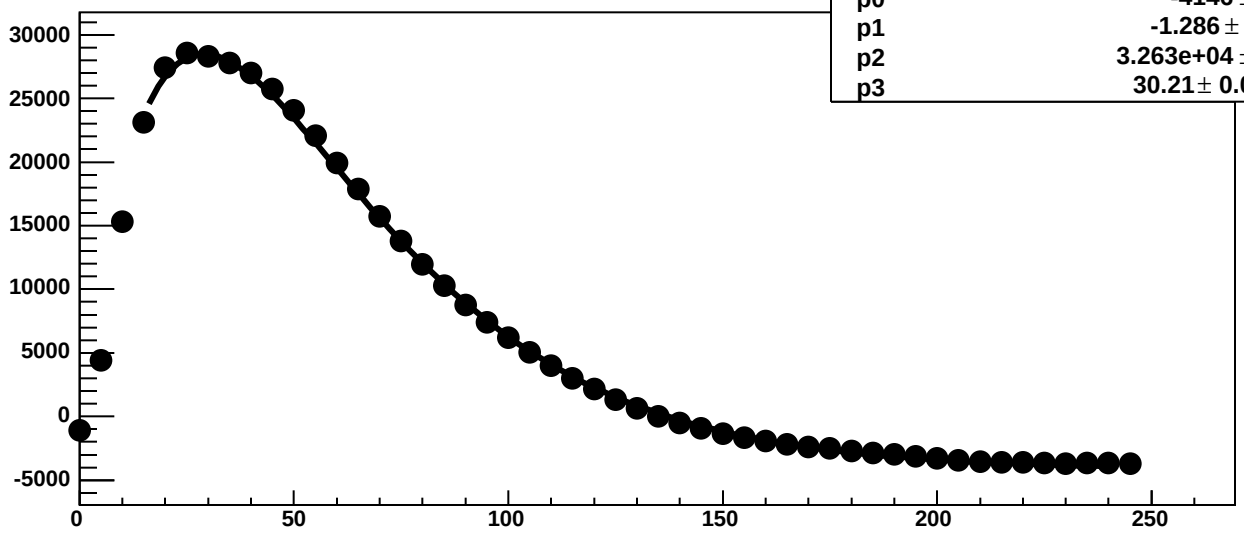



Chip 10, Channel 8, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 308.9 / 41
p0 -59.13 ± 3.518
p1 92.46 ± 0.4234
p2 -4.033e+08 ± 5.569e+06
p3 2.425e+07 ± 2.631e+05
p4 14.17 ± 0.1259
0
-200
-400
-600
-800
-1000
0
50
100
150
200
250

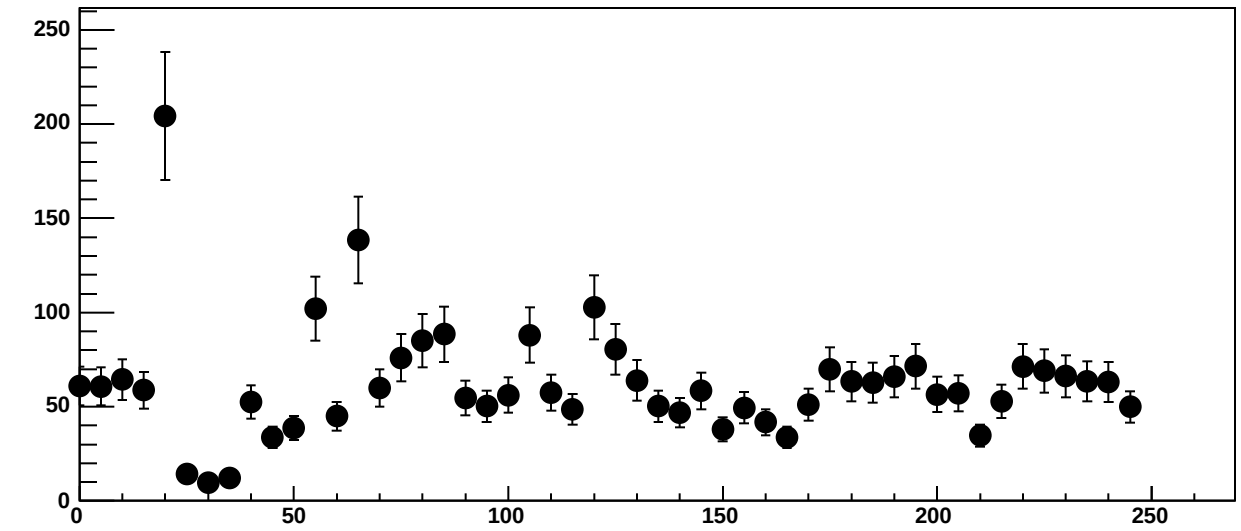

Chip 10, Channel 8, Enable 1, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 10, Channel 8, Enable 1, DAC=1600, ADC Residuals vs Hold
300
200
100
0
-100
0
50
100
150
200
250

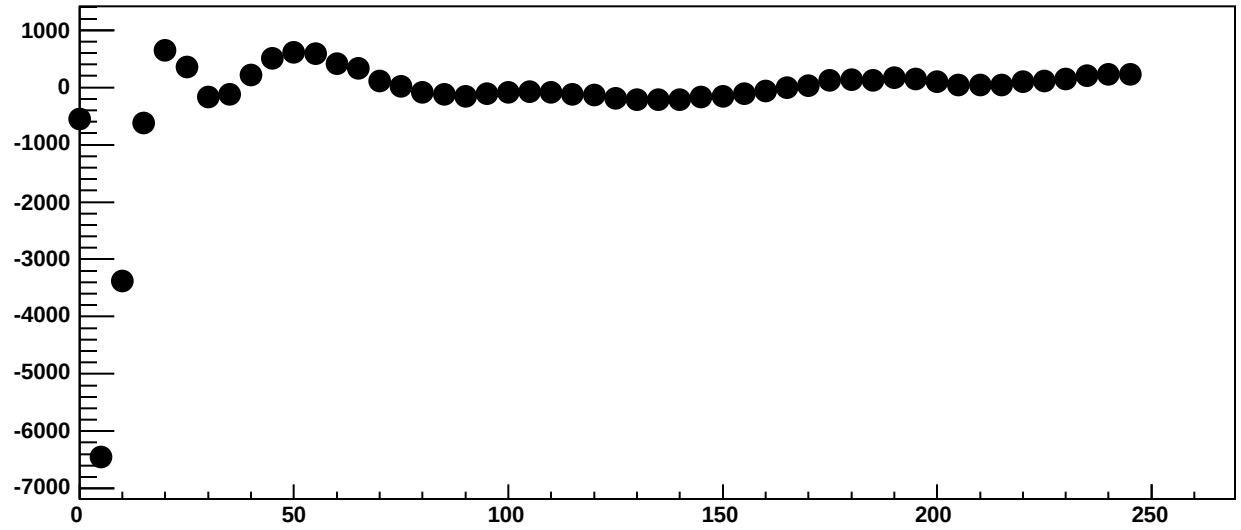
Chip 10, Channel 8, Enable 2!, DAC=1600, ADC Mean vs Hold



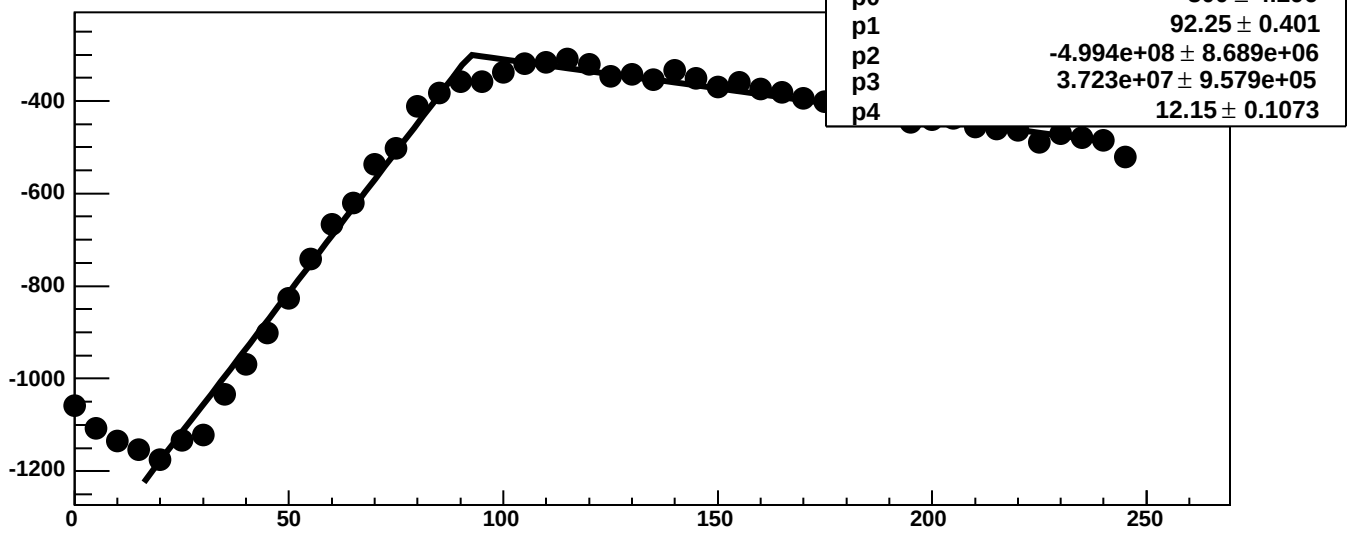
Chip 10, Channel 8, Enable 2!, DAC=1600, ADC Noise vs Hold



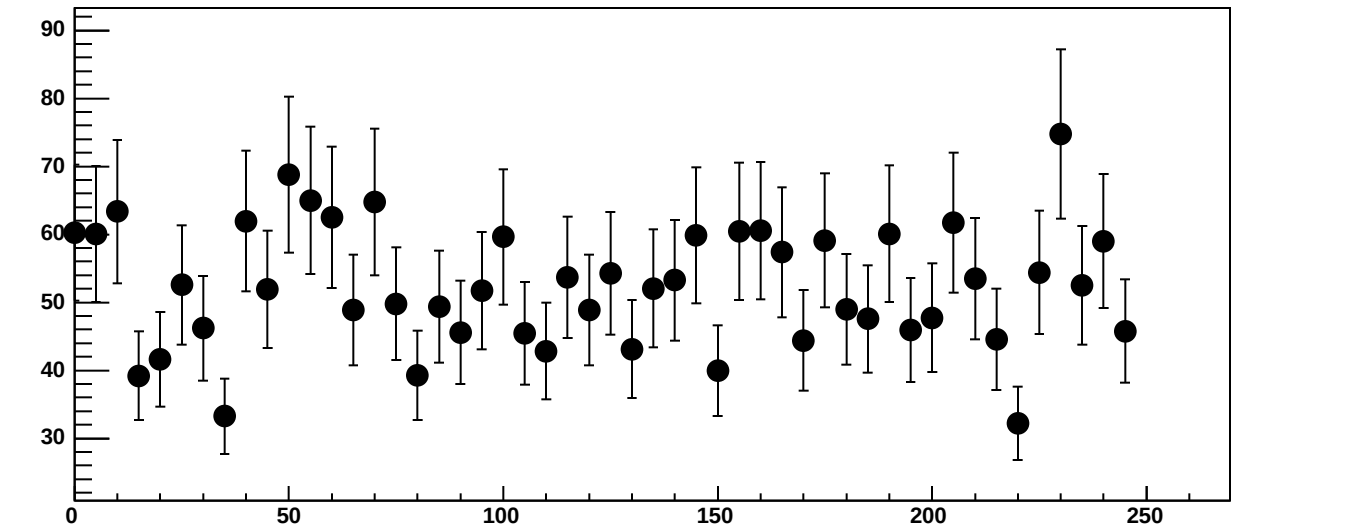
Chip 10, Channel 8, Enable 2!, DAC=1600, ADC Residuals vs Hold



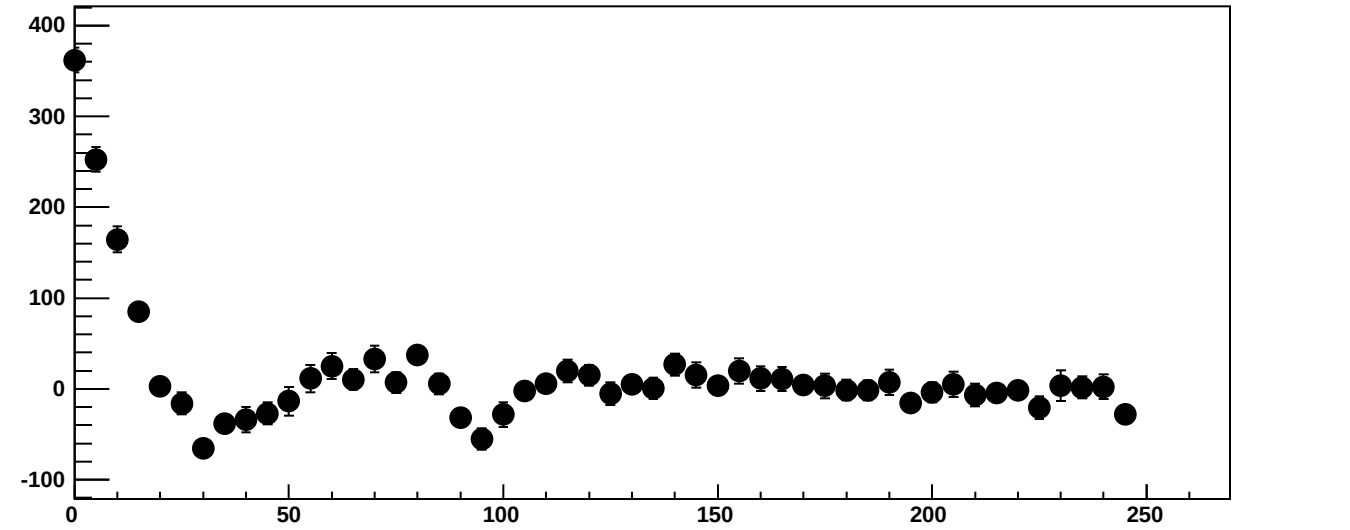
Chip 10, Channel 8, Enable 3, DAC=1600, ADC Mean vs Hold



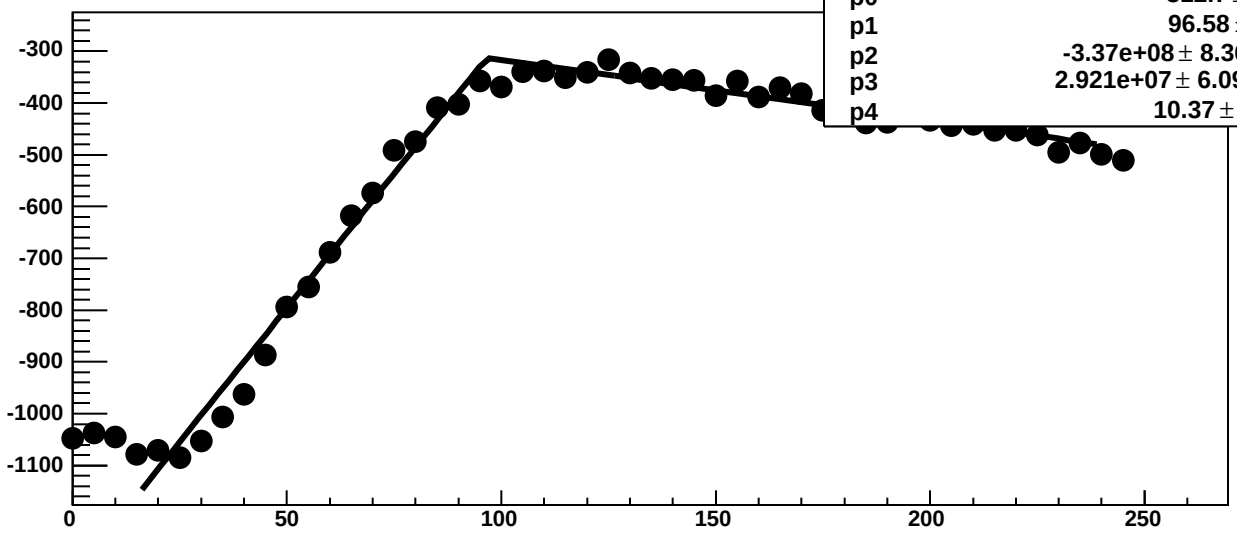
Chip 10, Channel 8, Enable 3, DAC=1600, ADC Noise vs Hold



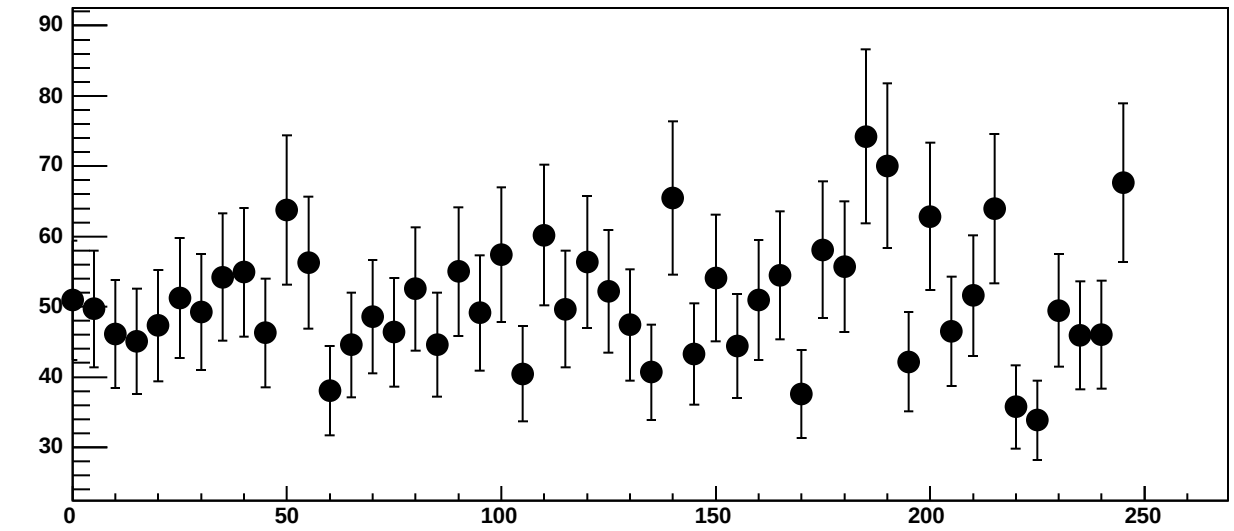
Chip 10, Channel 8, Enable 3, DAC=1600, ADC Residuals vs Hold



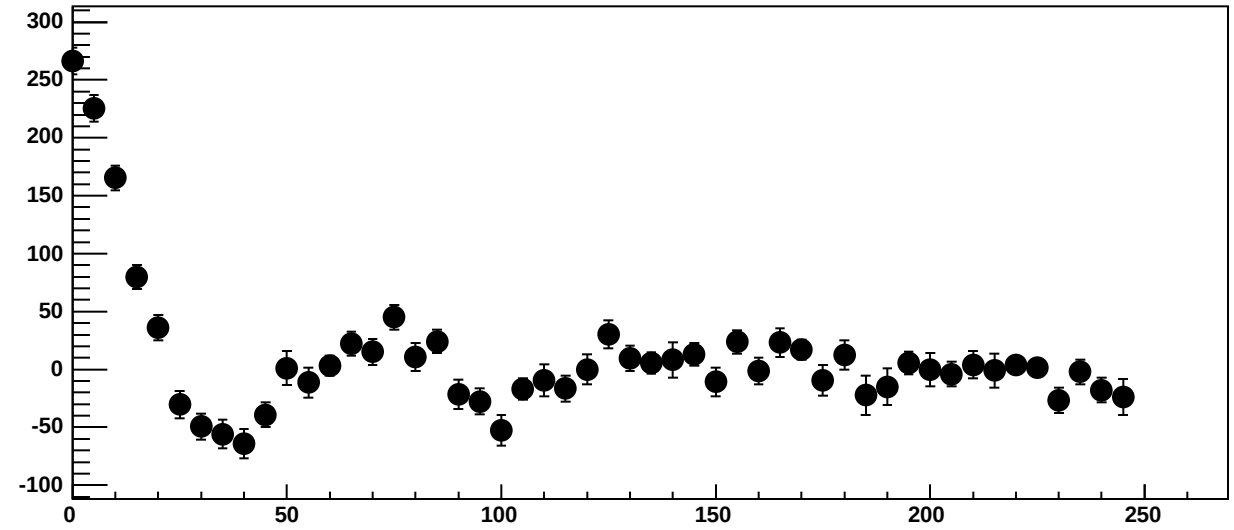
Chip 10, Channel 8, Enable 4, DAC=1600, ADC Mean vs Hold

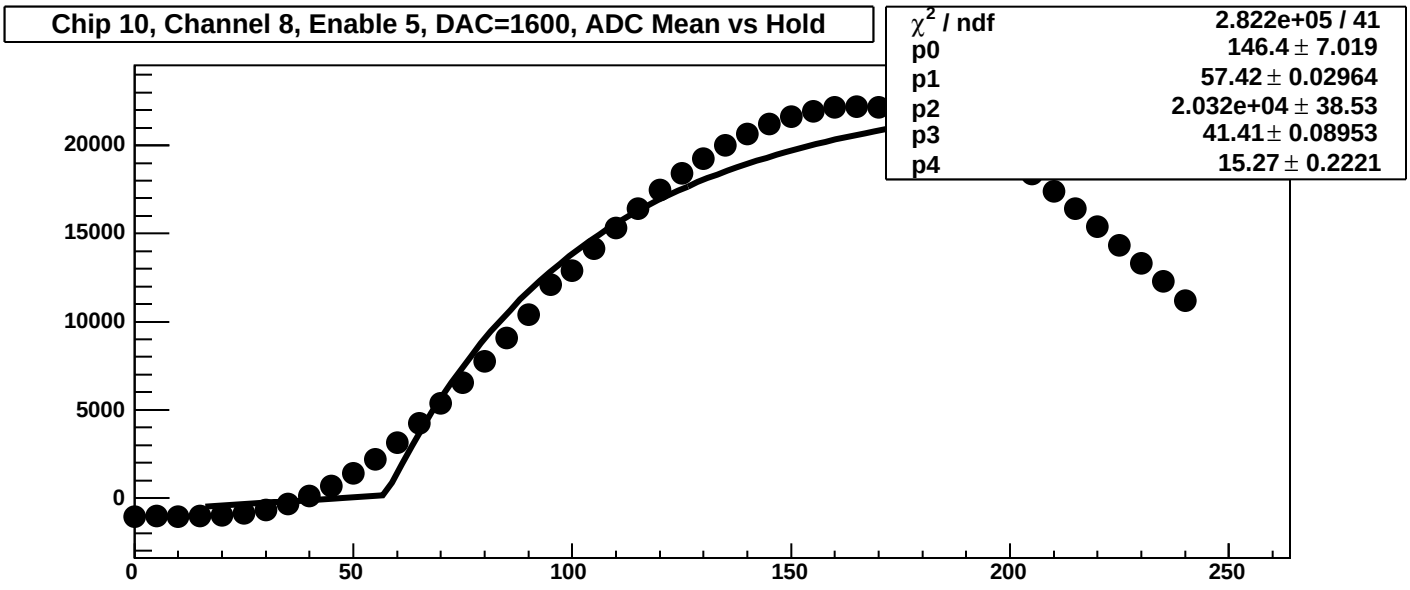


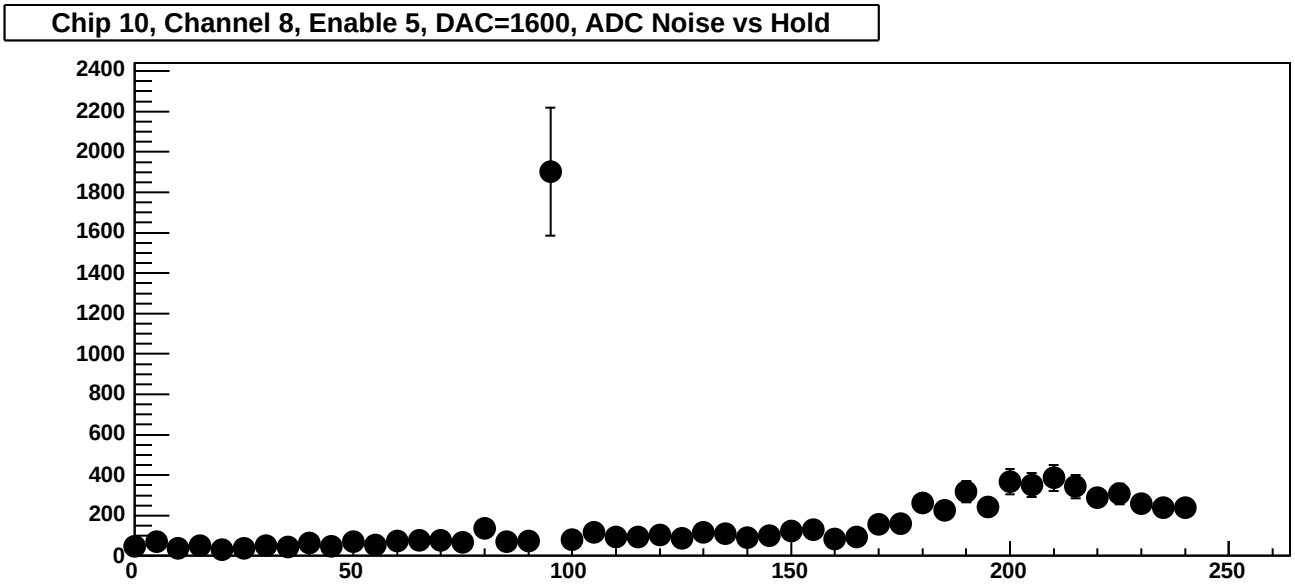
Chip 10, Channel 8, Enable 4, DAC=1600, ADC Noise vs Hold

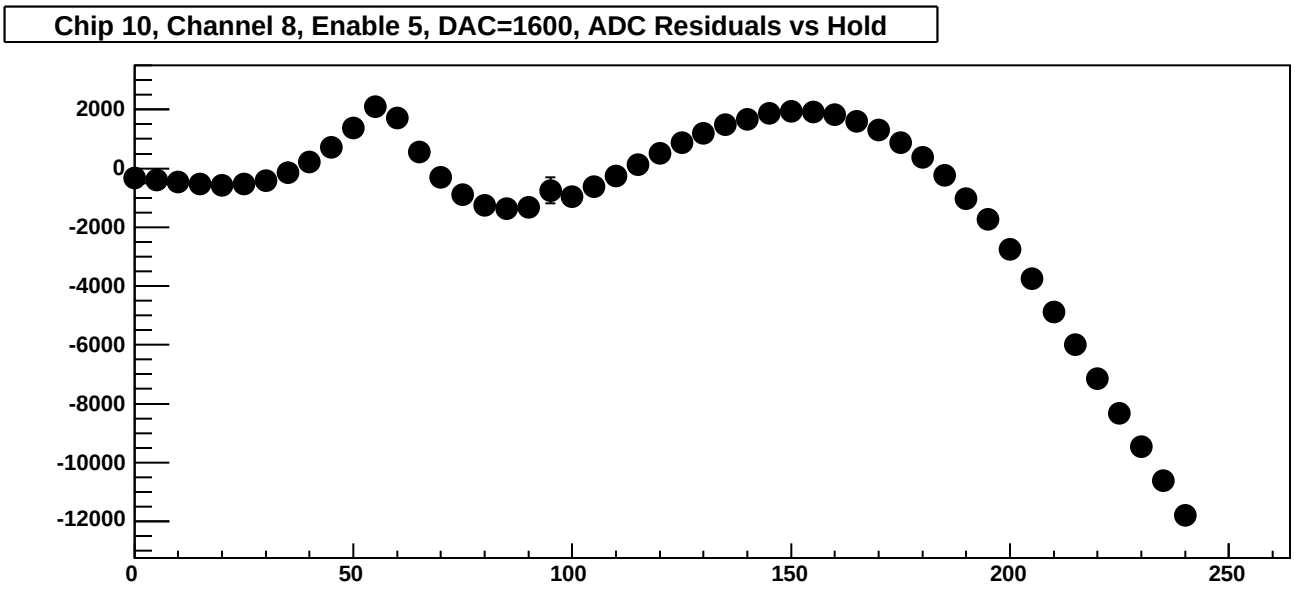


Chip 10, Channel 8, Enable 4, DAC=1600, ADC Residuals vs Hold

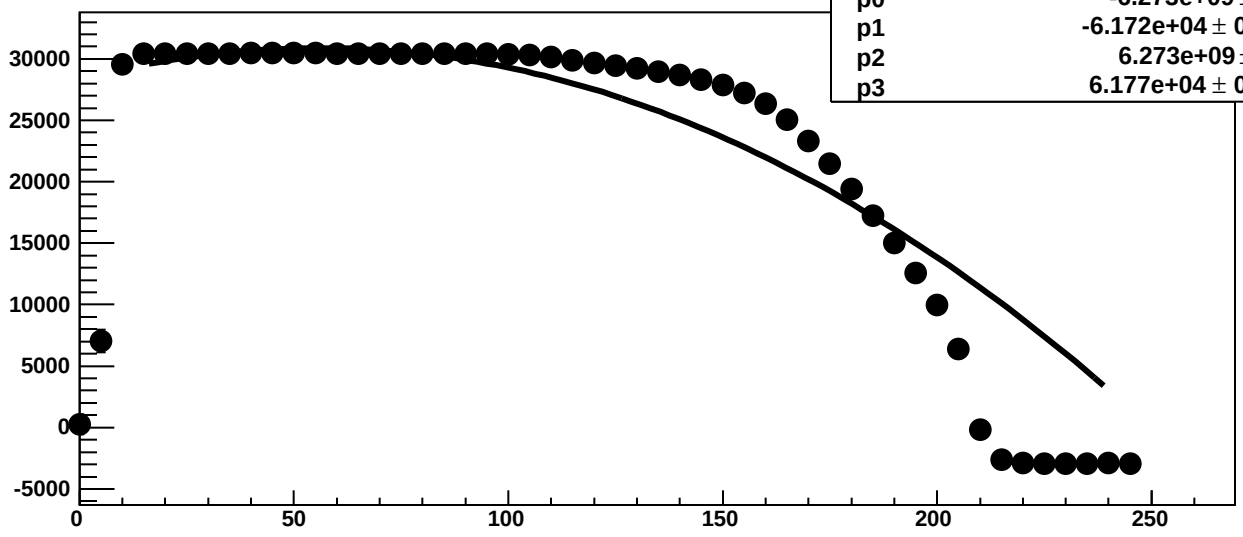



Chip 10, Channel 8, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.822e+05 / 41
p0 146.4 ± 7.019
p1 57.42 ± 0.02964
p2 2.032e+04 ± 38.53
p3 41.41 ± 0.08953
p4 15.27 ± 0.2221
20000
15000
10000
5000
0
0
50
100
150
200
250

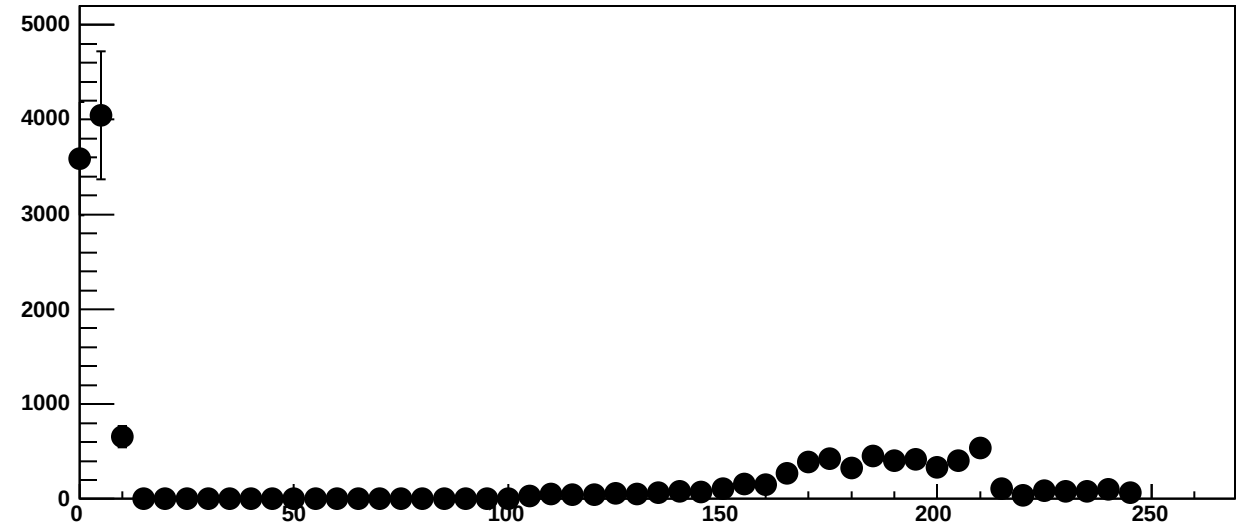

Chip 10, Channel 8, Enable 5, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 10, Channel 8, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
0
50
100
150
200
250

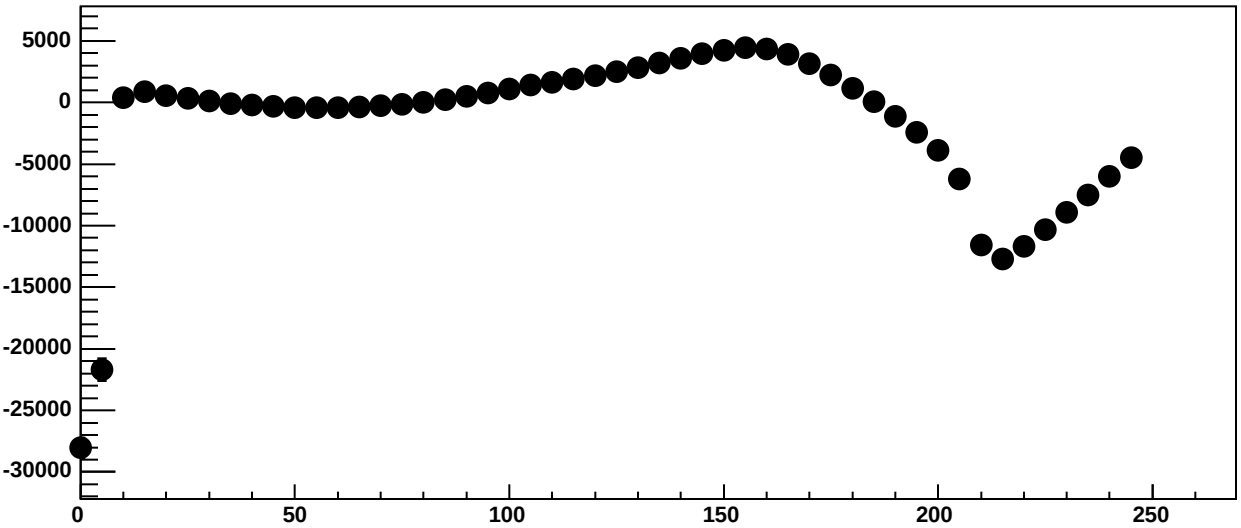
Chip 10, Channel 9, Enable 0!, DAC=1600, ADC Mean vs Hold



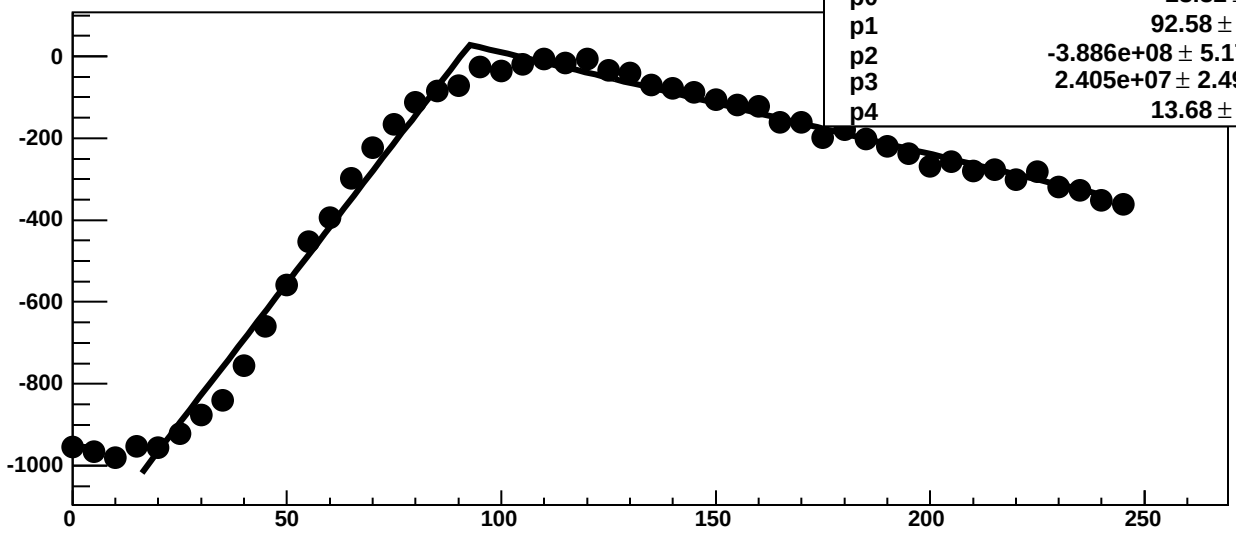
Chip 10, Channel 9, Enable 0!, DAC=1600, ADC Noise vs Hold



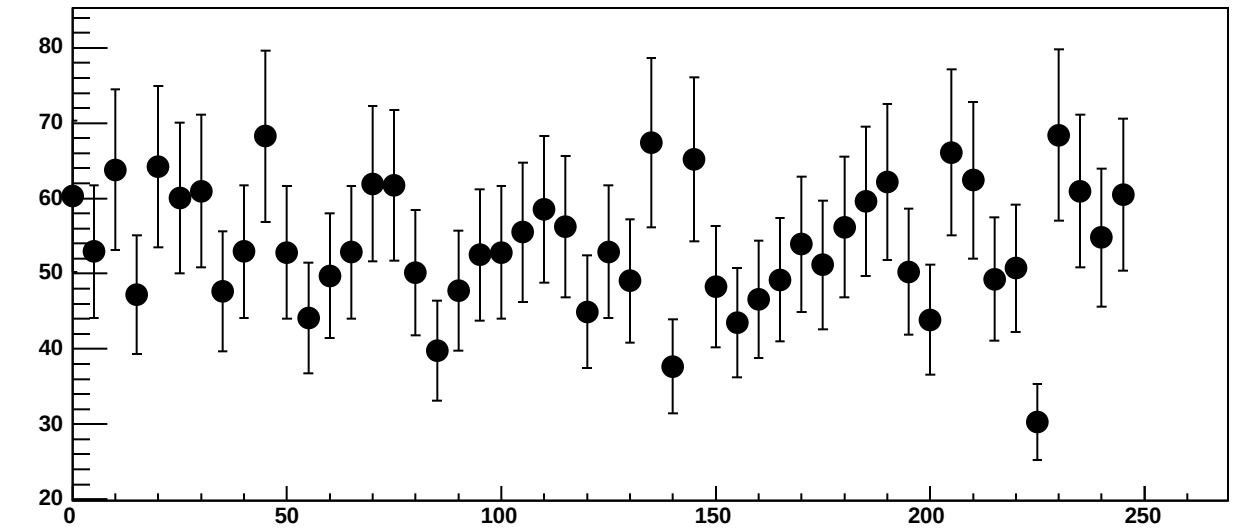
Chip 10, Channel 9, Enable 0!, DAC=1600, ADC Residuals vs Hold



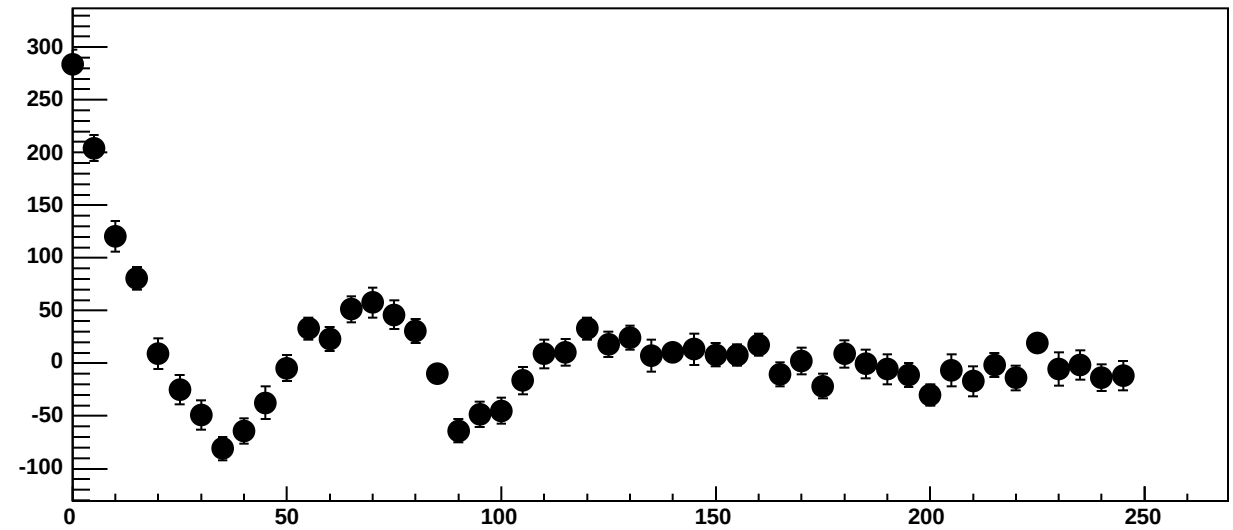
Chip 10, Channel 9, Enable 1, DAC=1600, ADC Mean vs Hold



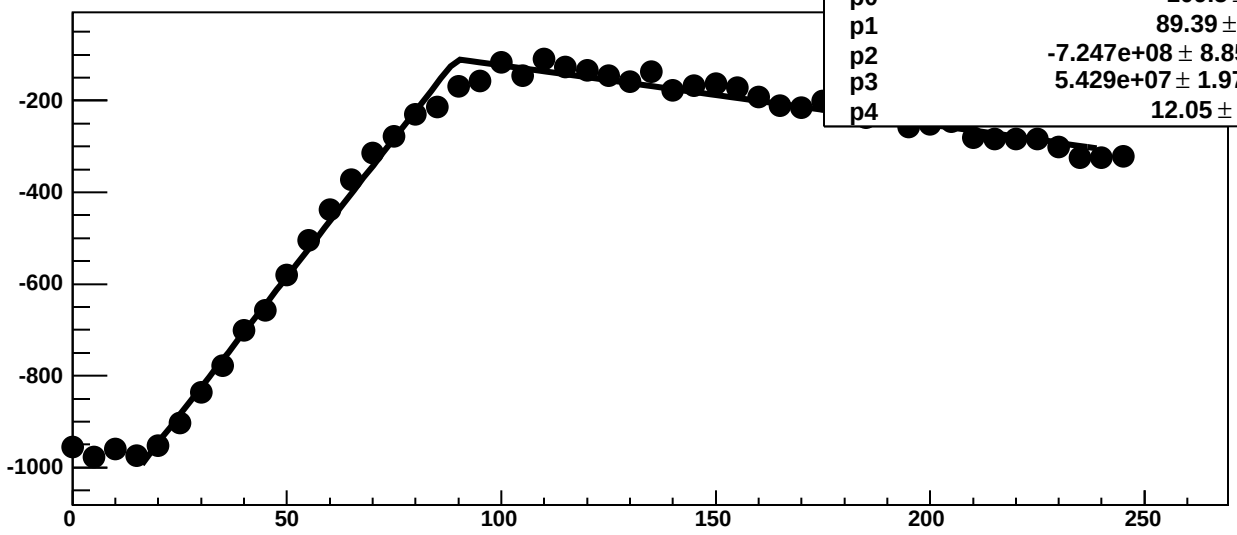
Chip 10, Channel 9, Enable 1, DAC=1600, ADC Noise vs Hold



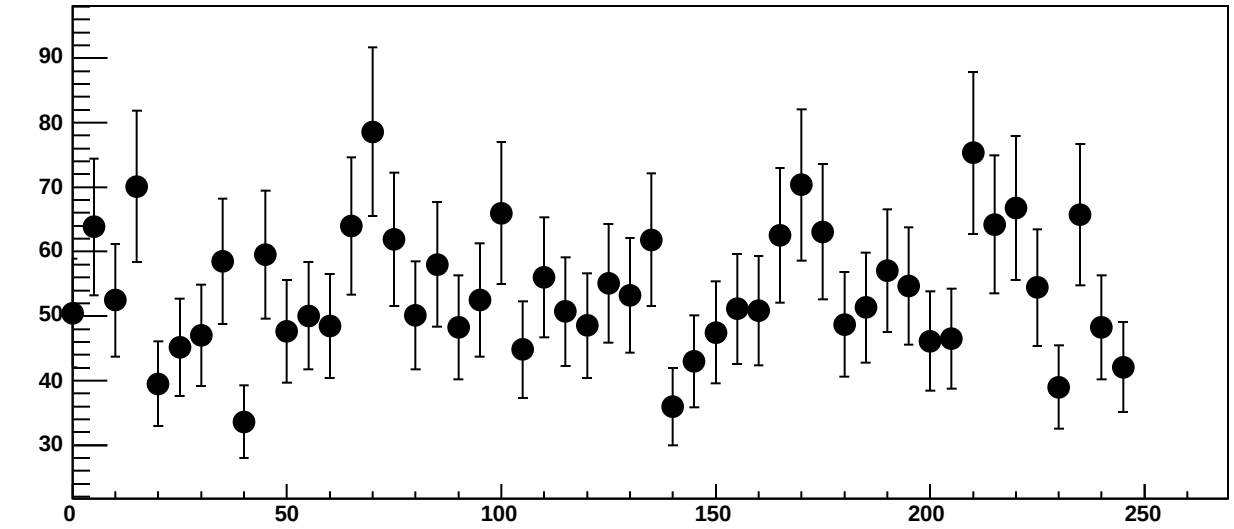
Chip 10, Channel 9, Enable 1, DAC=1600, ADC Residuals vs Hold



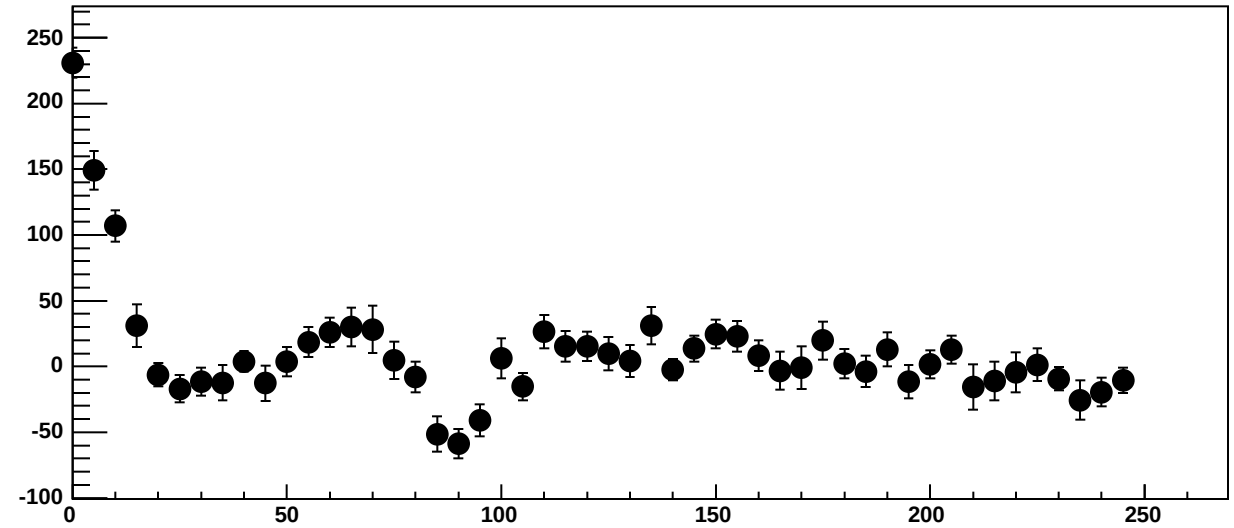
Chip 10, Channel 9, Enable 2, DAC=1600, ADC Mean vs Hold



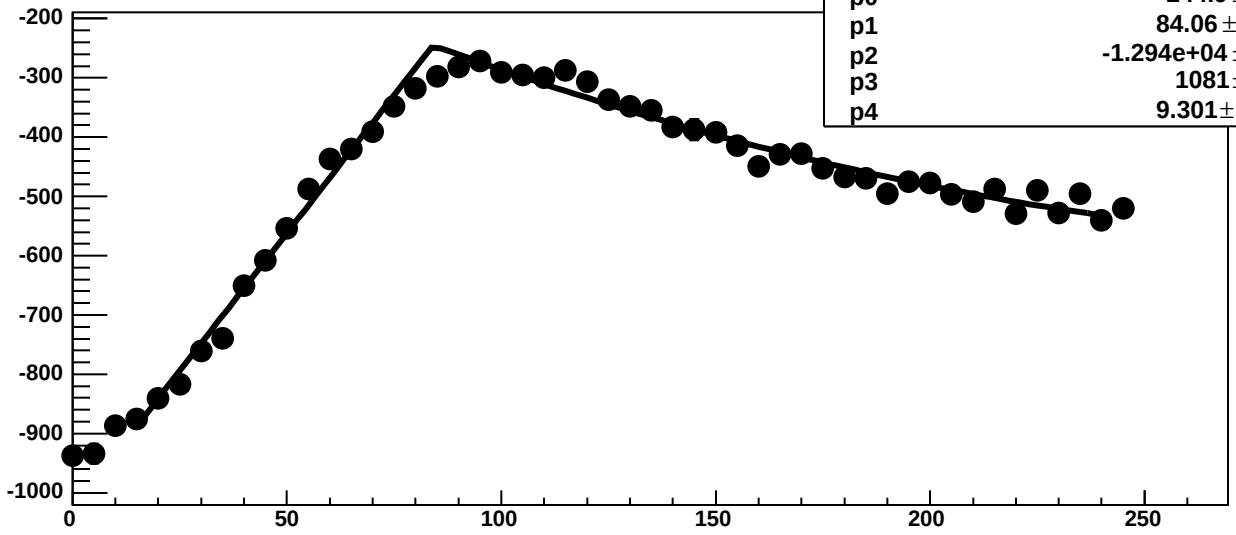
Chip 10, Channel 9, Enable 2, DAC=1600, ADC Noise vs Hold



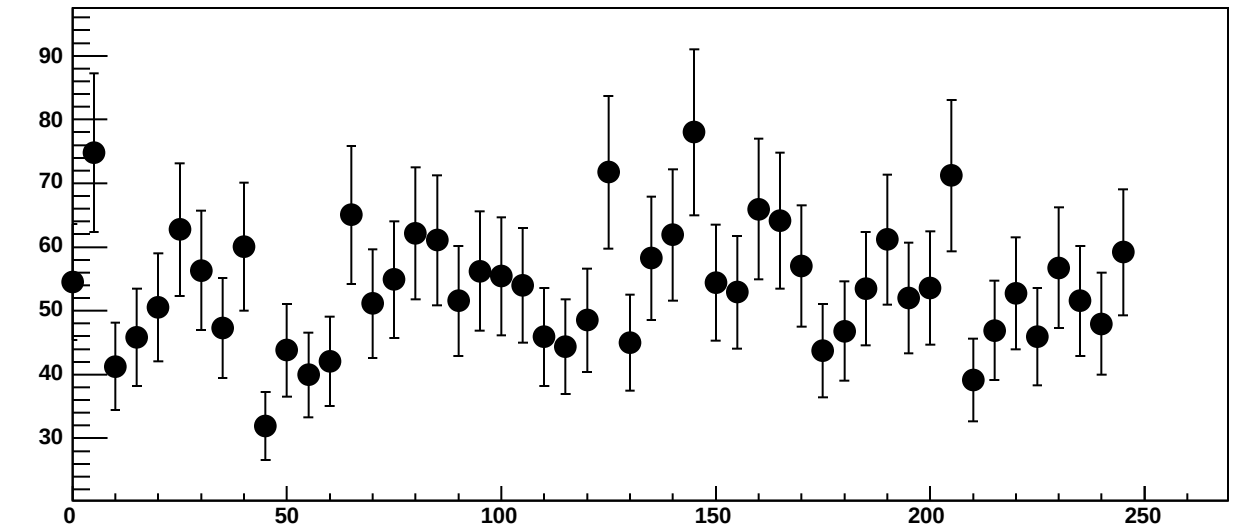
Chip 10, Channel 9, Enable 2, DAC=1600, ADC Residuals vs Hold



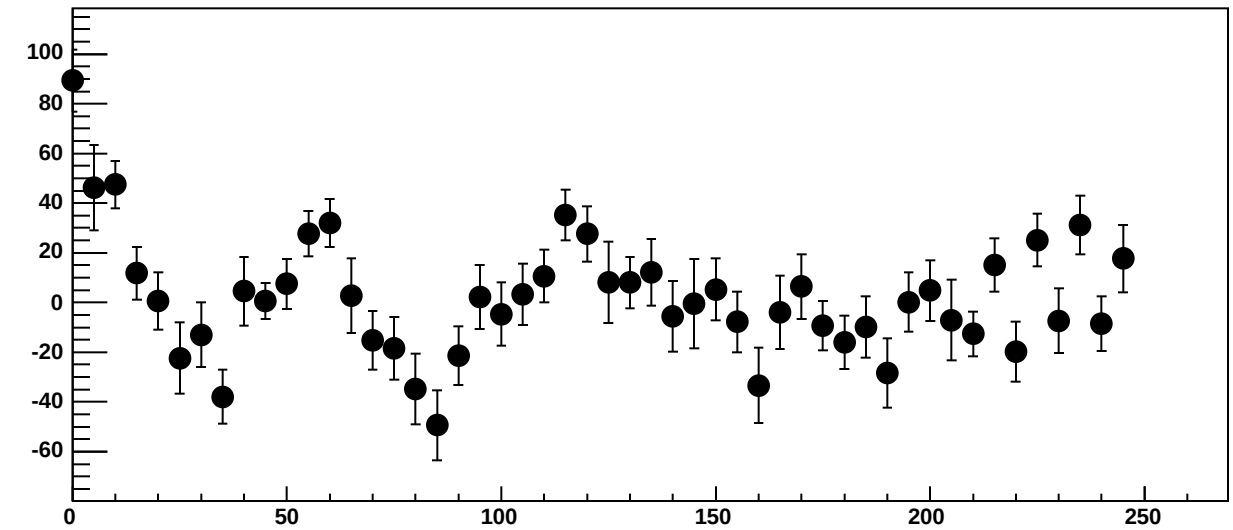
Chip 10, Channel 9, Enable 3, DAC=1600, ADC Mean vs Hold



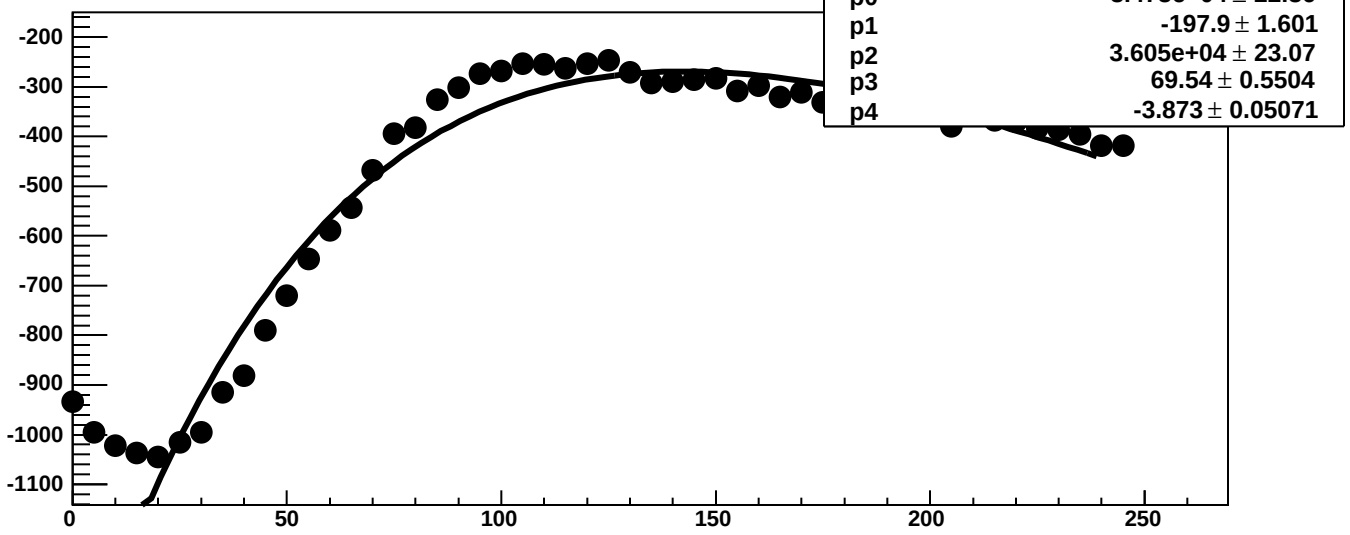
Chip 10, Channel 9, Enable 3, DAC=1600, ADC Noise vs Hold



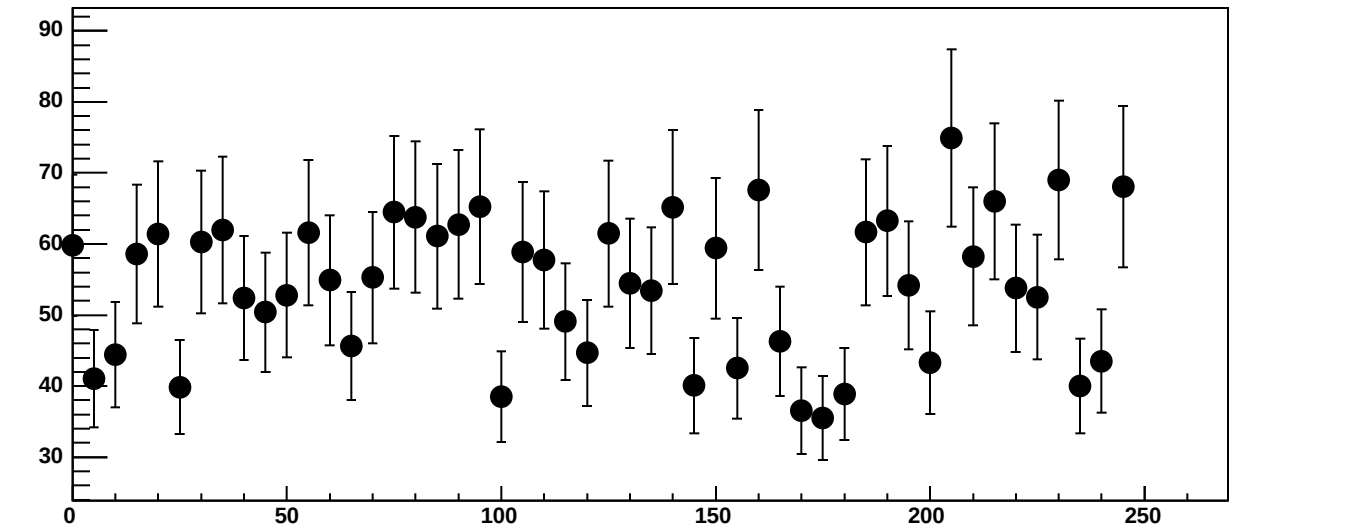
Chip 10, Channel 9, Enable 3, DAC=1600, ADC Residuals vs Hold



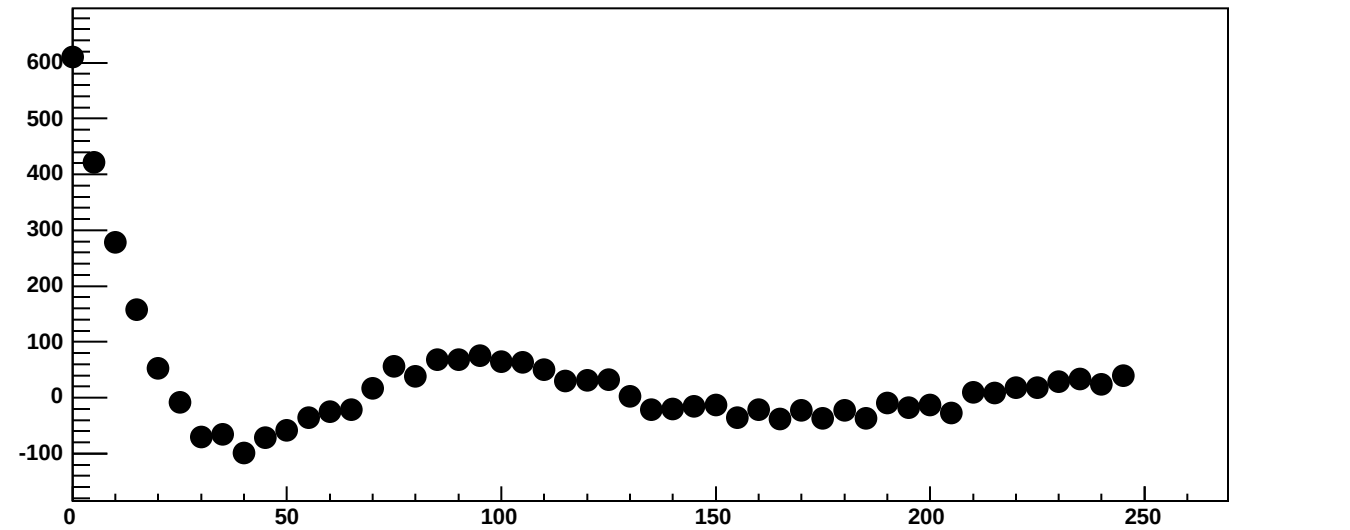
Chip 10, Channel 9, Enable 4, DAC=1600, ADC Mean vs Hold

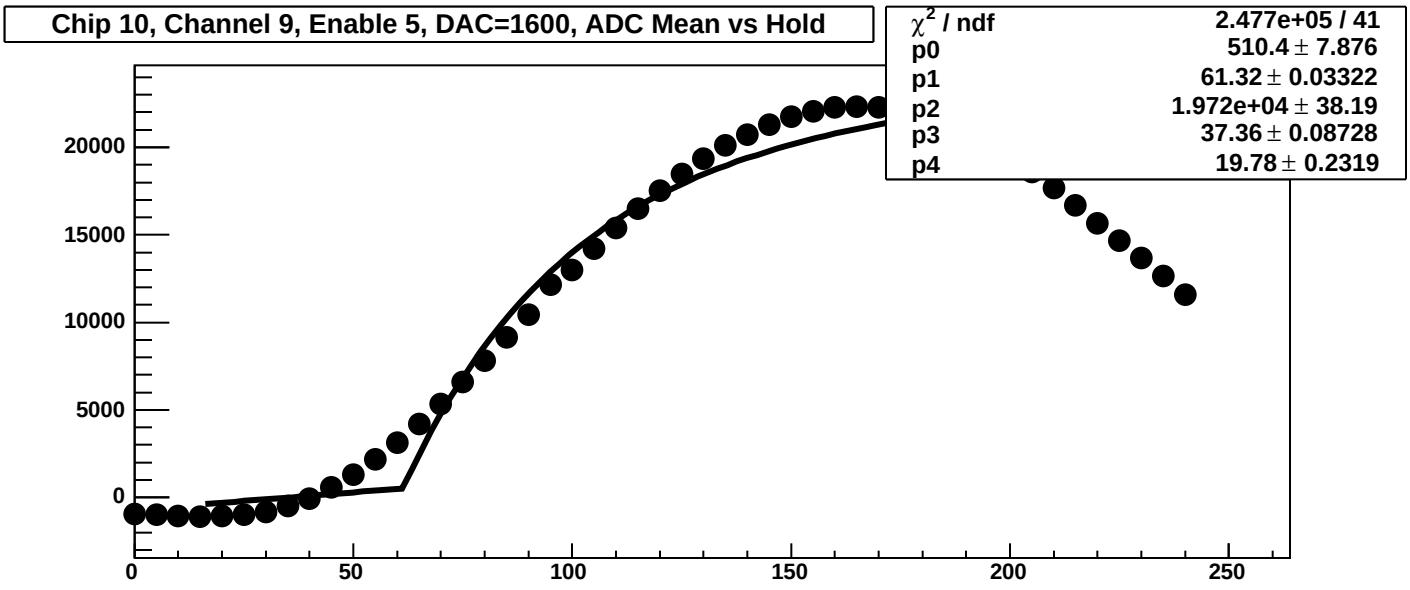


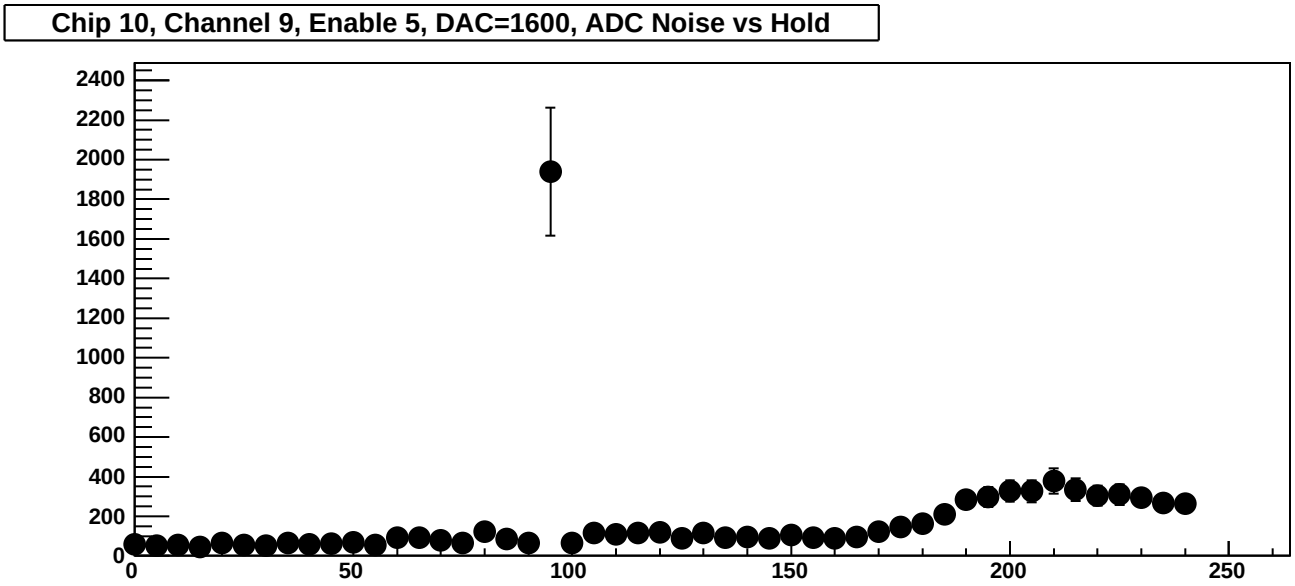
Chip 10, Channel 9, Enable 4, DAC=1600, ADC Noise vs Hold

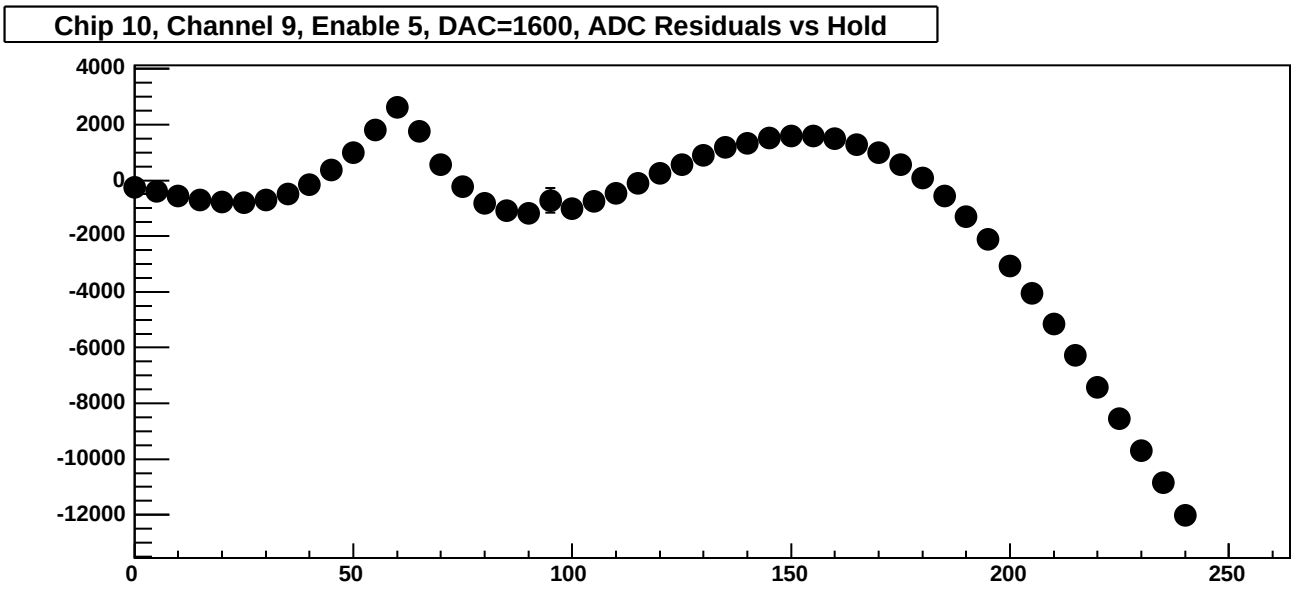


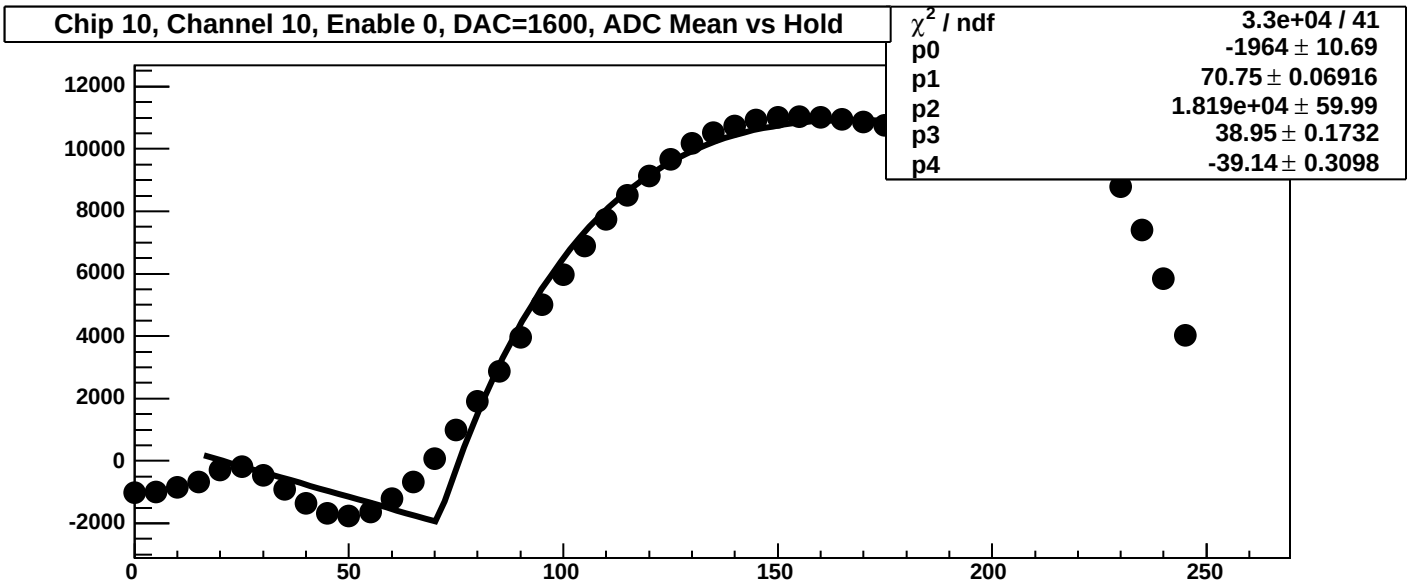
Chip 10, Channel 9, Enable 4, DAC=1600, ADC Residuals vs Hold

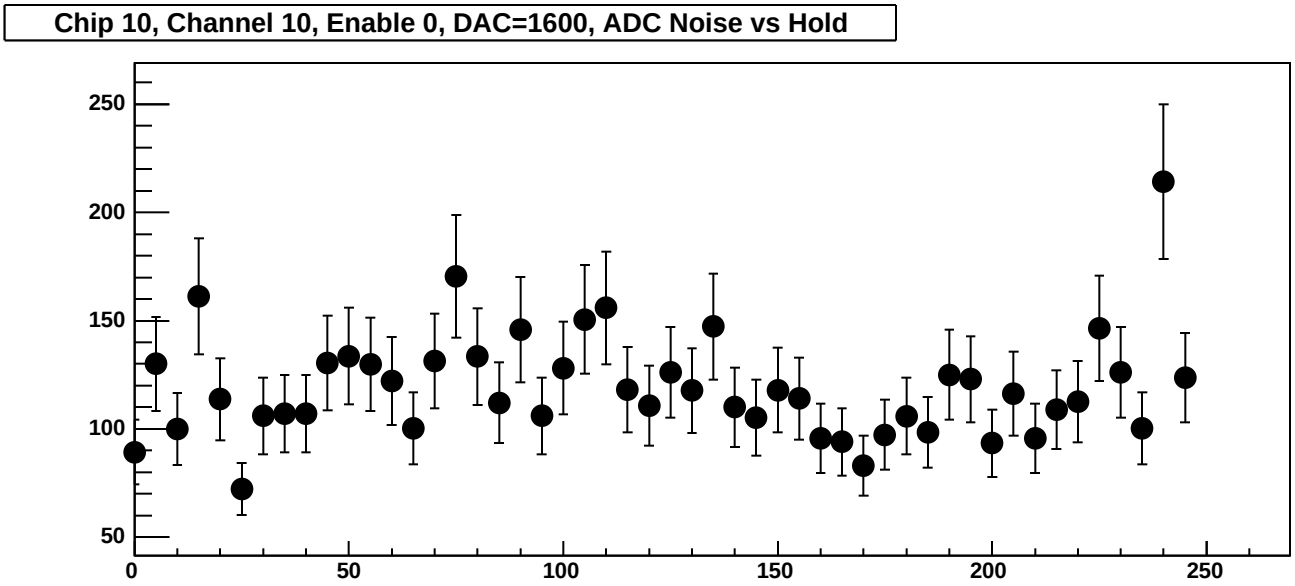


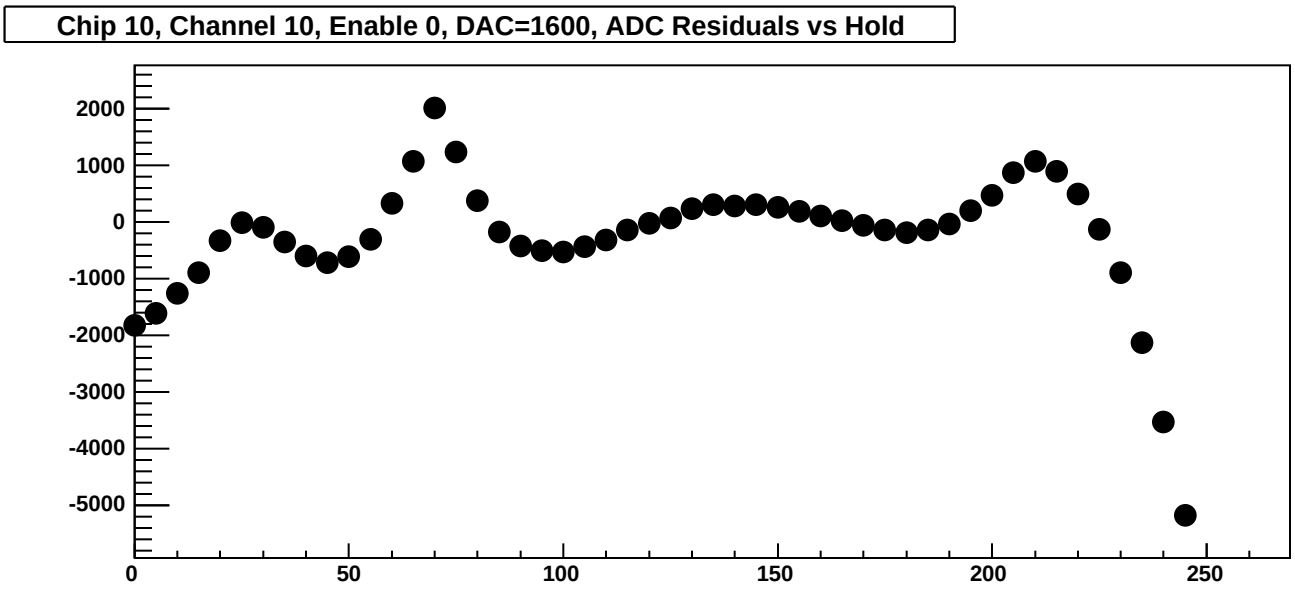

Chip 10, Channel 9, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.477e+05 / 41
p0 510.4 ± 7.876
p1 61.32 ± 0.03322
p2 1.972e+04 ± 38.19
p3 37.36 ± 0.08728
p4 19.78 ± 0.2319
20000
15000
10000
5000
0
0
50
100
150
200
250

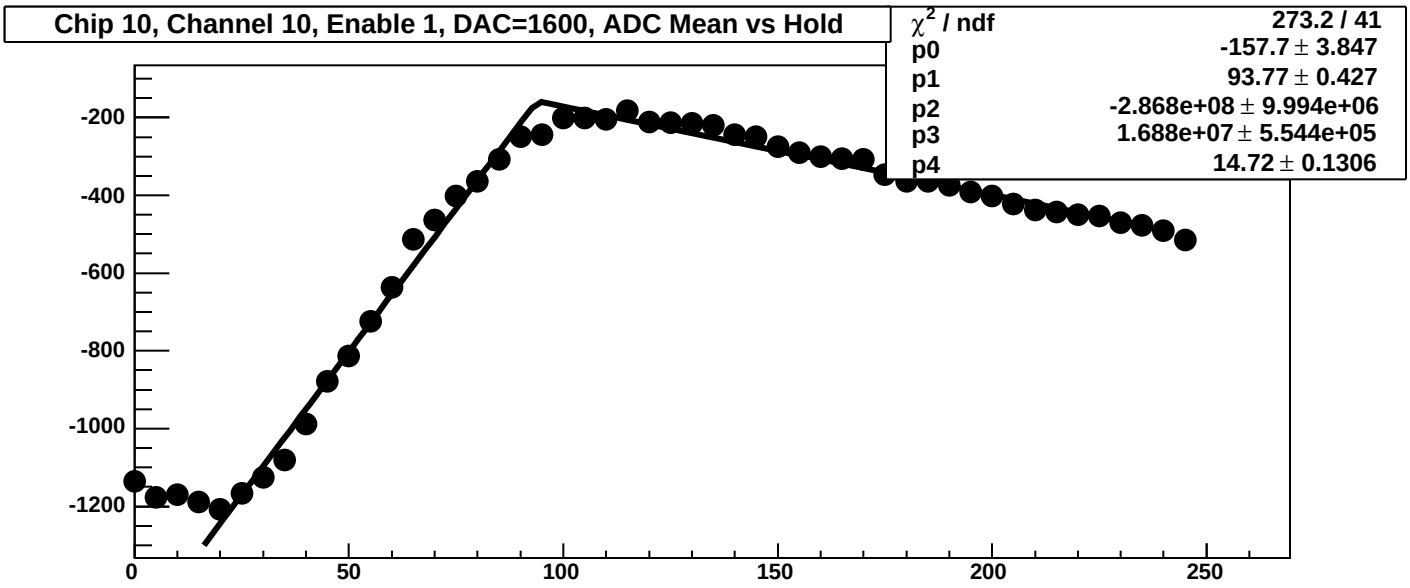

Chip 10, Channel 9, Enable 5, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

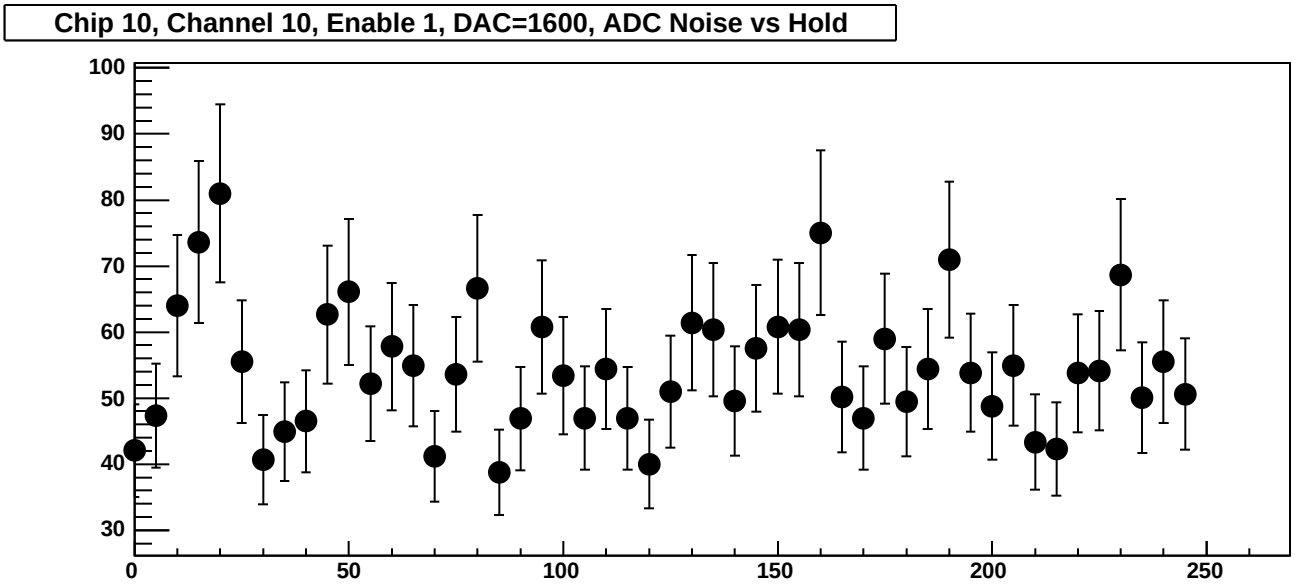

Chip 10, Channel 9, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
0
50
100
150
200
250

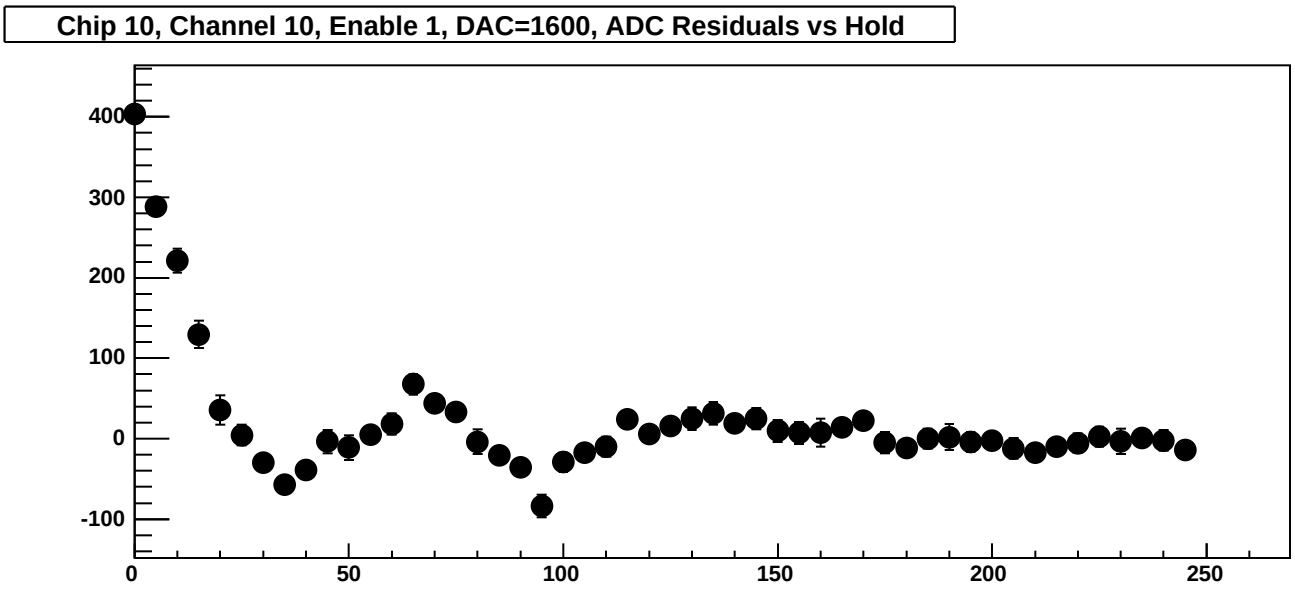

Chip 10, Channel 10, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.3e+04 / 41
p0 -1964 ± 10.69
p1 70.75 ± 0.06916
p2 1.819e+04 ± 59.99
p3 38.95 ± 0.1732
p4 -39.14 ± 0.3098
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

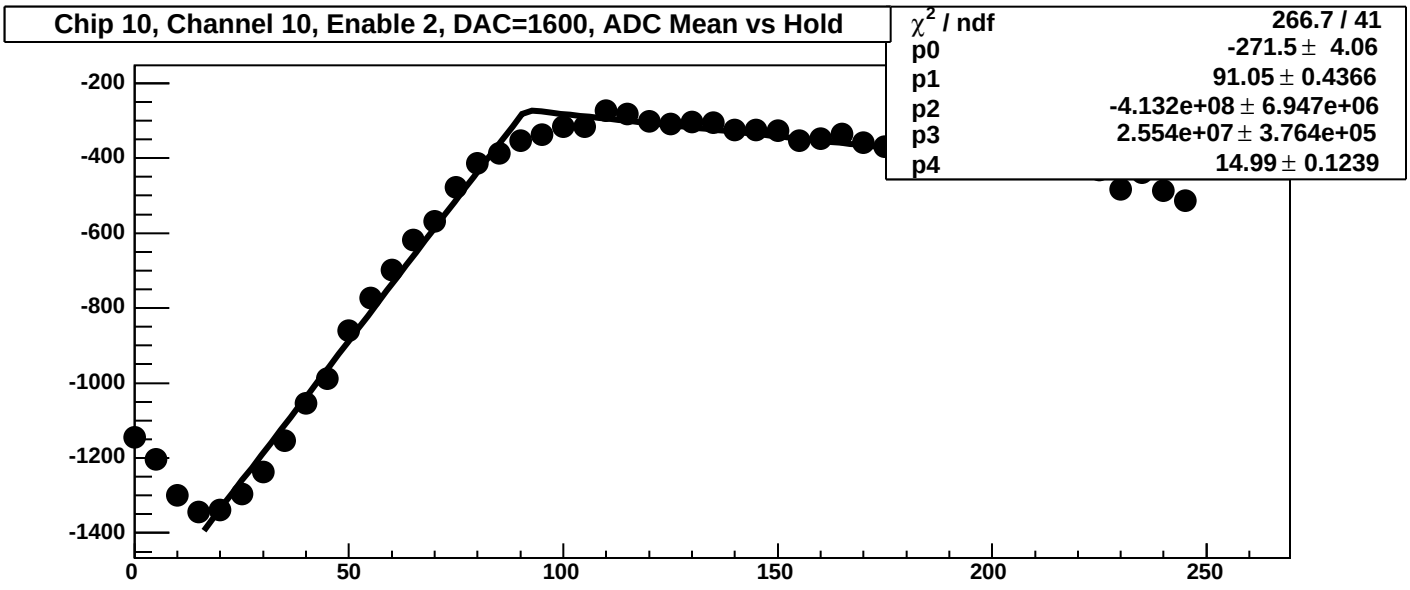

Chip 10, Channel 10, Enable 0, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
50
100
150
200
250

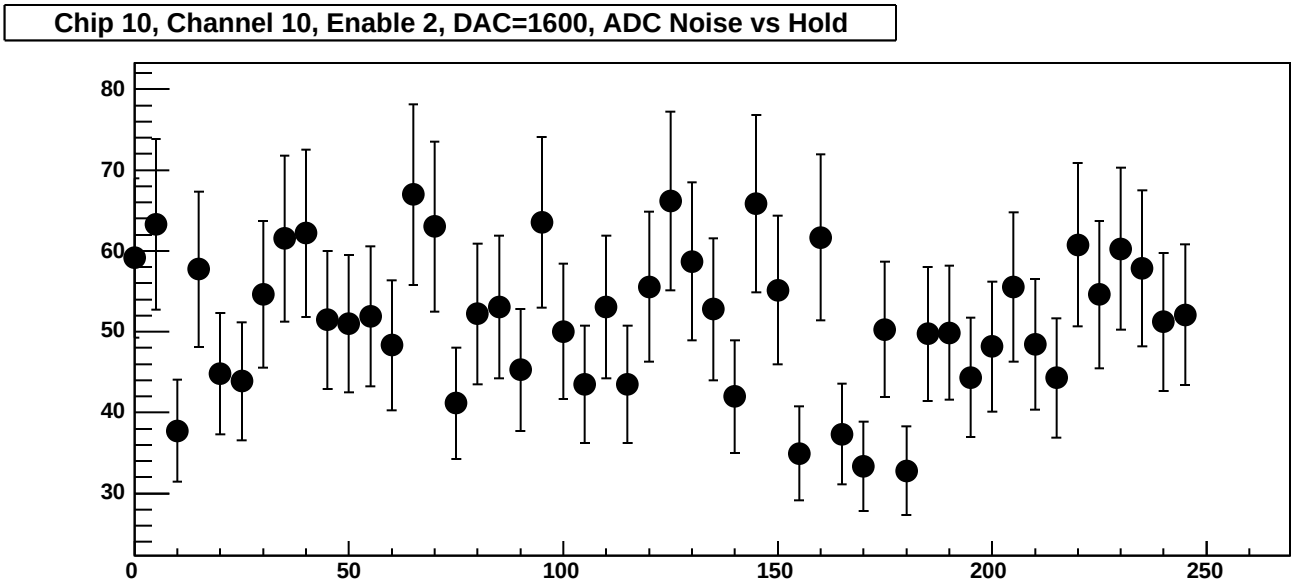

Chip 10, Channel 10, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

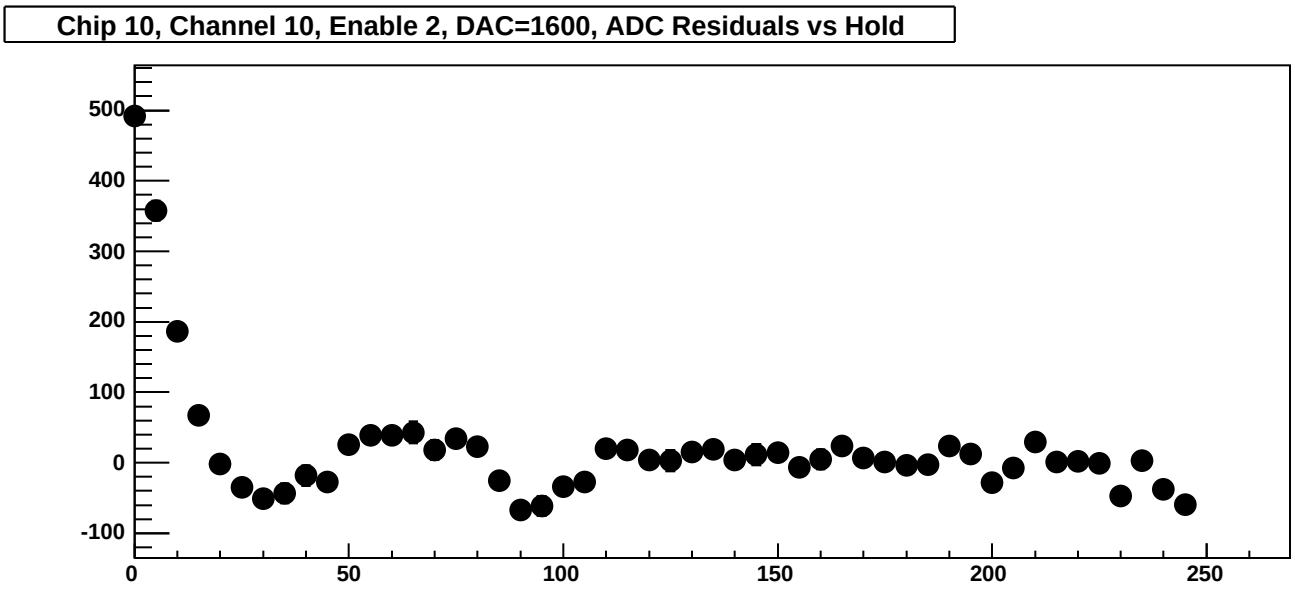

Chip 10, Channel 10, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 273.2 / 41
p0 -157.7 ± 3.847
p1 93.77 ± 0.427
p2 -2.868e+08 ± 9.994e+06
p3 1.688e+07 ± 5.544e+05
p4 14.72 ± 0.1306
-200
-400
-600
-800
-1000
-1200
0
50
100
150
200
250

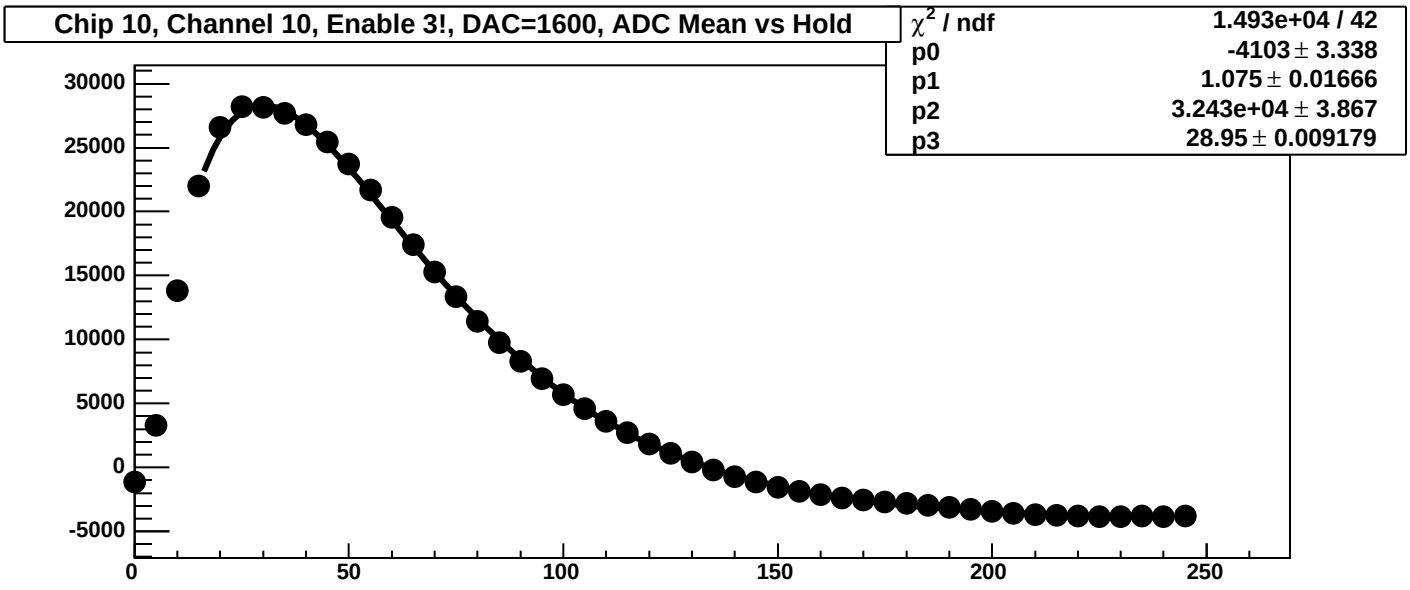

Chip 10, Channel 10, Enable 1, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250

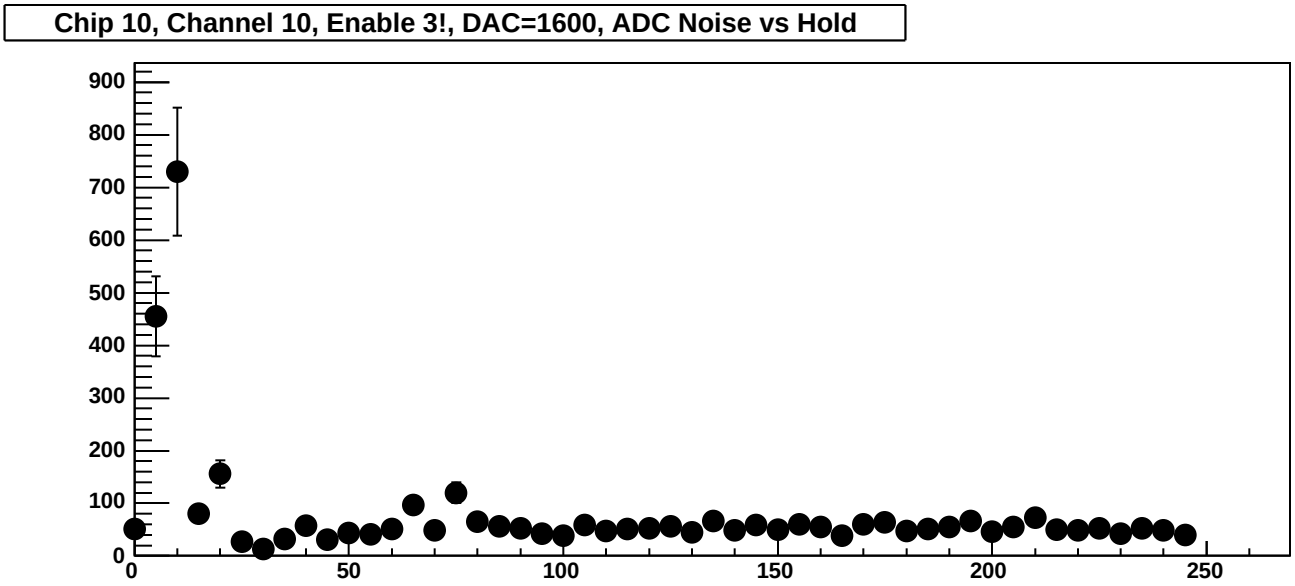

Chip 10, Channel 10, Enable 1, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

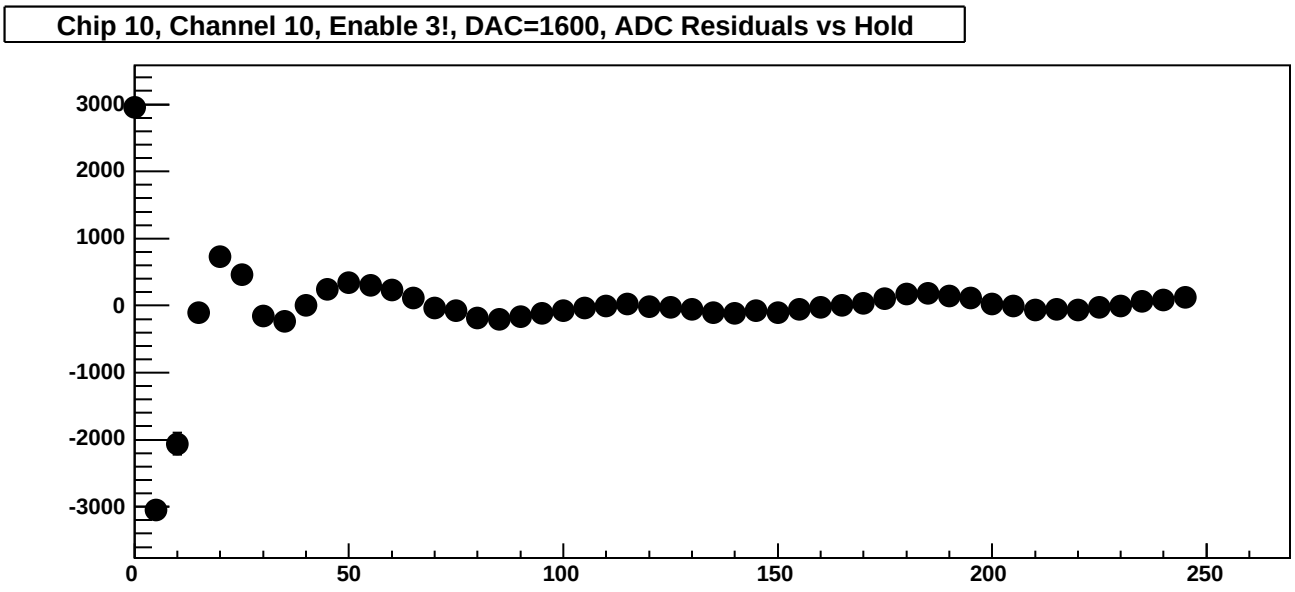

Chip 10, Channel 10, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 266.7 / 41
p0 -271.5 ± 4.06
p1 91.05 ± 0.4366
p2 -4.132e+08 ± 6.947e+06
p3 2.554e+07 ± 3.764e+05
p4 14.99 ± 0.1239
-200
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250


Chip 10, Channel 10, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

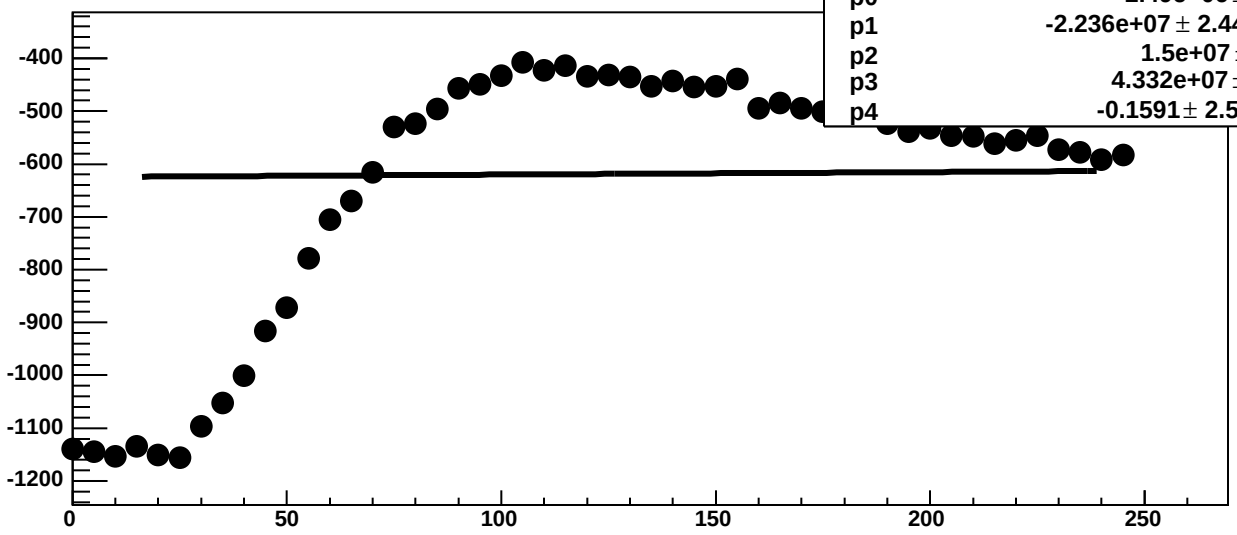

Chip 10, Channel 10, Enable 2, DAC=1600, ADC Residuals vs Hold
500
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 10, Channel 10, Enable 3!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.493e+04 / 42
p0 -4103 ± 3.338
p1 1.075 ± 0.01666
p2 3.243e+04 ± 3.867
p3 28.95 ± 0.009179
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

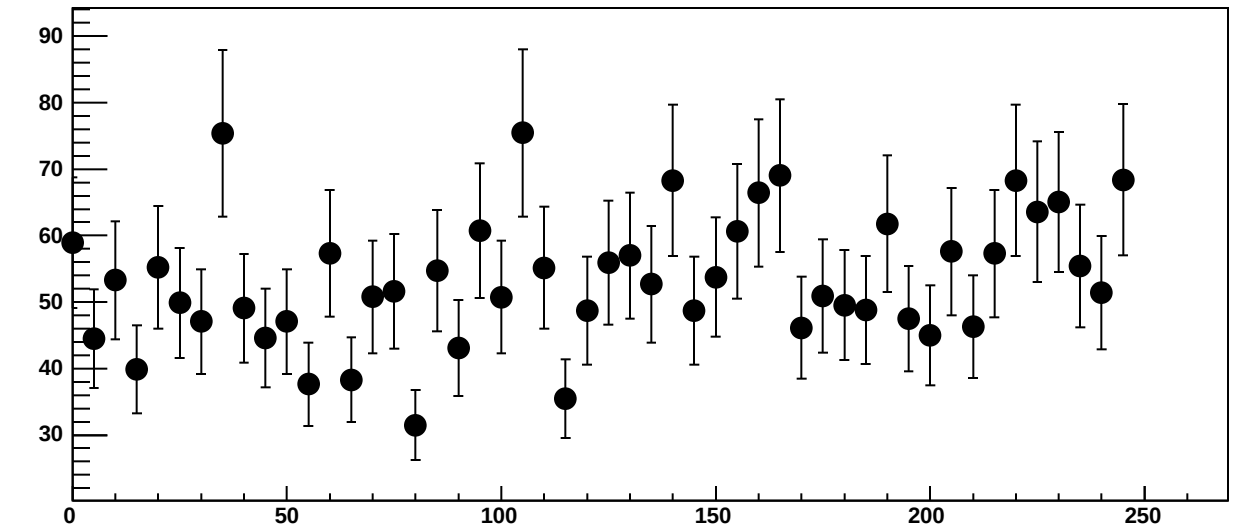

Chip 10, Channel 10, Enable 3!, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 10, Channel 10, Enable 3!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

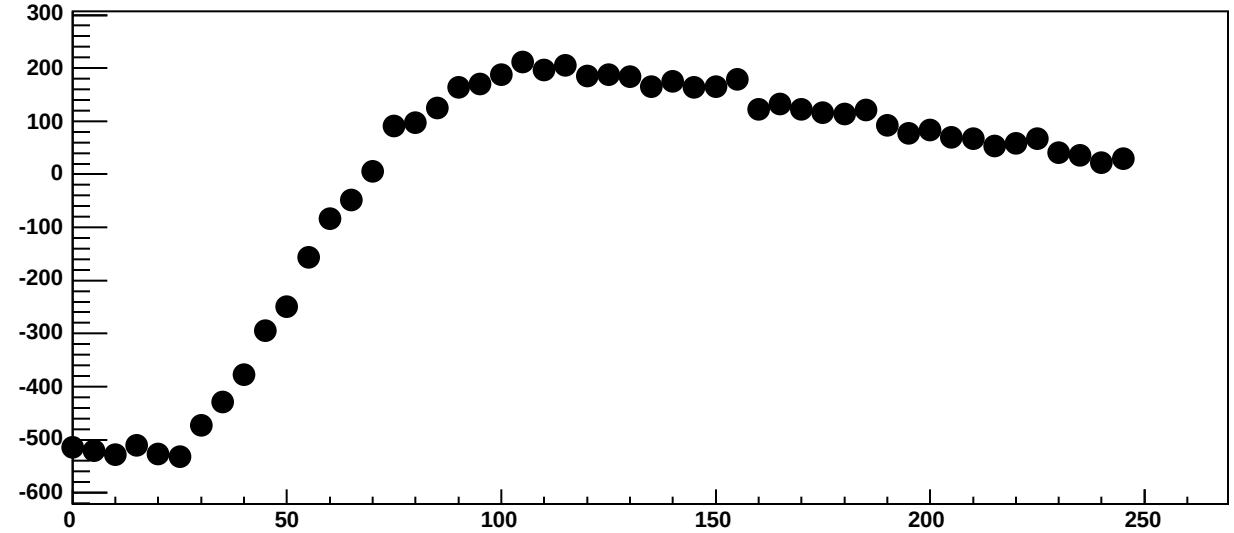
Chip 10, Channel 10, Enable 4, DAC=1600, ADC Mean vs Hold

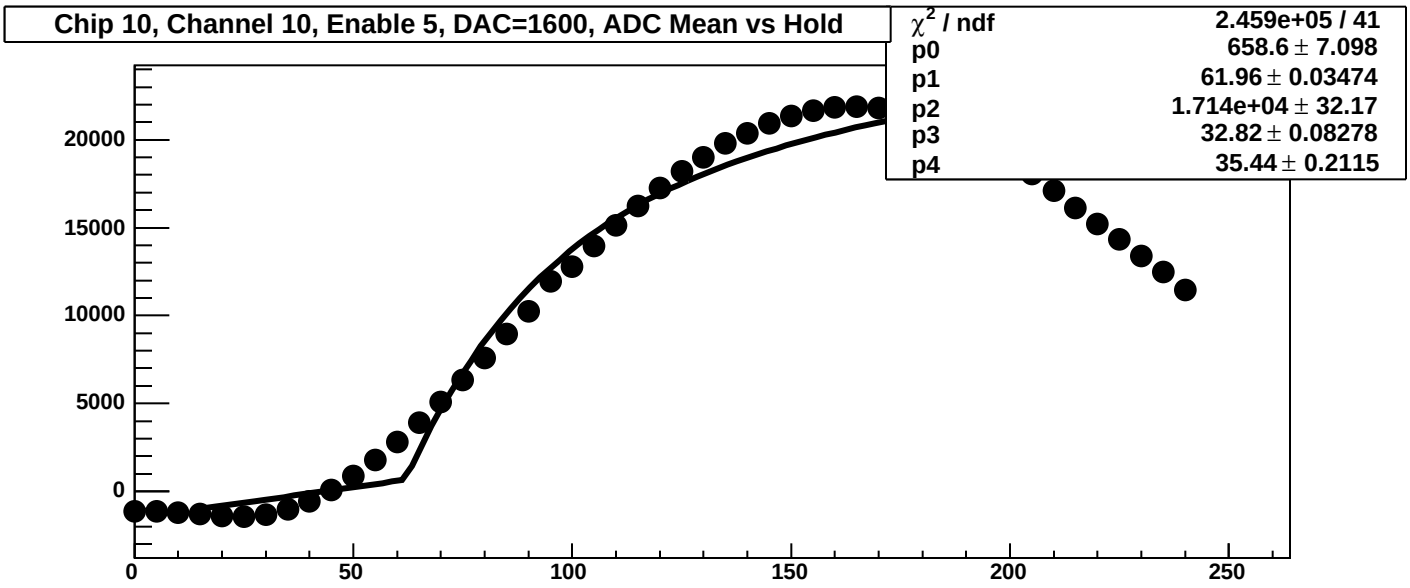


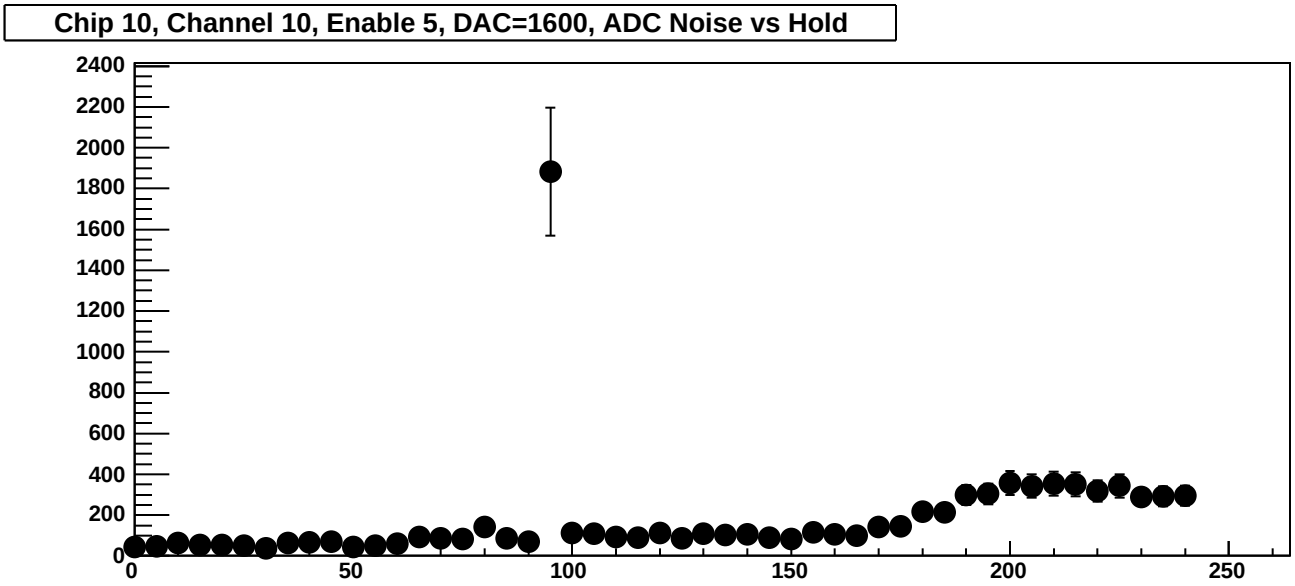
Chip 10, Channel 10, Enable 4, DAC=1600, ADC Noise vs Hold

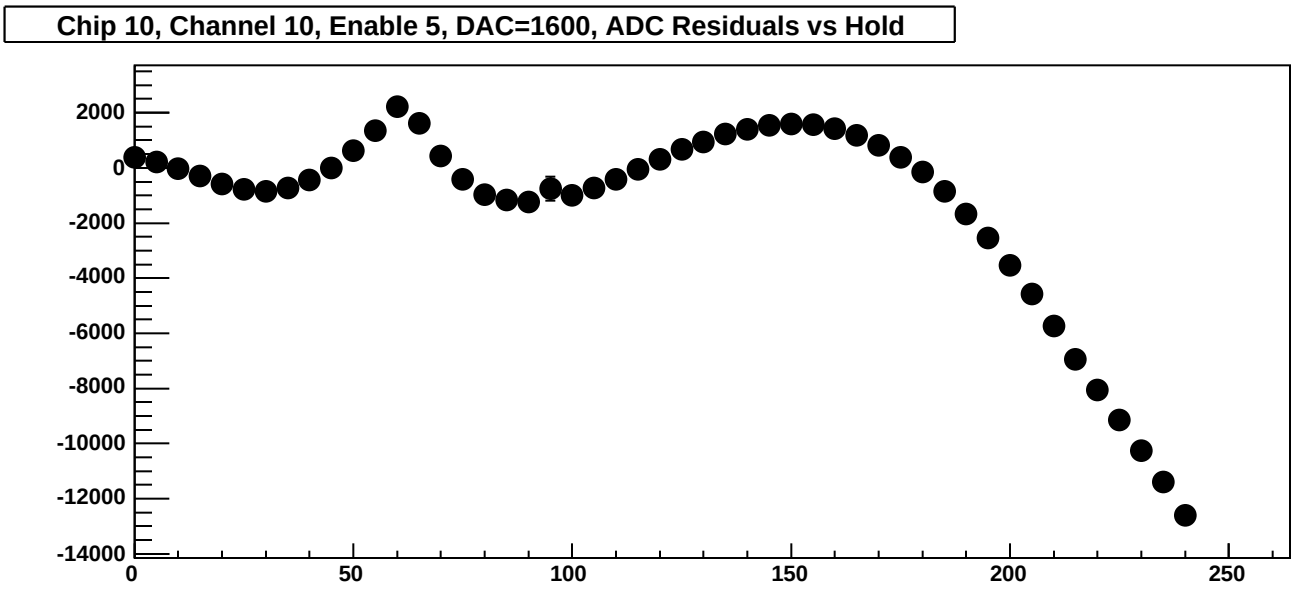


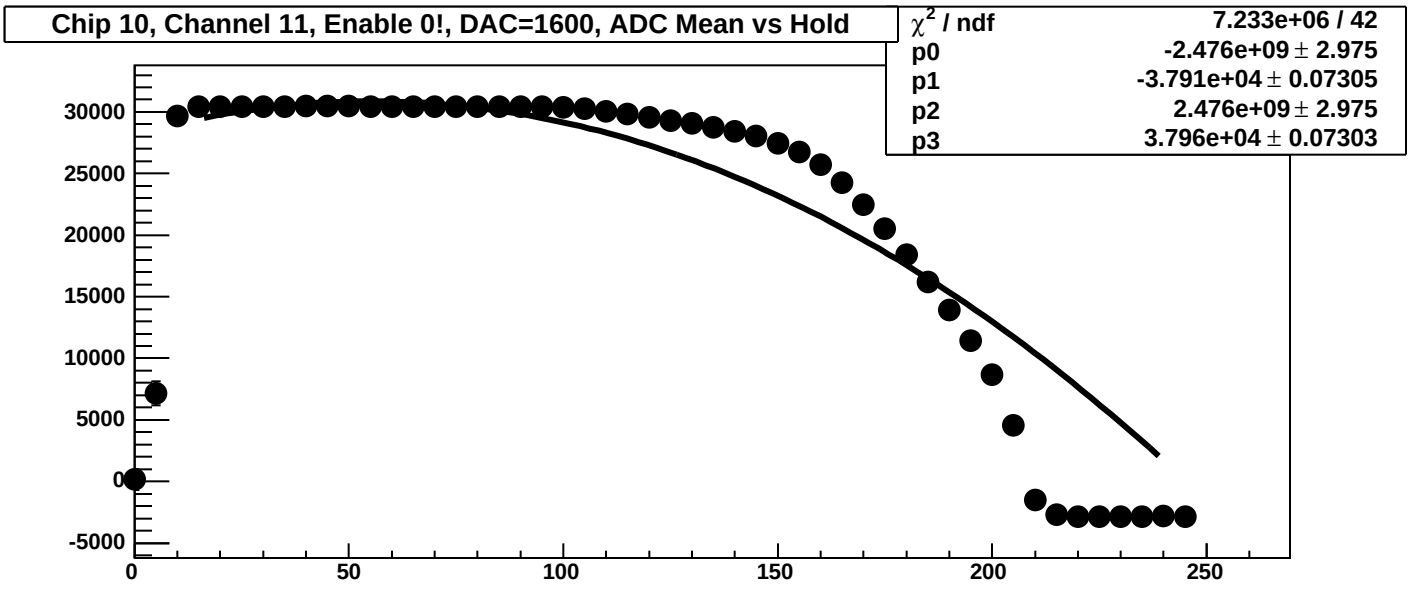
Chip 10, Channel 10, Enable 4, DAC=1600, ADC Residuals vs Hold

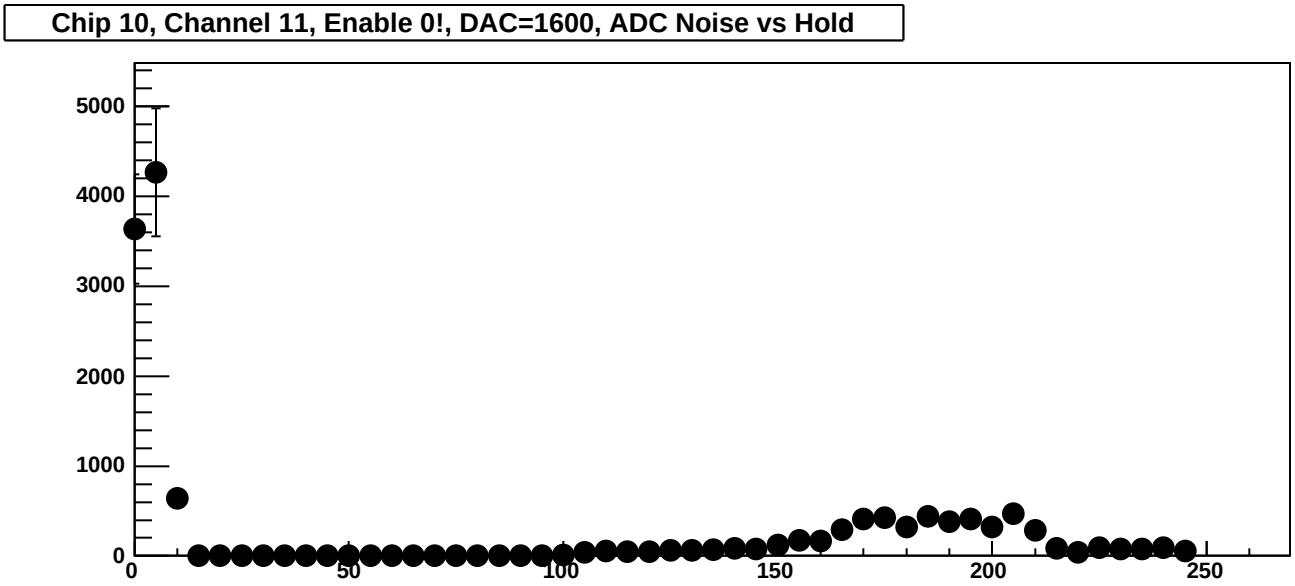


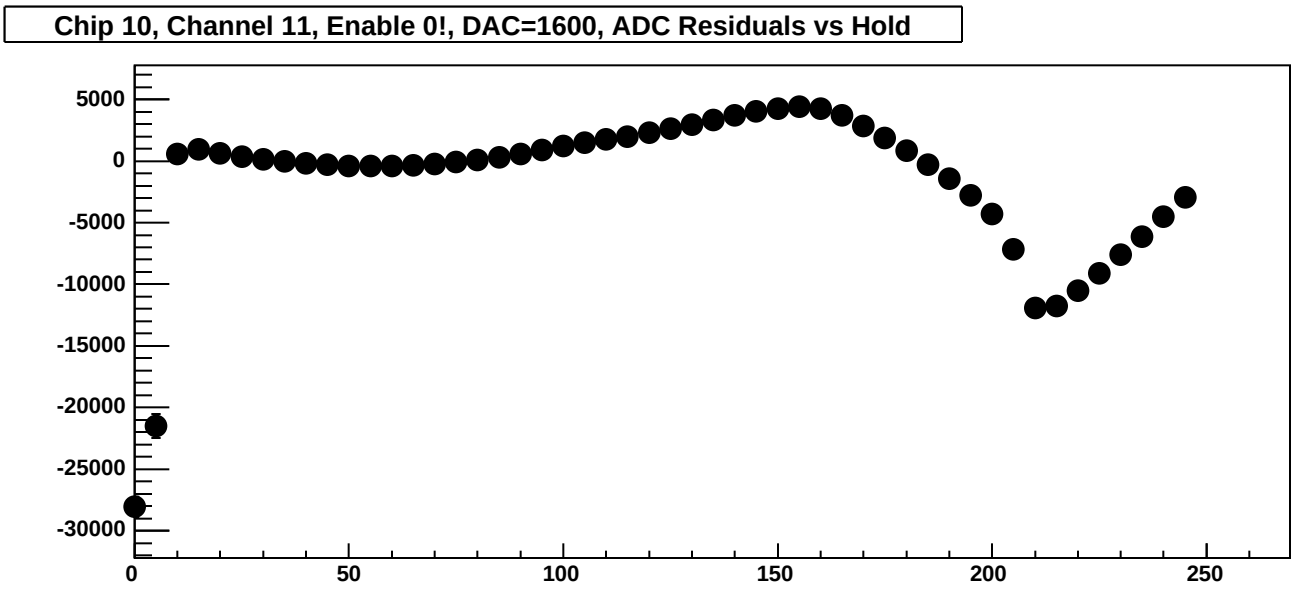

Chip 10, Channel 10, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.459e+05 / 41
p0 658.6 ± 7.098
p1 61.96 ± 0.03474
p2 1.714e+04 ± 32.17
p3 32.82 ± 0.08278
p4 35.44 ± 0.2115
20000
15000
10000
5000
0
0
50
100
150
200
250

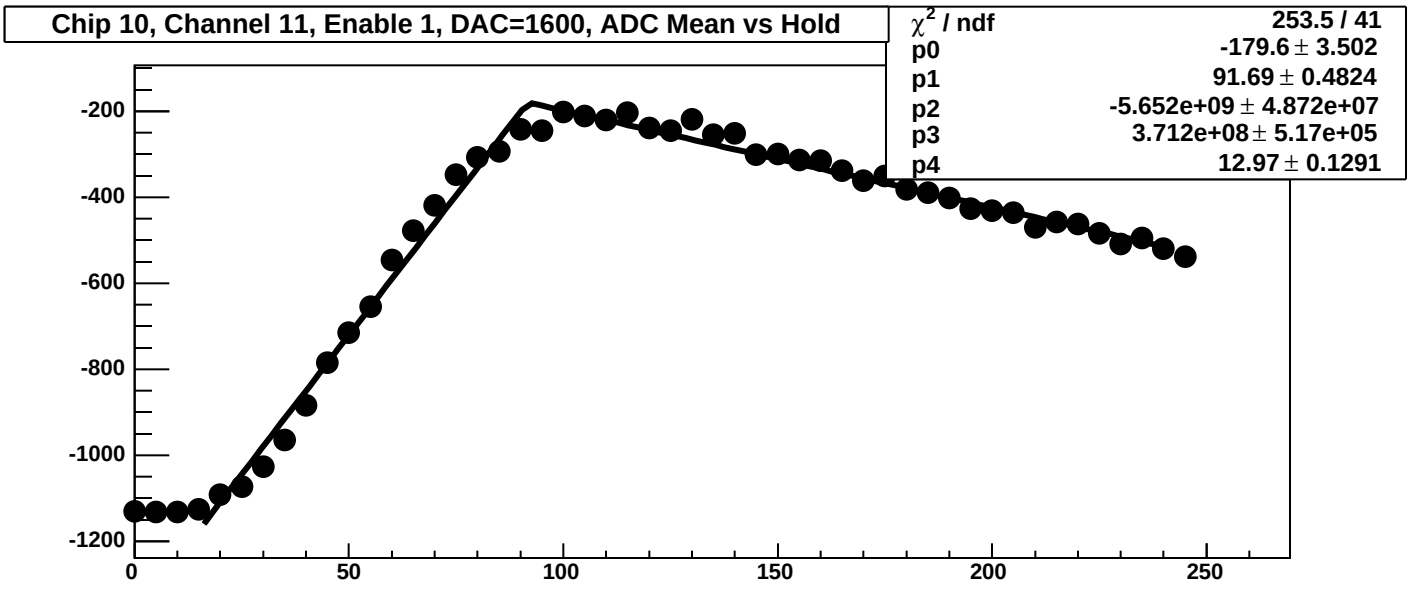

Chip 10, Channel 10, Enable 5, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

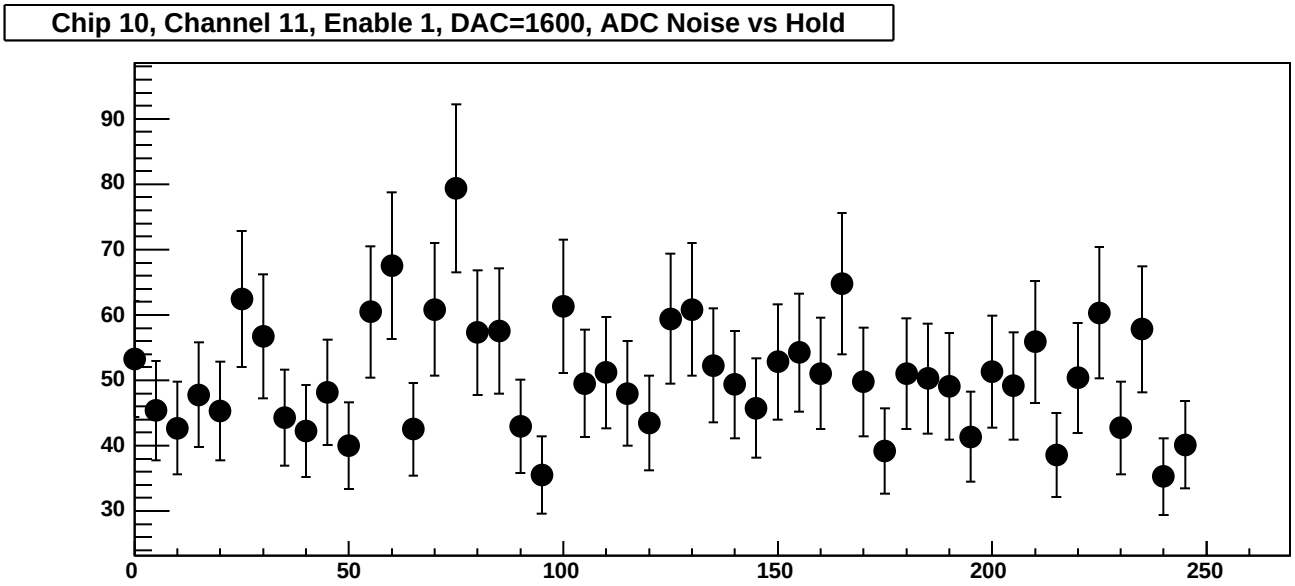

Chip 10, Channel 10, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
50
100
150
200
250

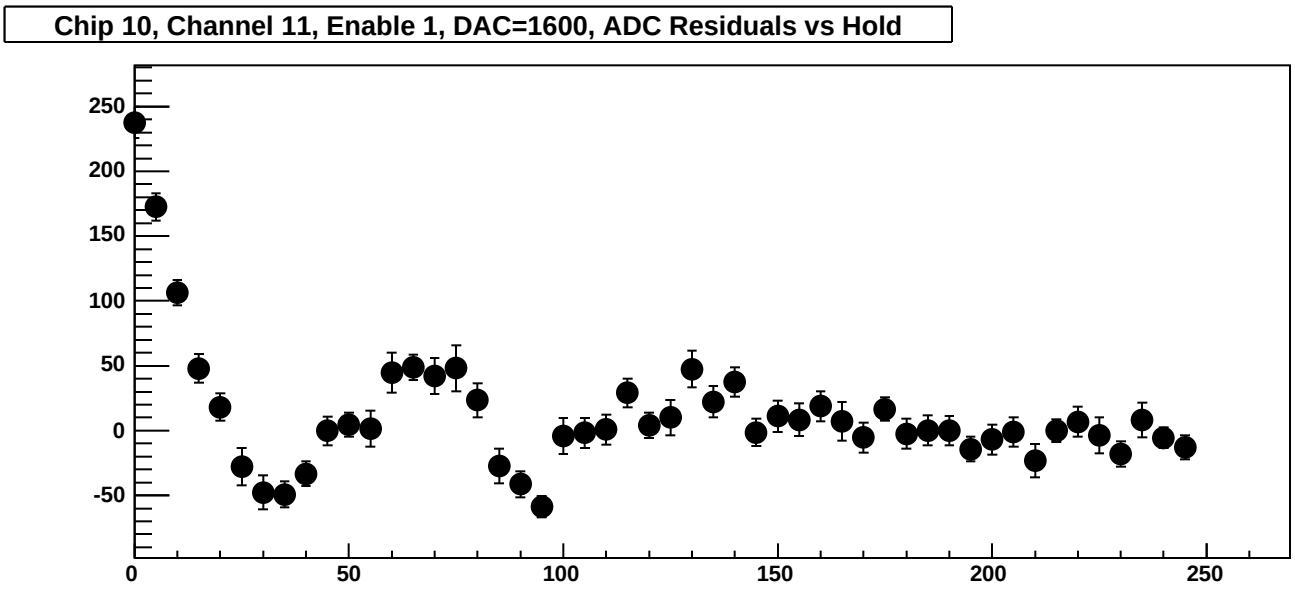

Chip 10, Channel 11, Enable 0!, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.233e+06 / 42
p0 -2.476e+09 ± 2.975
p1 -3.791e+04 ± 0.07305
p2 2.476e+09 ± 2.975
p3 3.796e+04 ± 0.07303
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 10, Channel 11, Enable 0!, DAC=1600, ADC Noise vs Hold
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

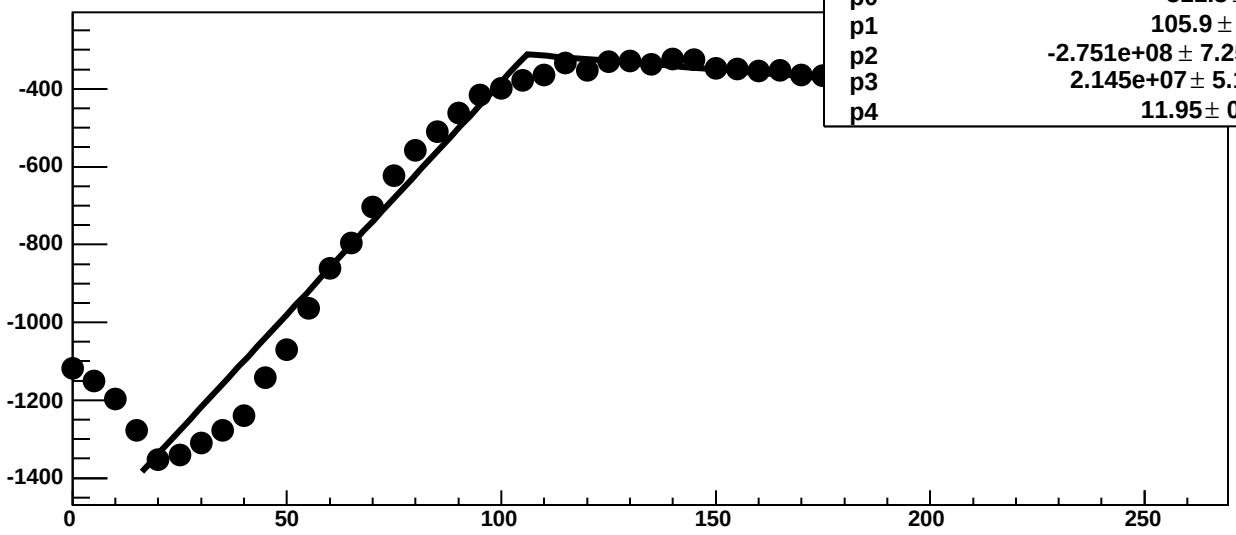

Chip 10, Channel 11, Enable 0!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
0
50
100
150
200
250


Chip 10, Channel 11, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 253.5 / 41
p0 -179.6 ± 3.502
p1 91.69 ± 0.4824
p2 -5.652e+09 ± 4.872e+07
p3 3.712e+08 ± 5.17e+05
p4 12.97 ± 0.1291
-200
-400
-600
-800
-1000
-1200
0
50
100
150
200
250

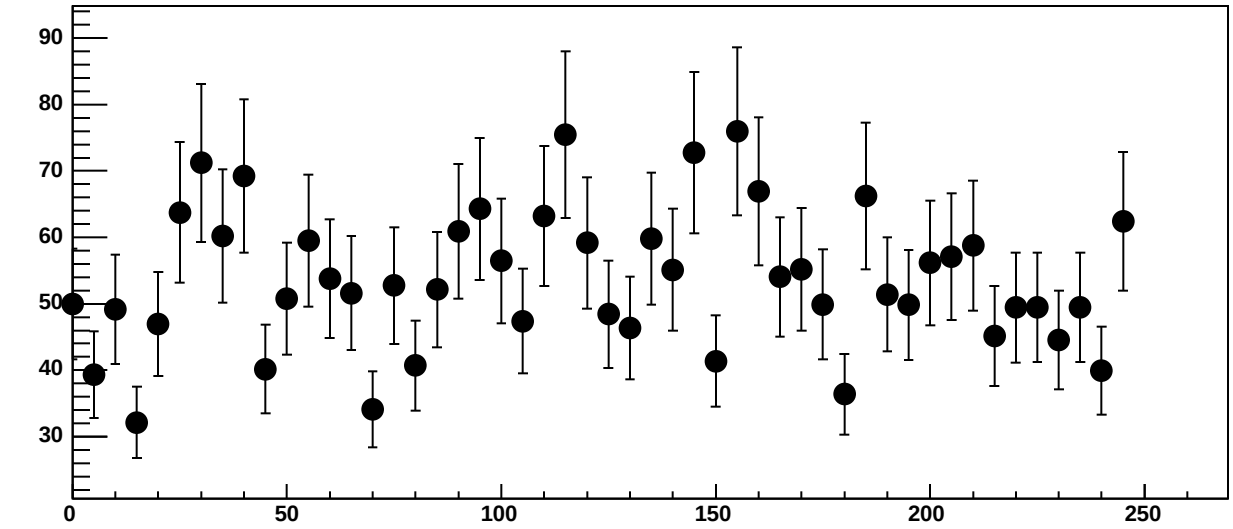

Chip 10, Channel 11, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 10, Channel 11, Enable 1, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
0
50
100
150
200
250

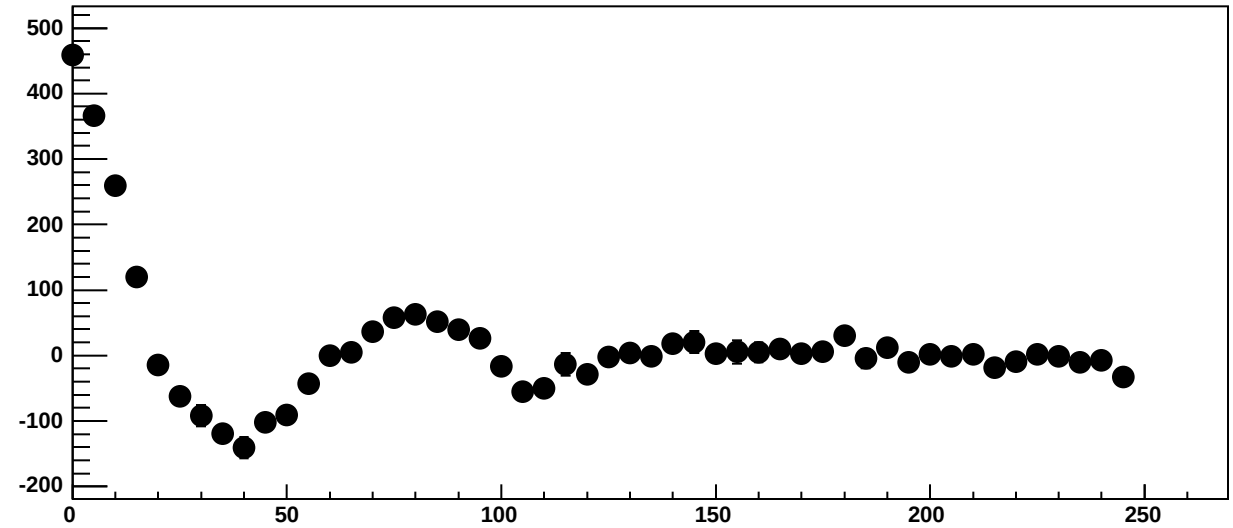
Chip 10, Channel 11, Enable 2, DAC=1600, ADC Mean vs Hold



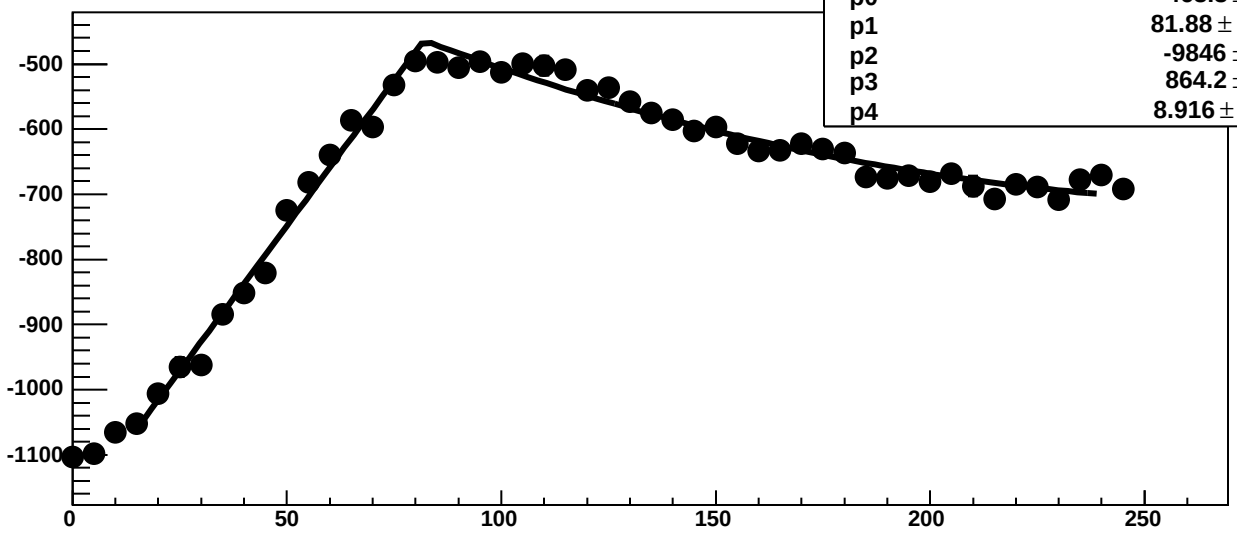
Chip 10, Channel 11, Enable 2, DAC=1600, ADC Noise vs Hold



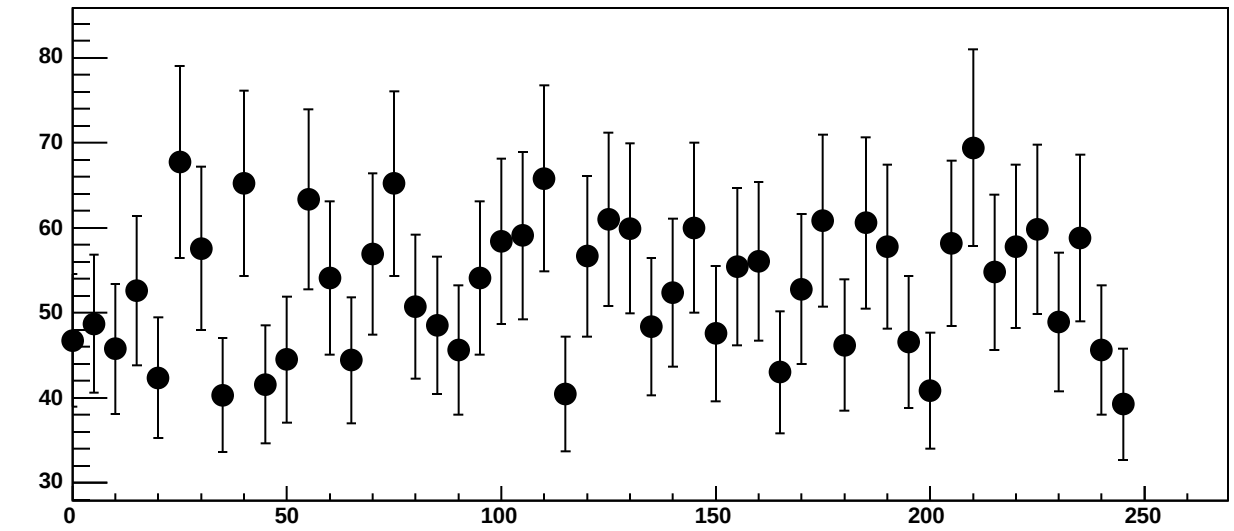
Chip 10, Channel 11, Enable 2, DAC=1600, ADC Residuals vs Hold



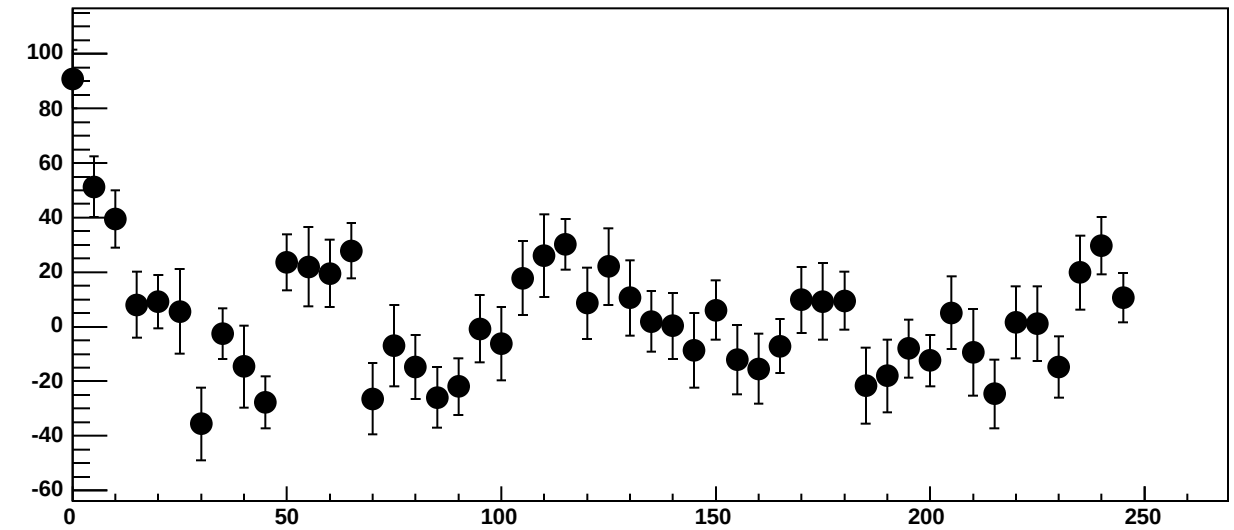
Chip 10, Channel 11, Enable 3, DAC=1600, ADC Mean vs Hold

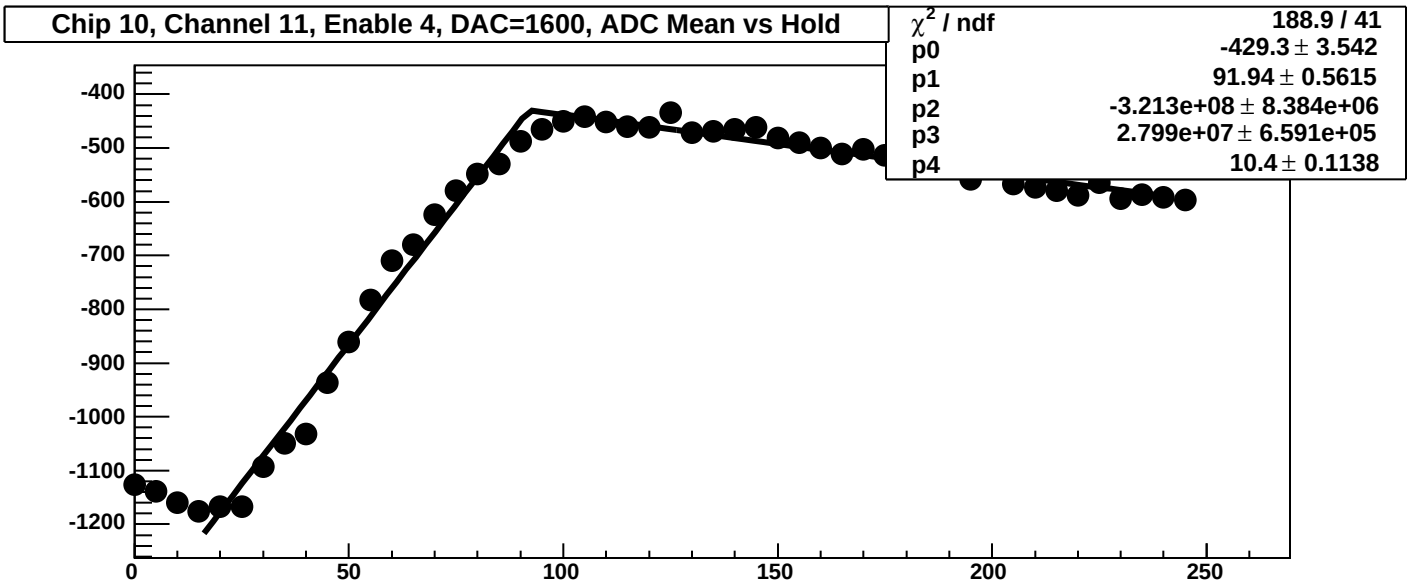


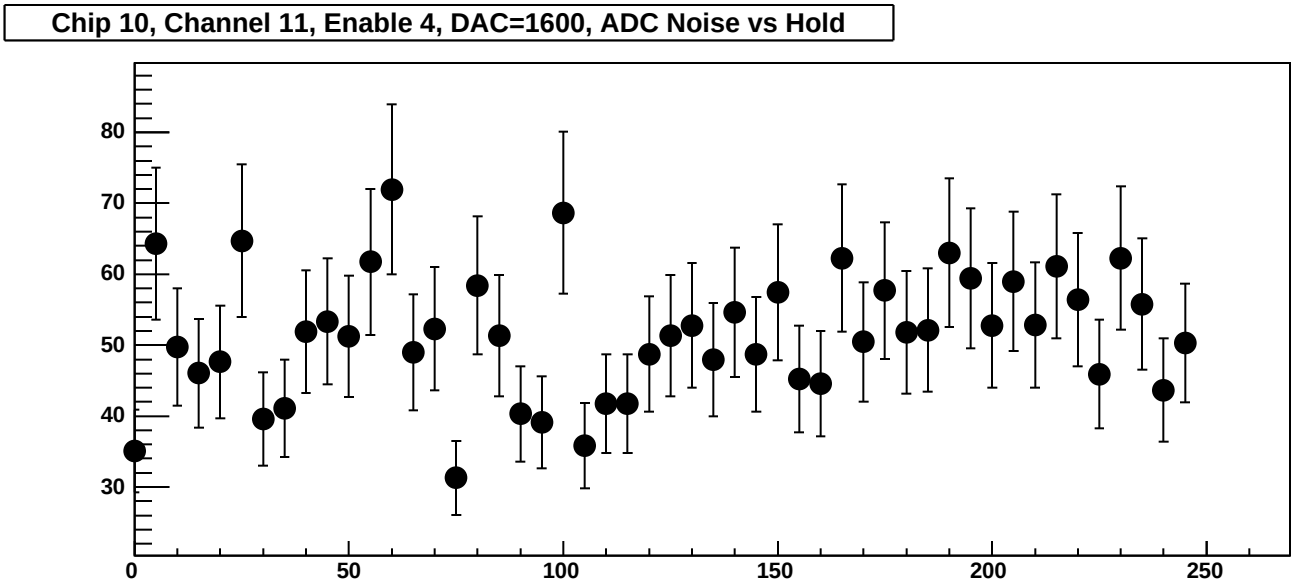
Chip 10, Channel 11, Enable 3, DAC=1600, ADC Noise vs Hold

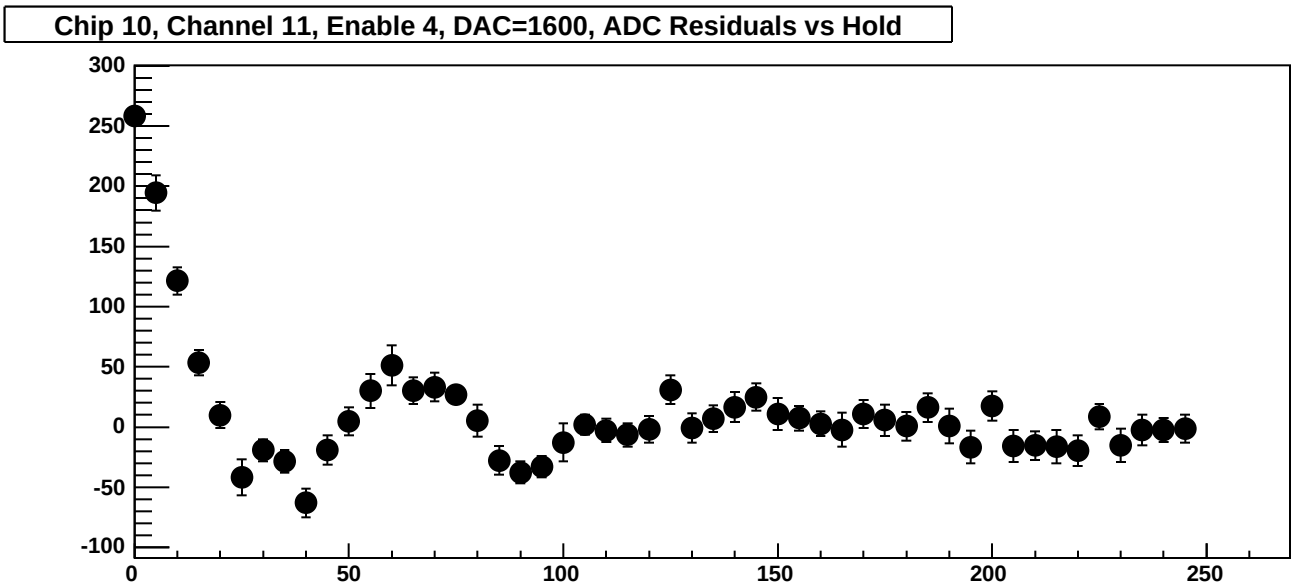


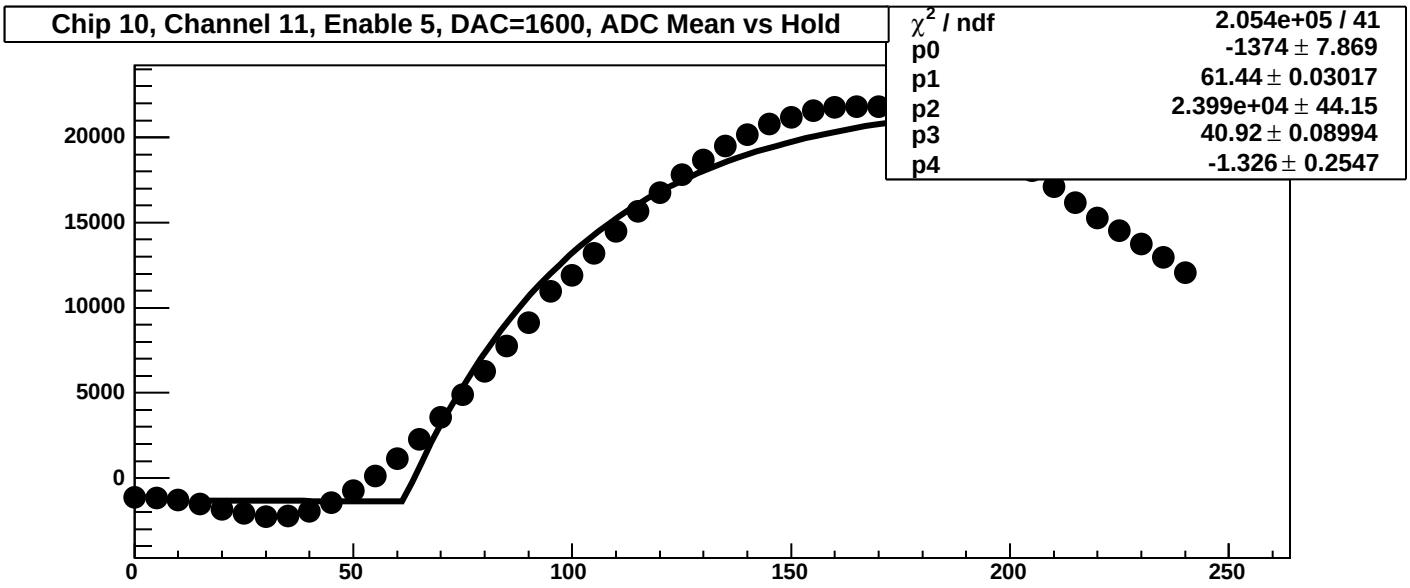
Chip 10, Channel 11, Enable 3, DAC=1600, ADC Residuals vs Hold

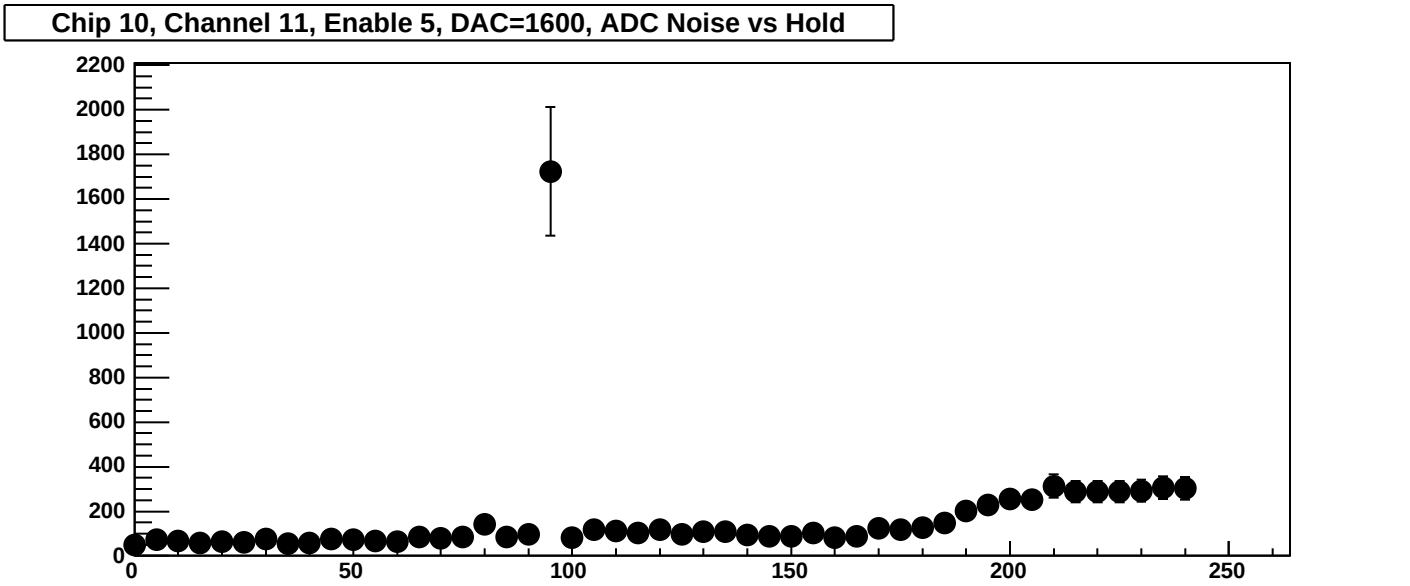


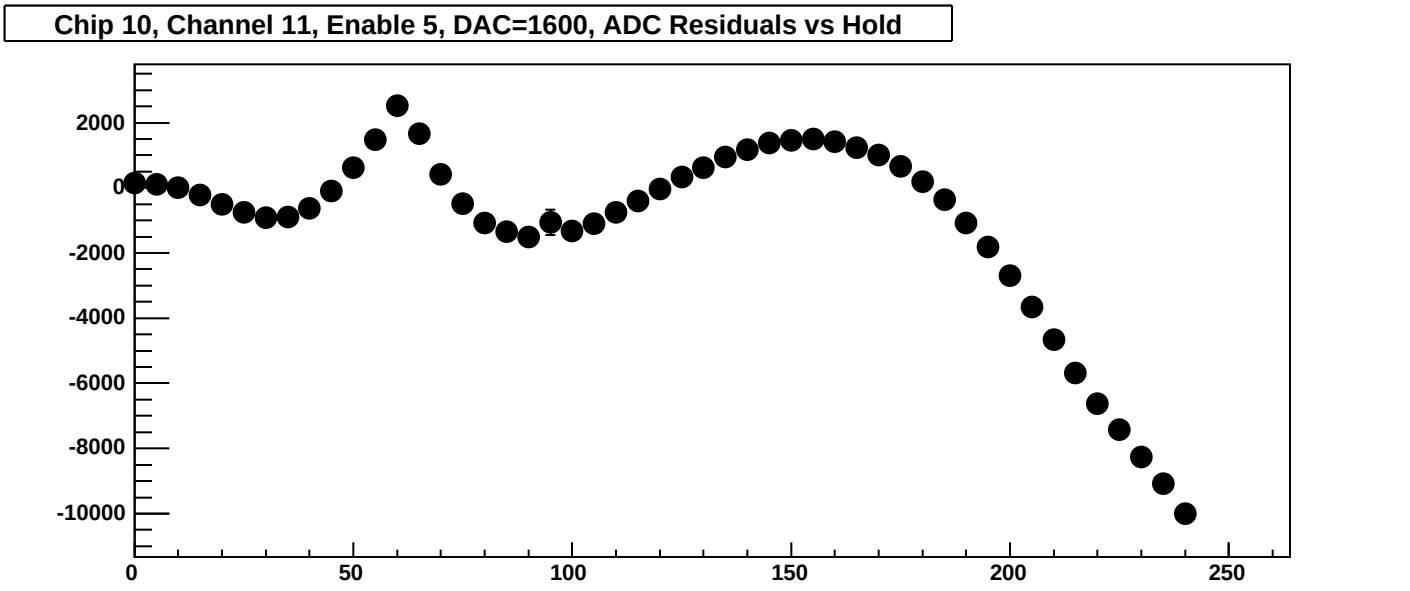

Chip 10, Channel 11, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 188.9 / 41
p0 -429.3 ± 3.542
p1 91.94 ± 0.5615
p2 -3.213e+08 ± 8.384e+06
p3 2.799e+07 ± 6.591e+05
p4 10.4 ± 0.1138
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
50
100
150
200
250

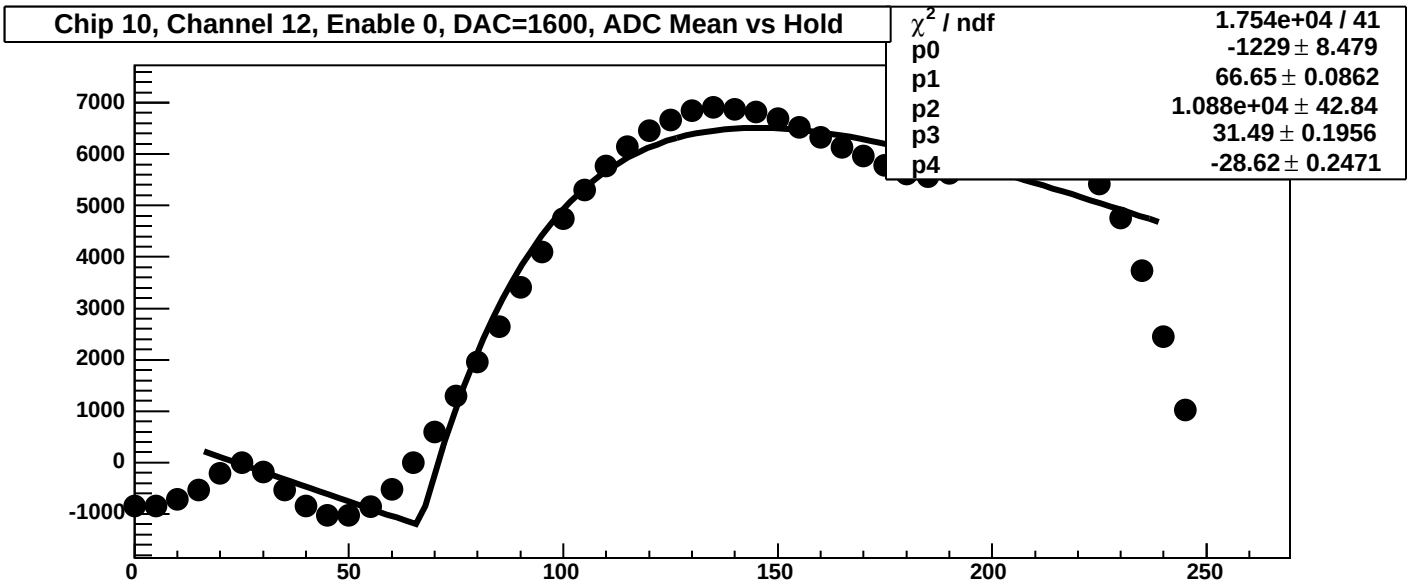

Chip 10, Channel 11, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

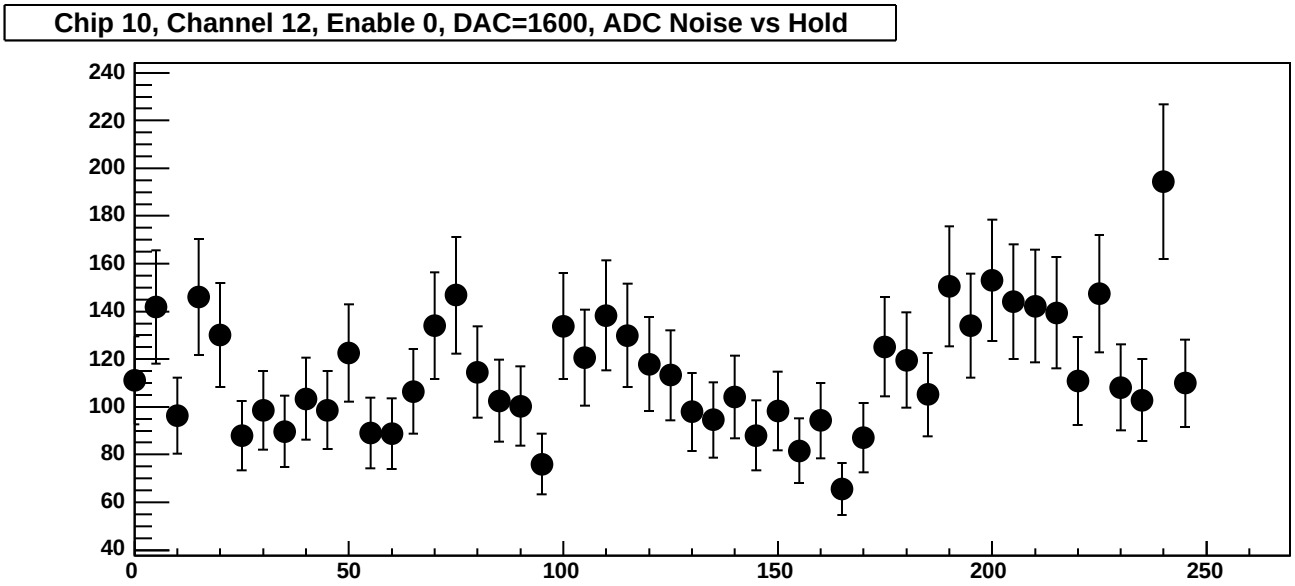

Chip 10, Channel 11, Enable 4, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

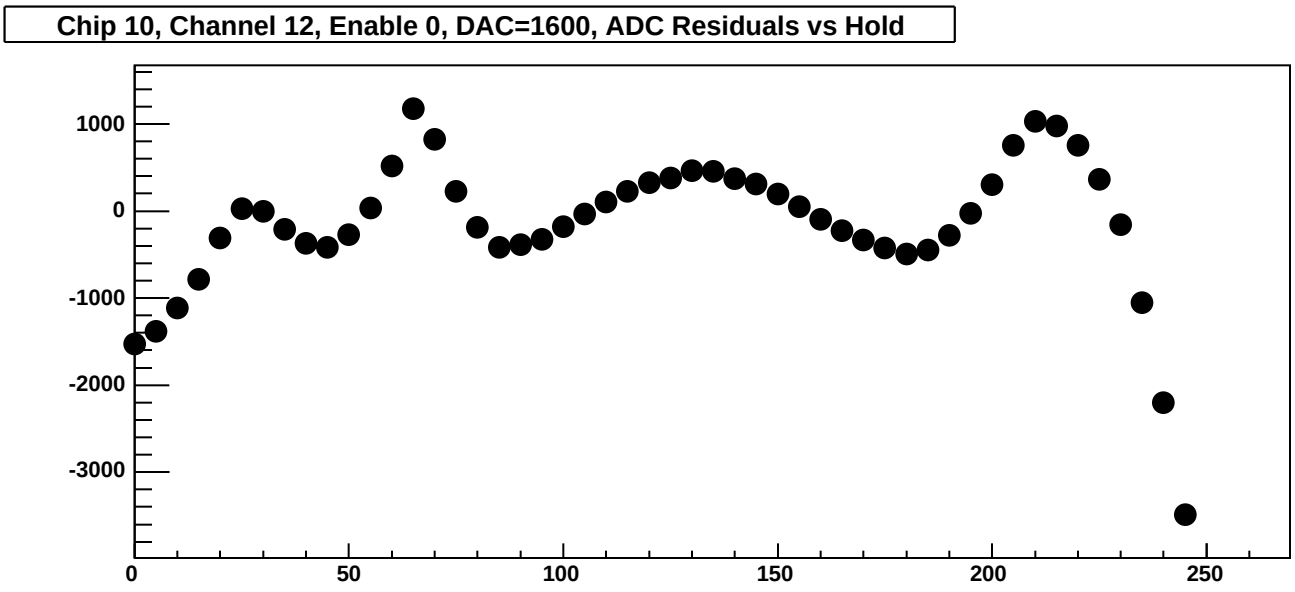

Chip 10, Channel 11, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.054e+05 / 41
p0 -1374 ± 7.869
p1 61.44 ± 0.03017
p2 2.399e+04 ± 44.15
p3 40.92 ± 0.08994
p4 -1.326 ± 0.2547
20000
15000
10000
5000
0
0
50
100
150
200
250

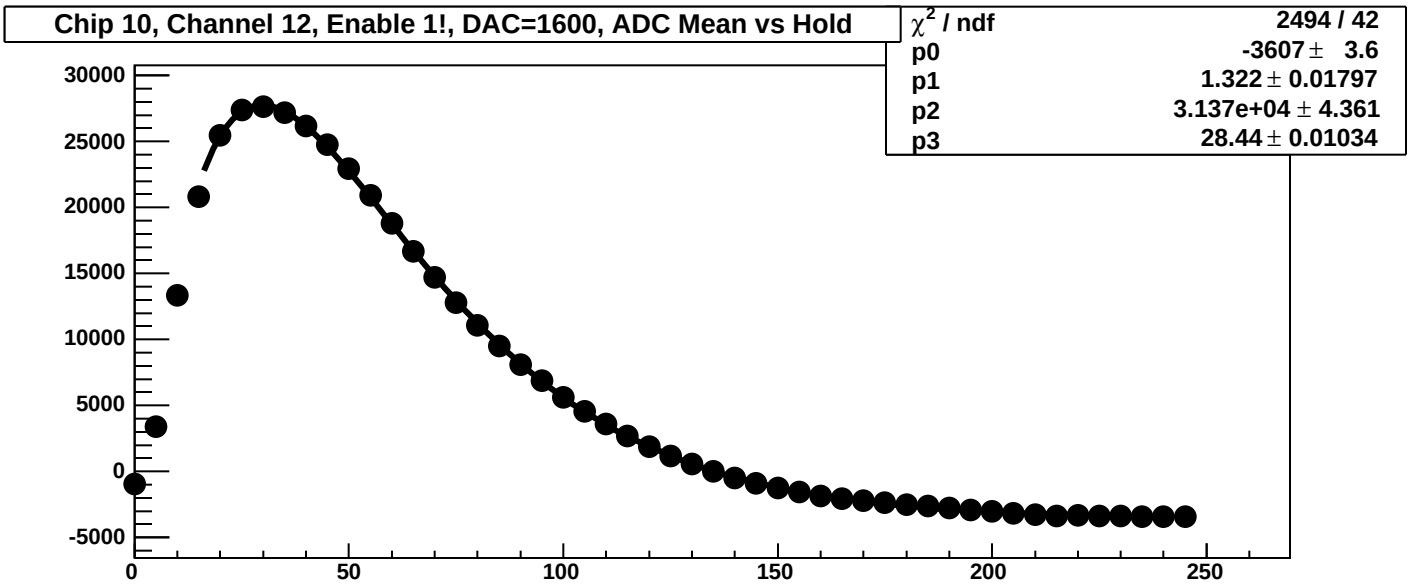

Chip 10, Channel 11, Enable 5, DAC=1600, ADC Noise vs Hold
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

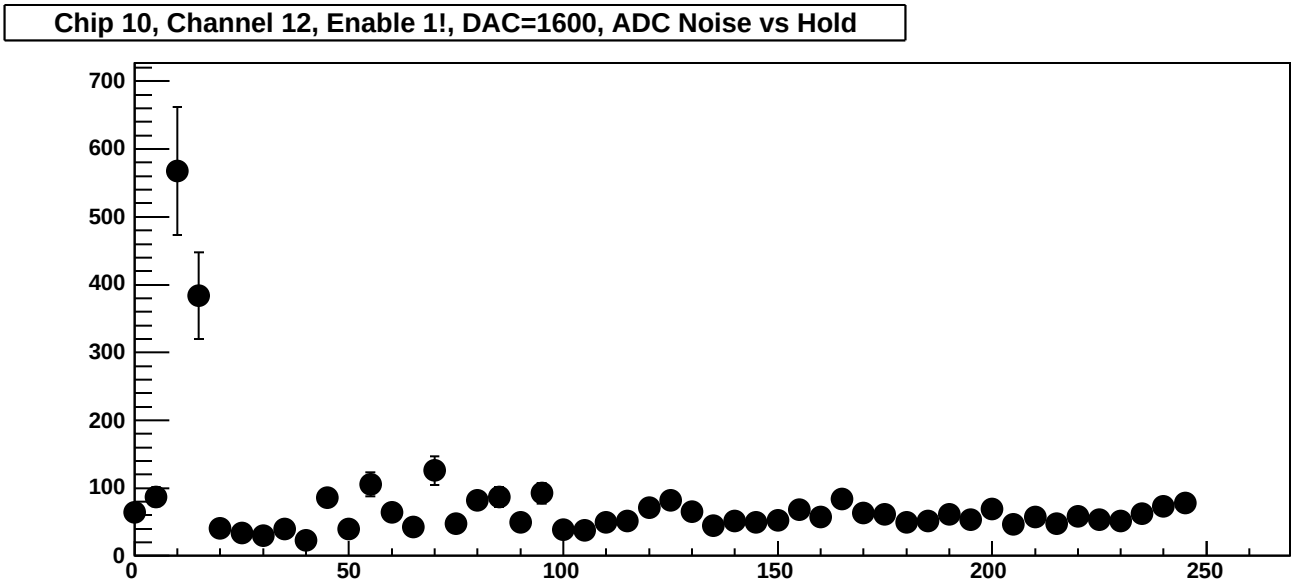

Chip 10, Channel 11, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

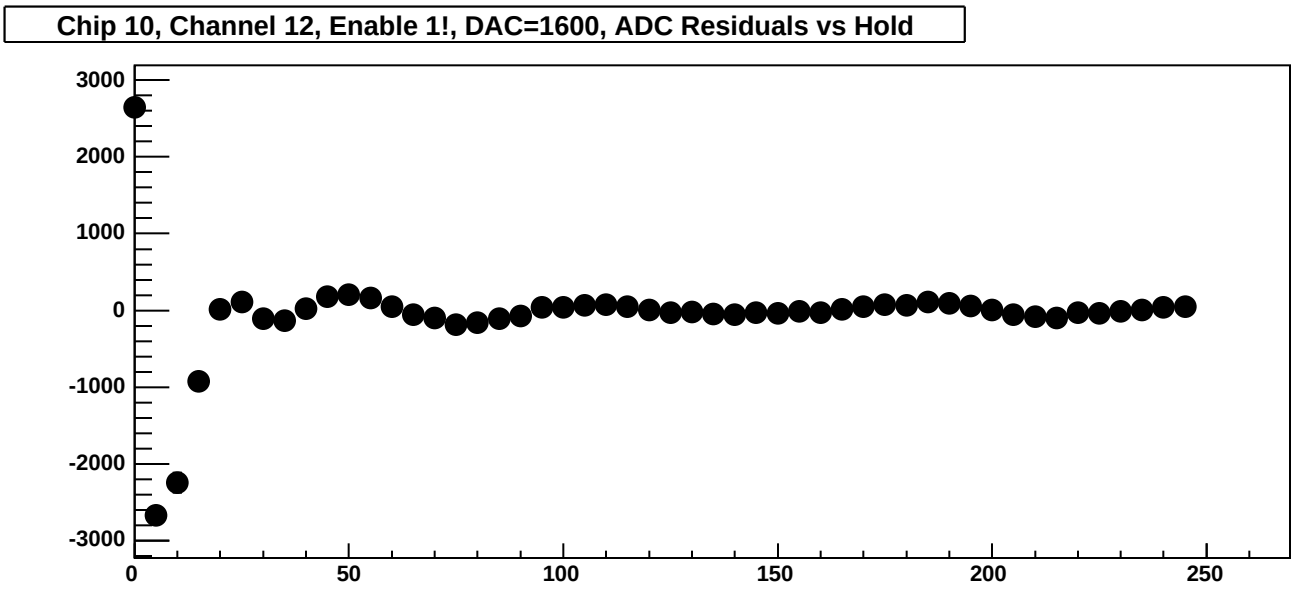

Chip 10, Channel 12, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.754e+04 / 41
p0 -1229 ± 8.479
p1 66.65 ± 0.0862
p2 1.088e+04 ± 42.84
p3 31.49 ± 0.1956
p4 -28.62 ± 0.2471
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

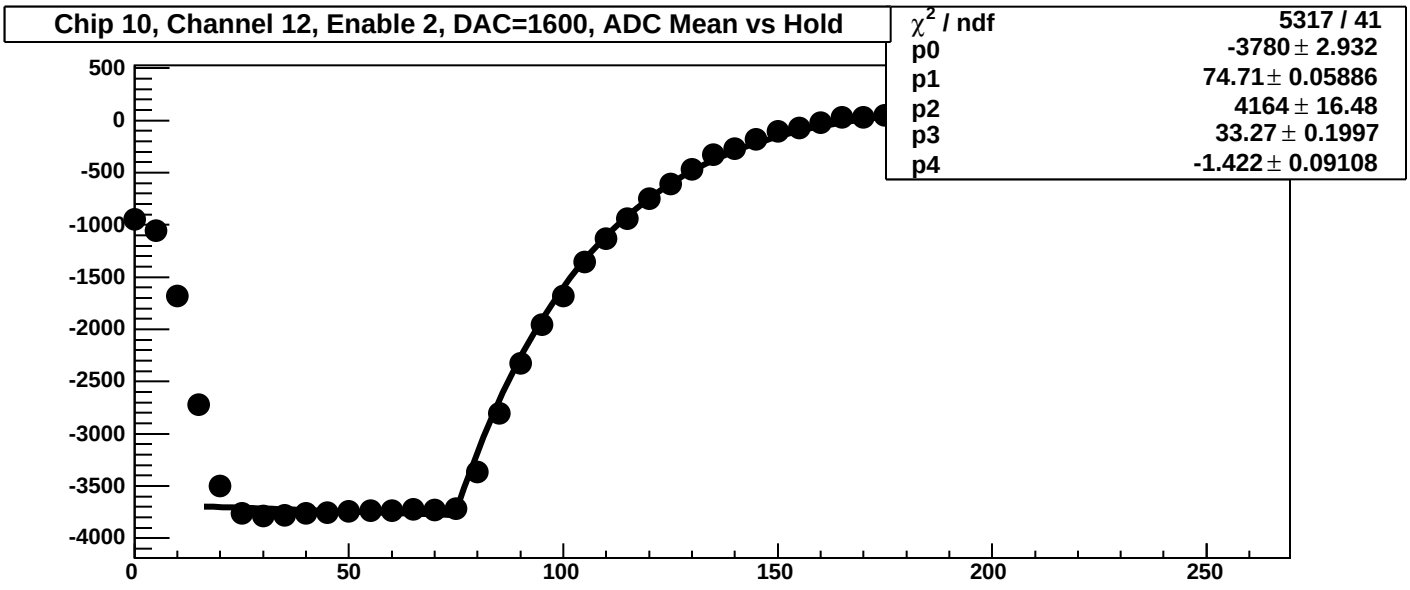

Chip 10, Channel 12, Enable 0, DAC=1600, ADC Noise vs Hold
240
220
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250

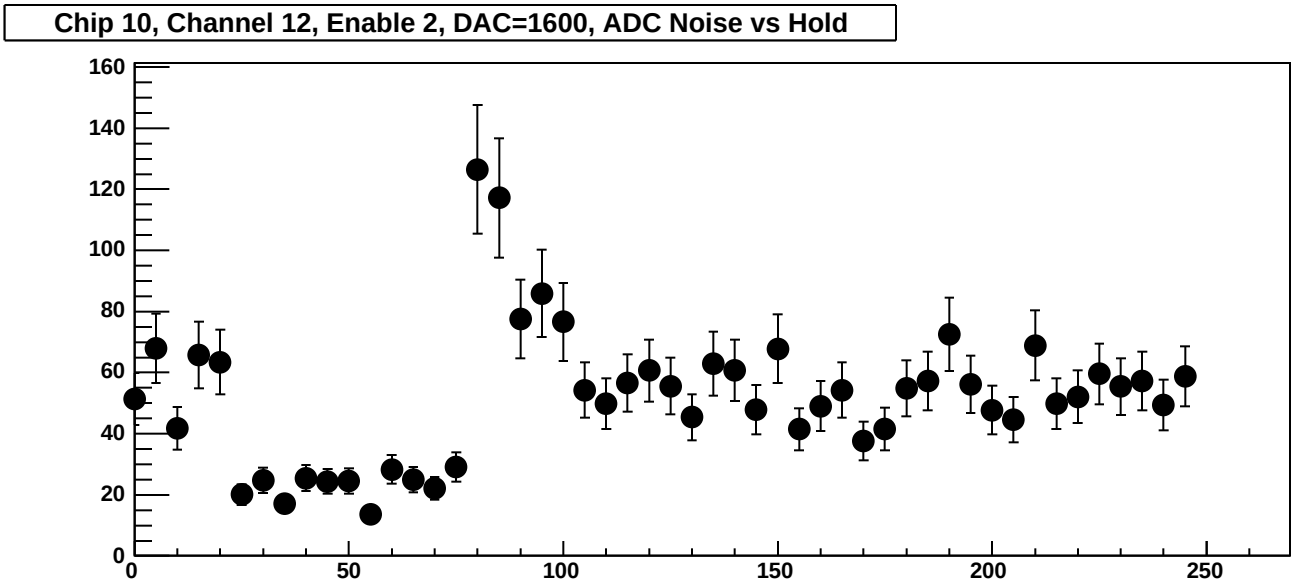

Chip 10, Channel 12, Enable 0, DAC=1600, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

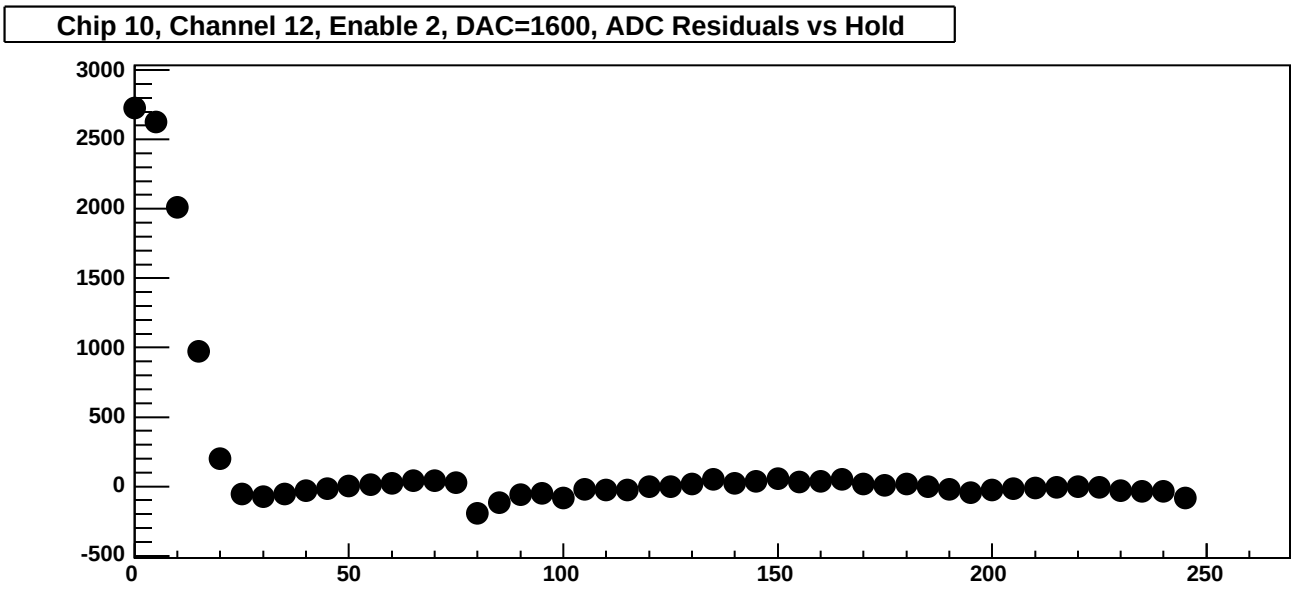

Chip 10, Channel 12, Enable 1!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2494 / 42
p0 -3607 ± 3.6
p1 1.322 ± 0.01797
p2 3.137e+04 ± 4.361
p3 28.44 ± 0.01034
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 10, Channel 12, Enable 1!, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250

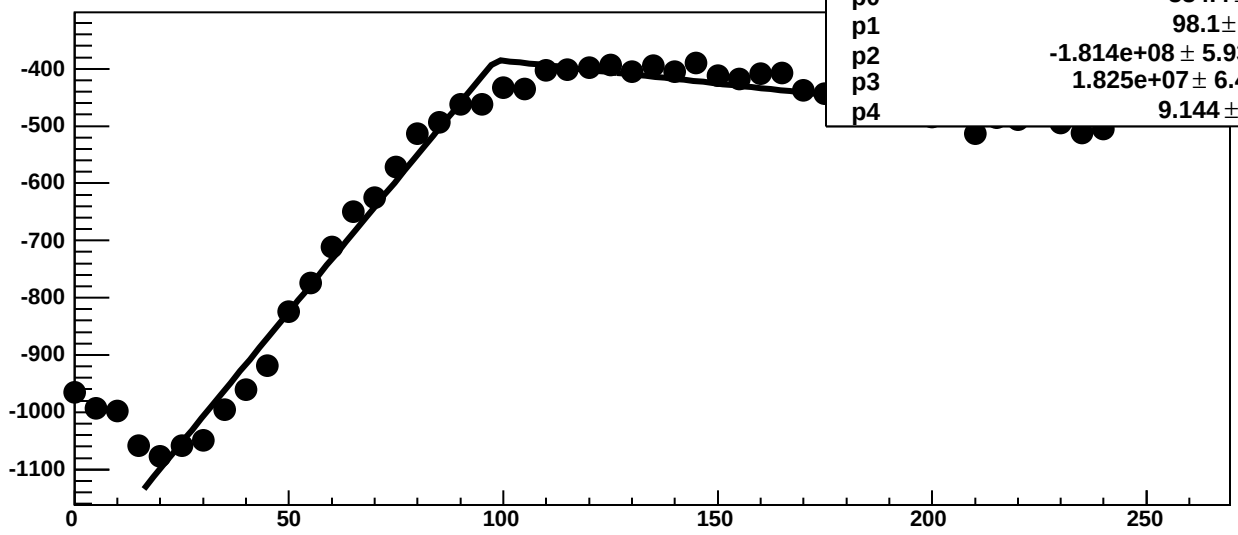

Chip 10, Channel 12, Enable 1!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250


Chip 10, Channel 12, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 5317 / 41
p0 -3780 ± 2.932
p1 74.71 ± 0.05886
p2 4164 ± 16.48
p3 33.27 ± 0.1997
p4 -1.422 ± 0.09108
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
0
50
100
150
200
250

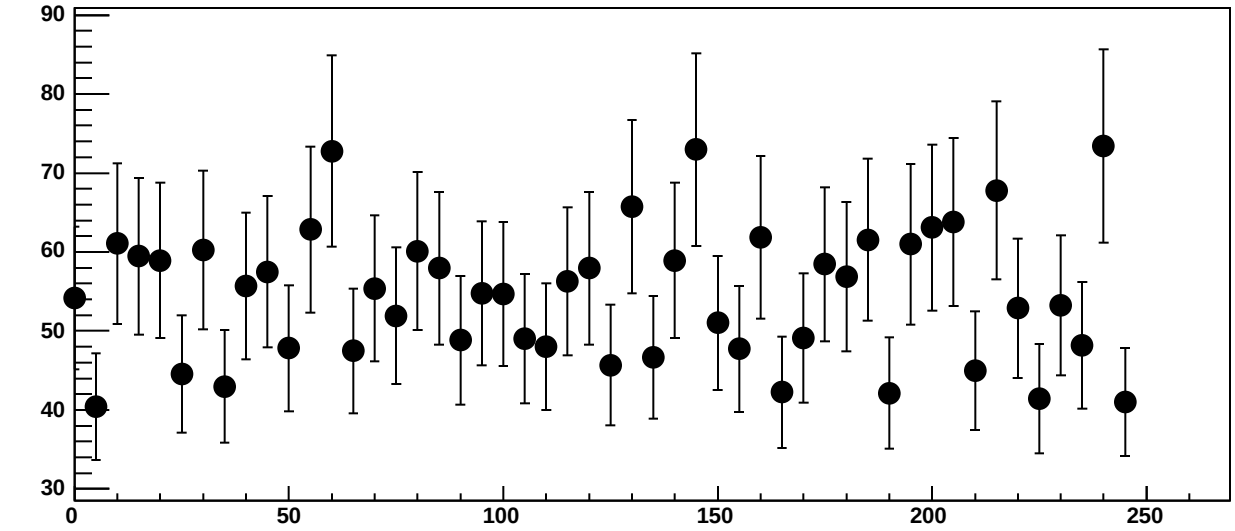

Chip 10, Channel 12, Enable 2, DAC=1600, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 10, Channel 12, Enable 2, DAC=1600, ADC Residuals vs Hold
3000
2500
2000
1500
1000
500
0
-500
0
50
100
150
200
250

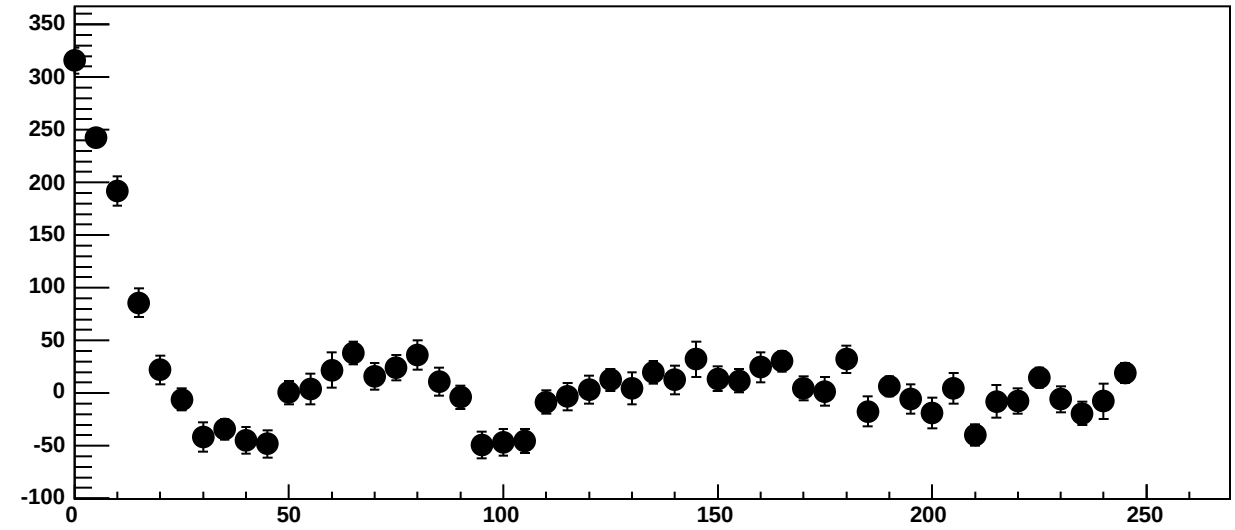
Chip 10, Channel 12, Enable 3, DAC=1600, ADC Mean vs Hold

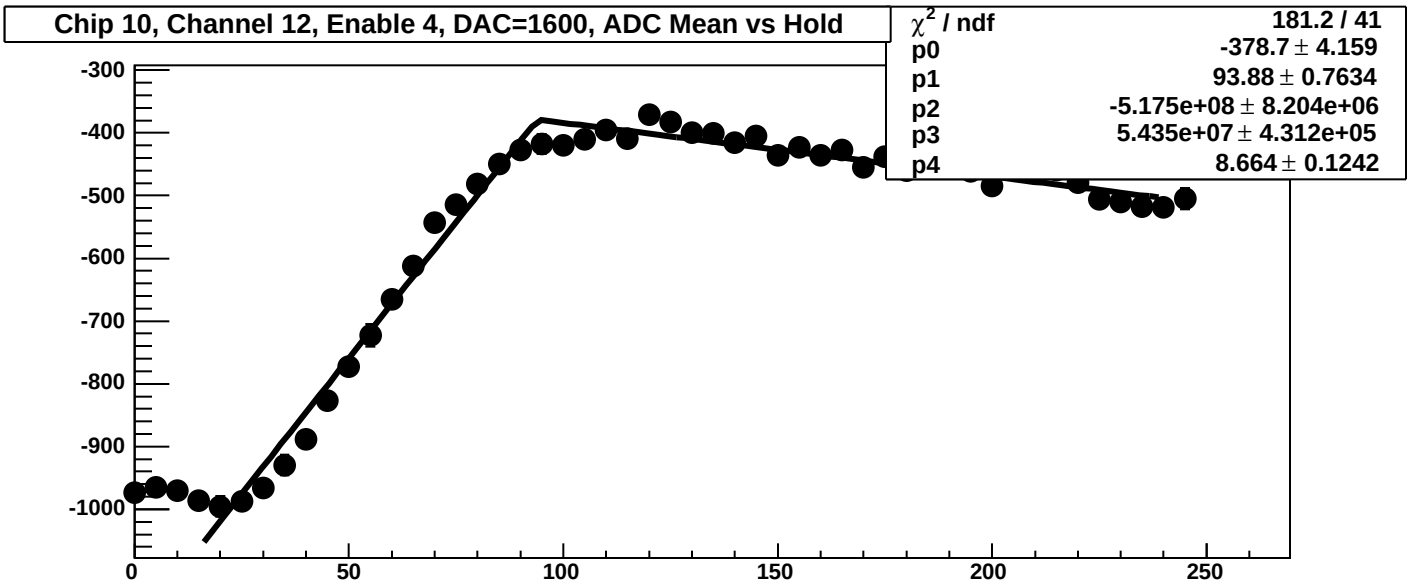


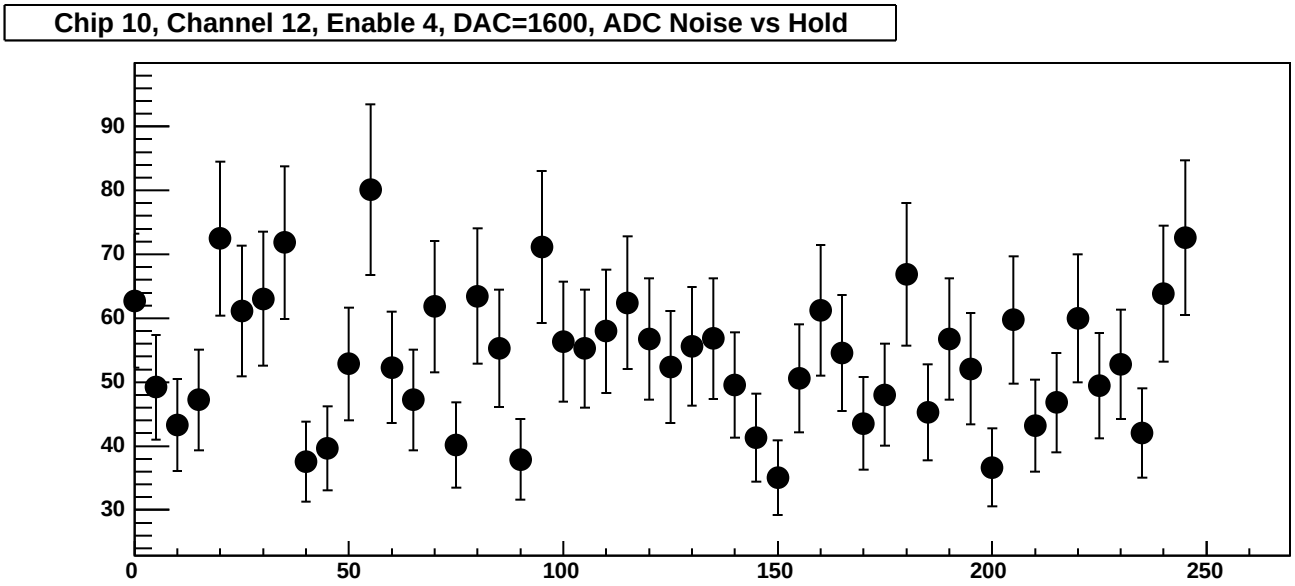
Chip 10, Channel 12, Enable 3, DAC=1600, ADC Noise vs Hold

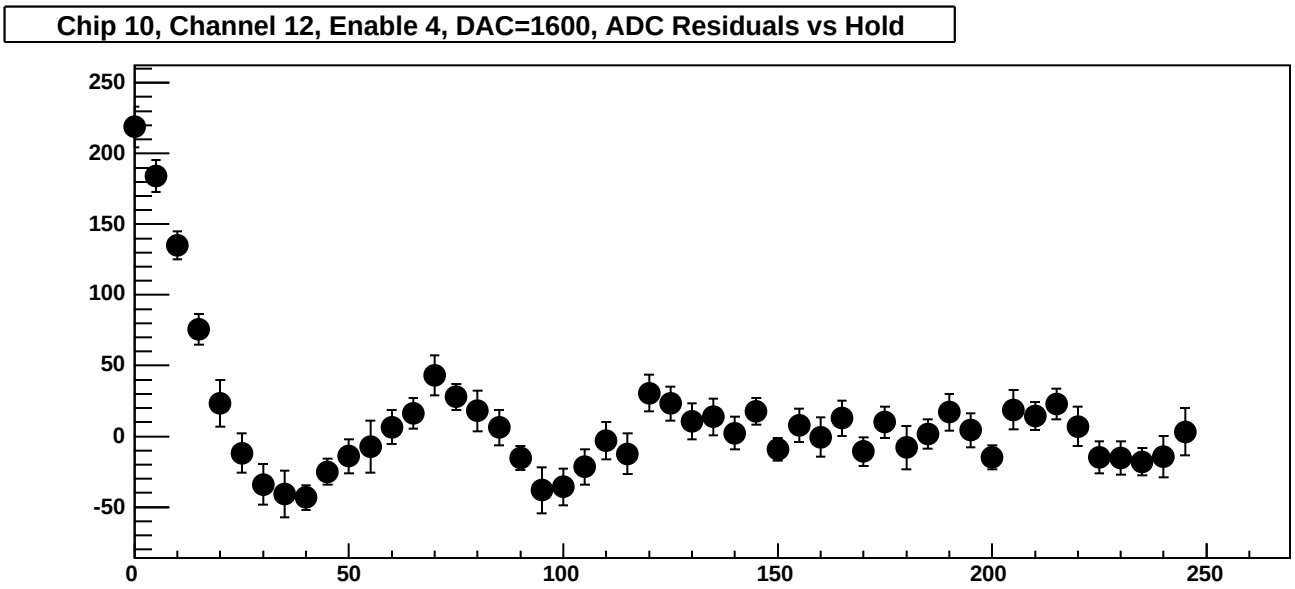


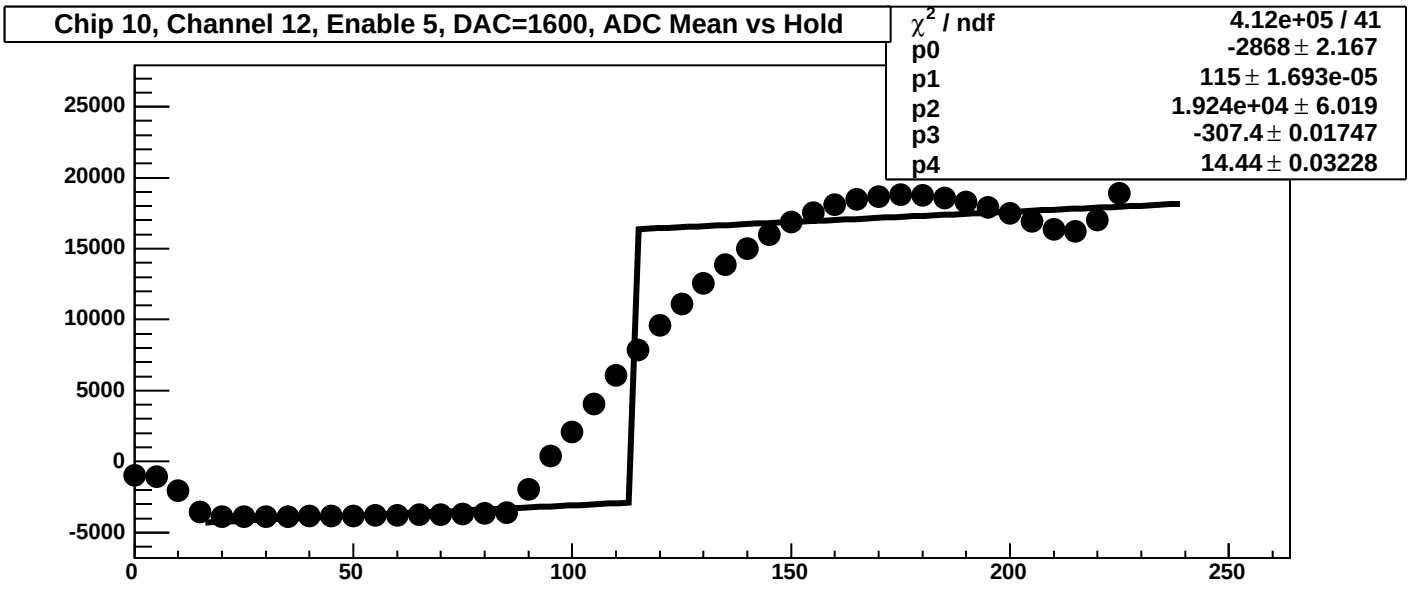
Chip 10, Channel 12, Enable 3, DAC=1600, ADC Residuals vs Hold

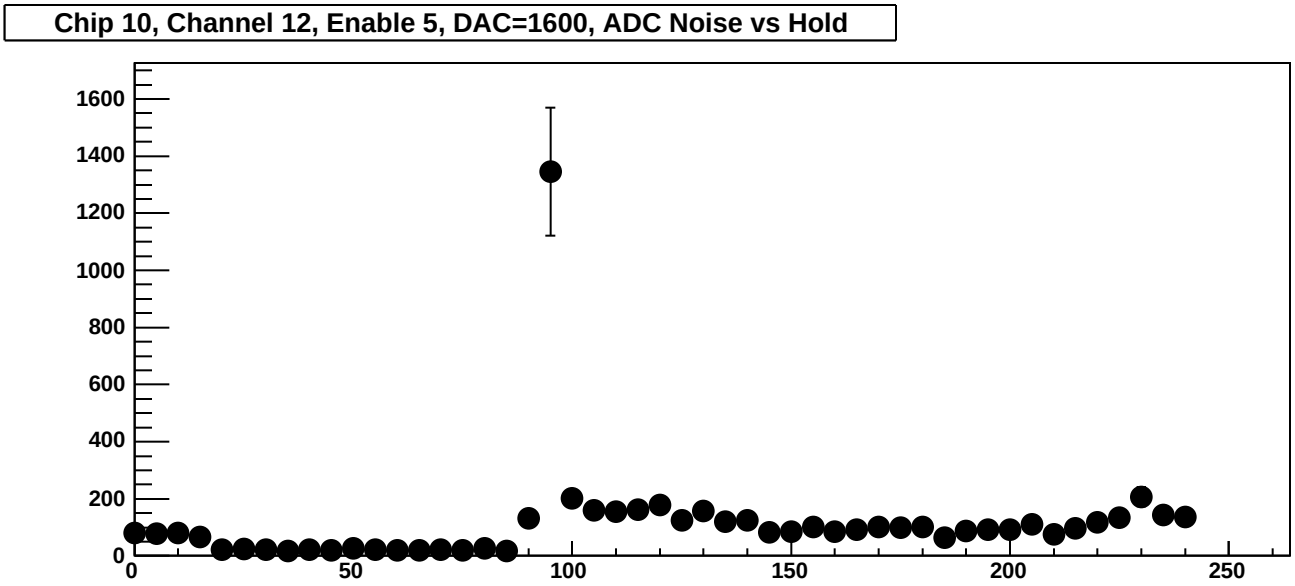


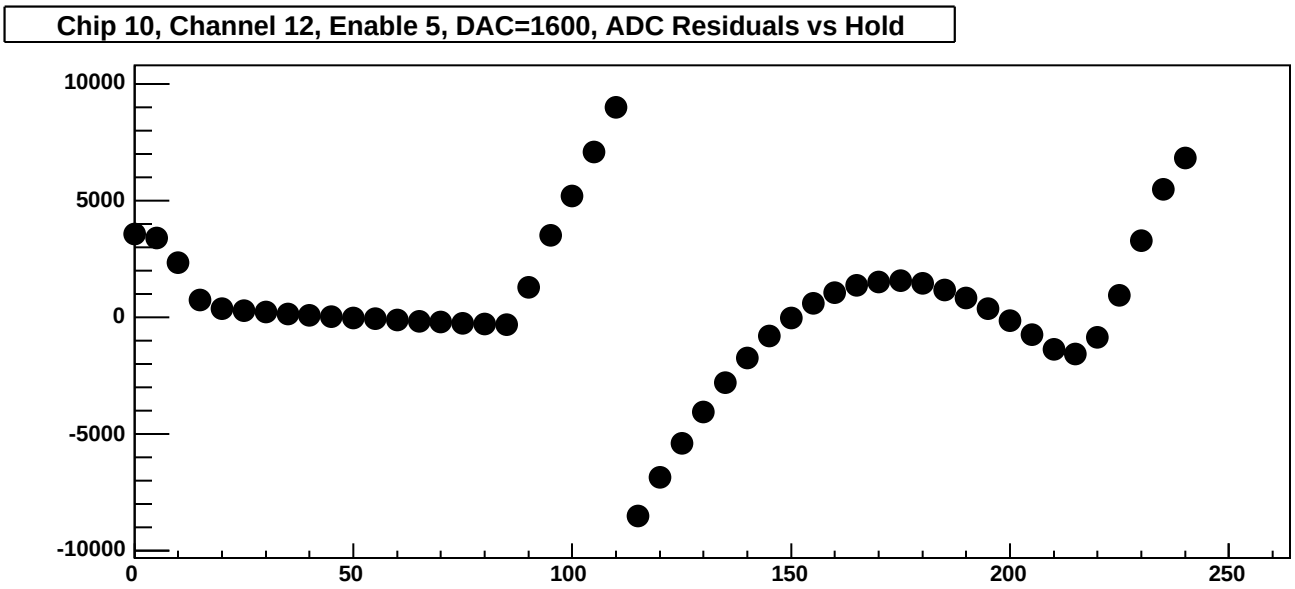

Chip 10, Channel 12, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 181.2 / 41
p0 -378.7 ± 4.159
p1 93.88 ± 0.7634
p2 -5.175e+08 ± 8.204e+06
p3 5.435e+07 ± 4.312e+05
p4 8.664 ± 0.1242
-300
-400
-500
-600
-700
-800
-900
-1000
0
50
100
150
200
250

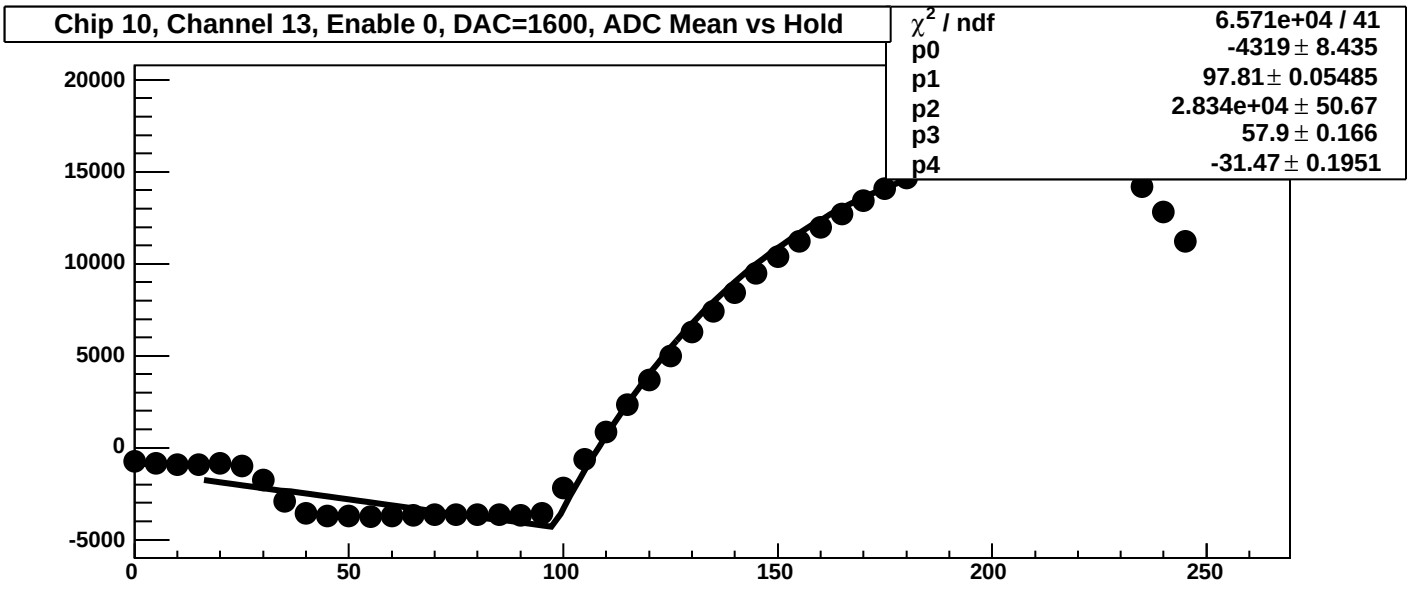

Chip 10, Channel 12, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

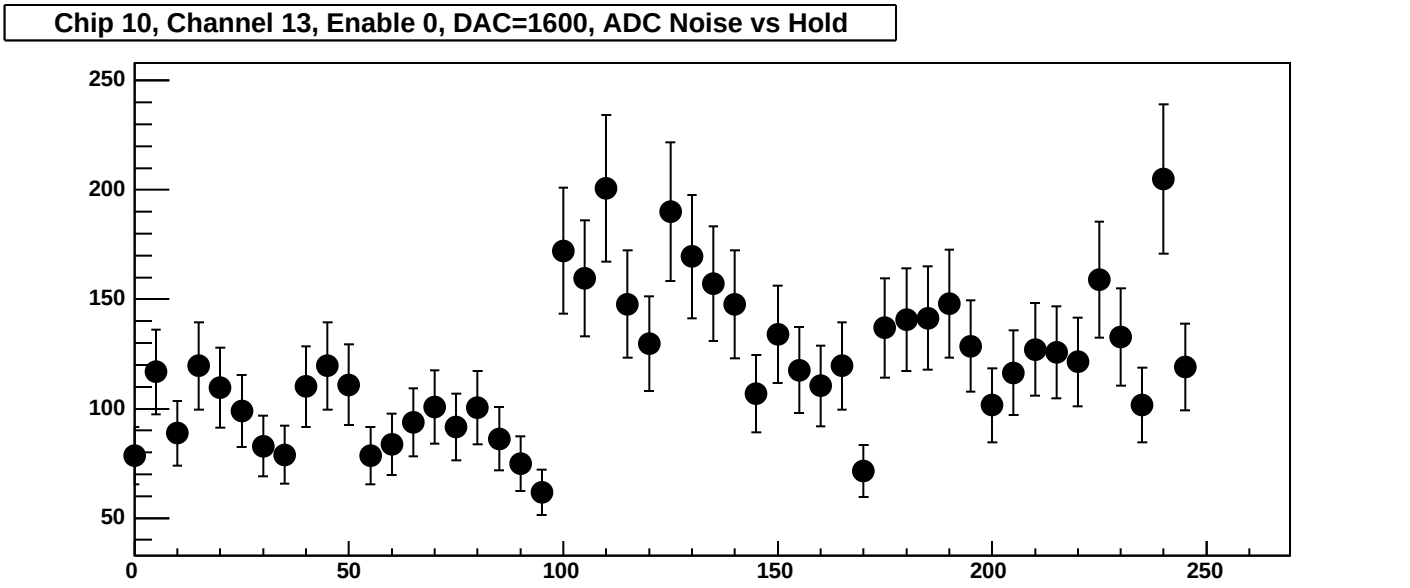

Chip 10, Channel 12, Enable 4, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
0
50
100
150
200
250

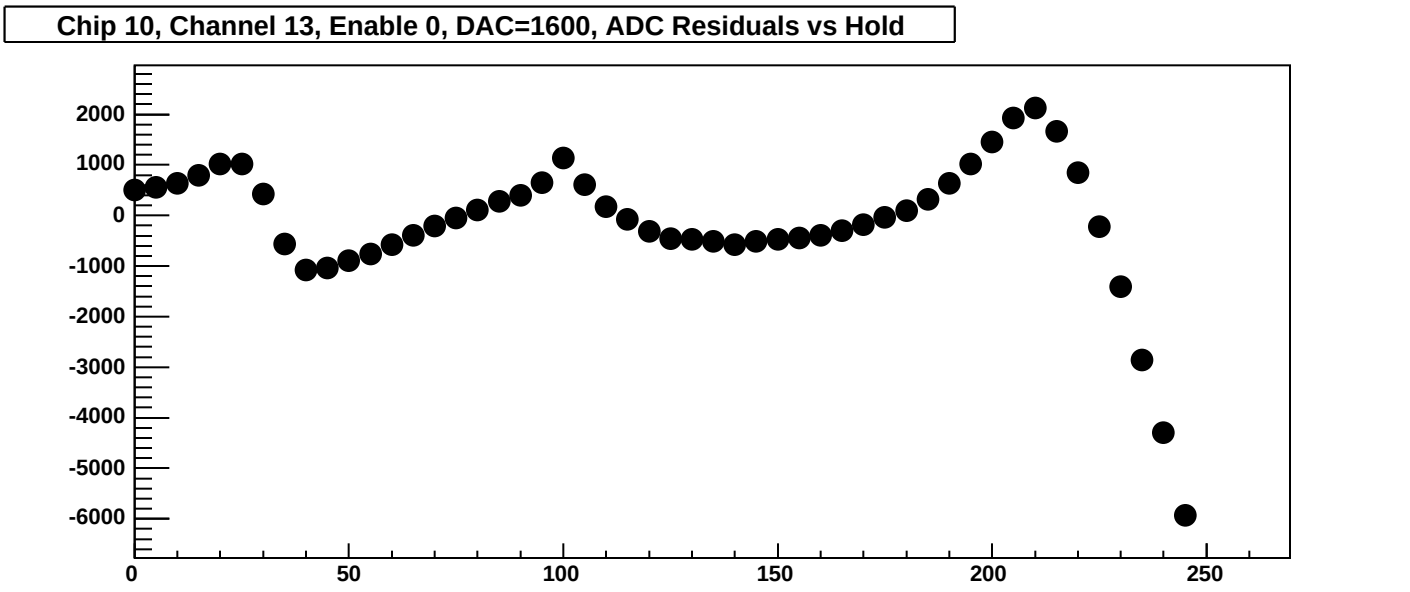

Chip 10, Channel 12, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.12e+05 / 41
p0 -2868 ± 2.167
p1 115 ± 1.693e-05
p2 1.924e+04 ± 6.019
p3 -307.4 ± 0.01747
p4 14.44 ± 0.03228
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 10, Channel 12, Enable 5, DAC=1600, ADC Noise vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

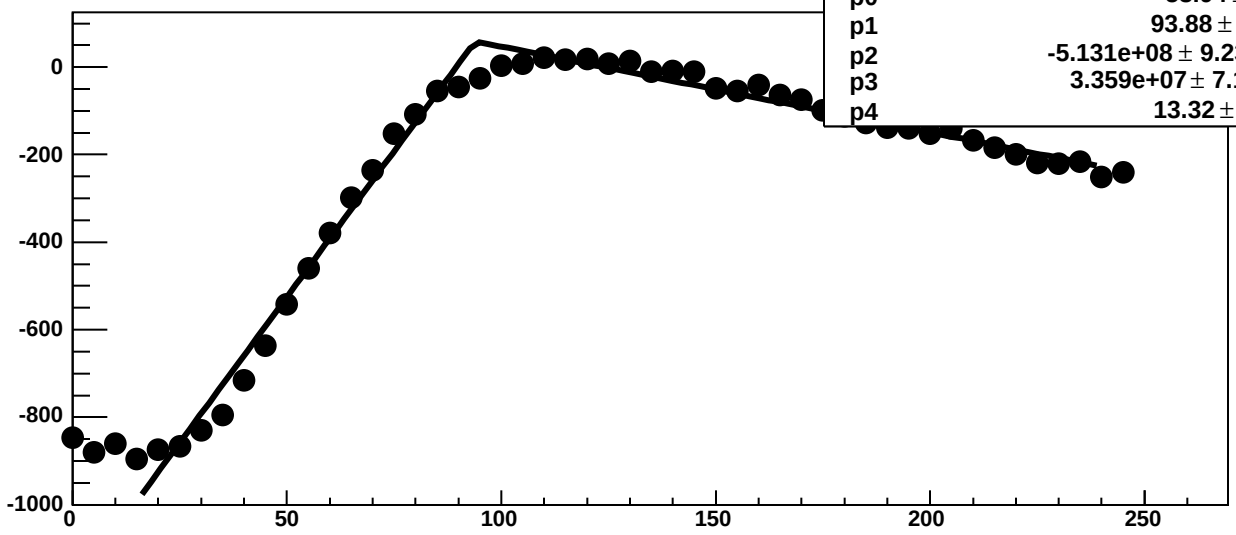

Chip 10, Channel 12, Enable 5, DAC=1600, ADC Residuals vs Hold
10000
5000
0
-5000
-10000
0
50
100
150
200
250


Chip 10, Channel 13, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 6.571e+04 / 41
p0 -4319 ± 8.435
p1 97.81 ± 0.05485
p2 2.834e+04 ± 50.67
p3 57.9 ± 0.166
p4 -31.47 ± 0.1951
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

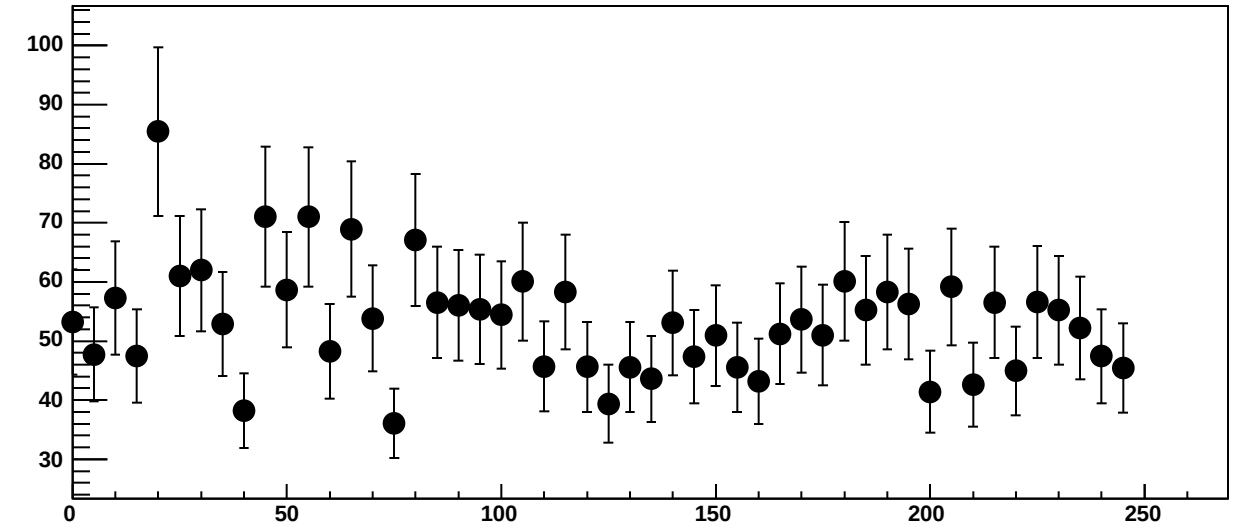

Chip 10, Channel 13, Enable 0, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
50
100
150
200
250


Chip 10, Channel 13, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
50
100
150
200
250

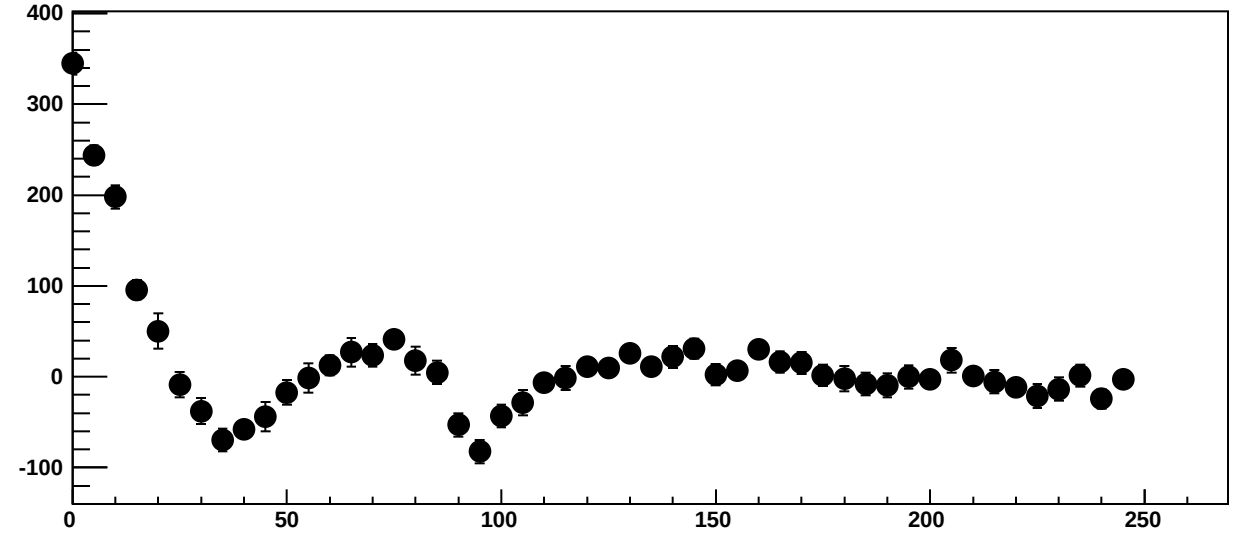
Chip 10, Channel 13, Enable 1, DAC=1600, ADC Mean vs Hold

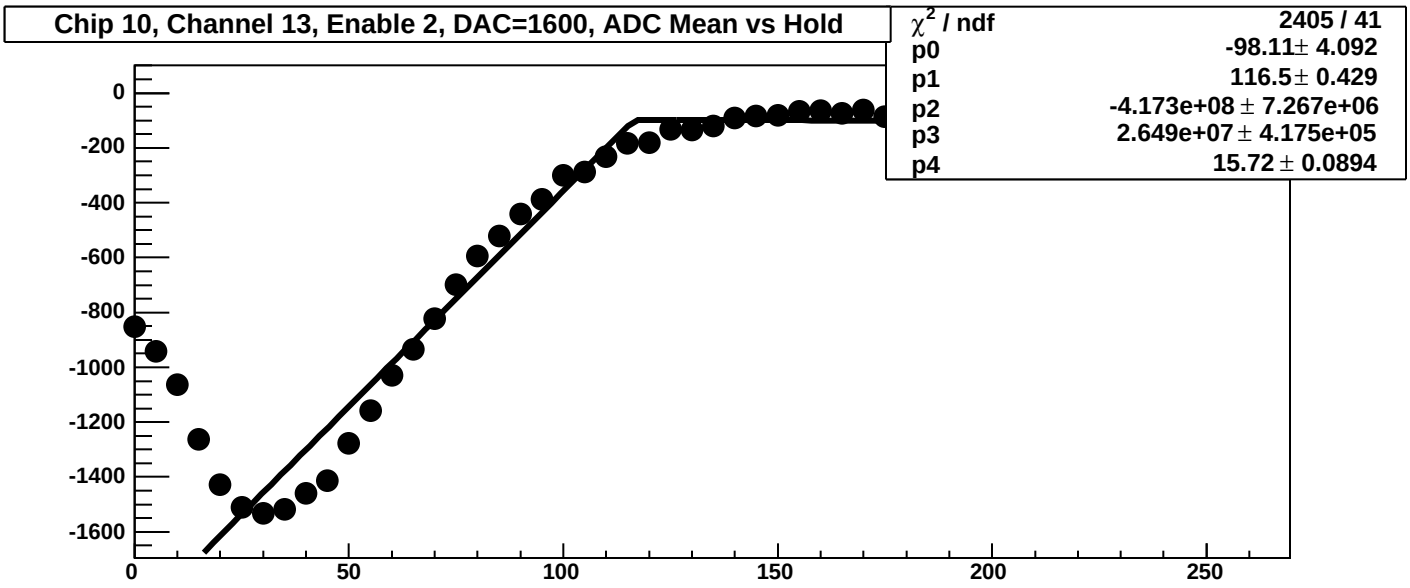


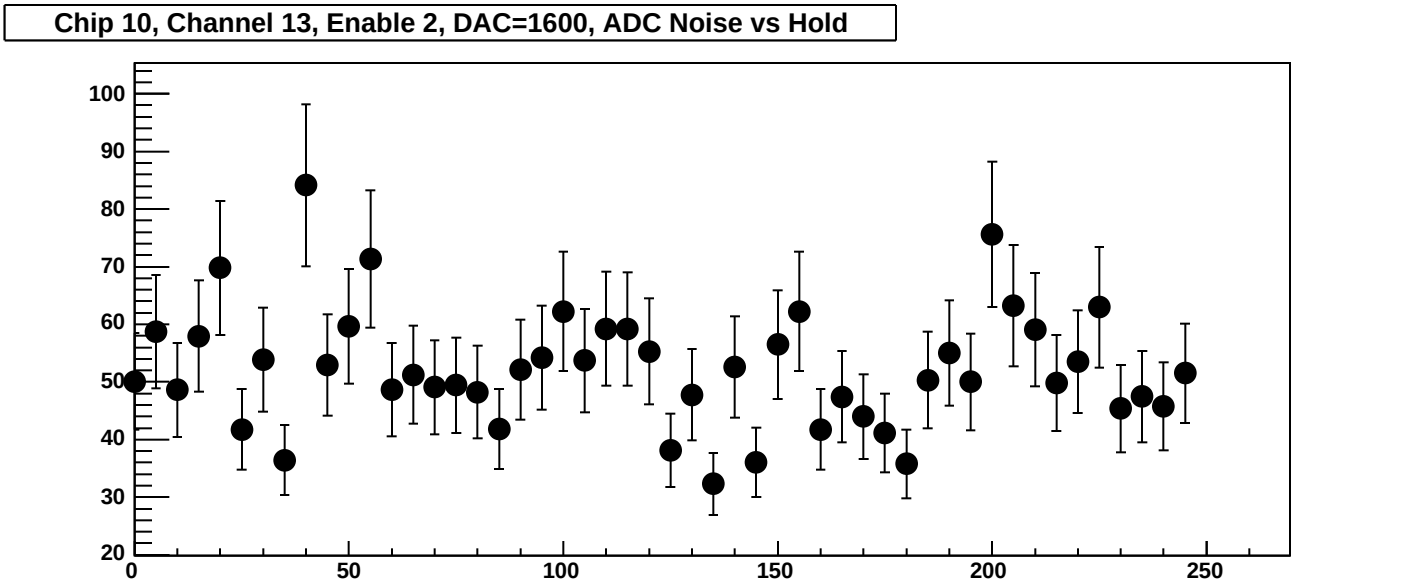
Chip 10, Channel 13, Enable 1, DAC=1600, ADC Noise vs Hold

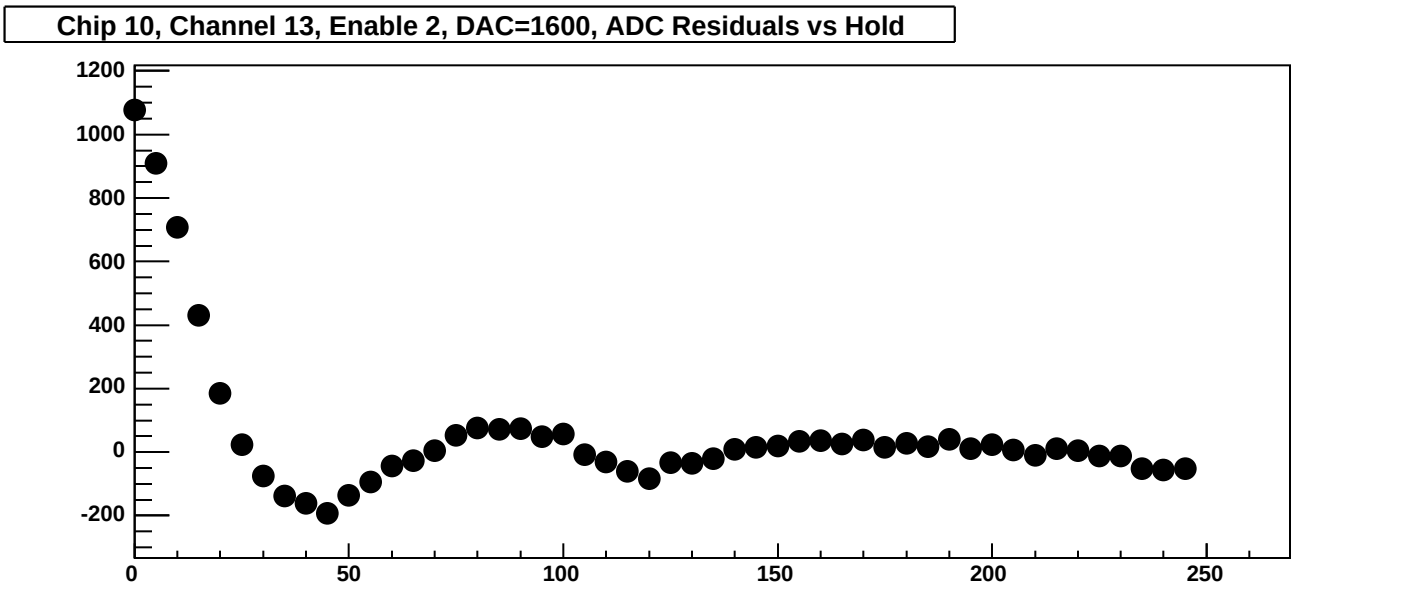


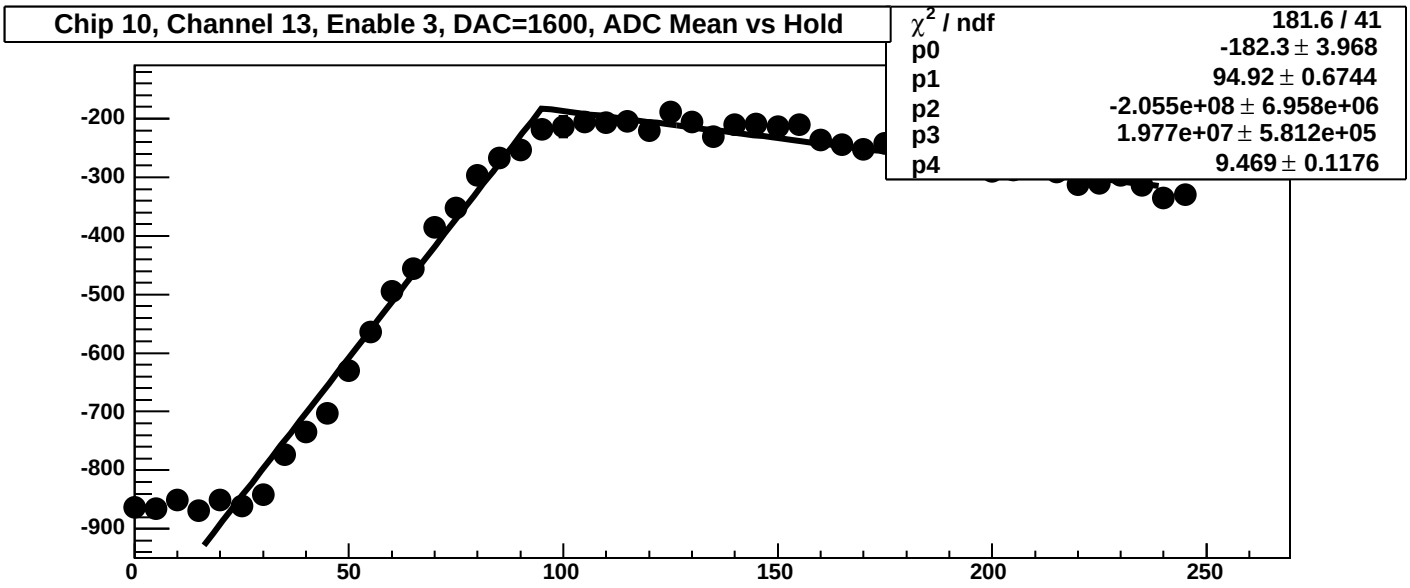
Chip 10, Channel 13, Enable 1, DAC=1600, ADC Residuals vs Hold

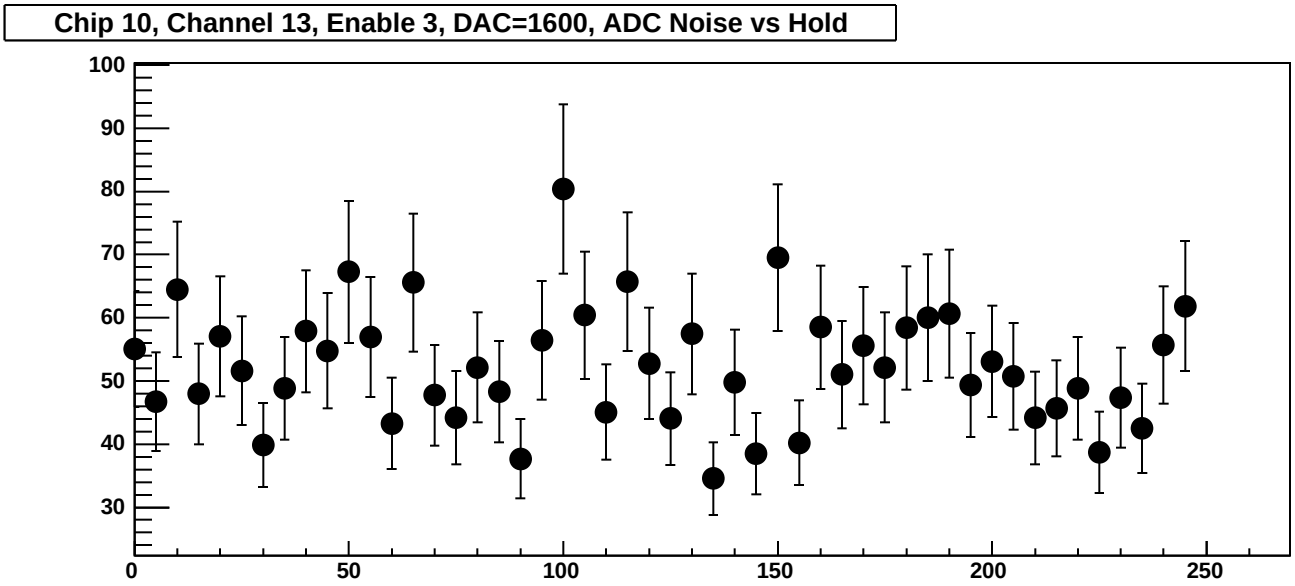


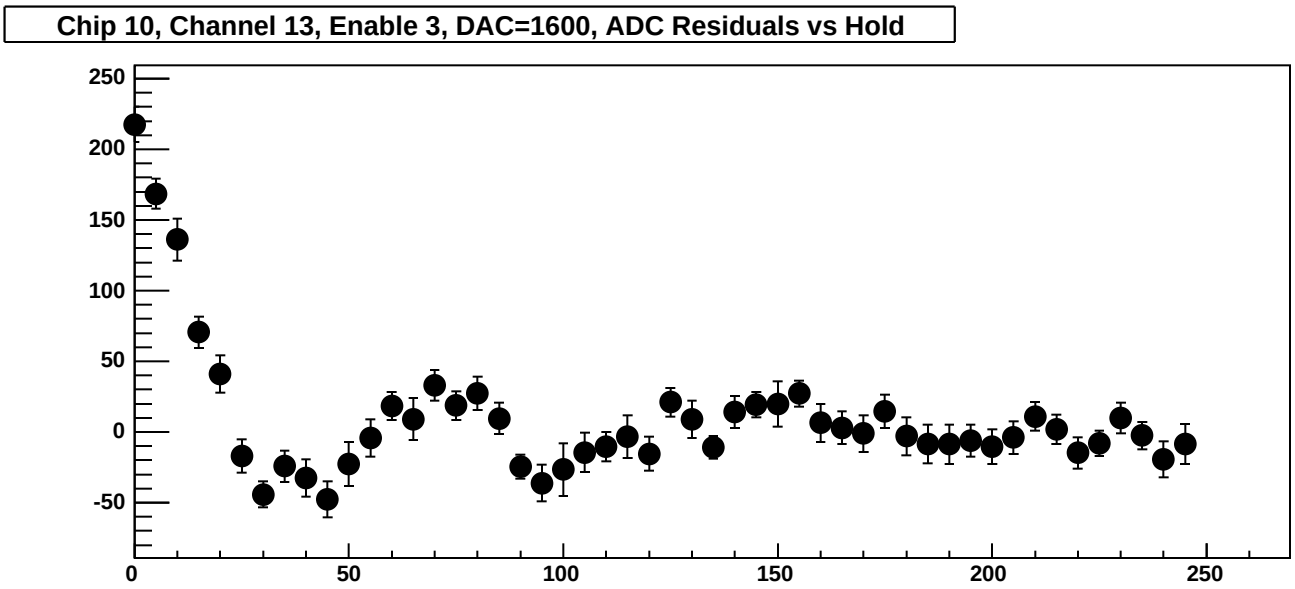

Chip 10, Channel 13, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 2405 / 41
p0 -98.11 ± 4.092
p1 116.5 ± 0.429
p2 -4.173e+08 ± 7.267e+06
p3 2.649e+07 ± 4.175e+05
p4 15.72 ± 0.0894
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250

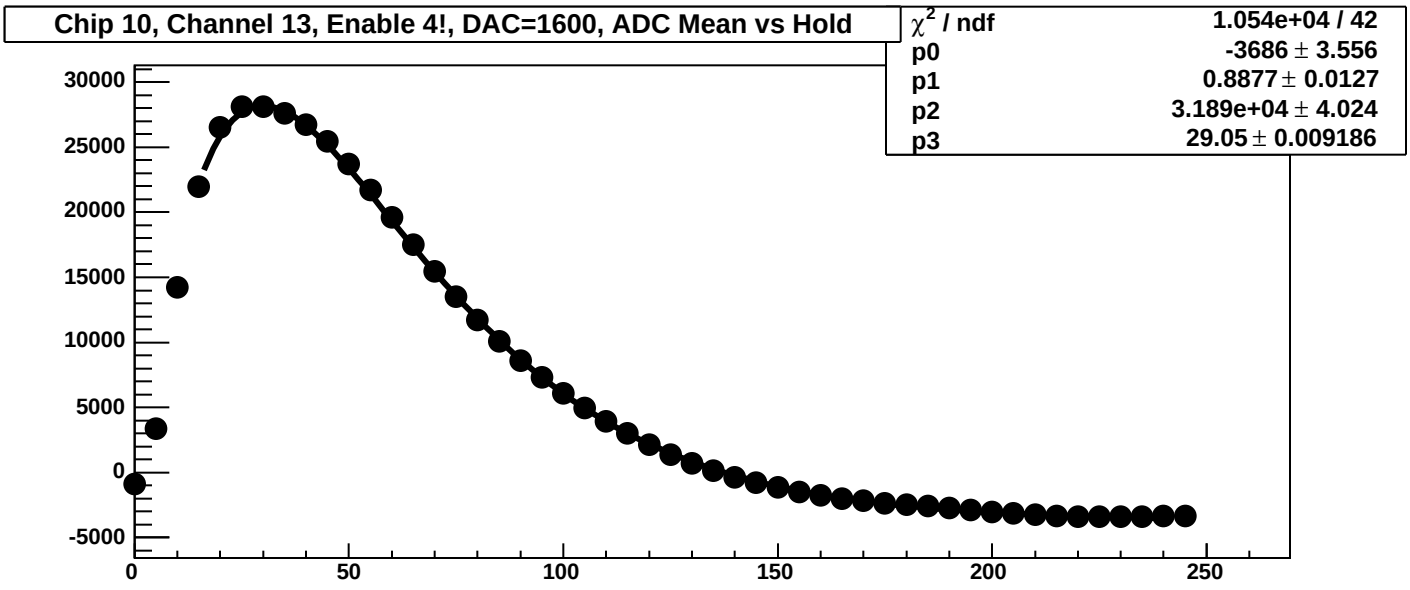

Chip 10, Channel 13, Enable 2, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
20
0
50
100
150
200
250

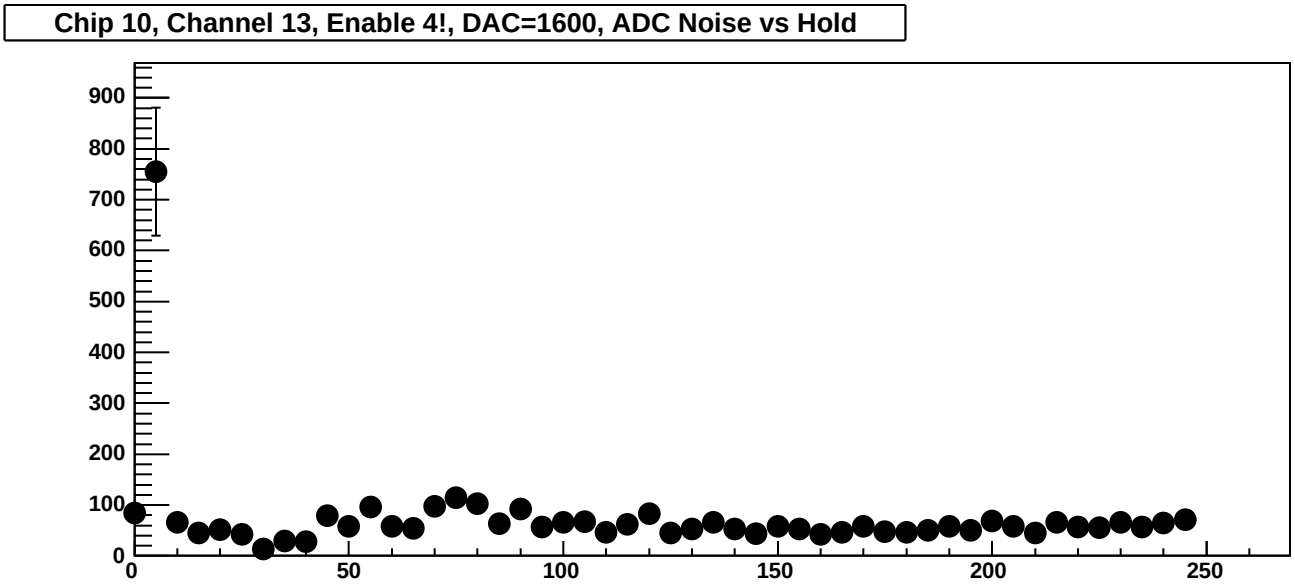

Chip 10, Channel 13, Enable 2, DAC=1600, ADC Residuals vs Hold
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

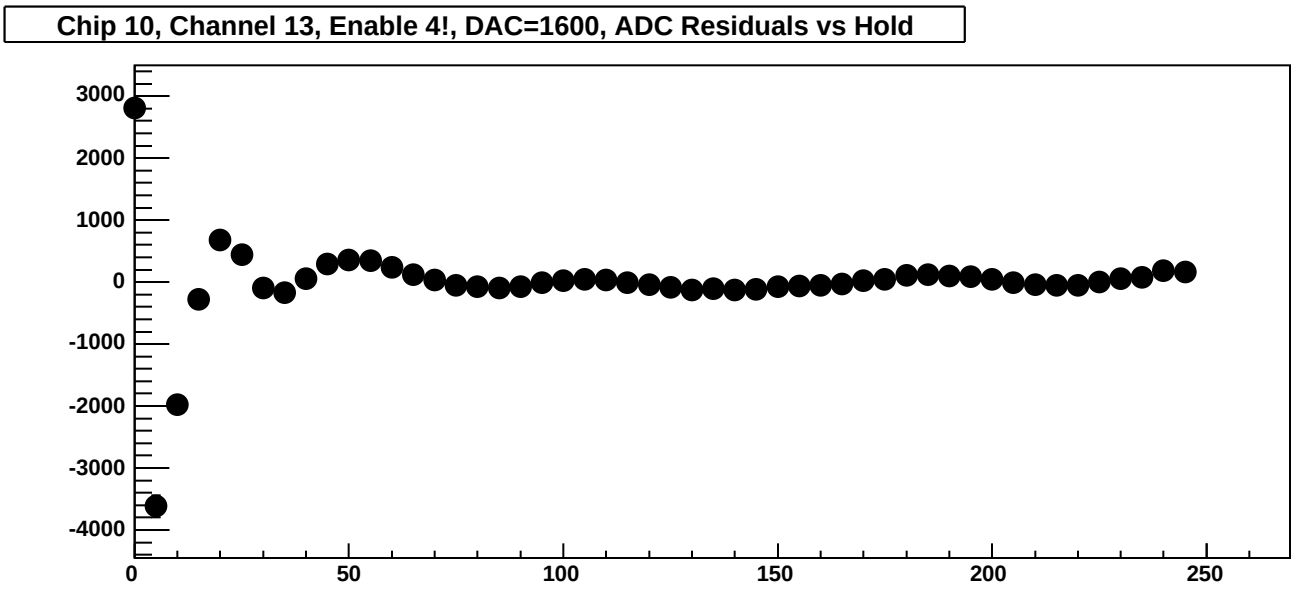

Chip 10, Channel 13, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 181.6 / 41
p0 -182.3 ± 3.968
p1 94.92 ± 0.6744
p2 -2.055e+08 ± 6.958e+06
p3 1.977e+07 ± 5.812e+05
p4 9.469 ± 0.1176
-200
-300
-400
-500
-600
-700
-800
-900
0
50
100
150
200
250

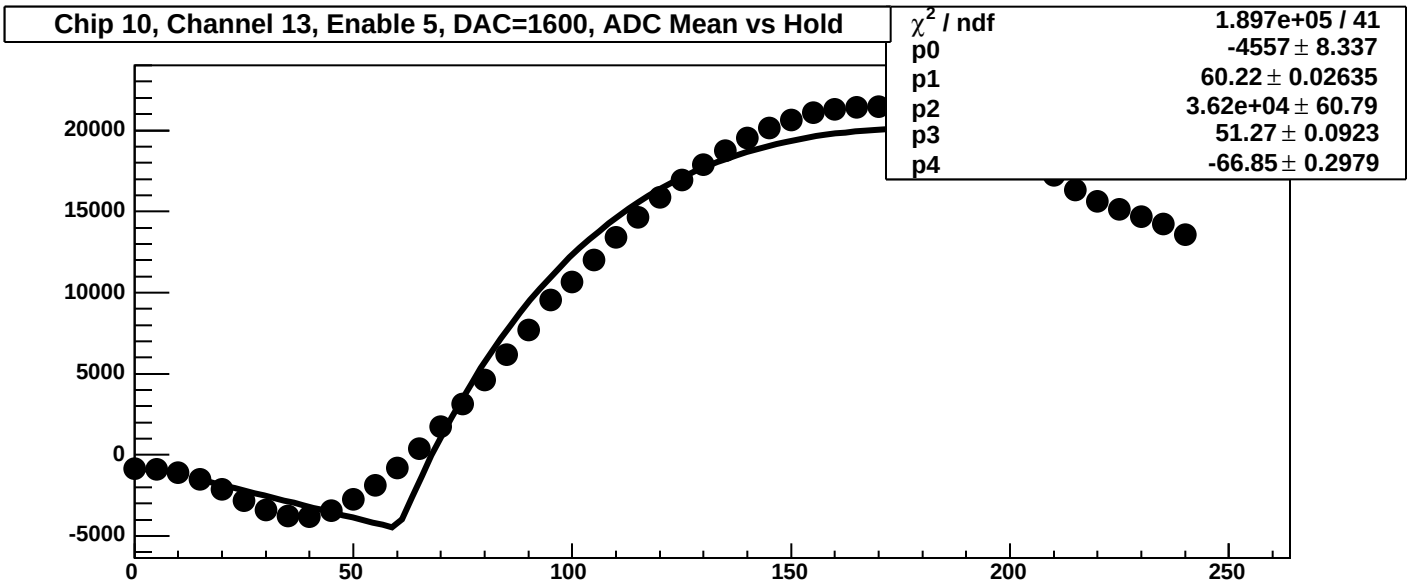

Chip 10, Channel 13, Enable 3, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
0
50
100
150
200
250

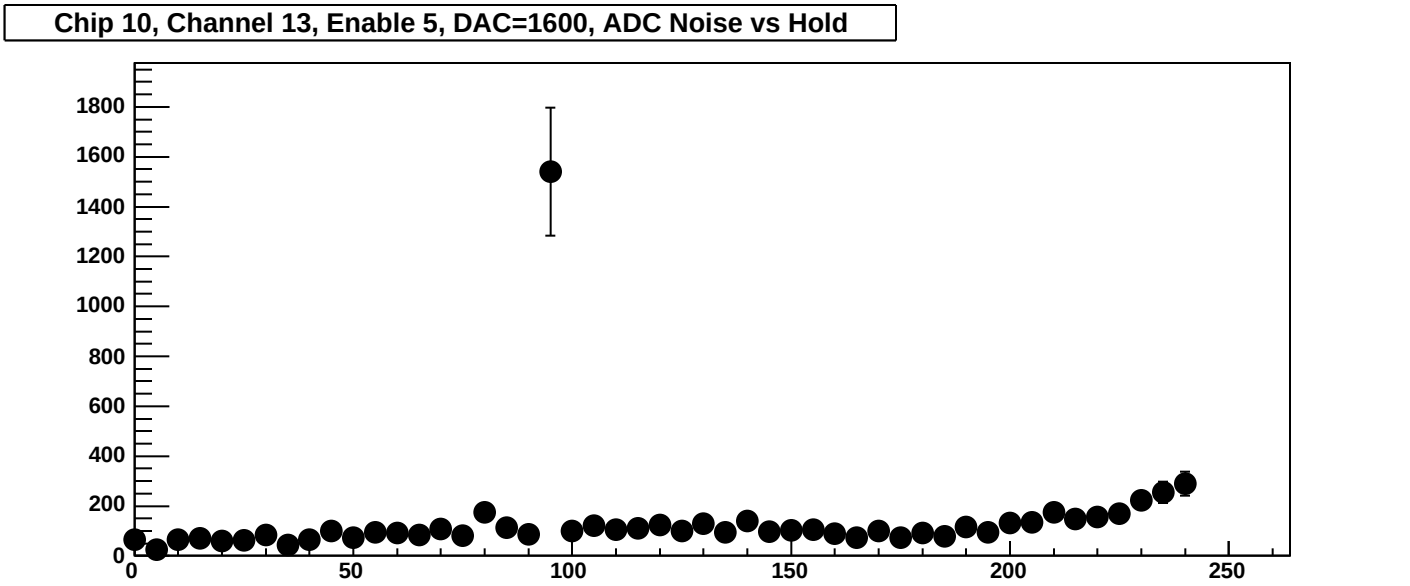

Chip 10, Channel 13, Enable 3, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
0
50
100
150
200
250

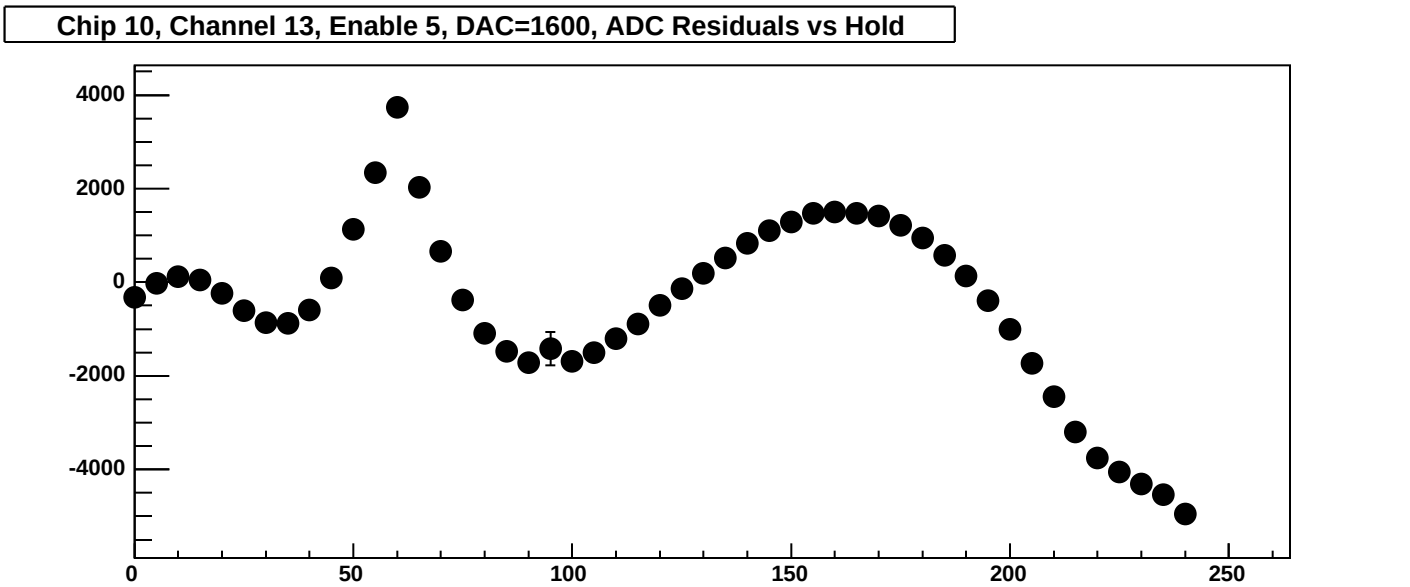

Chip 10, Channel 13, Enable 4!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.054e+04 / 42
p0 -3686 ± 3.556
p1 0.8877 ± 0.0127
p2 3.189e+04 ± 4.024
p3 29.05 ± 0.009186
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

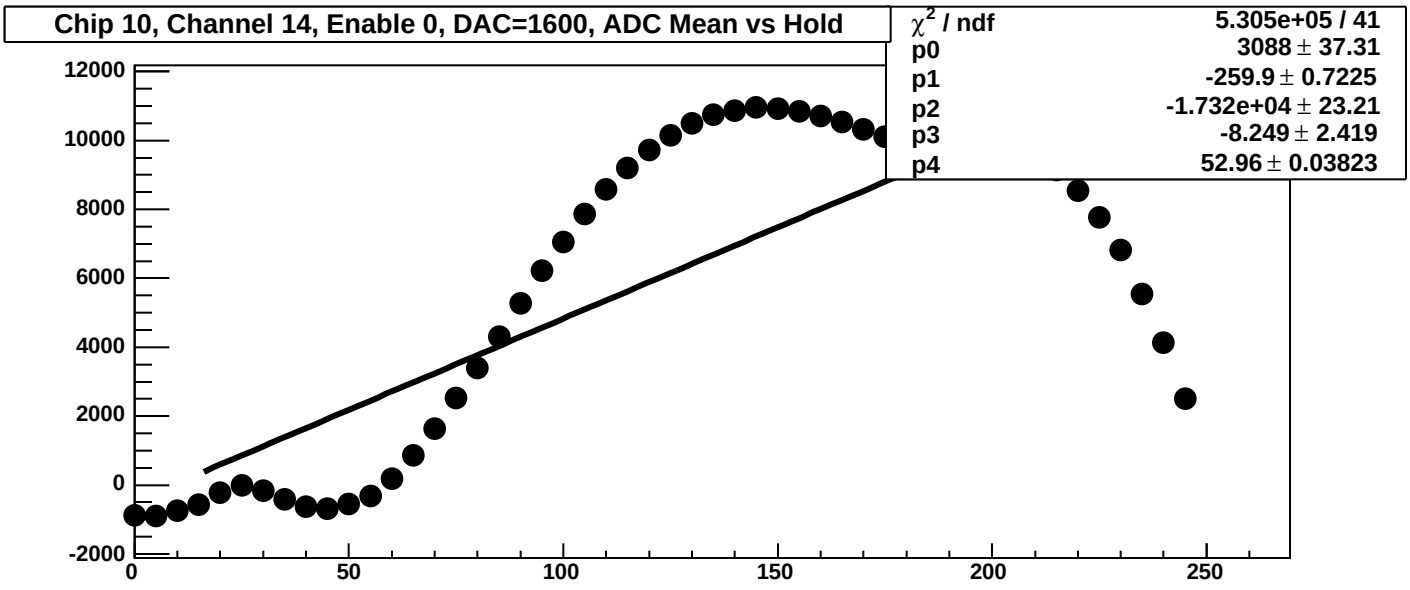

Chip 10, Channel 13, Enable 4!, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250

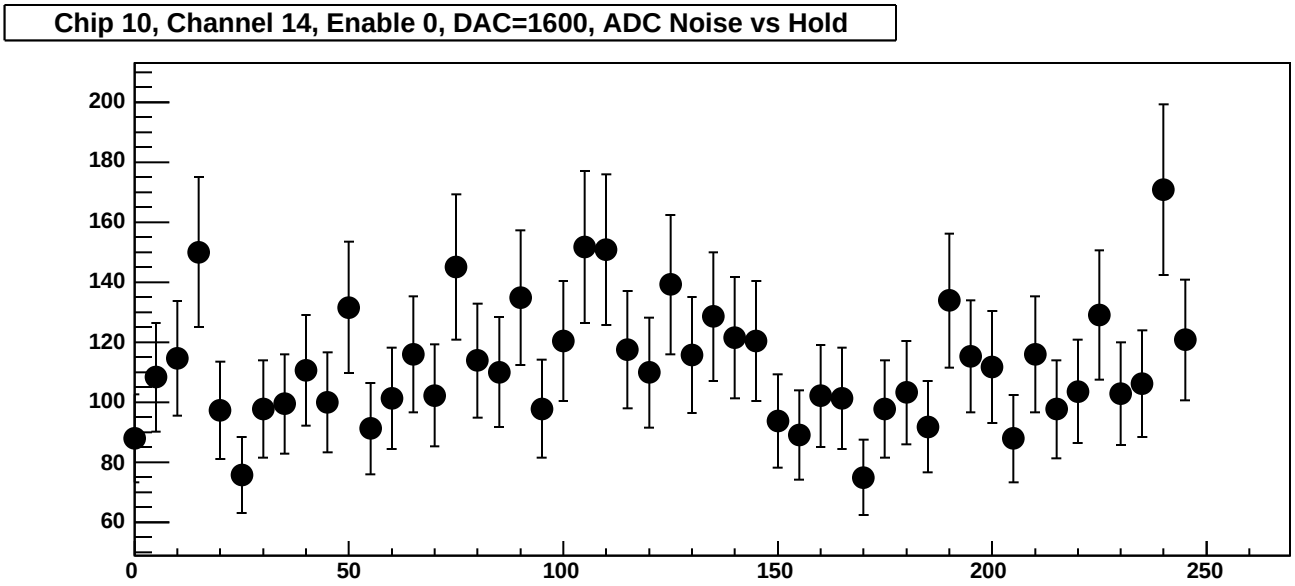

Chip 10, Channel 13, Enable 4!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

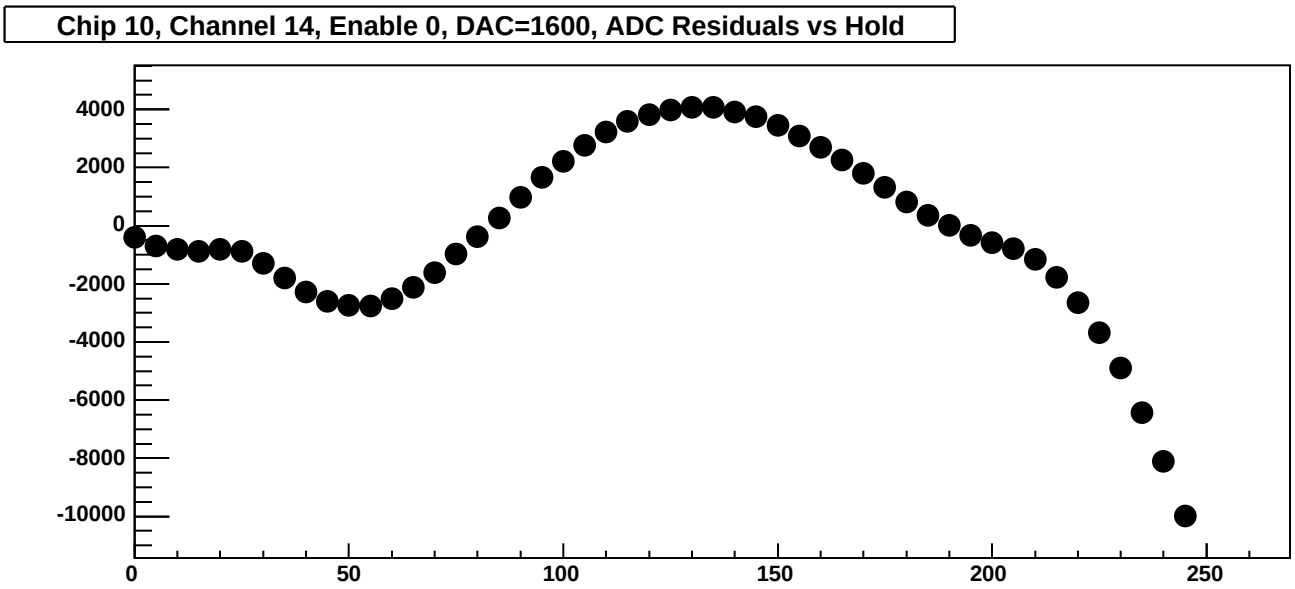

Chip 10, Channel 13, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.897e+05 / 41
p0 -4557 ± 8.337
p1 60.22 ± 0.02635
p2 3.62e+04 ± 60.79
p3 51.27 ± 0.0923
p4 -66.85 ± 0.2979
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 10, Channel 13, Enable 5, DAC=1600, ADC Noise vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 10, Channel 13, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
0
50
100
150
200
250

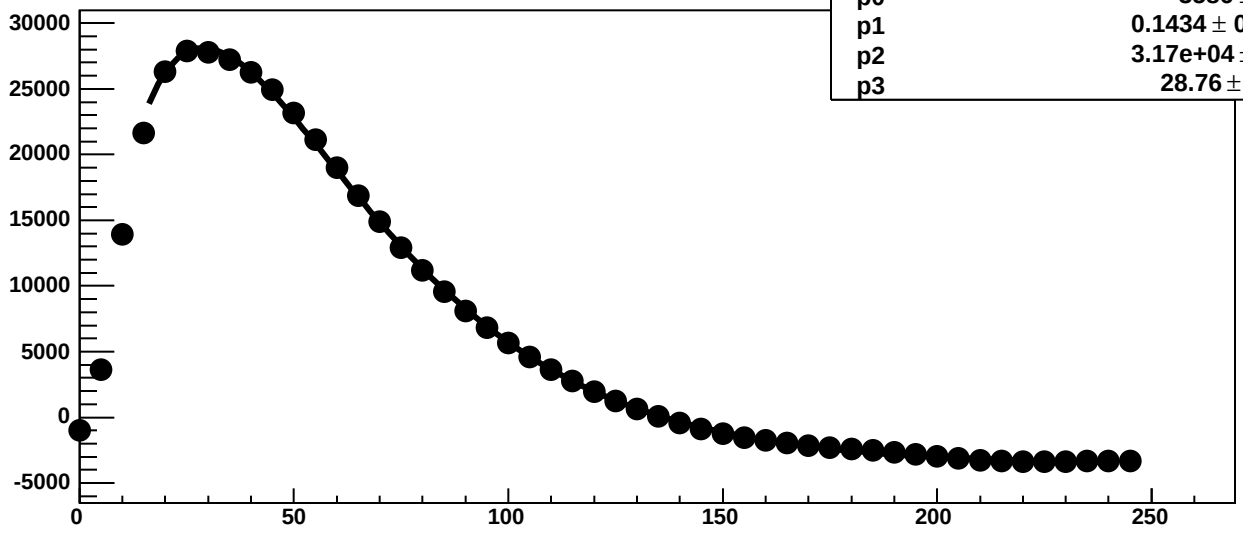

Chip 10, Channel 14, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.305e+05 / 41
p0 3088 ± 37.31
p1 -259.9 ± 0.7225
p2 -1.732e+04 ± 23.21
p3 -8.249 ± 2.419
p4 52.96 ± 0.03823
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250


Chip 10, Channel 14, Enable 0, DAC=1600, ADC Noise vs Hold
200
180
160
140
120
100
80
60
0
50
100
150
200
250

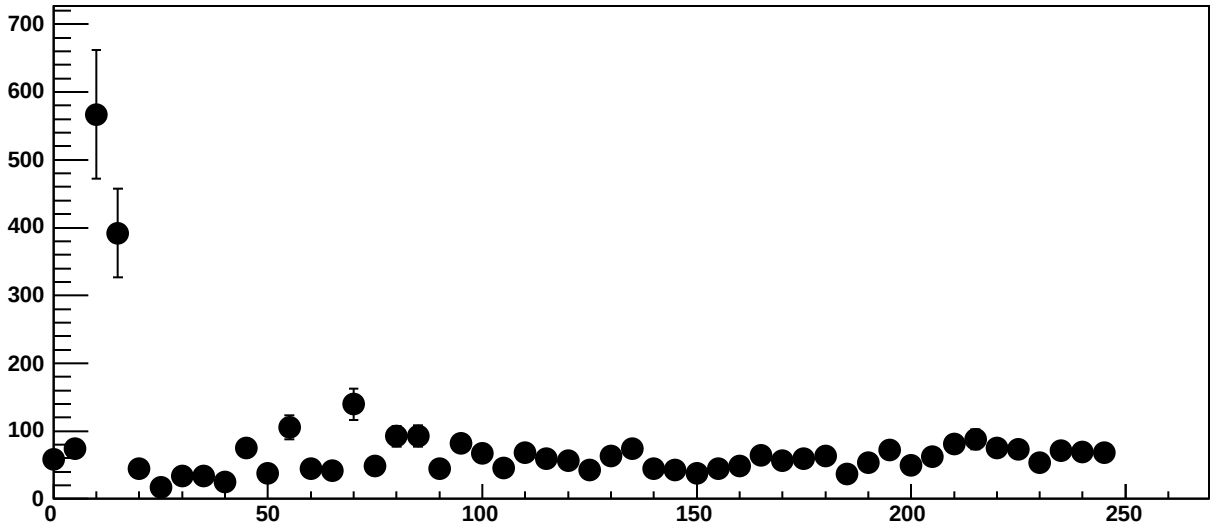

Chip 10, Channel 14, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

Chip 10, Channel 14, Enable 1!, DAC=1600, ADC Mean vs Hold

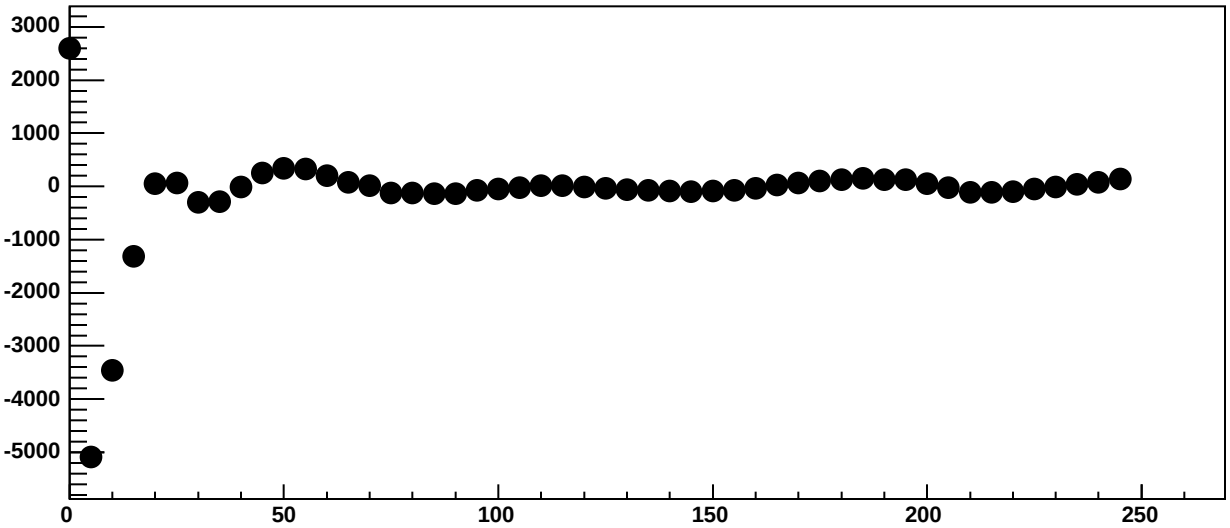
χ^2 / ndf	7560 / 42
p0	-3586 ± 3.862
p1	0.1434 ± 0.01973
p2	3.17e+04 ± 4.213
p3	28.76 ± 0.0115

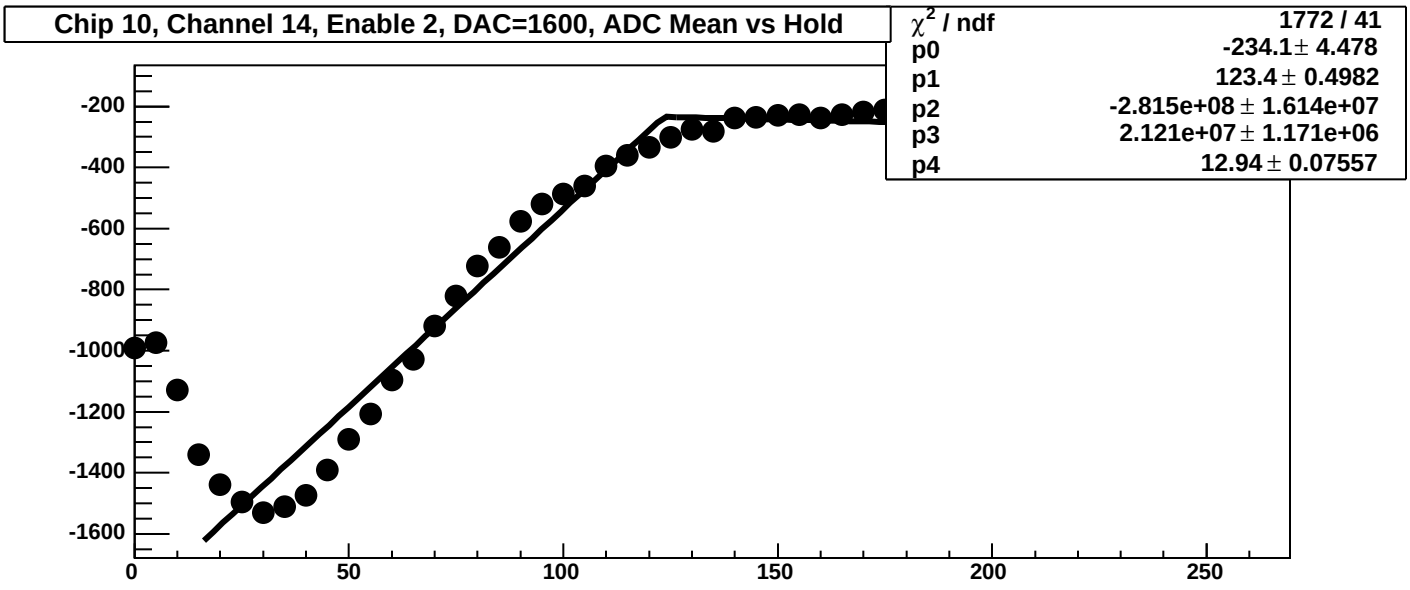


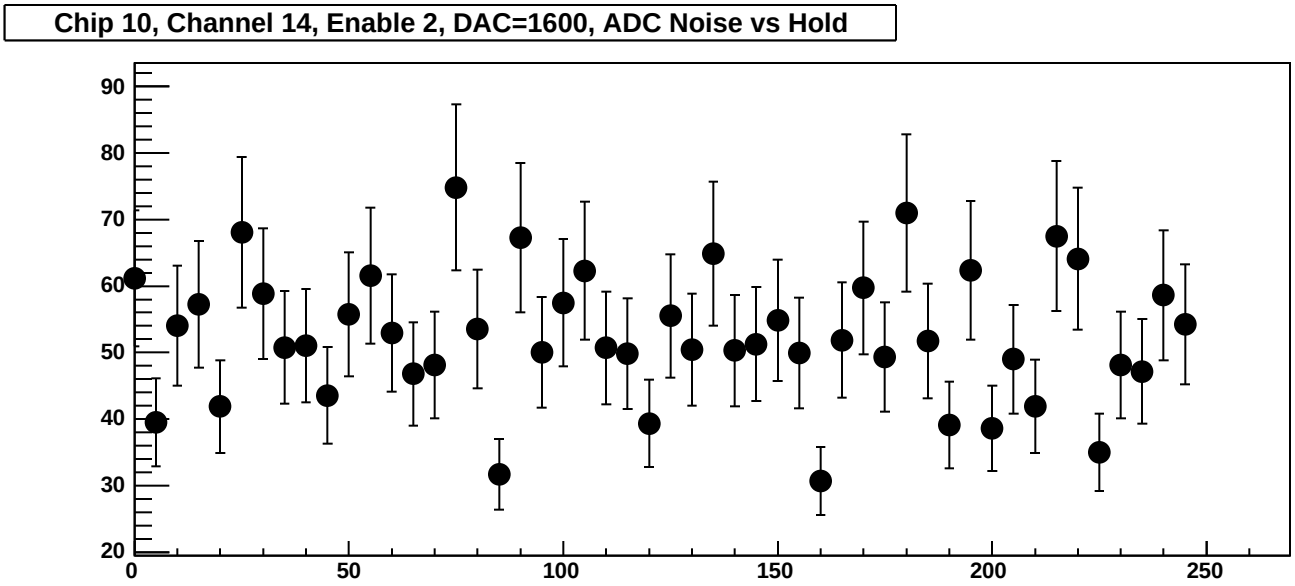
Chip 10, Channel 14, Enable 1!, DAC=1600, ADC Noise vs Hold

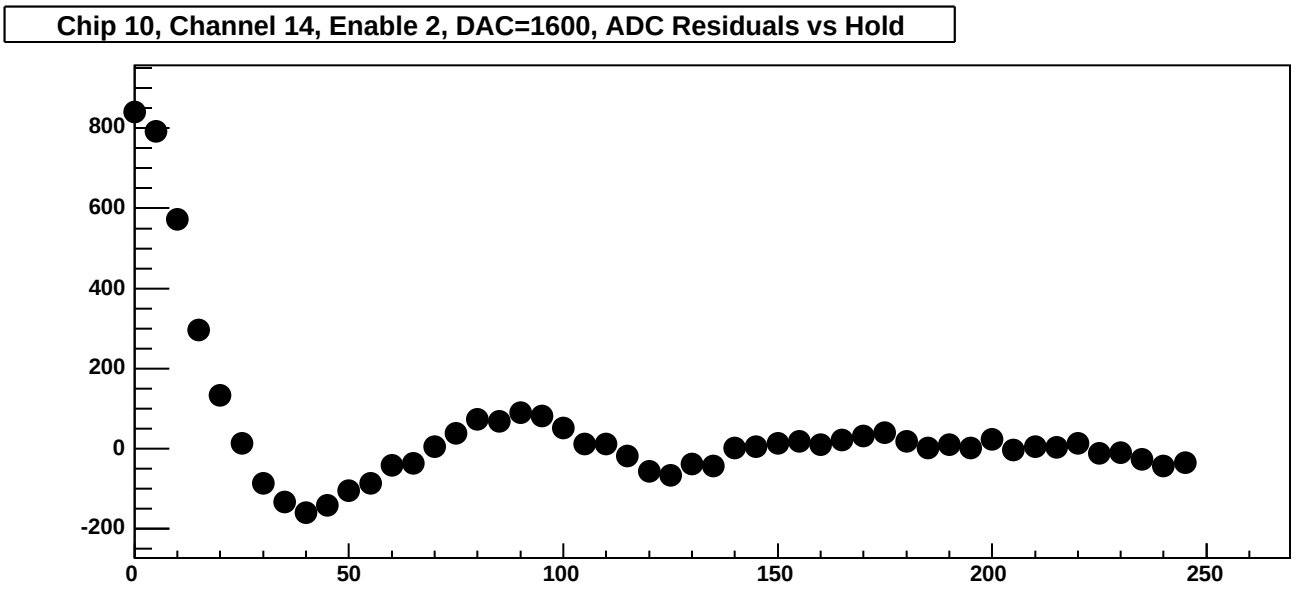


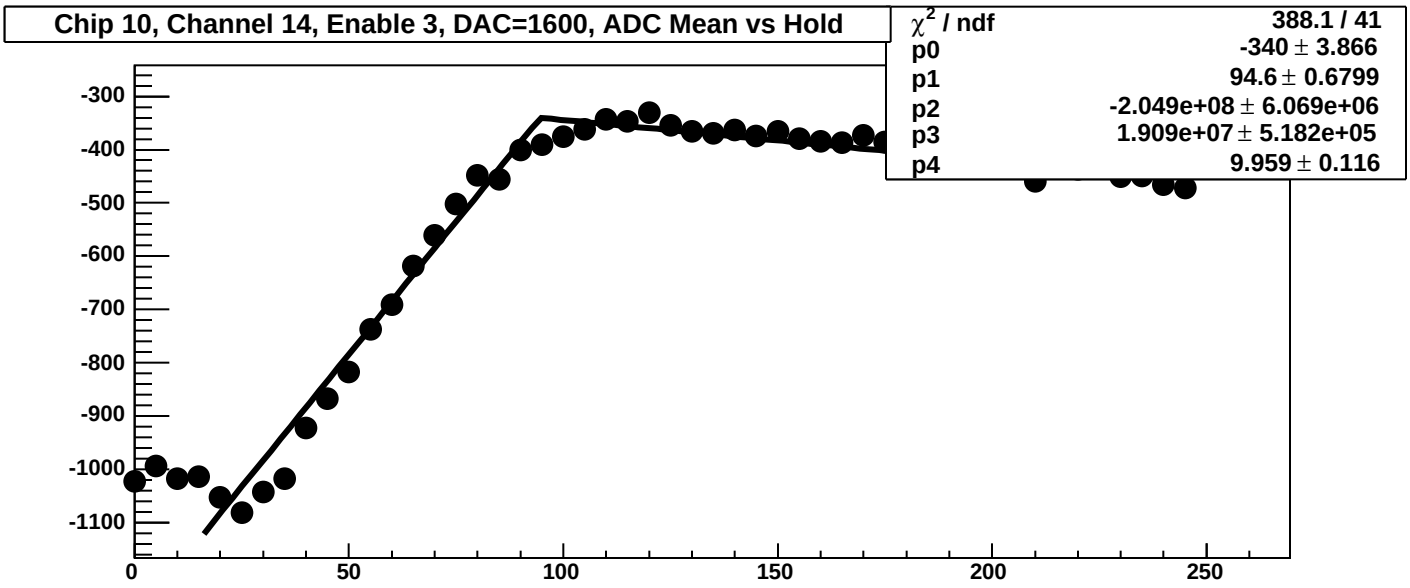
Chip 10, Channel 14, Enable 1!, DAC=1600, ADC Residuals vs Hold

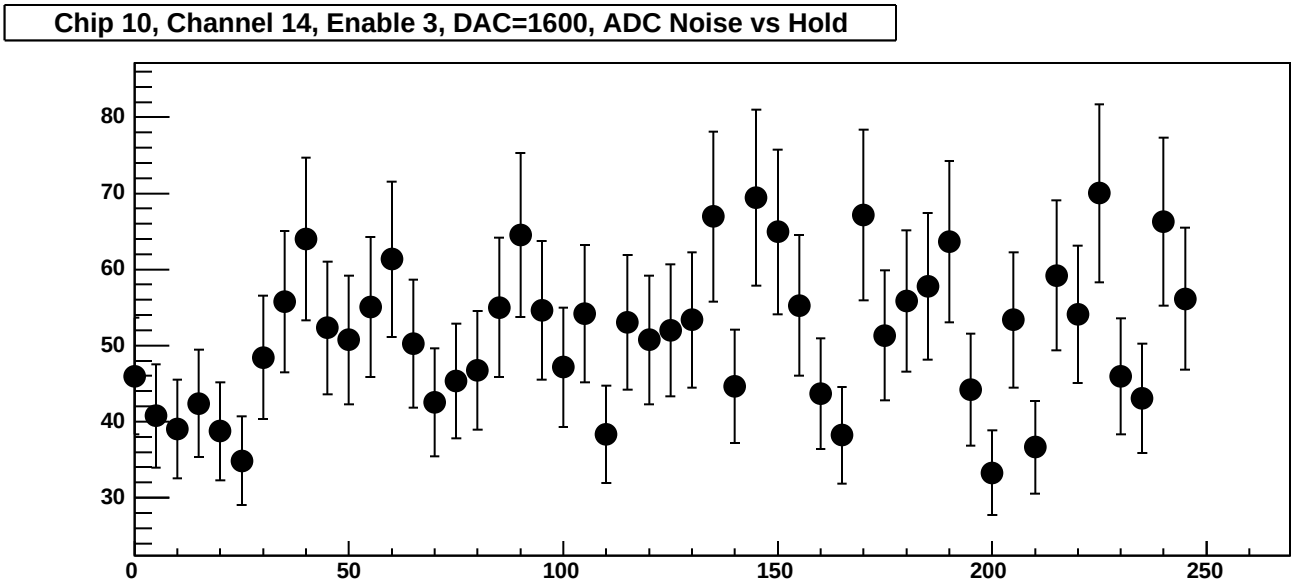


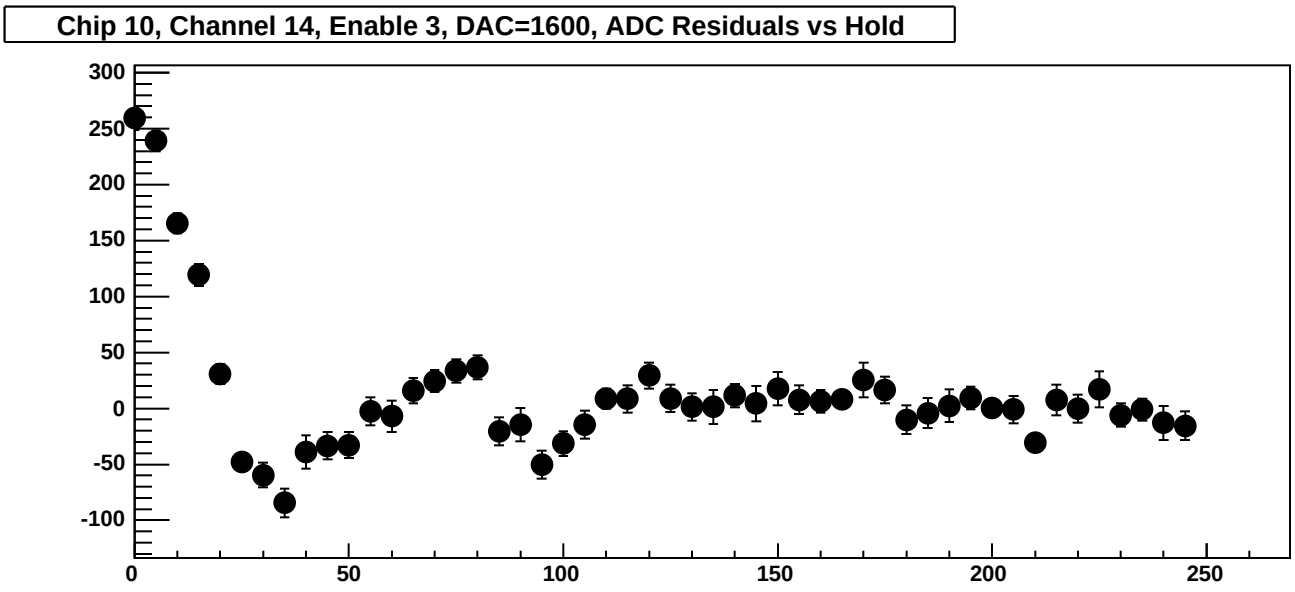

Chip 10, Channel 14, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 1772 / 41
p0 -234.1 ± 4.478
p1 123.4 ± 0.4982
p2 -2.815e+08 ± 1.614e+07
p3 2.121e+07 ± 1.171e+06
p4 12.94 ± 0.07557
-200
-400
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250

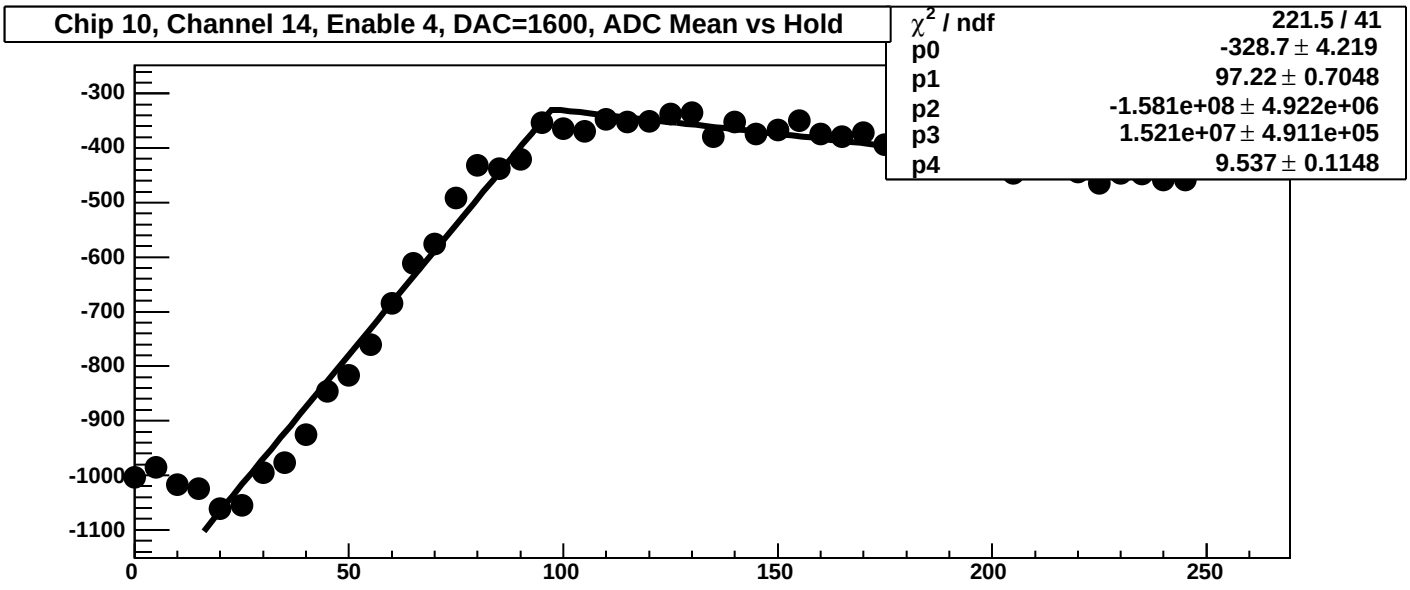

Chip 10, Channel 14, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
20
0
50
100
150
200
250

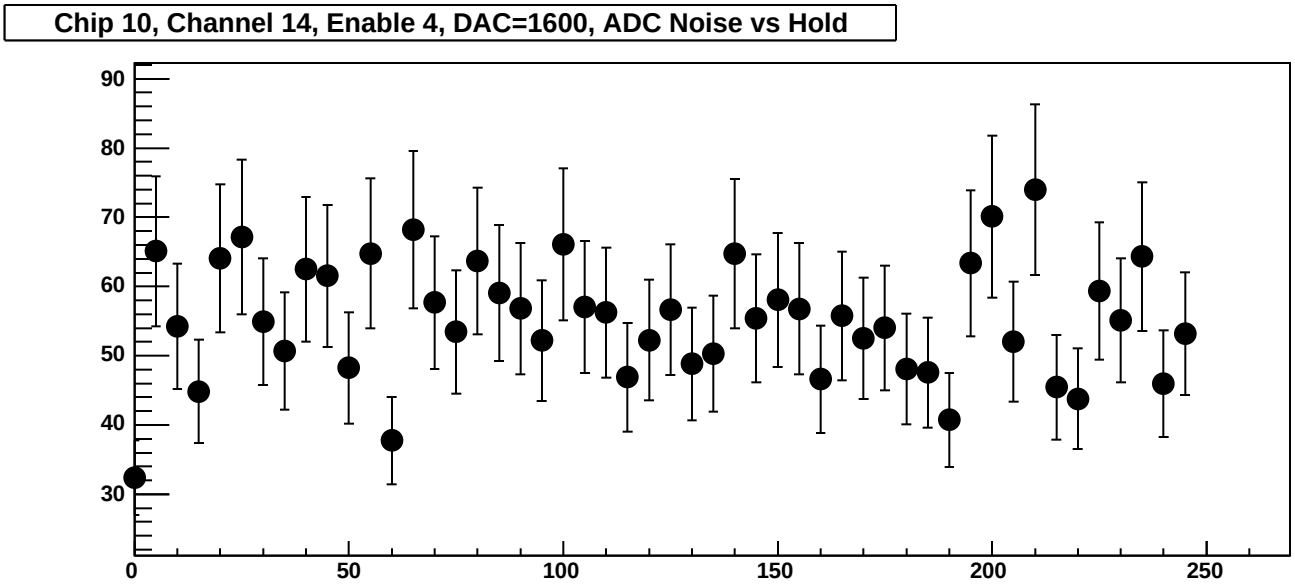

Chip 10, Channel 14, Enable 2, DAC=1600, ADC Residuals vs Hold
800
600
400
200
0
-200
0
50
100
150
200
250

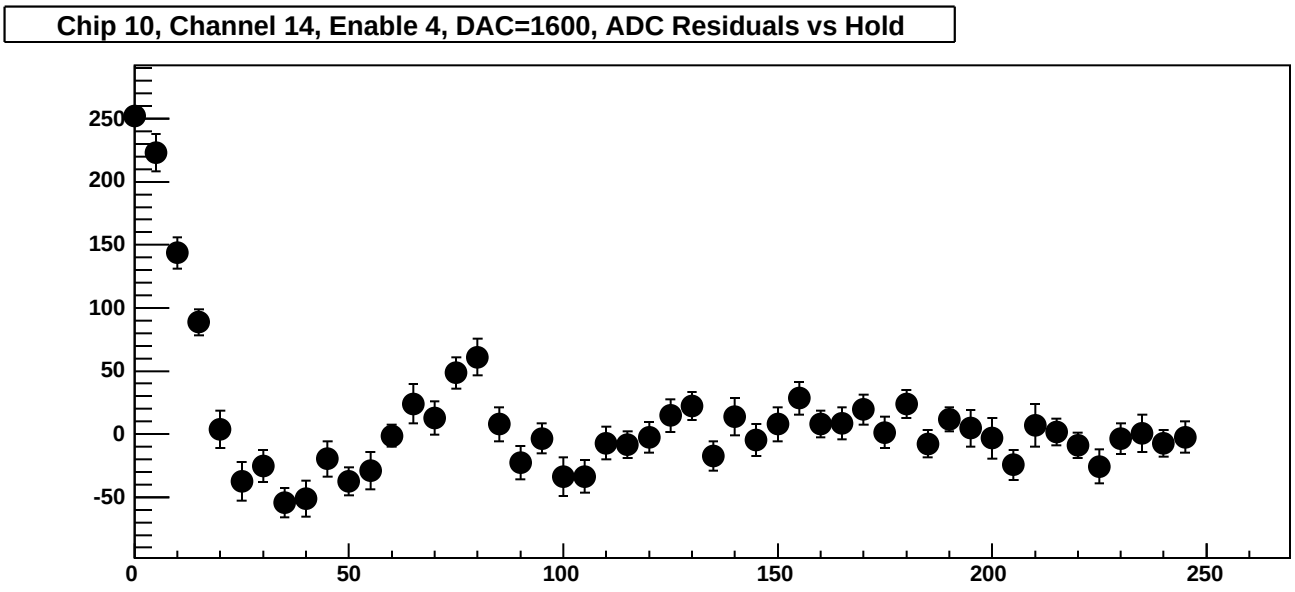

Chip 10, Channel 14, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 388.1 / 41
p0 -340 ± 3.866
p1 94.6 ± 0.6799
p2 -2.049e+08 ± 6.069e+06
p3 1.909e+07 ± 5.182e+05
p4 9.959 ± 0.116
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
50
100
150
200
250

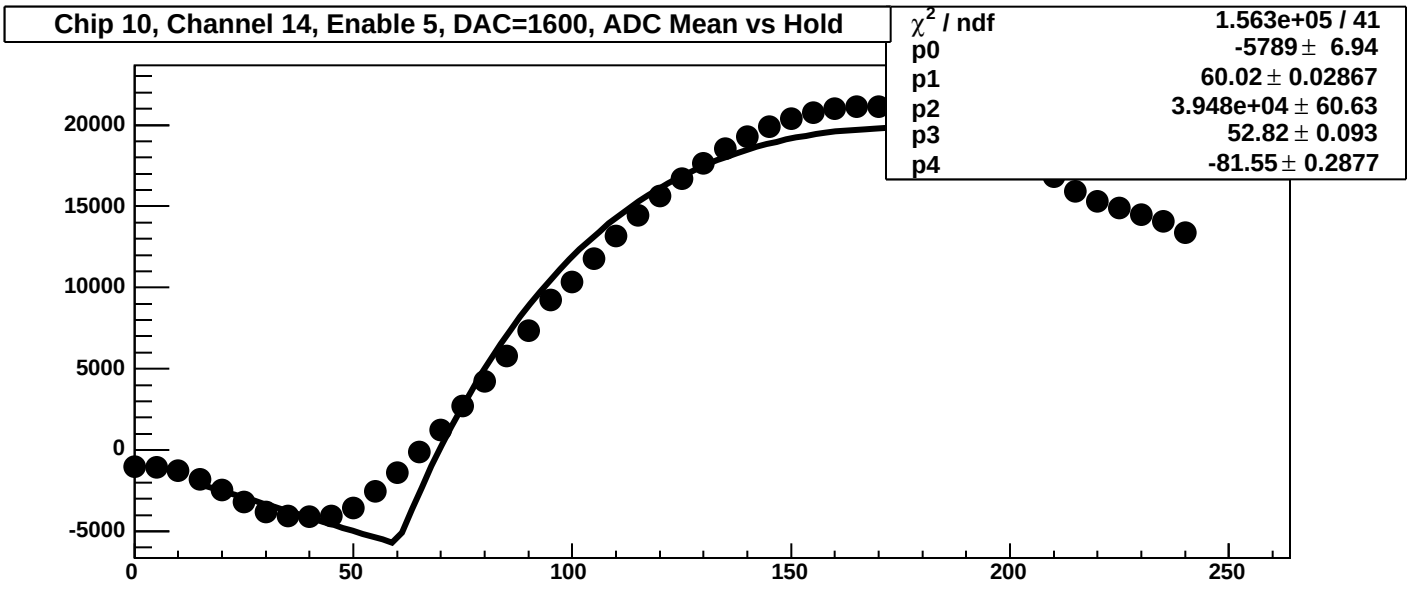

Chip 10, Channel 14, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

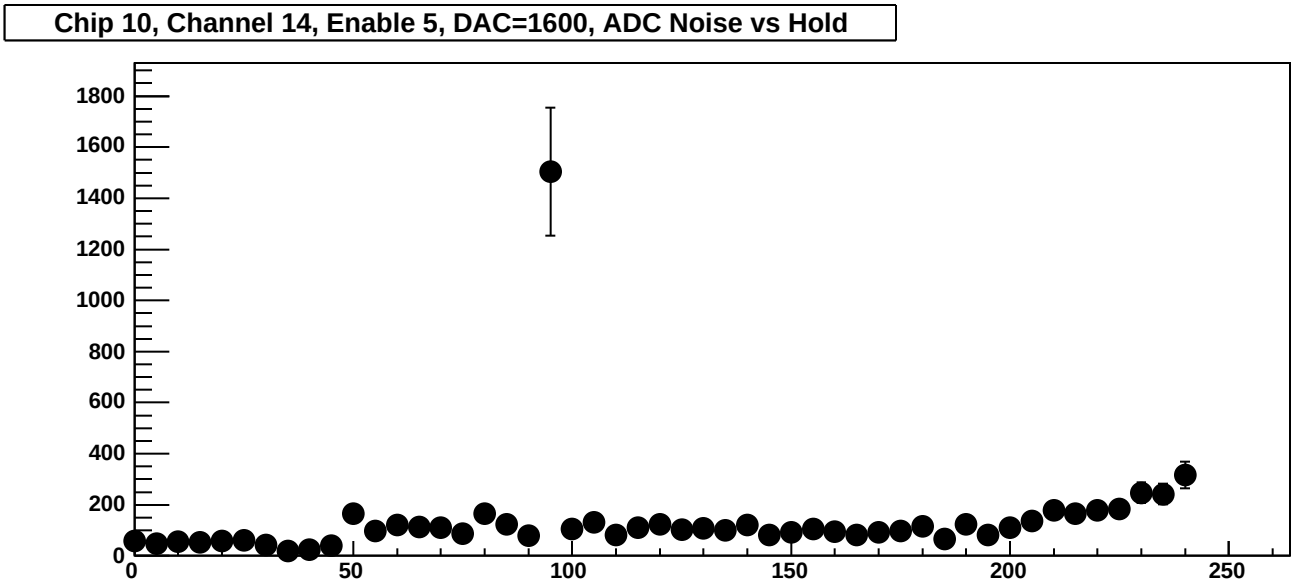

Chip 10, Channel 14, Enable 3, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

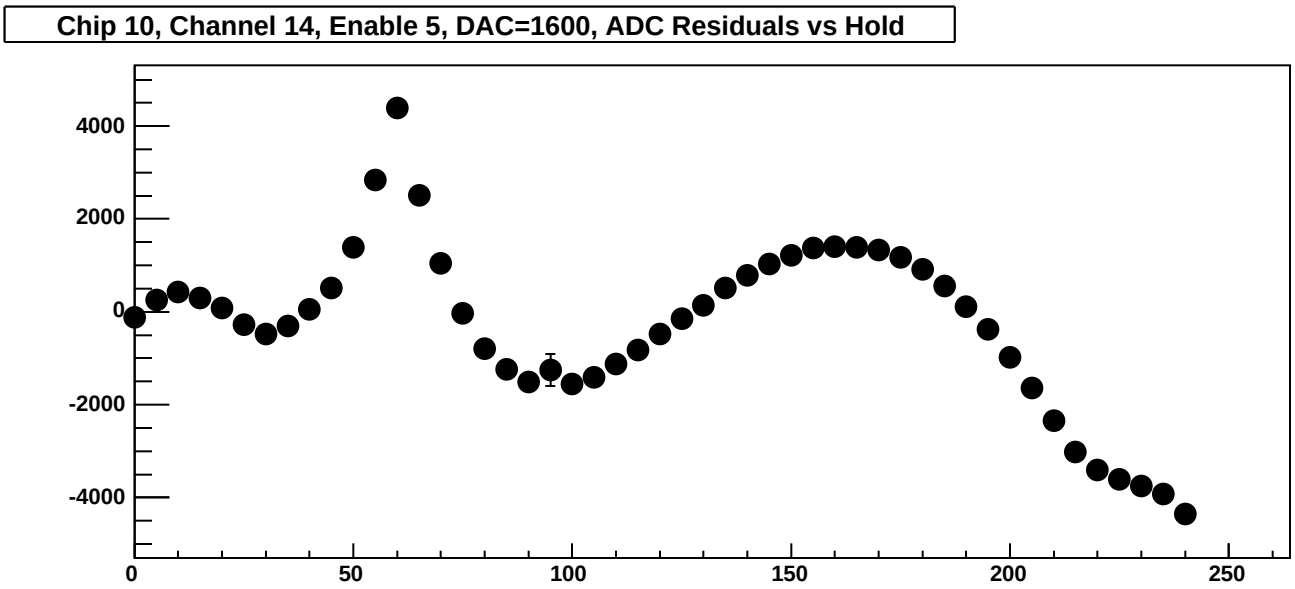

Chip 10, Channel 14, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 221.5 / 41
p0 -328.7 ± 4.219
p1 97.22 ± 0.7048
p2 -1.581e+08 ± 4.922e+06
p3 1.521e+07 ± 4.911e+05
p4 9.537 ± 0.1148
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
50
100
150
200
250


Chip 10, Channel 14, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

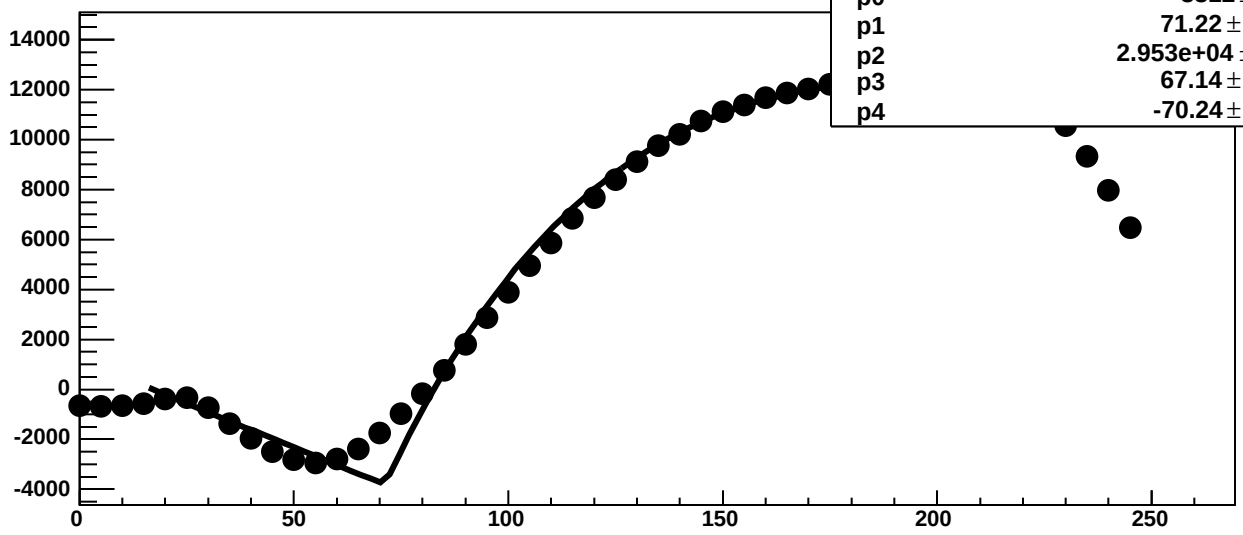

Chip 10, Channel 14, Enable 4, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
0
50
100
150
200
250


Chip 10, Channel 14, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.563e+05 / 41
p0 -5789 ± 6.94
p1 60.02 ± 0.02867
p2 3.948e+04 ± 60.63
p3 52.82 ± 0.093
p4 -81.55 ± 0.2877
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

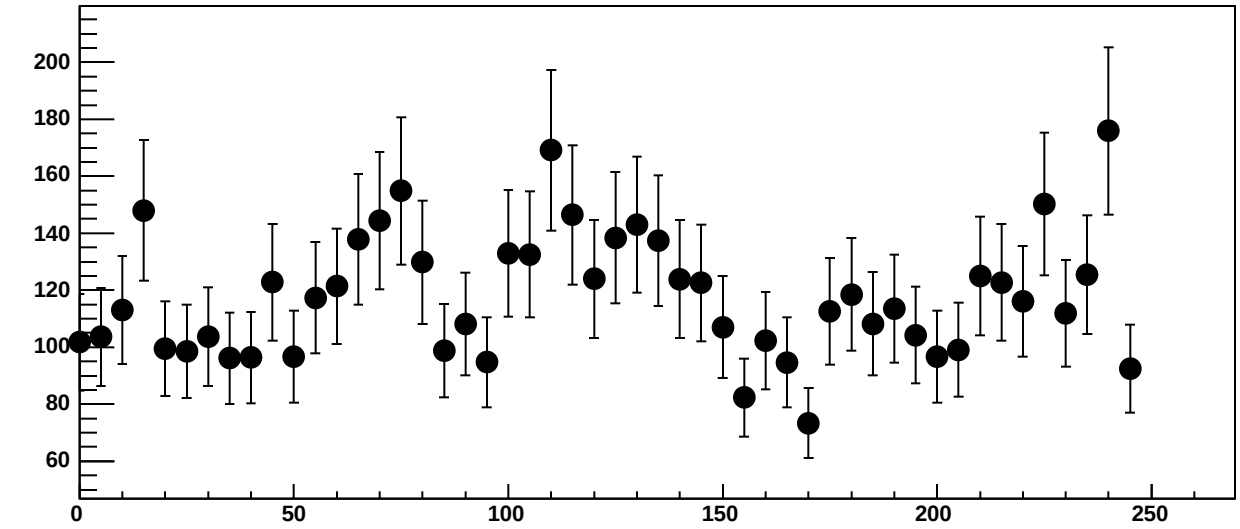

Chip 10, Channel 14, Enable 5, DAC=1600, ADC Noise vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 10, Channel 14, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
0
50
100
150
200
250

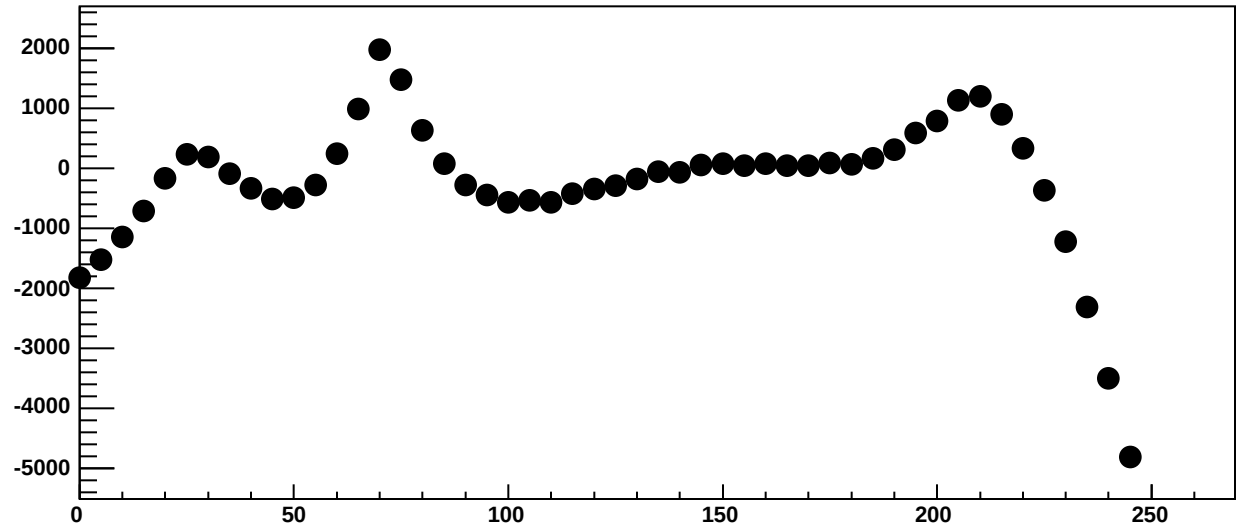
Chip 10, Channel 15, Enable 0, DAC=1600, ADC Mean vs Hold

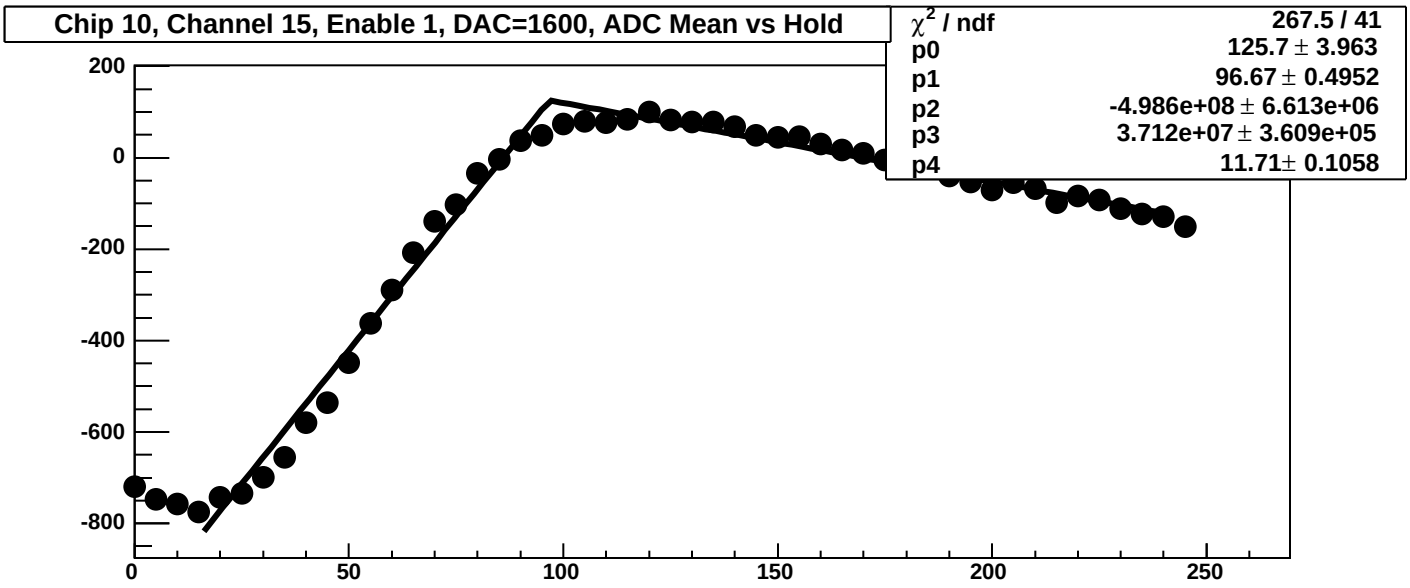


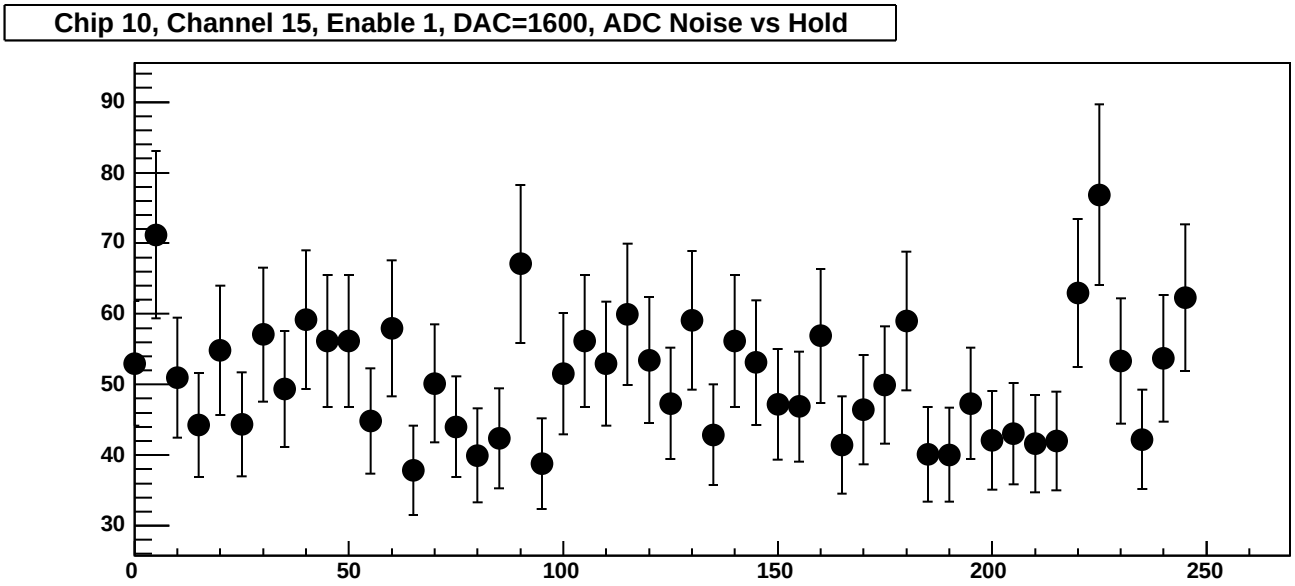
Chip 10, Channel 15, Enable 0, DAC=1600, ADC Noise vs Hold

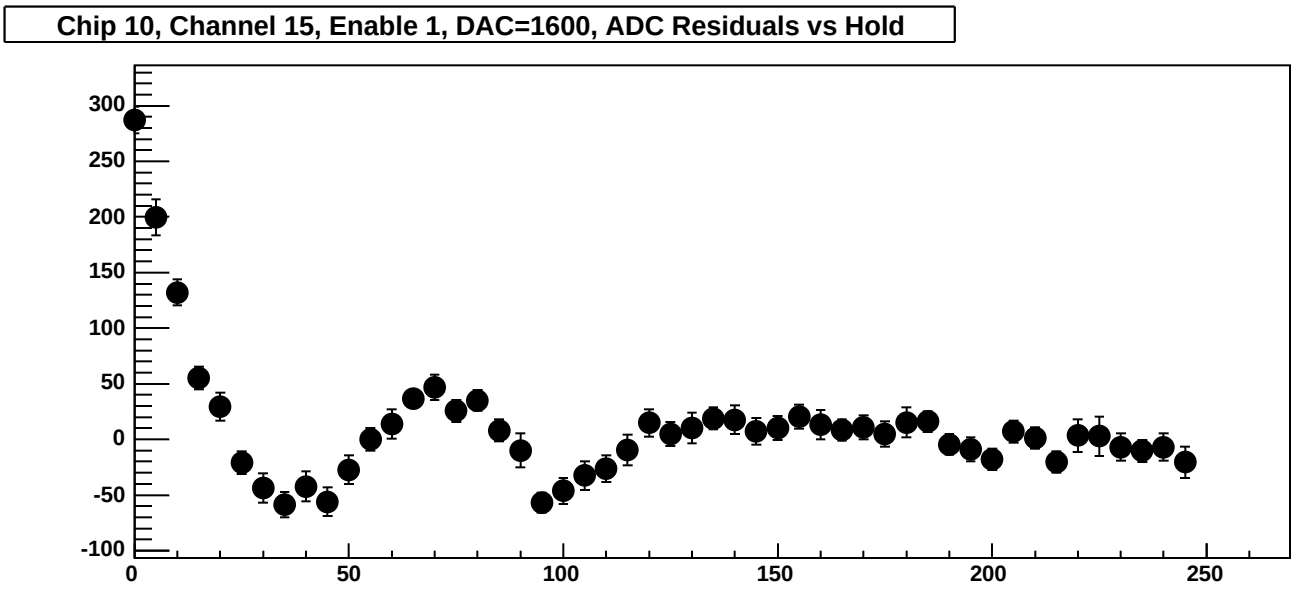


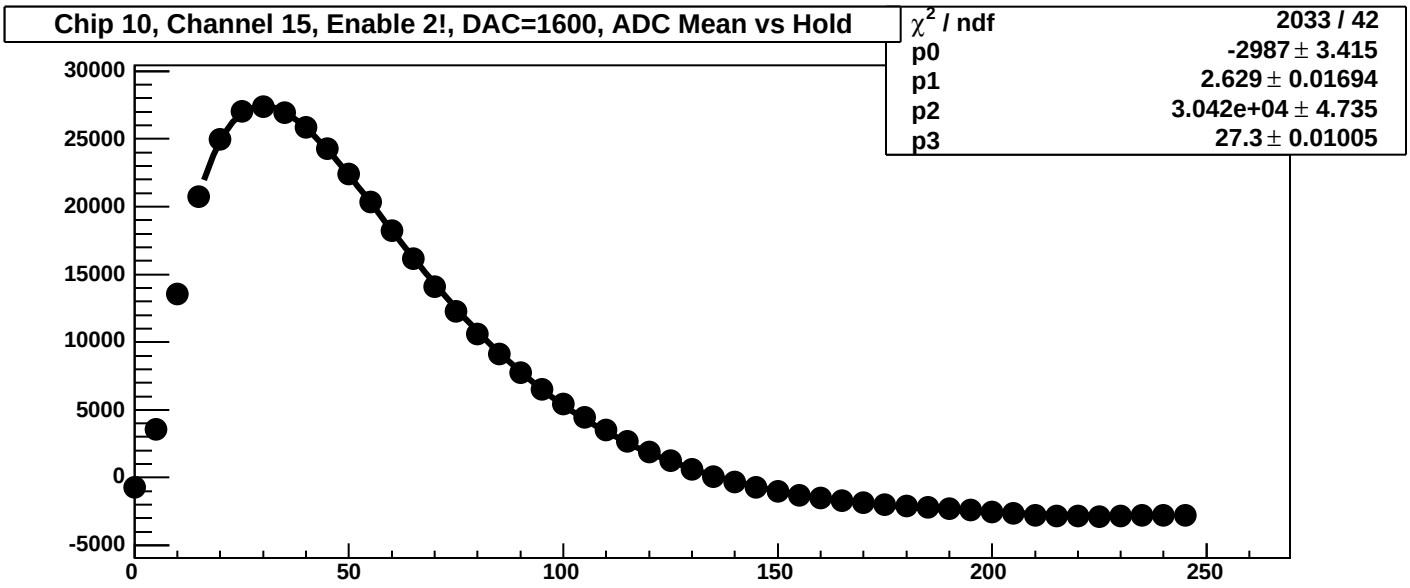
Chip 10, Channel 15, Enable 0, DAC=1600, ADC Residuals vs Hold

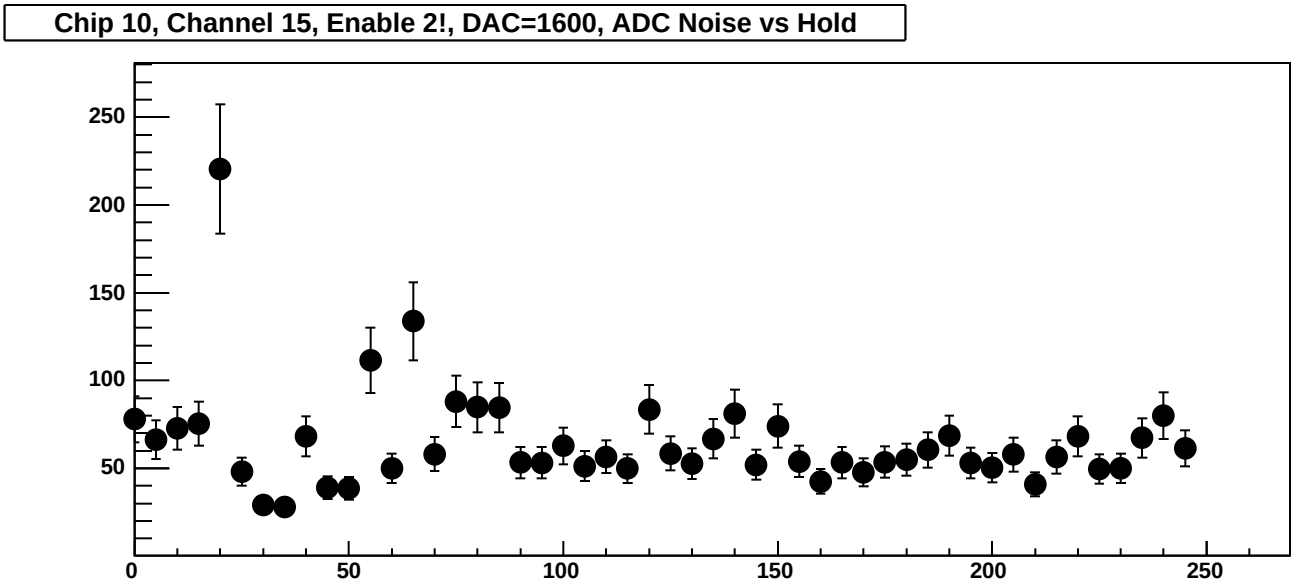


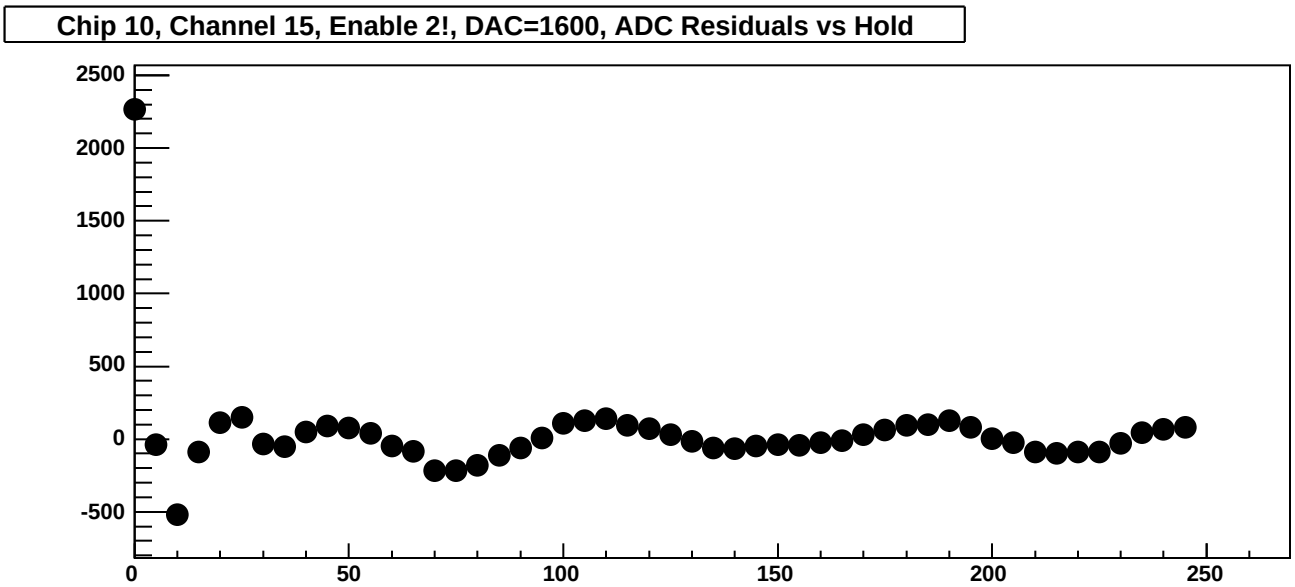

Chip 10, Channel 15, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 267.5 / 41
p0 125.7 ± 3.963
p1 96.67 ± 0.4952
p2 -4.986e+08 ± 6.613e+06
p3 3.712e+07 ± 3.609e+05
p4 11.71 ± 0.1058
200
0
-200
-400
-600
-800
0
50
100
150
200
250


Chip 10, Channel 15, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

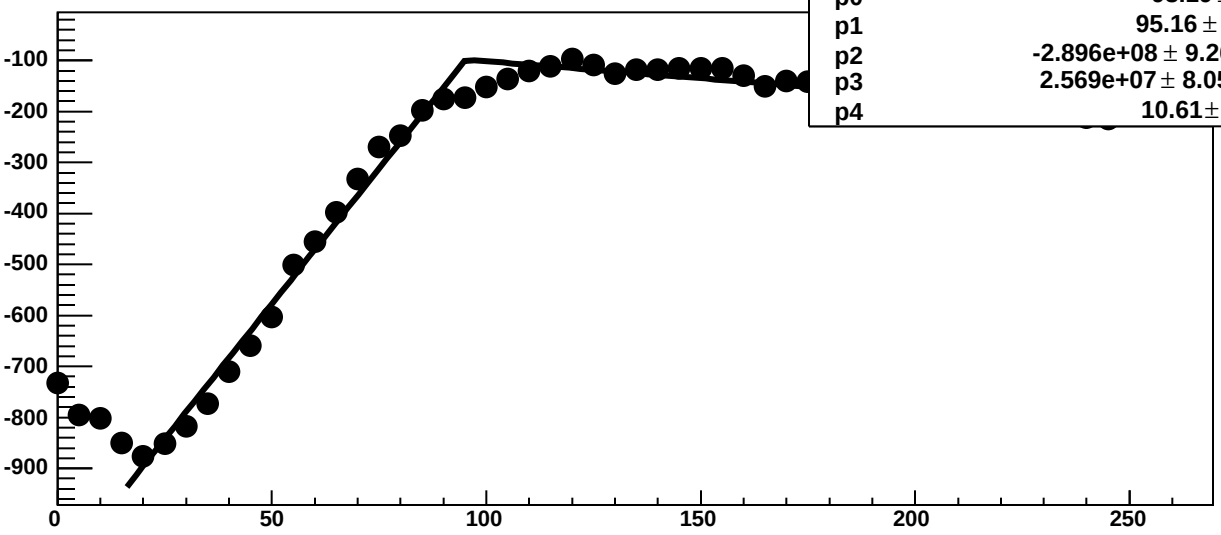

Chip 10, Channel 15, Enable 1, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250


Chip 10, Channel 15, Enable 2!, DAC=1600, ADC Mean vs Hold
χ² / ndf 2033 / 42
p0 -2987 ± 3.415
p1 2.629 ± 0.01694
p2 3.042e+04 ± 4.735
p3 27.3 ± 0.01005
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

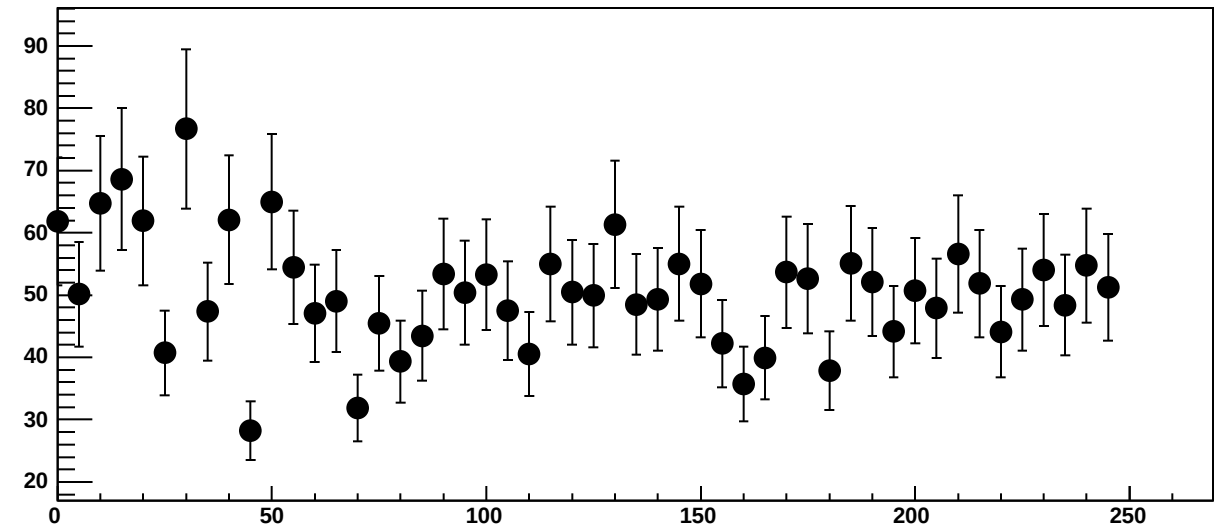

Chip 10, Channel 15, Enable 2!, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
50
100
150
200
250


Chip 10, Channel 15, Enable 2!, DAC=1600, ADC Residuals vs Hold
2500
2000
1500
1000
500
0
-500
0
50
100
150
200
250

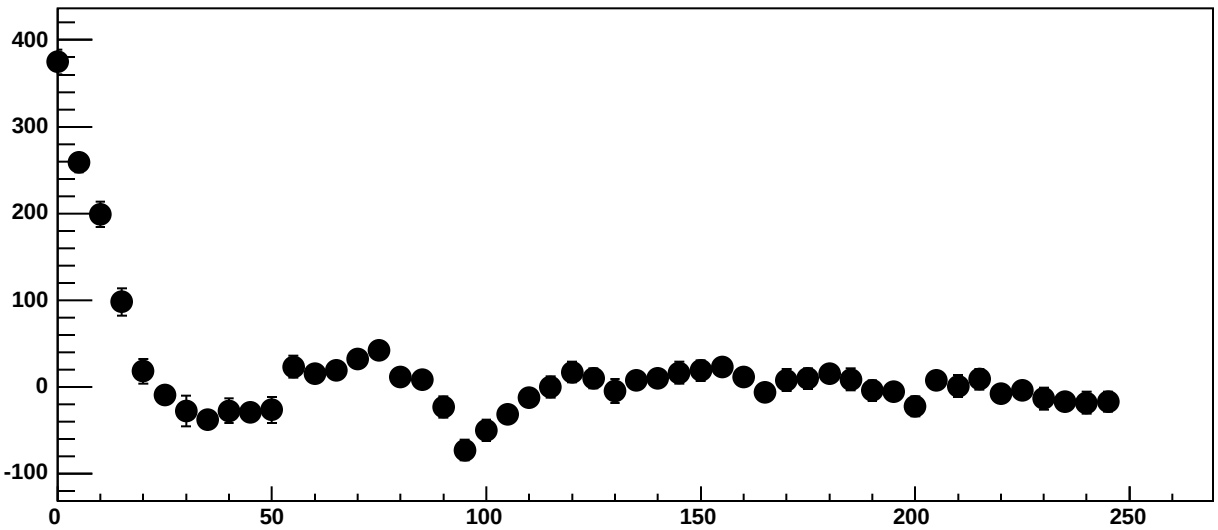
Chip 10, Channel 15, Enable 3, DAC=1600, ADC Mean vs Hold

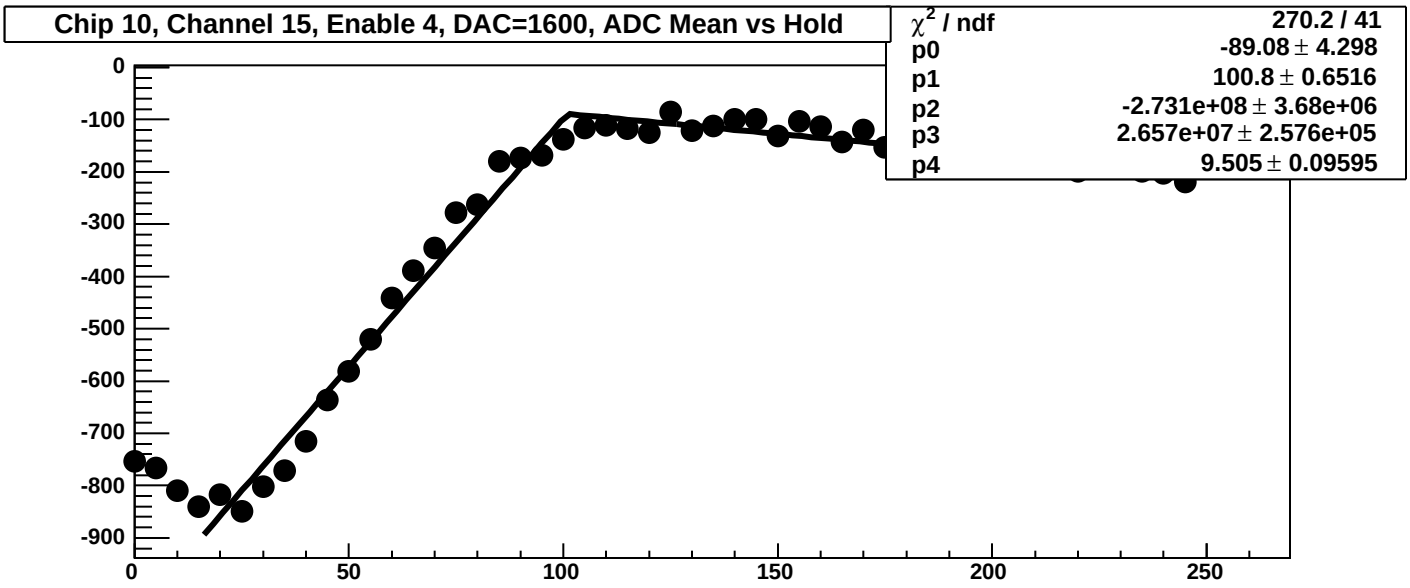


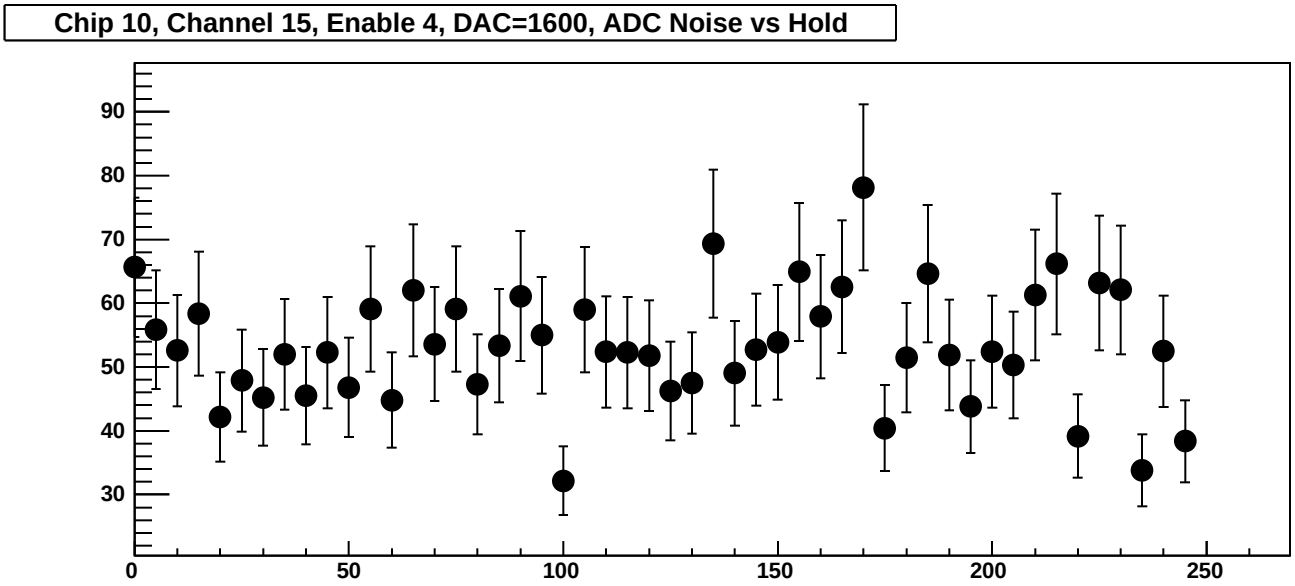
Chip 10, Channel 15, Enable 3, DAC=1600, ADC Noise vs Hold

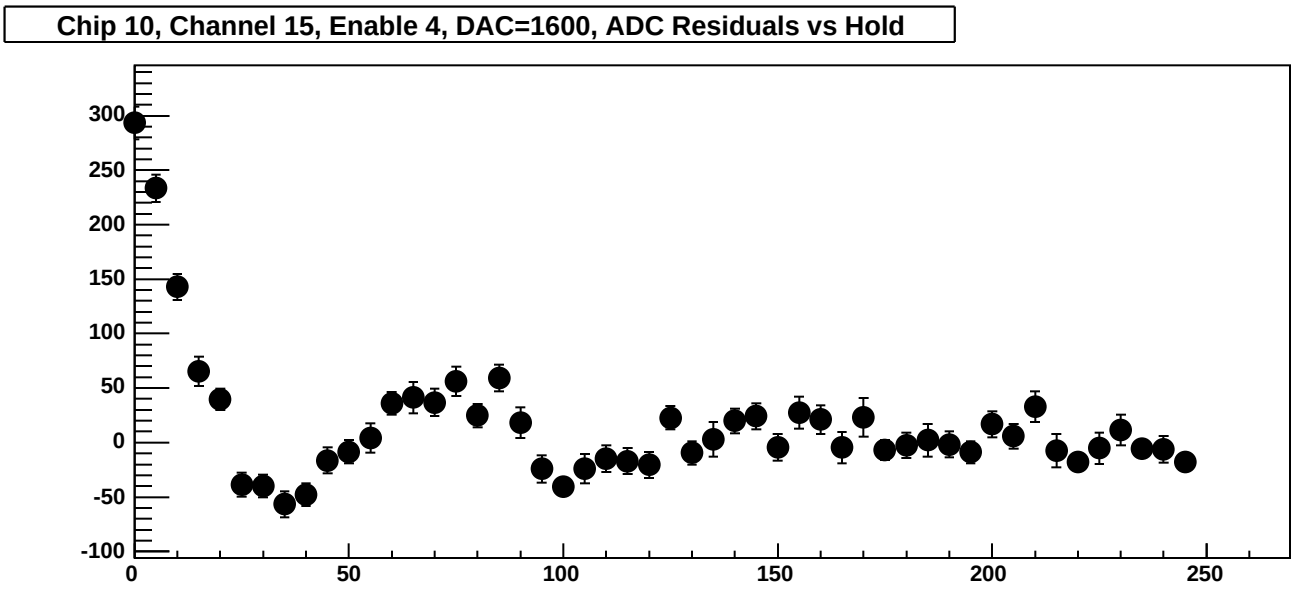


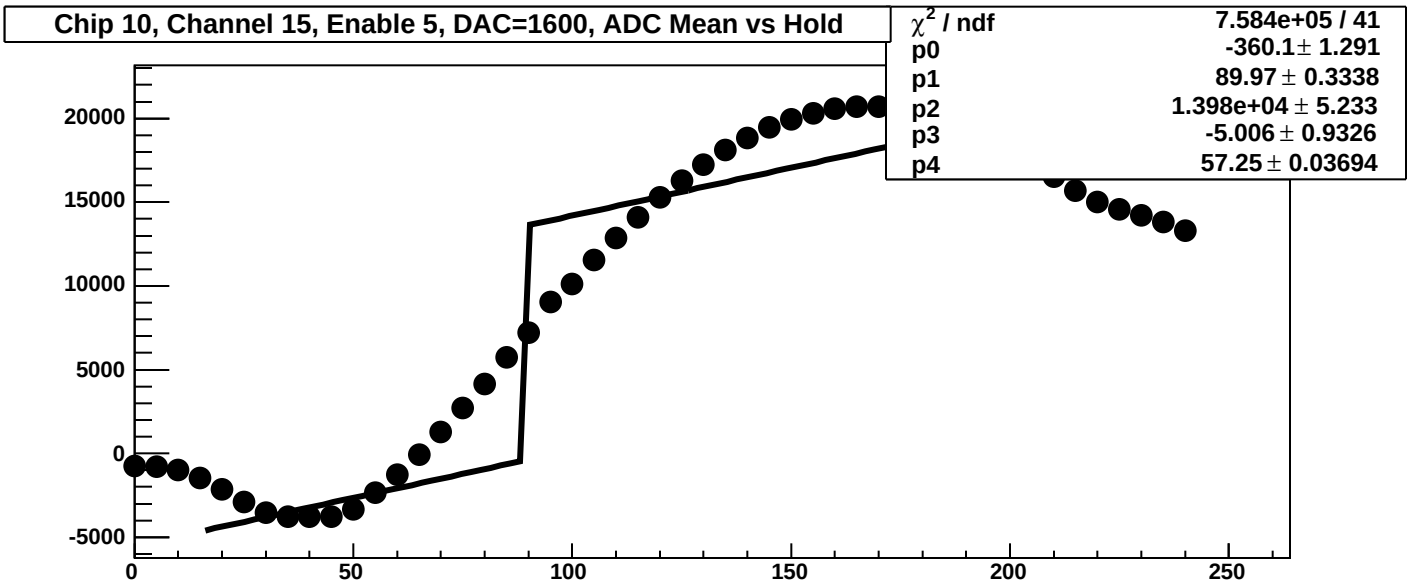
Chip 10, Channel 15, Enable 3, DAC=1600, ADC Residuals vs Hold

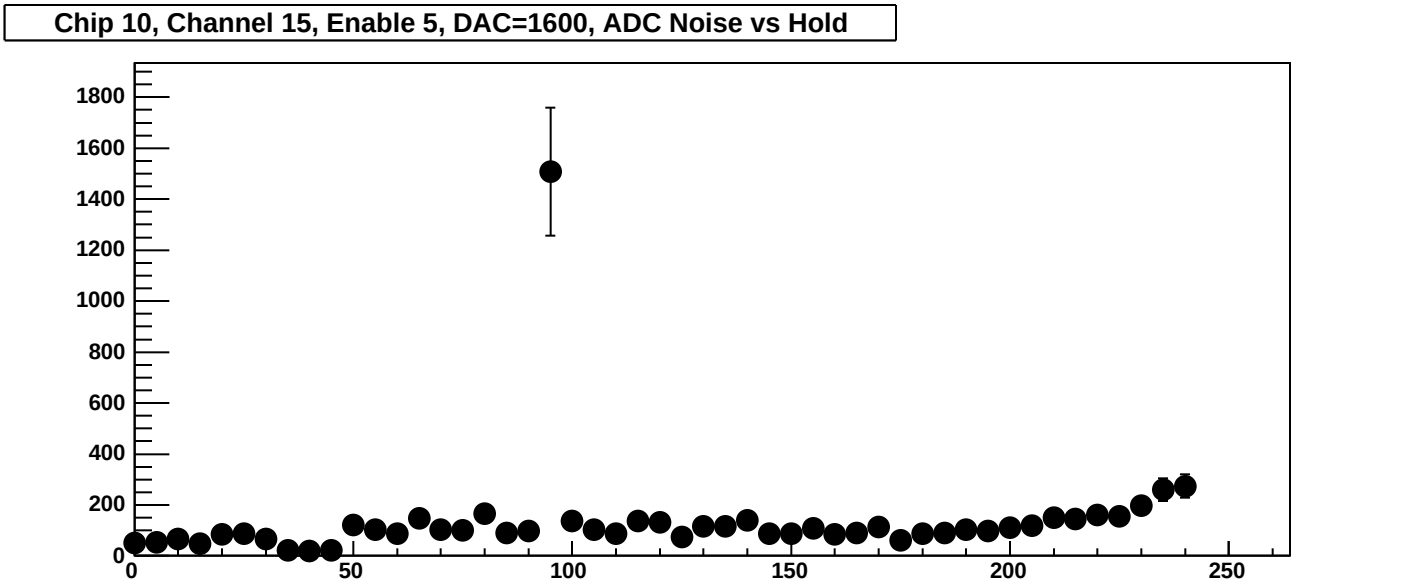


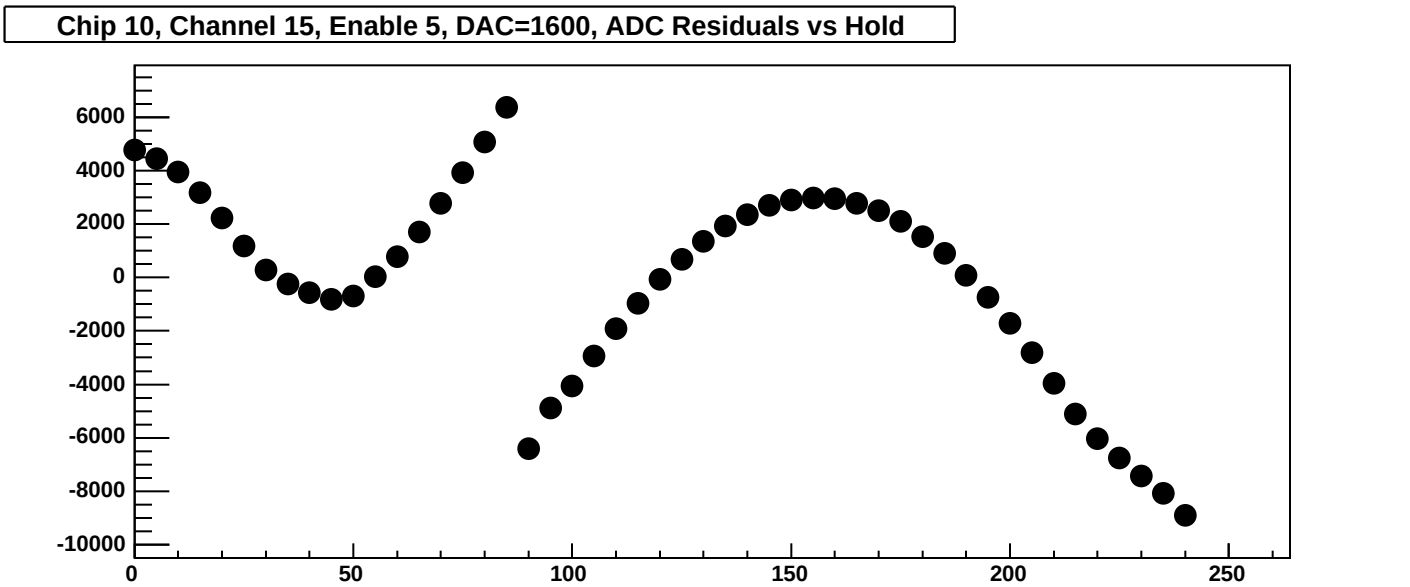

Chip 10, Channel 15, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 270.2 / 41
p0 -89.08 ± 4.298
p1 100.8 ± 0.6516
p2 -2.731e+08 ± 3.68e+06
p3 2.657e+07 ± 2.576e+05
p4 9.505 ± 0.09595
0
-100
-200
-300
-400
-500
-600
-700
-800
-900
0
50
100
150
200
250

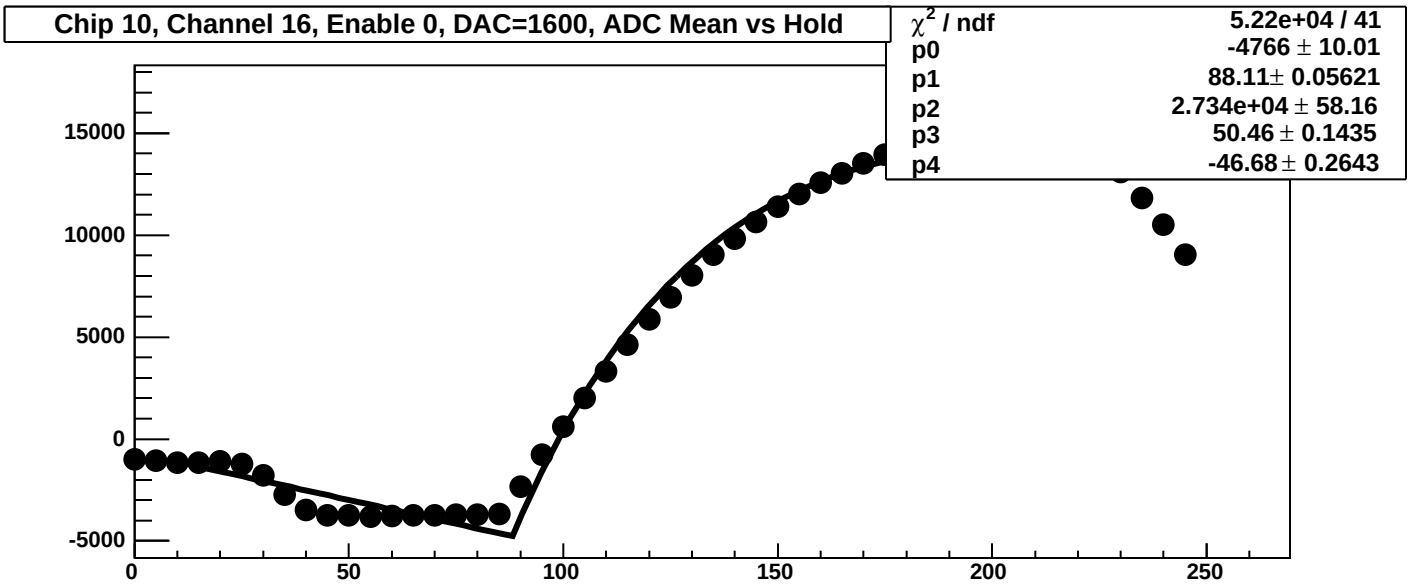

Chip 10, Channel 15, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

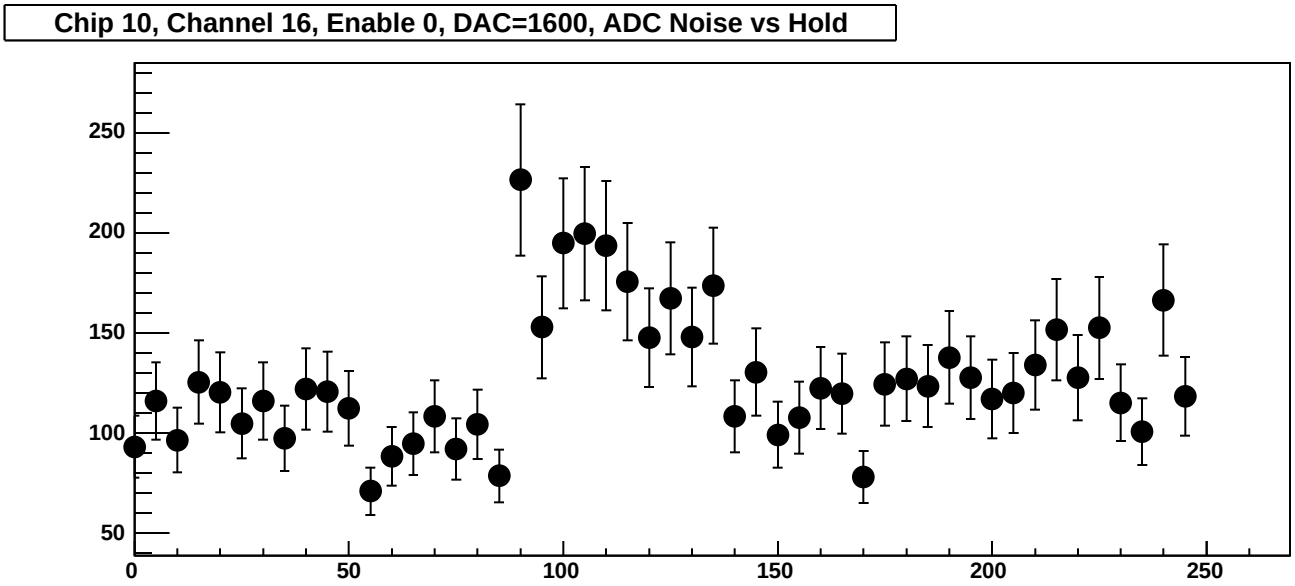

Chip 10, Channel 15, Enable 4, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

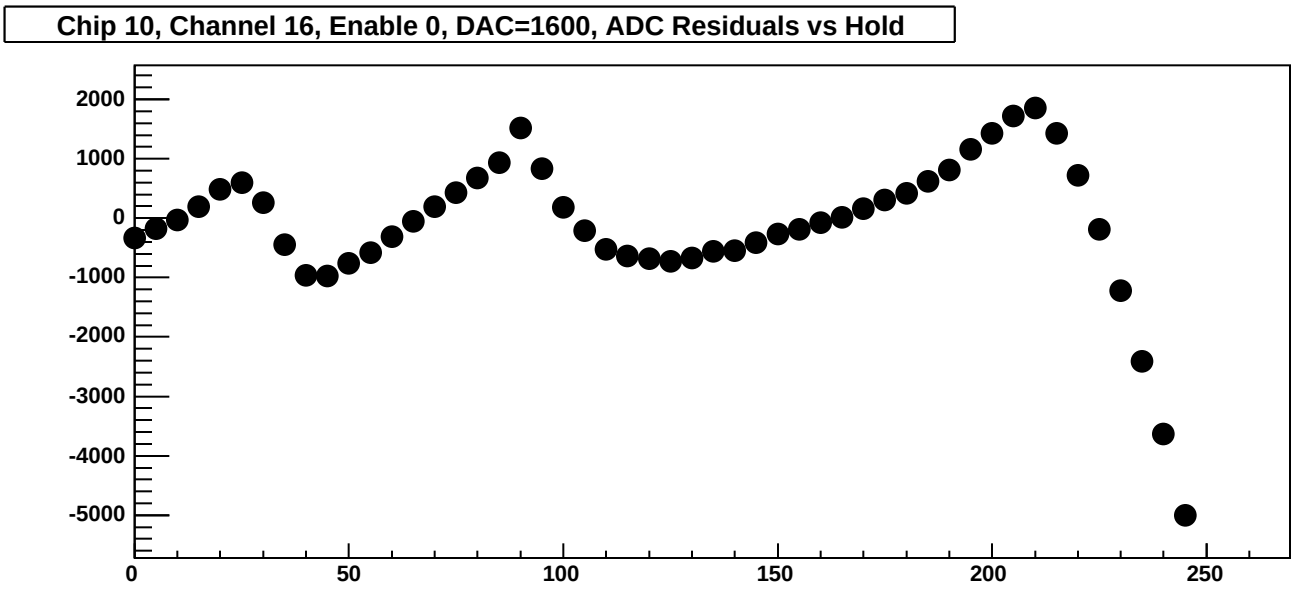

Chip 10, Channel 15, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.584e+05 / 41
p0 -360.1 ± 1.291
p1 89.97 ± 0.3338
p2 1.398e+04 ± 5.233
p3 -5.006 ± 0.9326
p4 57.25 ± 0.03694
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

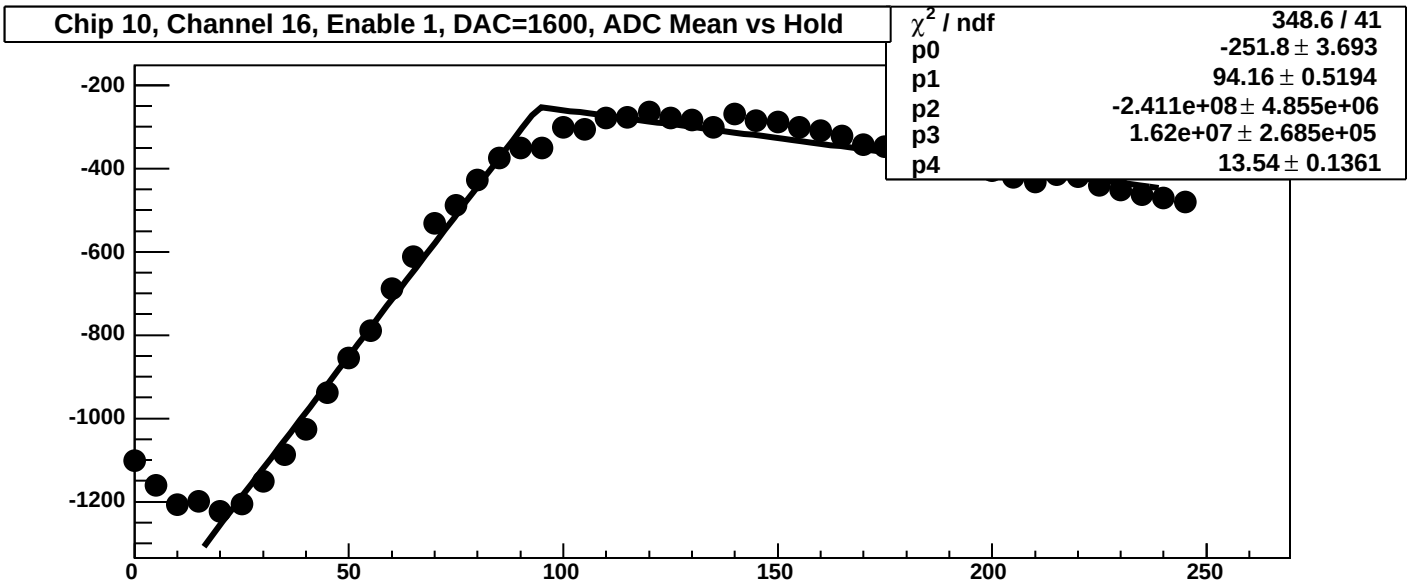

Chip 10, Channel 15, Enable 5, DAC=1600, ADC Noise vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

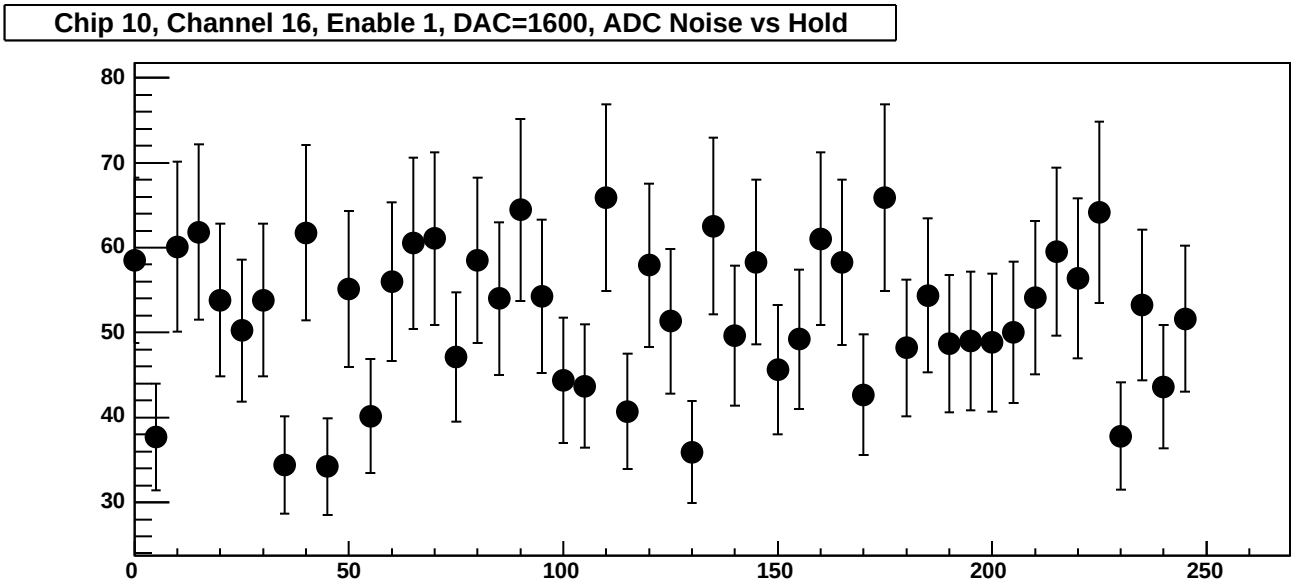

Chip 10, Channel 15, Enable 5, DAC=1600, ADC Residuals vs Hold
6000
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

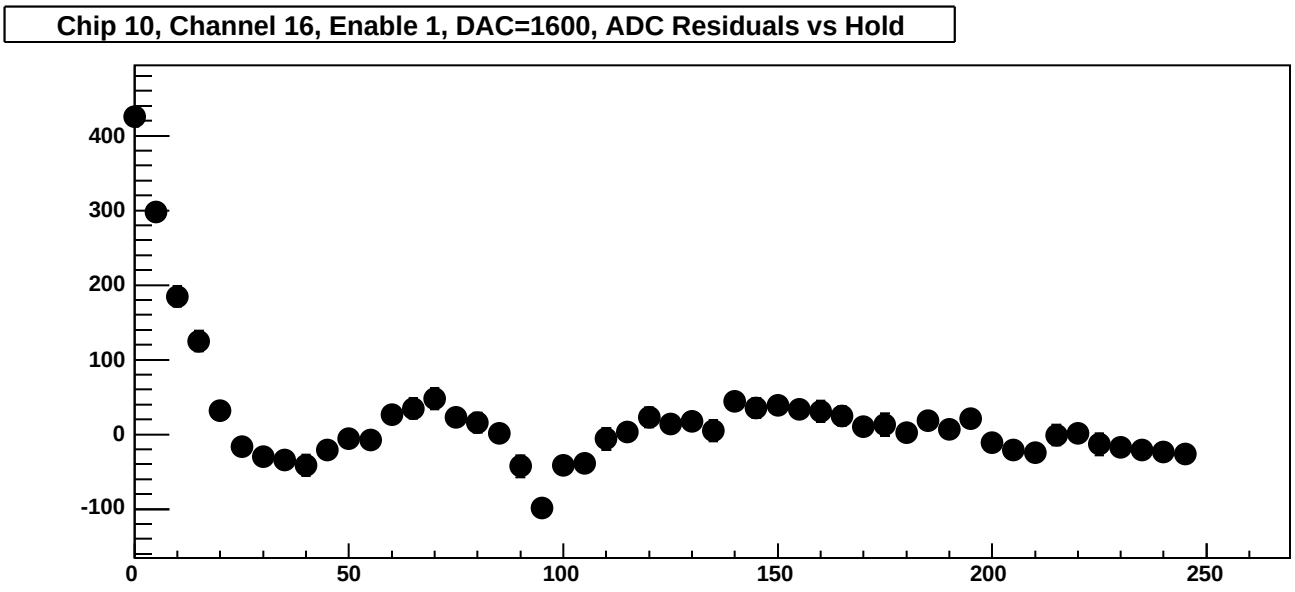

Chip 10, Channel 16, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.22e+04 / 41
p0 -4766 ± 10.01
p1 88.11± 0.05621
p2 2.734e+04 ± 58.16
p3 50.46 ± 0.1435
p4 -46.68 ± 0.2643
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 10, Channel 16, Enable 0, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
50
100
150
200
250

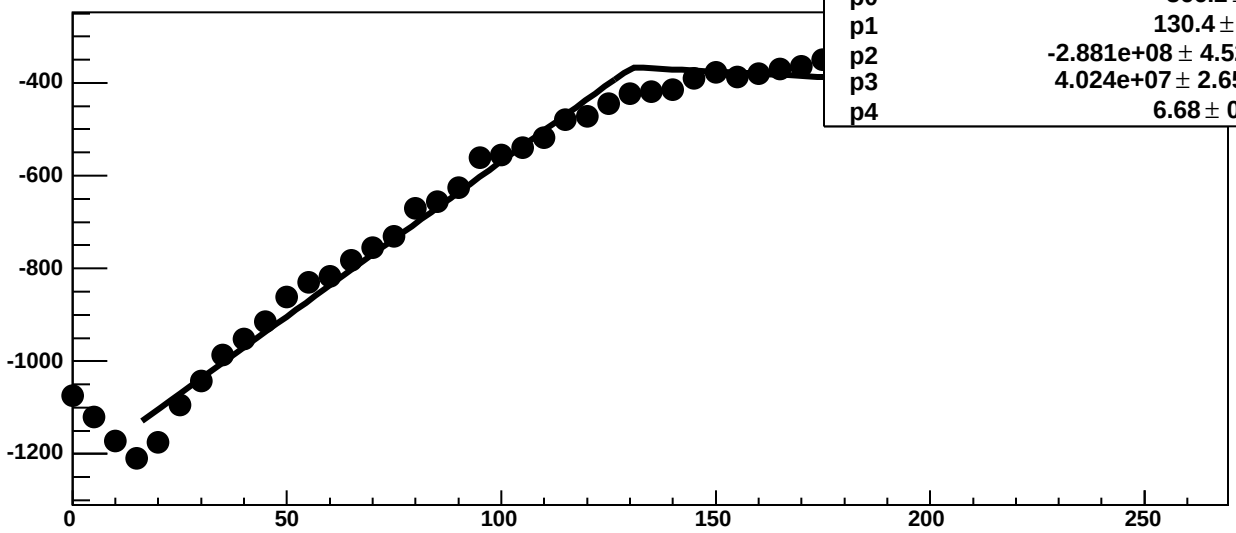

Chip 10, Channel 16, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250


Chip 10, Channel 16, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 348.6 / 41
p0 -251.8 ± 3.693
p1 94.16 ± 0.5194
p2 -2.411e+08 ± 4.855e+06
p3 1.62e+07 ± 2.685e+05
p4 13.54 ± 0.1361
-200
-400
-600
-800
-1000
-1200
0
50
100
150
200
250

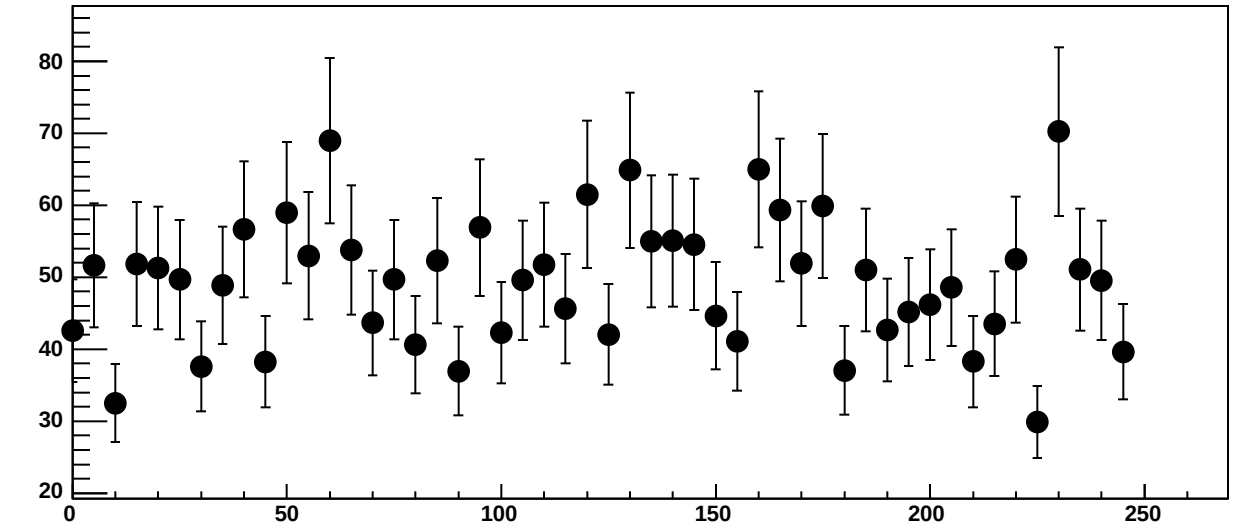

Chip 10, Channel 16, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 10, Channel 16, Enable 1, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

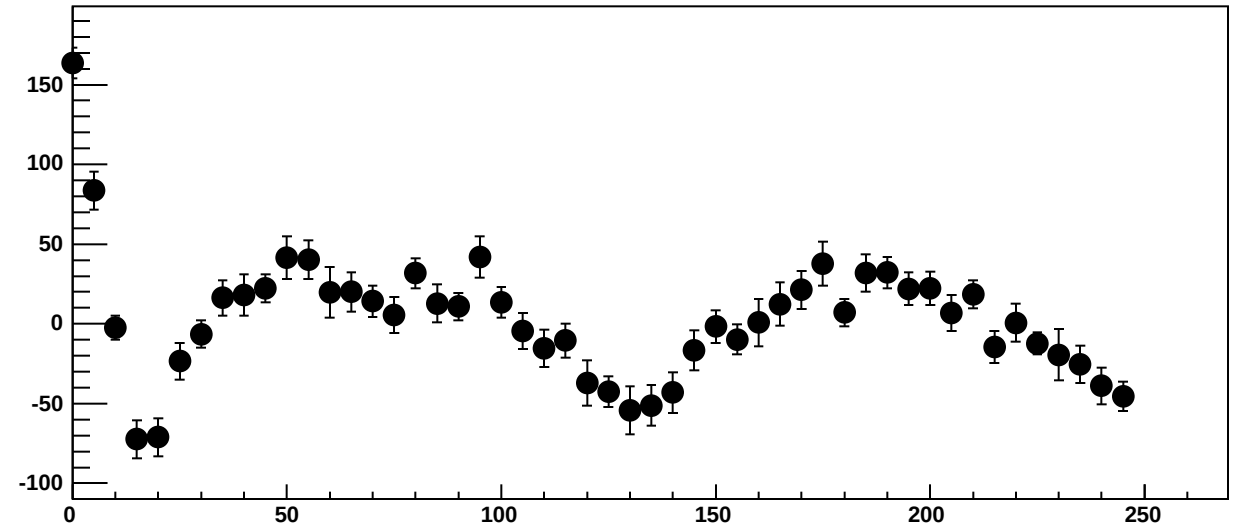
Chip 10, Channel 16, Enable 2, DAC=1600, ADC Mean vs Hold

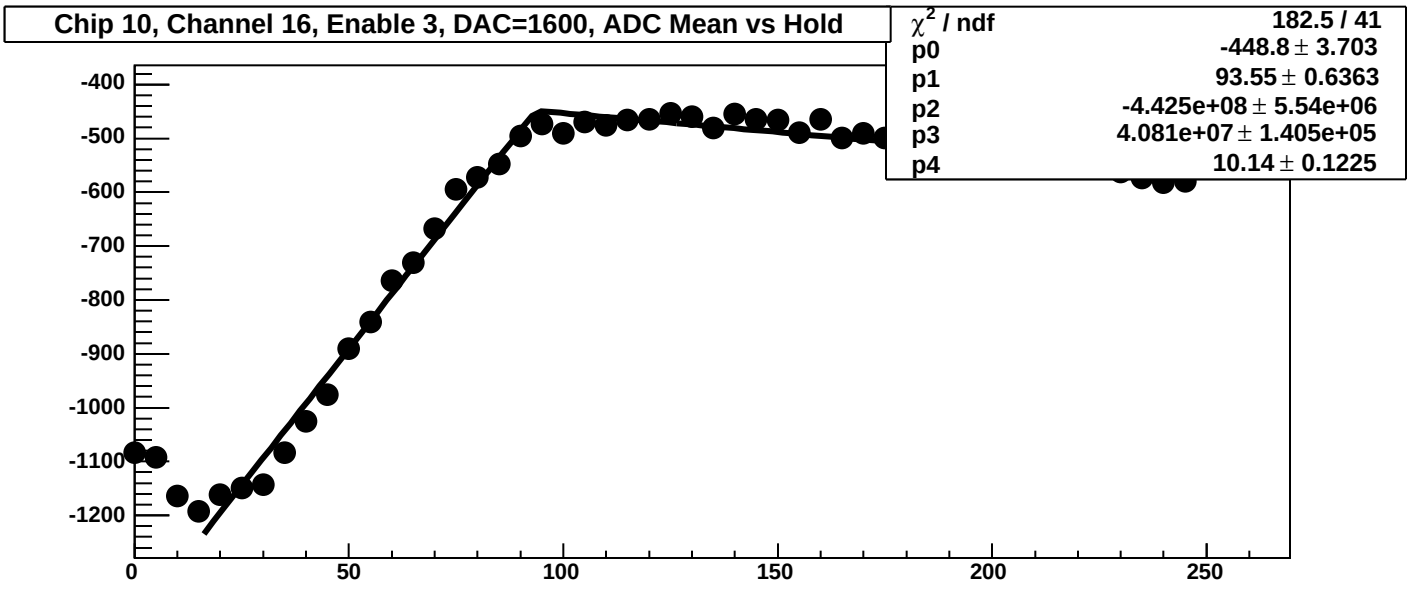


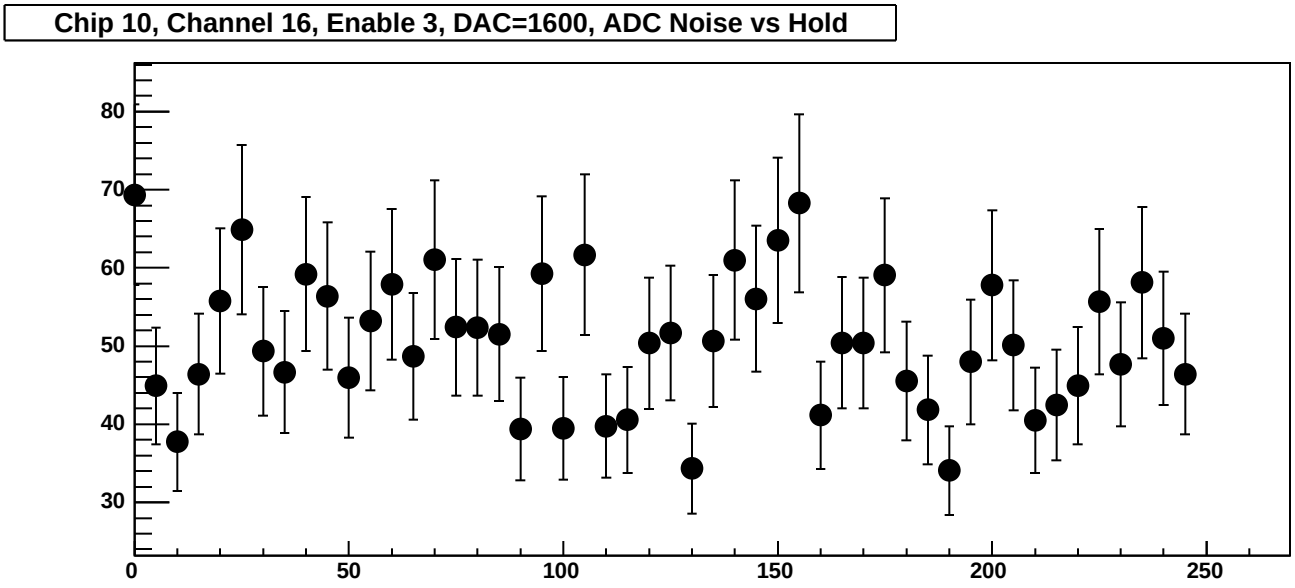
Chip 10, Channel 16, Enable 2, DAC=1600, ADC Noise vs Hold

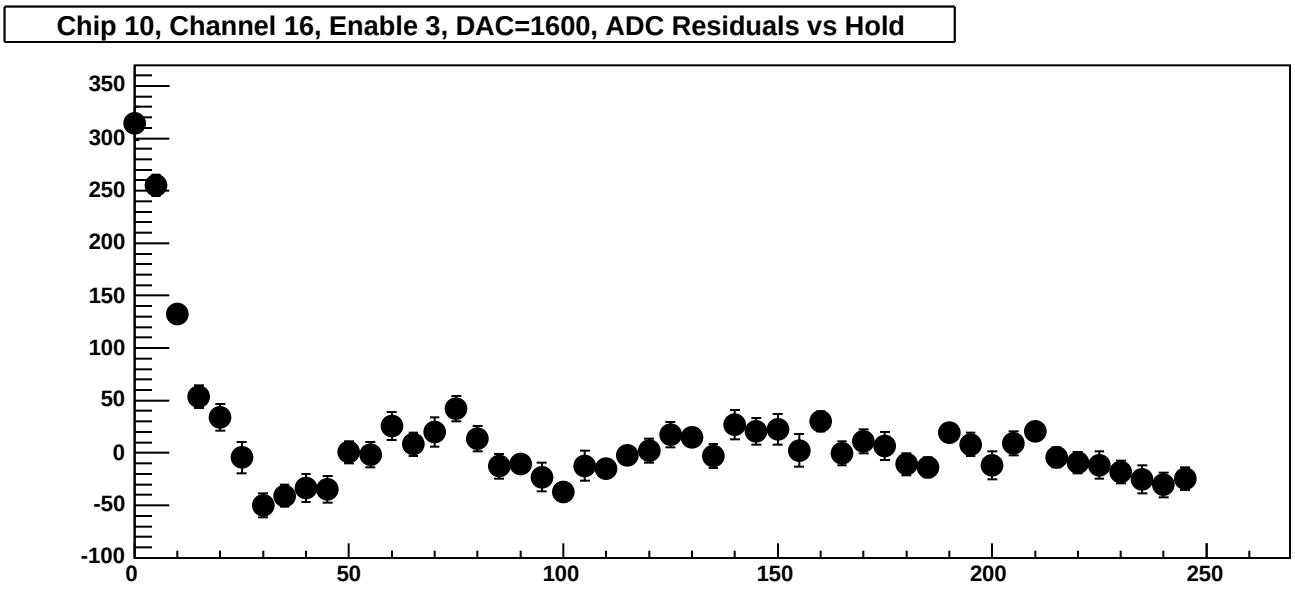


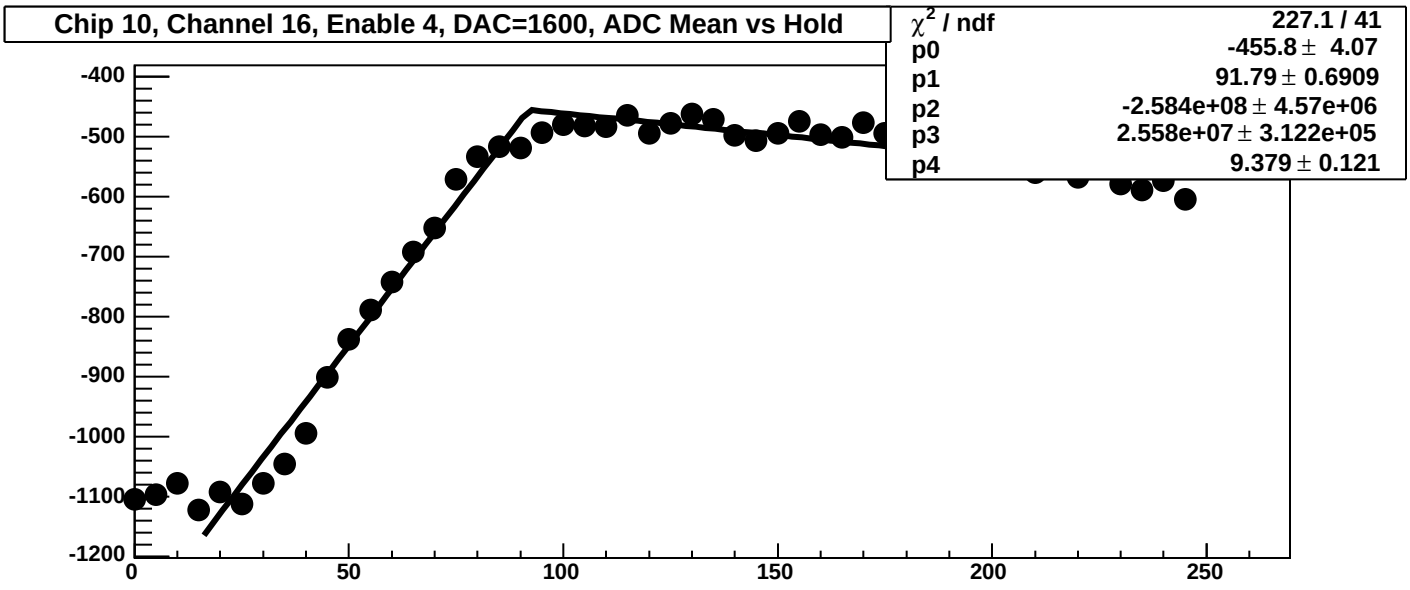
Chip 10, Channel 16, Enable 2, DAC=1600, ADC Residuals vs Hold

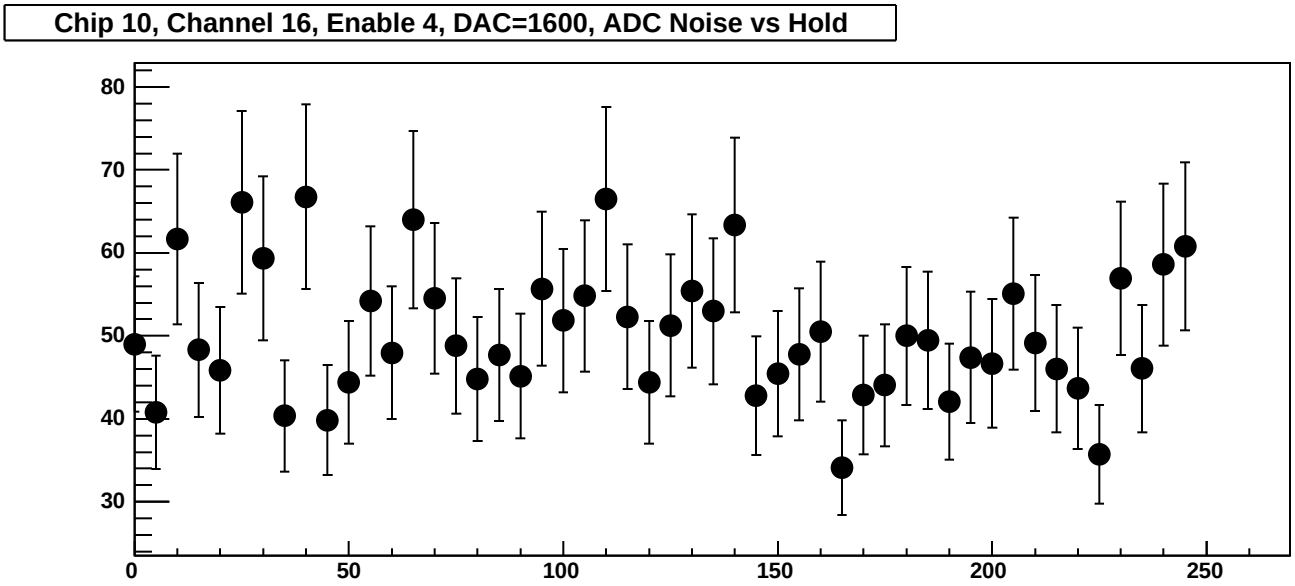


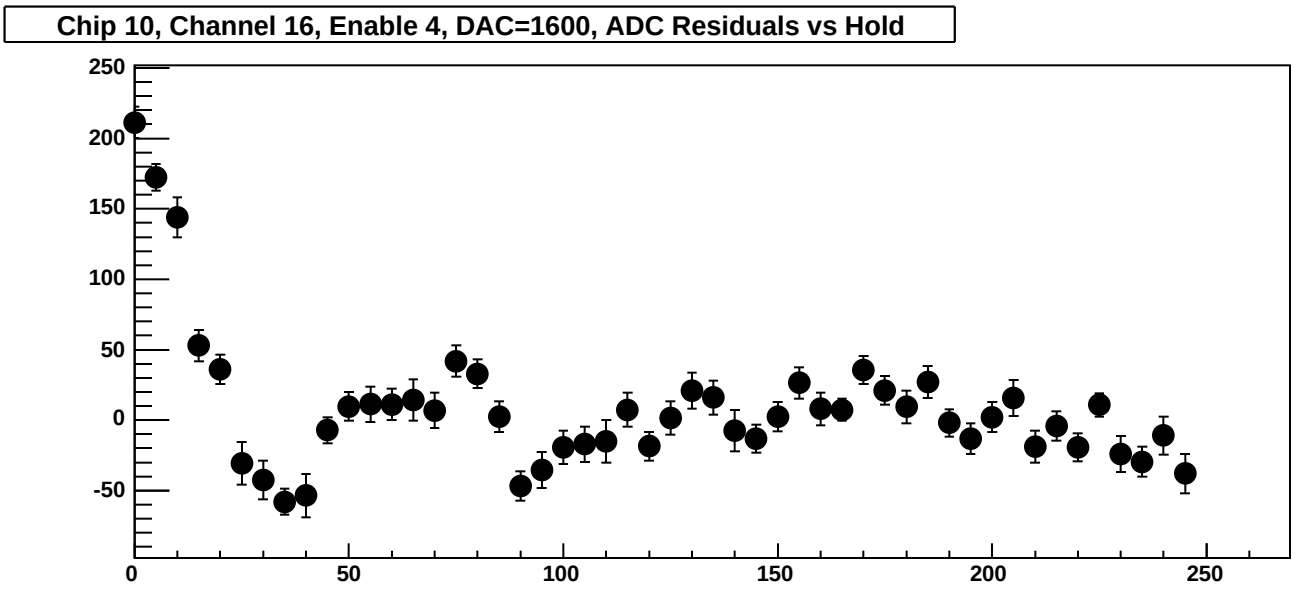

Chip 10, Channel 16, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 182.5 / 41
p0 -448.8 ± 3.703
p1 93.55 ± 0.6363
p2 -4.425e+08 ± 5.54e+06
p3 4.081e+07 ± 1.405e+05
p4 10.14 ± 0.1225
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
50
100
150
200
250

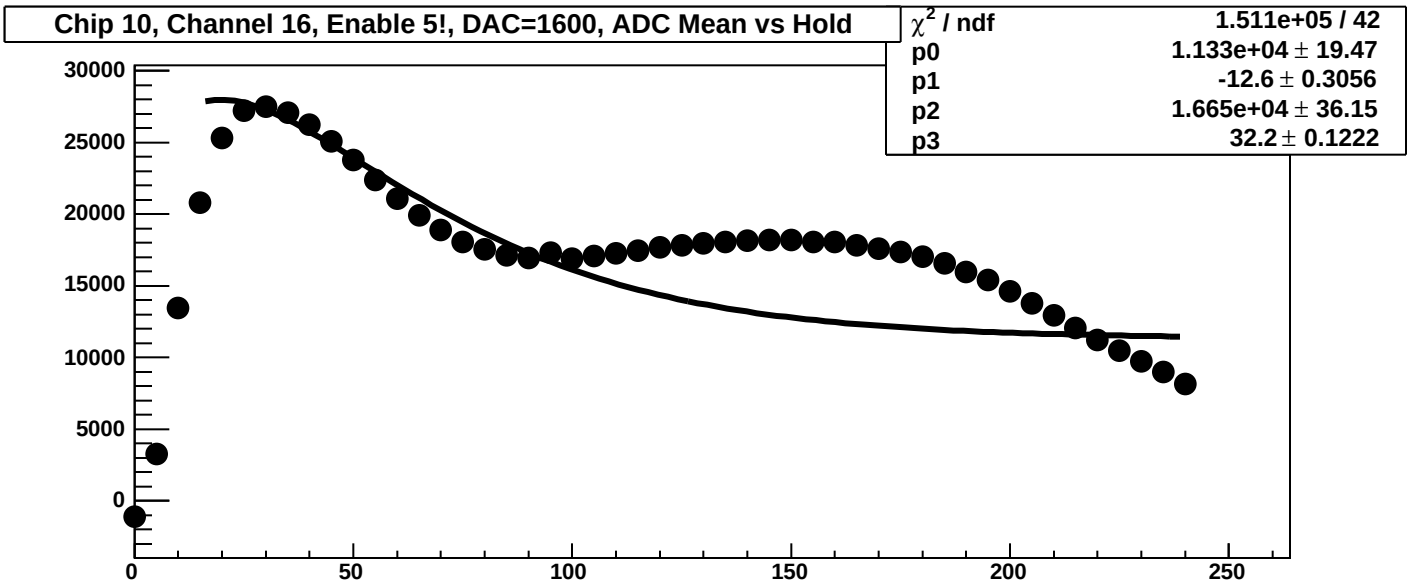

Chip 10, Channel 16, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

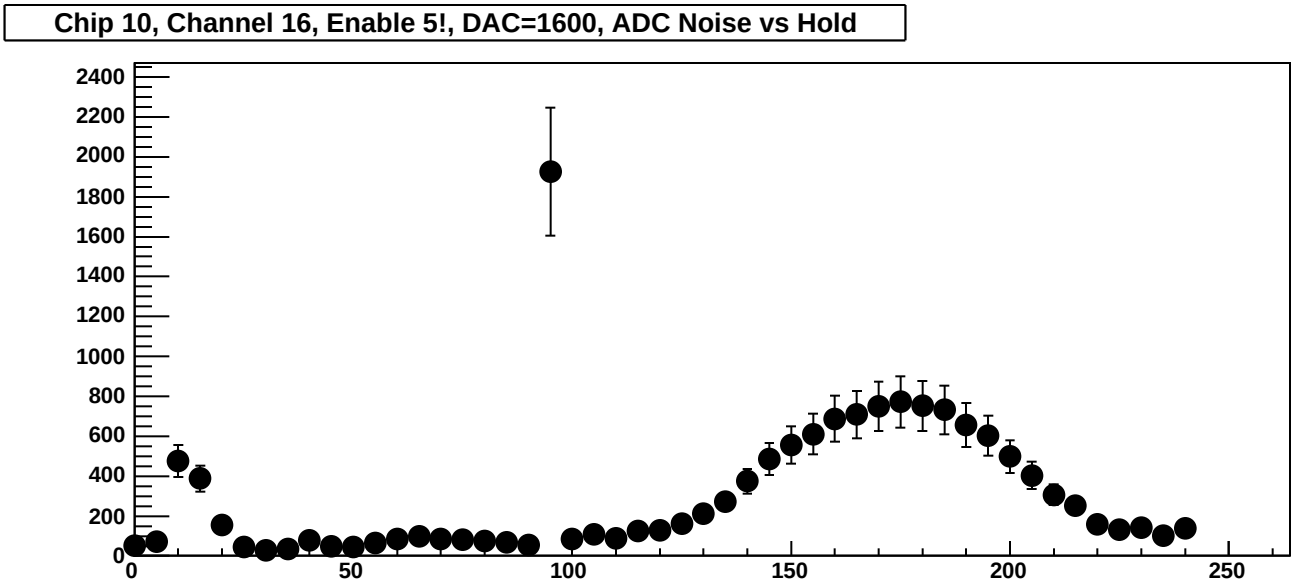

Chip 10, Channel 16, Enable 3, DAC=1600, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

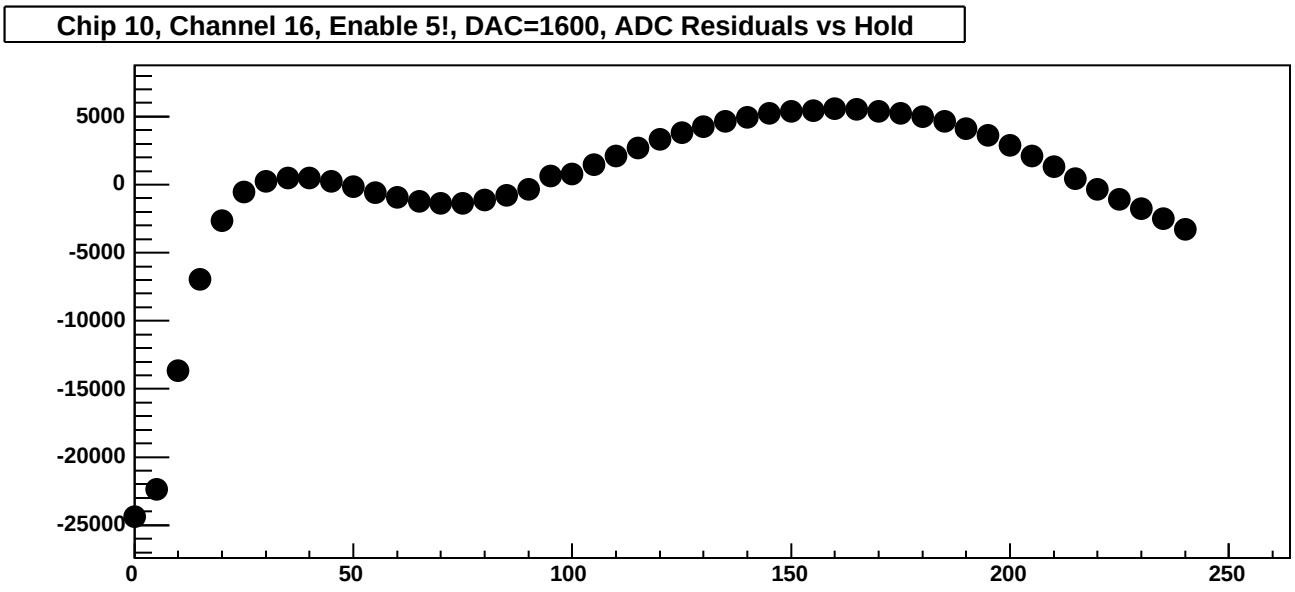

Chip 10, Channel 16, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 227.1 / 41
p0 -455.8 ± 4.07
p1 91.79 ± 0.6909
p2 -2.584e+08 ± 4.57e+06
p3 2.558e+07 ± 3.122e+05
p4 9.379 ± 0.121
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
50
100
150
200
250

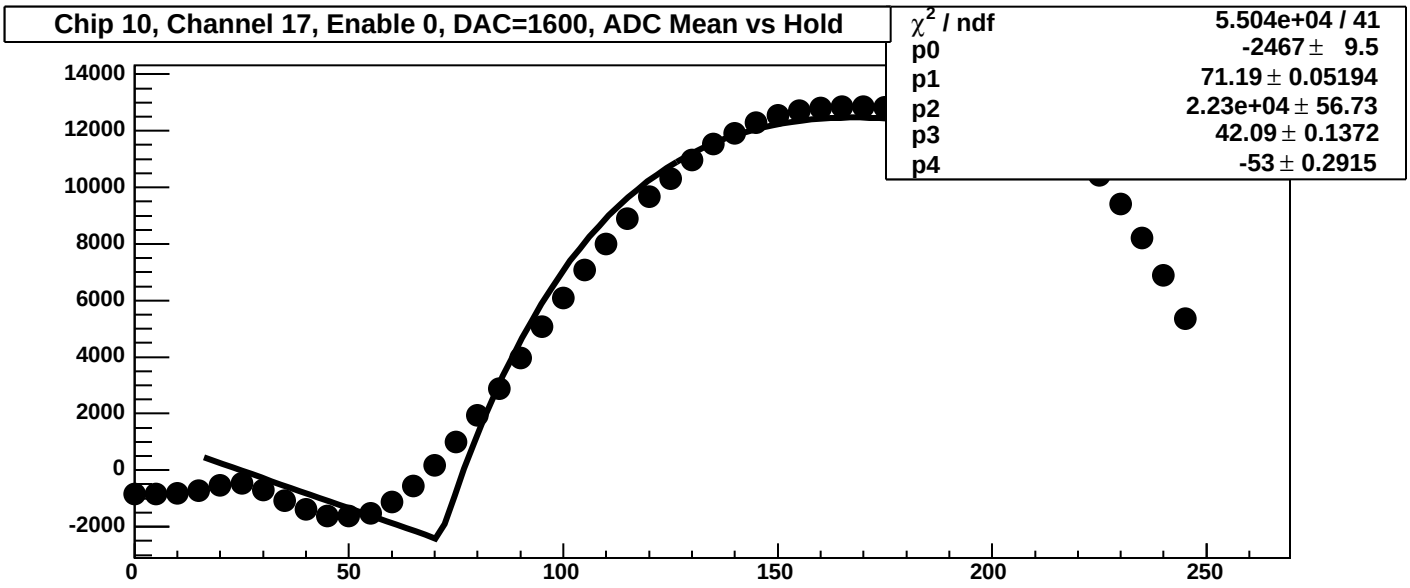

Chip 10, Channel 16, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

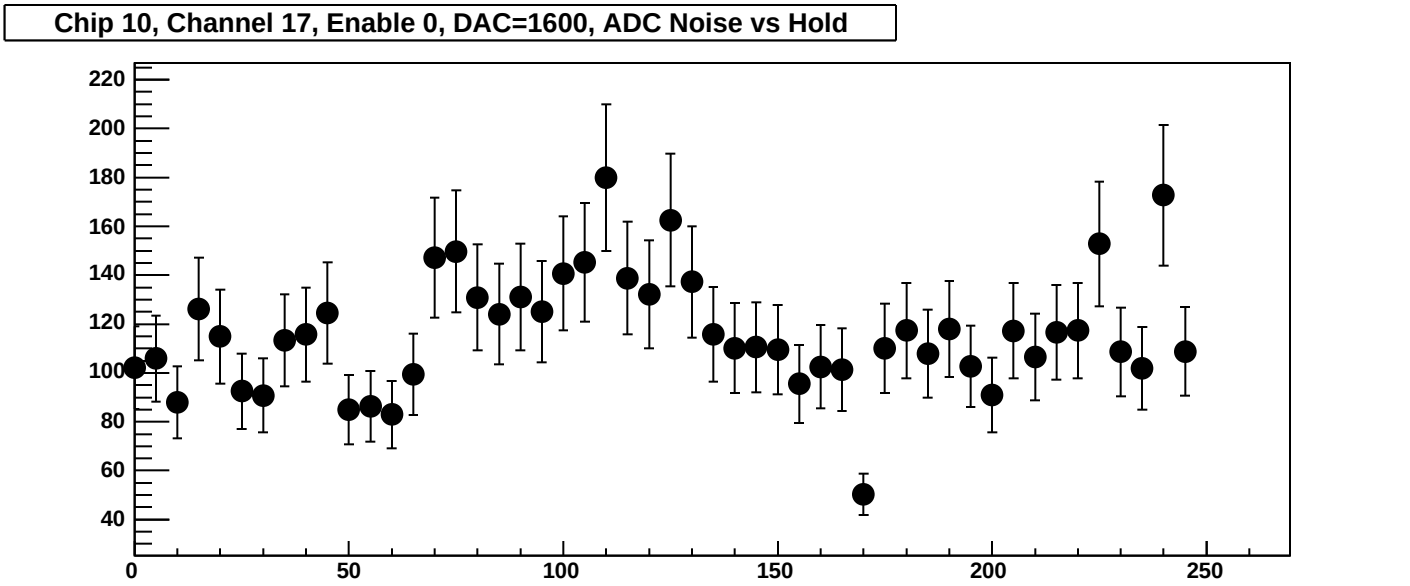

Chip 10, Channel 16, Enable 4, DAC=1600, ADC Residuals vs Hold
250
200
150
100
50
0
-50
0
50
100
150
200
250

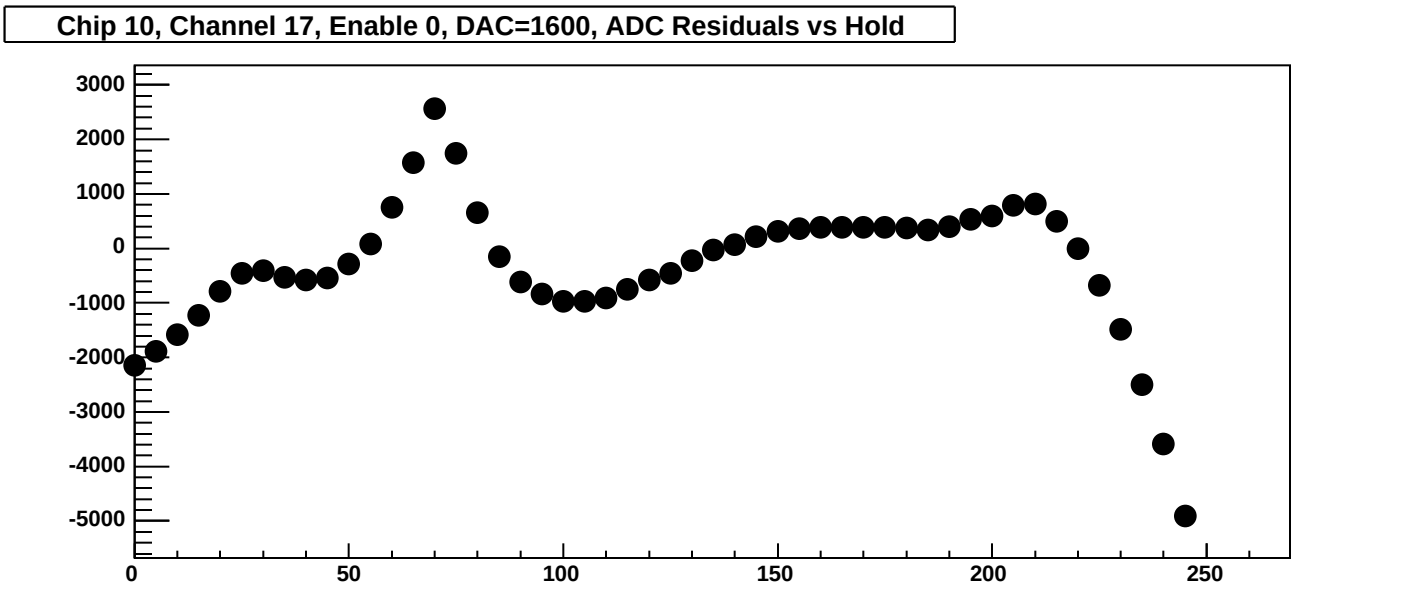

Chip 10, Channel 16, Enable 5!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.511e+05 / 42
p0 1.133e+04 ± 19.47
p1 -12.6 ± 0.3056
p2 1.665e+04 ± 36.15
p3 32.2 ± 0.1222
30000
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

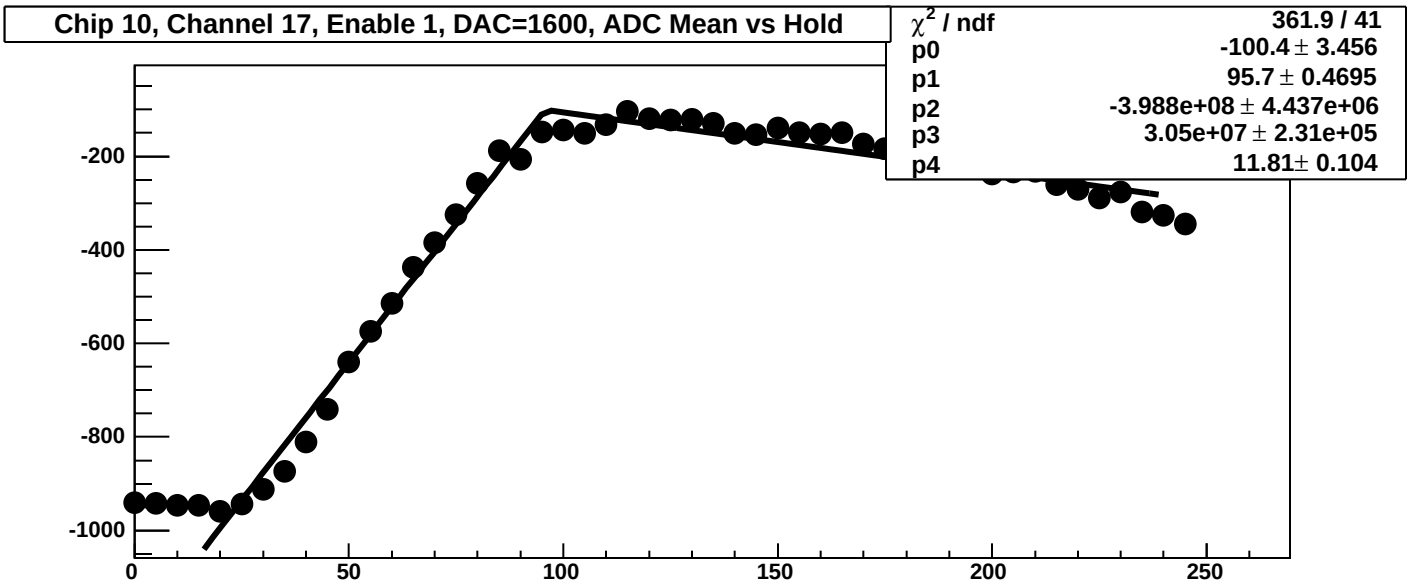

Chip 10, Channel 16, Enable 5!, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

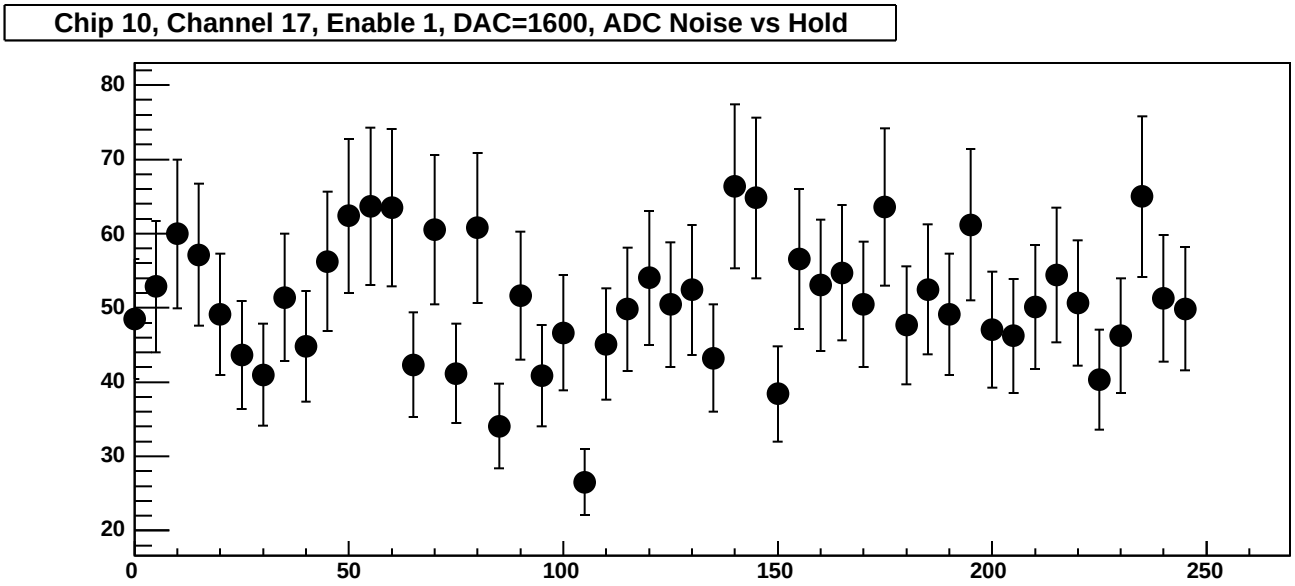

Chip 10, Channel 16, Enable 5!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
0
50
100
150
200
250

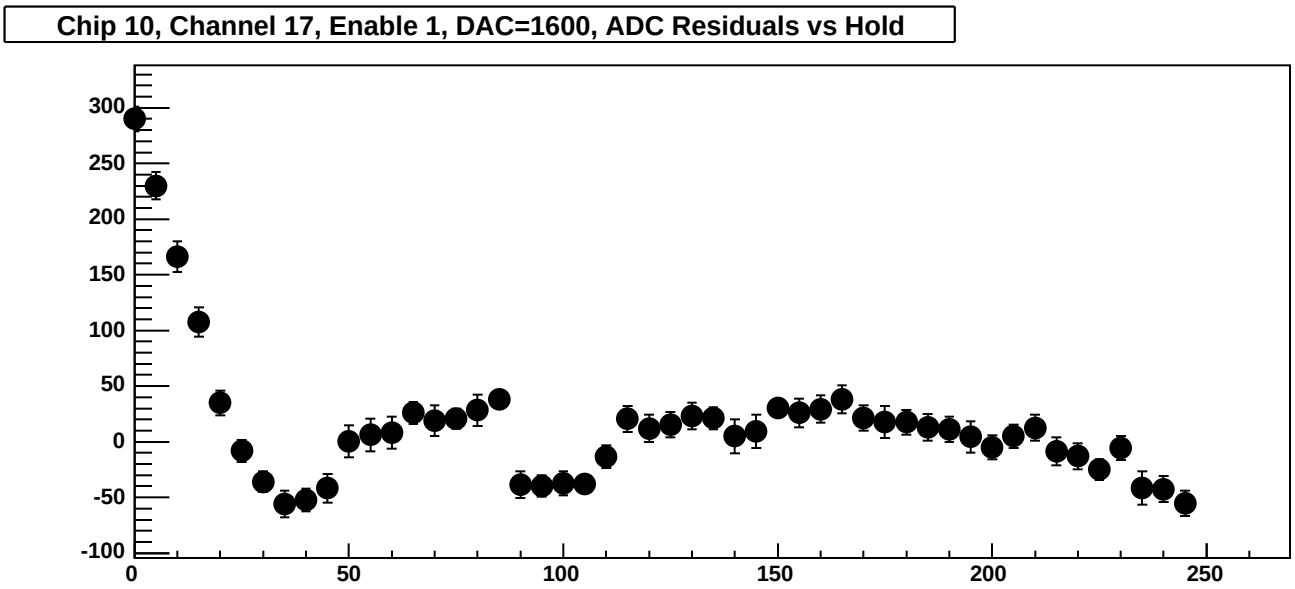

Chip 10, Channel 17, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.504e+04 / 41
p0 -2467 ± 9.5
p1 71.19 ± 0.05194
p2 2.23e+04 ± 56.73
p3 42.09 ± 0.1372
p4 -53 ± 0.2915
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250


Chip 10, Channel 17, Enable 0, DAC=1600, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250

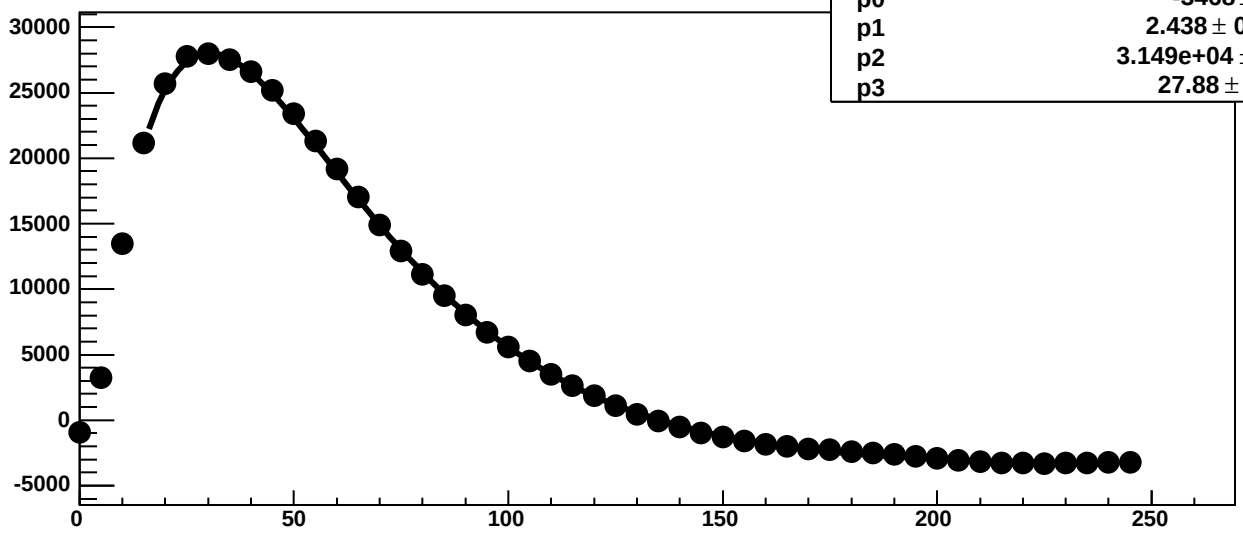

Chip 10, Channel 17, Enable 0, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250


Chip 10, Channel 17, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 361.9 / 41
p0 -100.4 ± 3.456
p1 95.7 ± 0.4695
p2 -3.988e+08 ± 4.437e+06
p3 3.05e+07 ± 2.31e+05
p4 11.81± 0.104
-200
-400
-600
-800
-1000
0
50
100
150
200
250

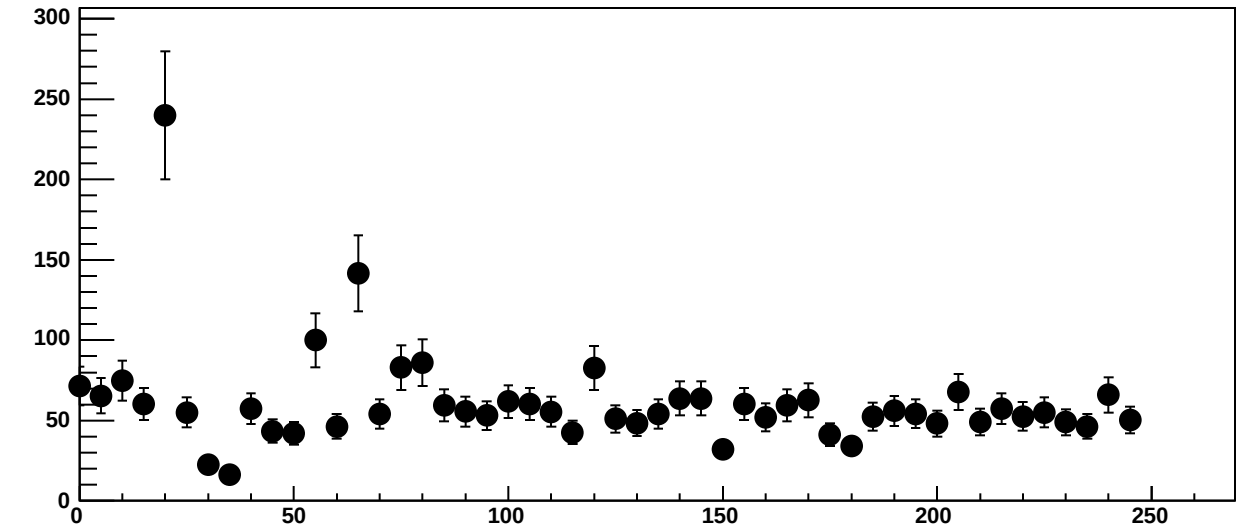

Chip 10, Channel 17, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 10, Channel 17, Enable 1, DAC=1600, ADC Residuals vs Hold
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250

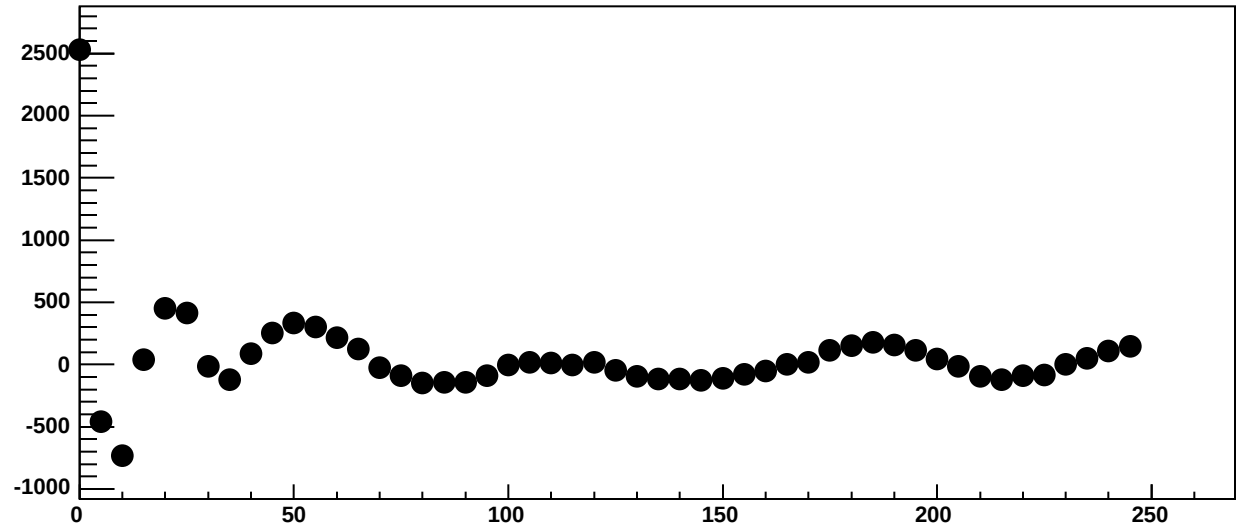
Chip 10, Channel 17, Enable 2!, DAC=1600, ADC Mean vs Hold

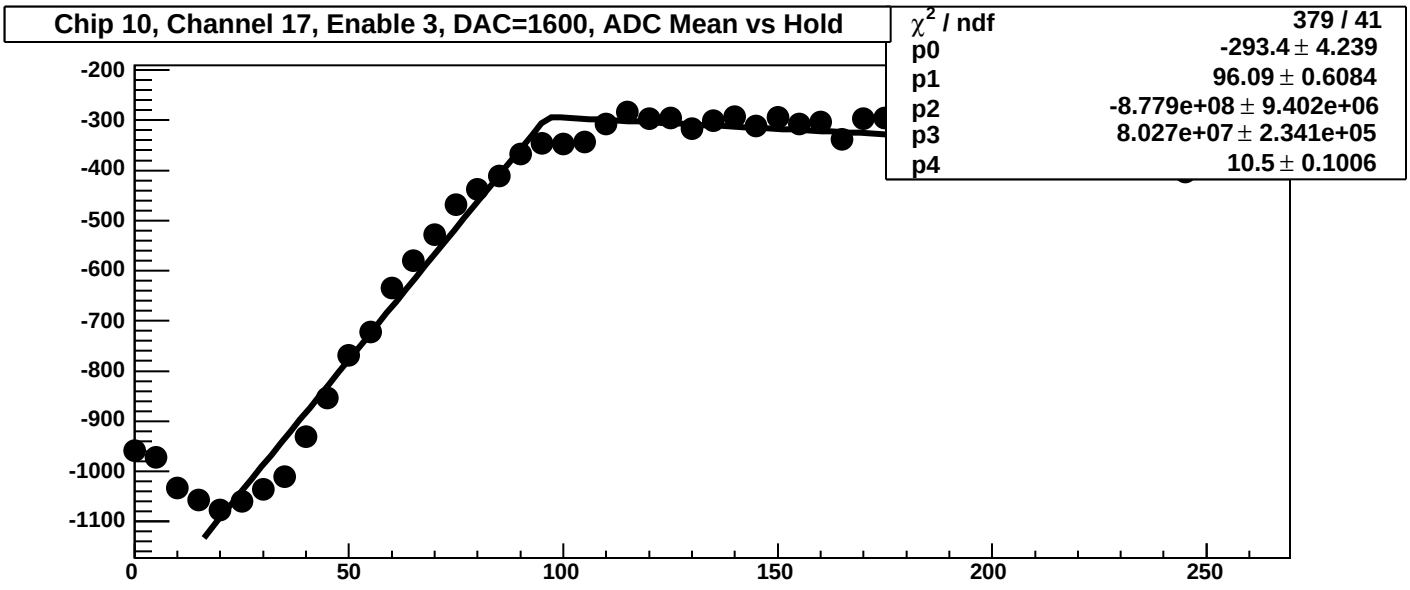


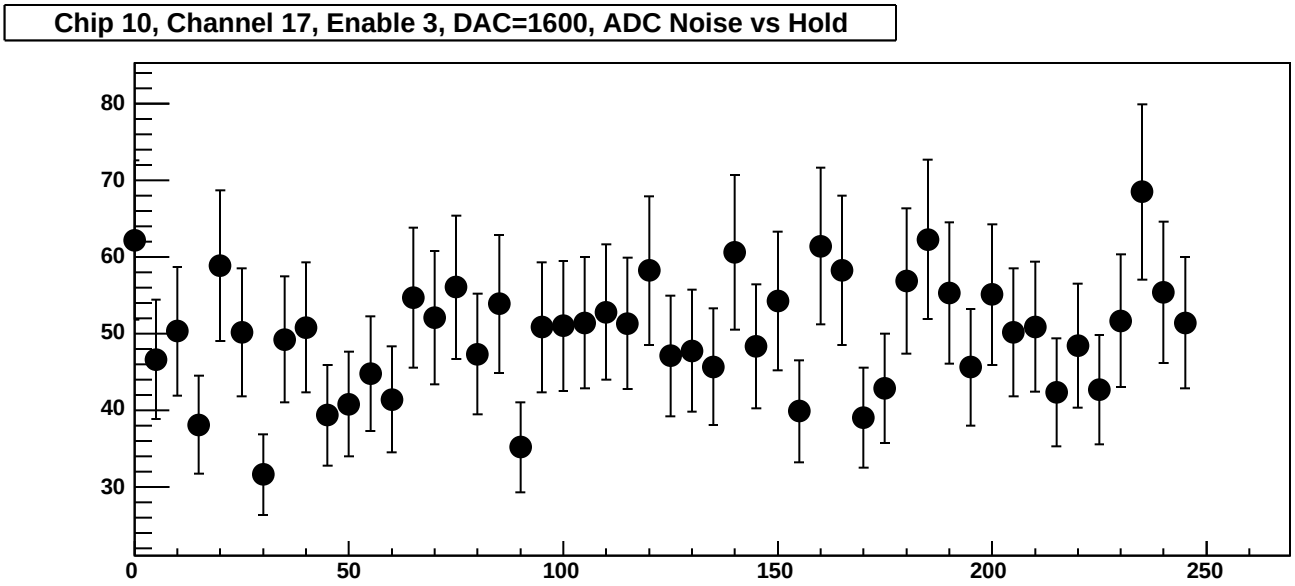
Chip 10, Channel 17, Enable 2!, DAC=1600, ADC Noise vs Hold

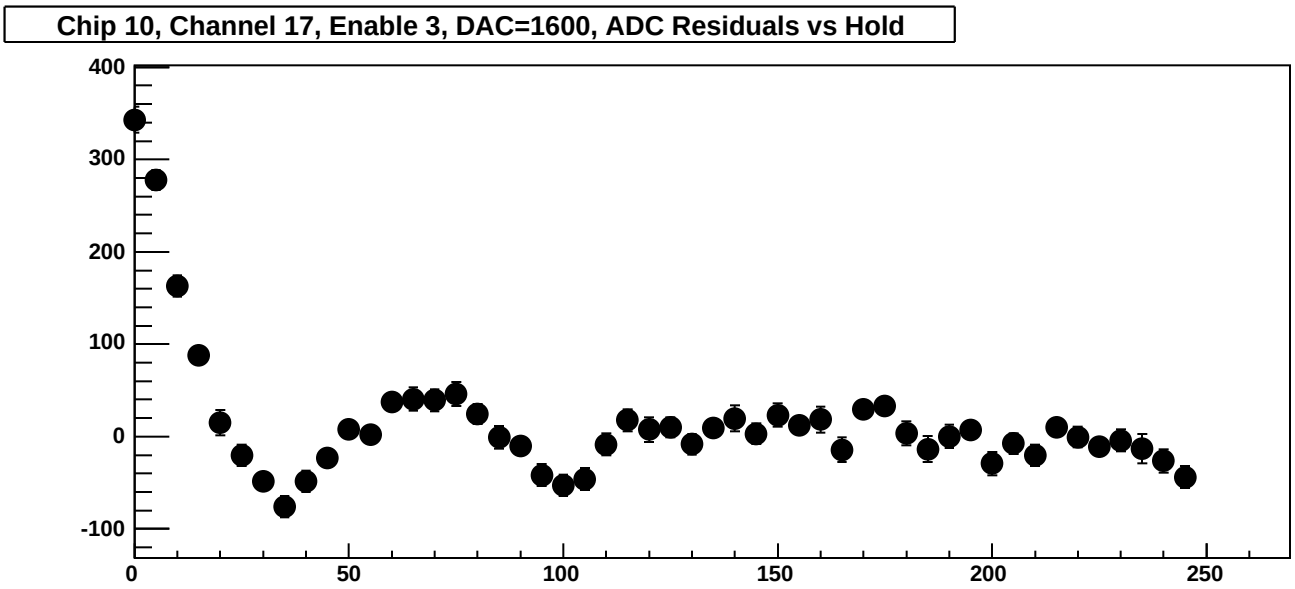


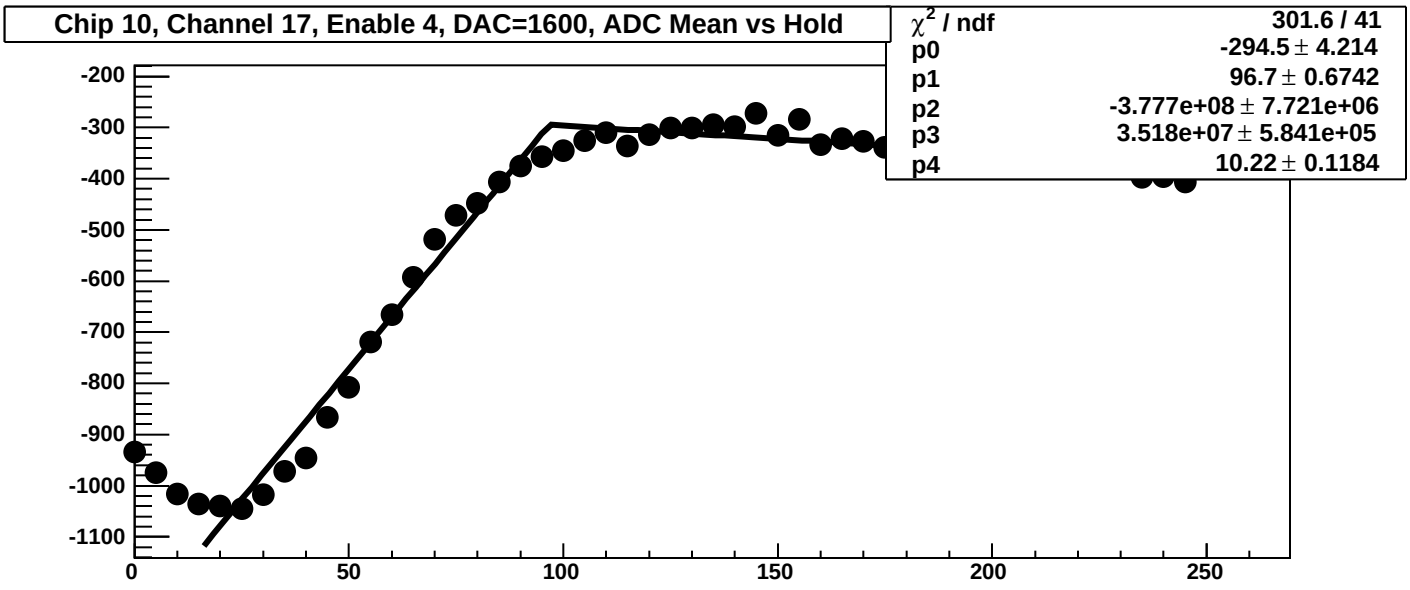
Chip 10, Channel 17, Enable 2!, DAC=1600, ADC Residuals vs Hold

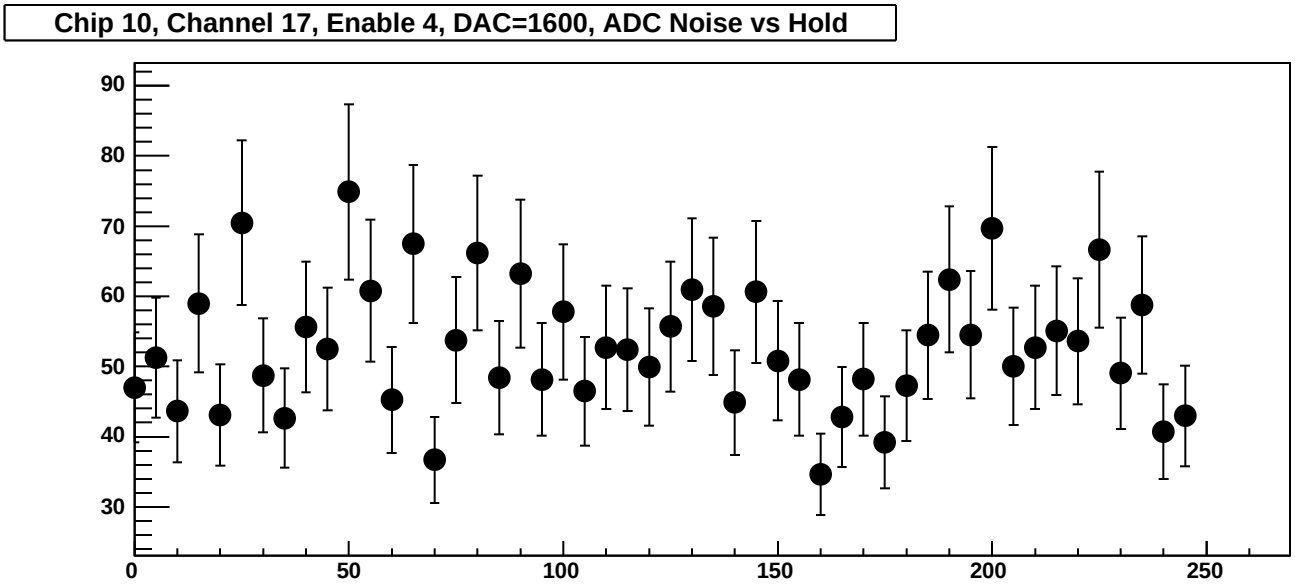


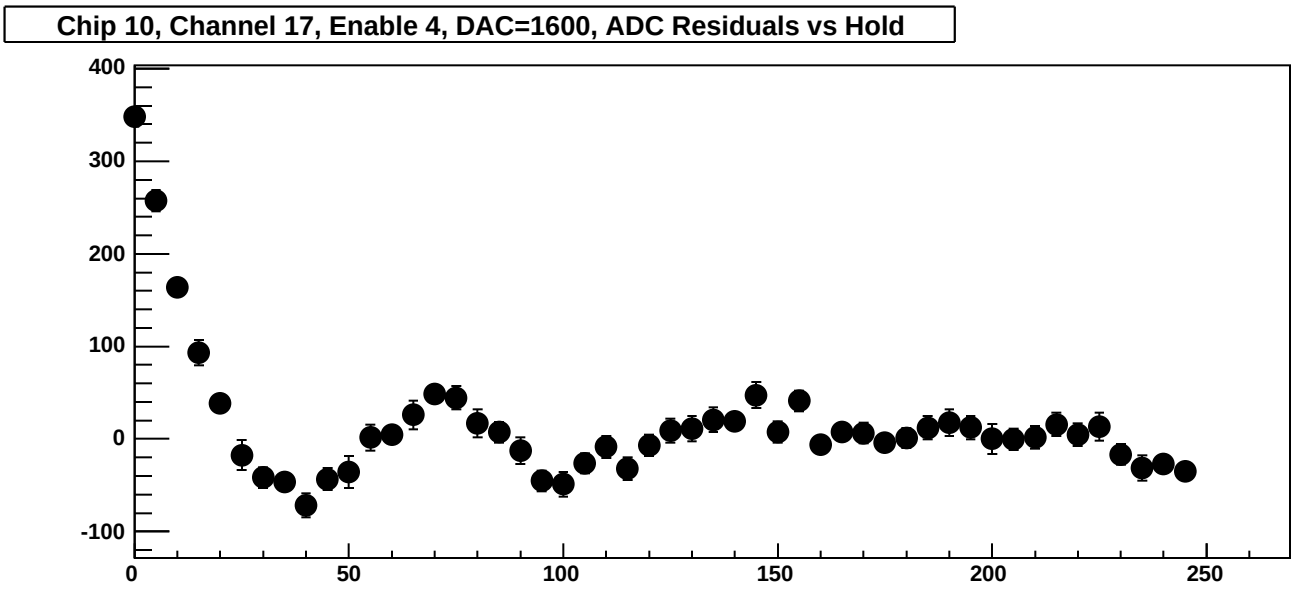

Chip 10, Channel 17, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 379 / 41
p0 -293.4 ± 4.239
p1 96.09 ± 0.6084
p2 -8.779e+08 ± 9.402e+06
p3 8.027e+07 ± 2.341e+05
p4 10.5 ± 0.1006
-200
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
50
100
150
200
250

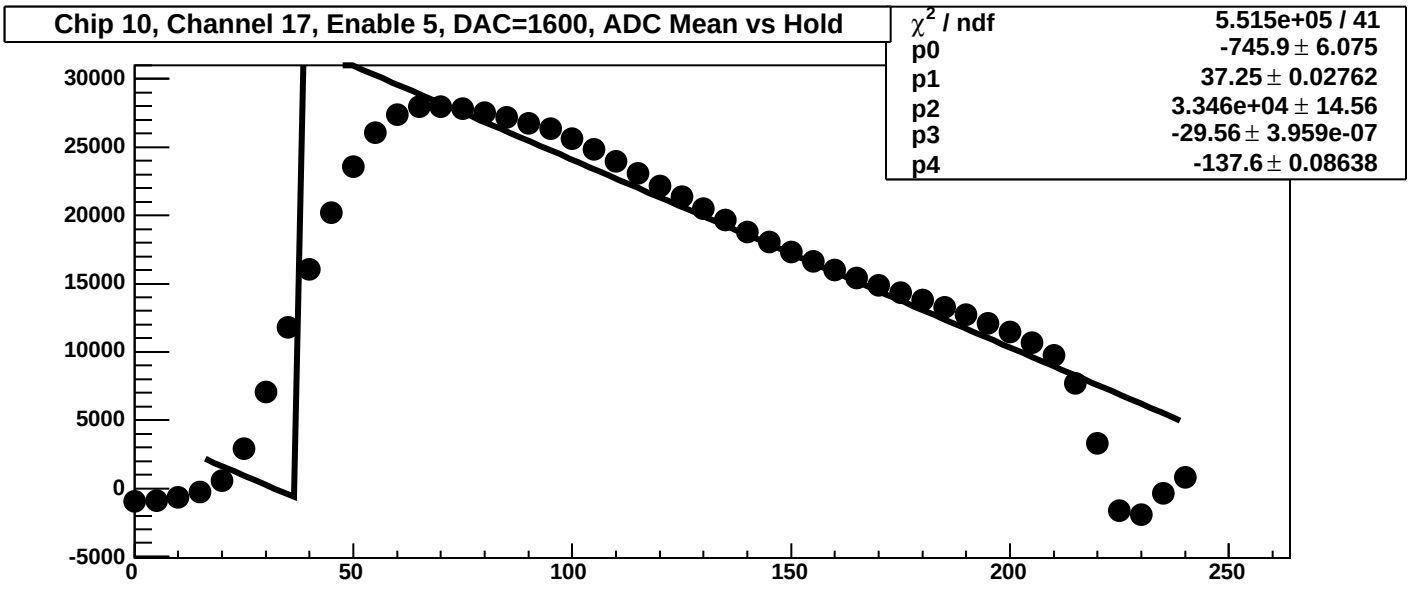

Chip 10, Channel 17, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

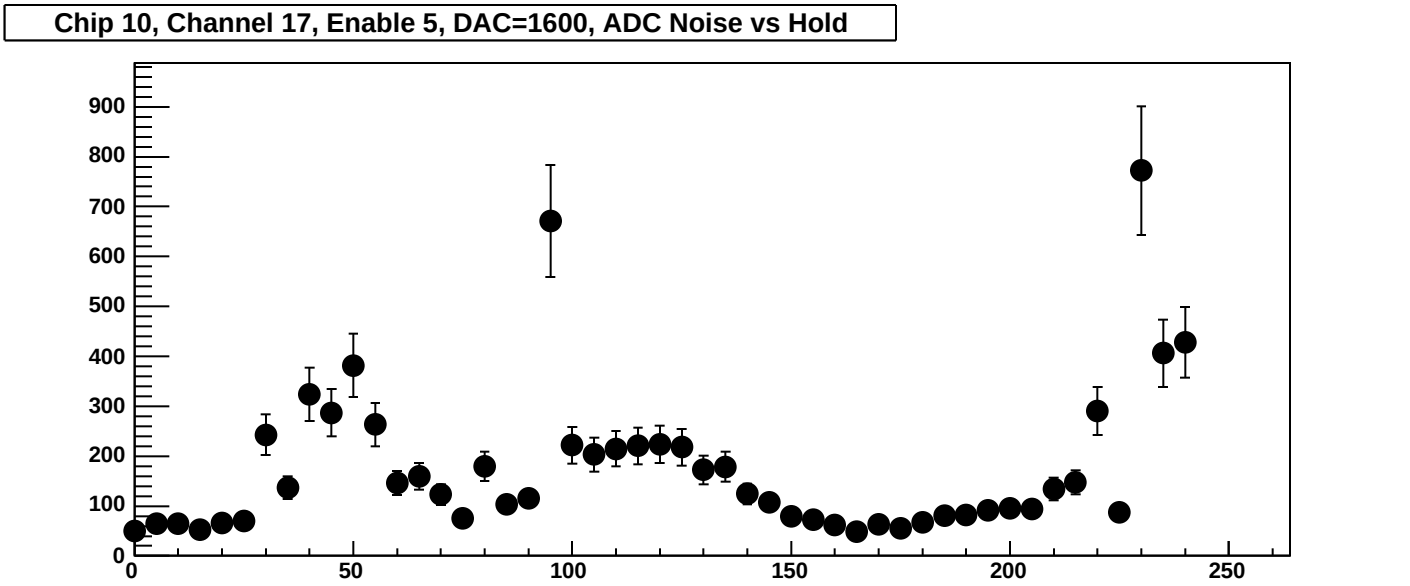

Chip 10, Channel 17, Enable 3, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

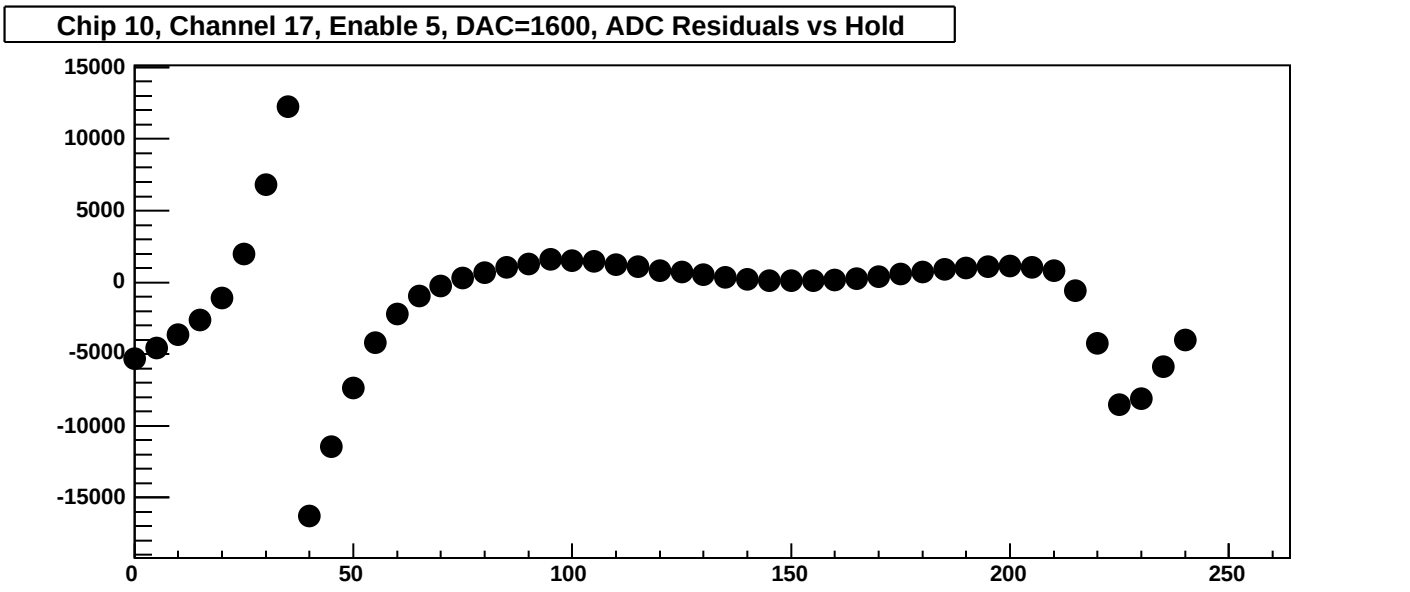

Chip 10, Channel 17, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 301.6 / 41
p0 -294.5 ± 4.214
p1 96.7 ± 0.6742
p2 -3.777e+08 ± 7.721e+06
p3 3.518e+07 ± 5.841e+05
p4 10.22 ± 0.1184
-200
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
50
100
150
200
250


Chip 10, Channel 17, Enable 4, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

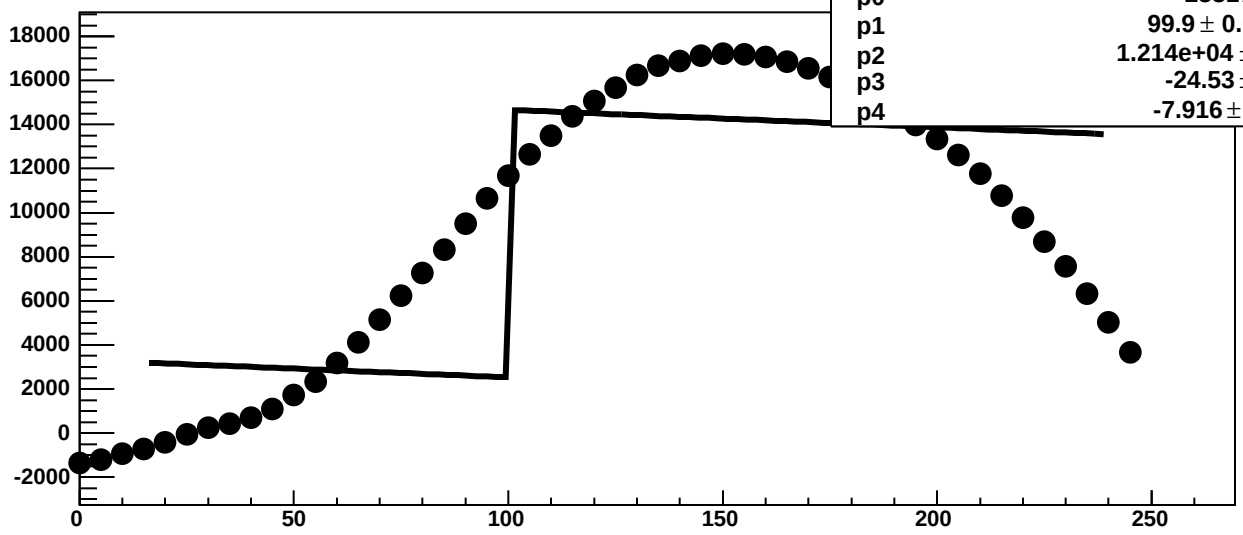

Chip 10, Channel 17, Enable 4, DAC=1600, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 10, Channel 17, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 5.515e+05 / 41
p0 -745.9 ± 6.075
p1 37.25 ± 0.02762
p2 3.346e+04 ± 14.56
p3 -29.56 ± 3.959e-07
p4 -137.6 ± 0.08638
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

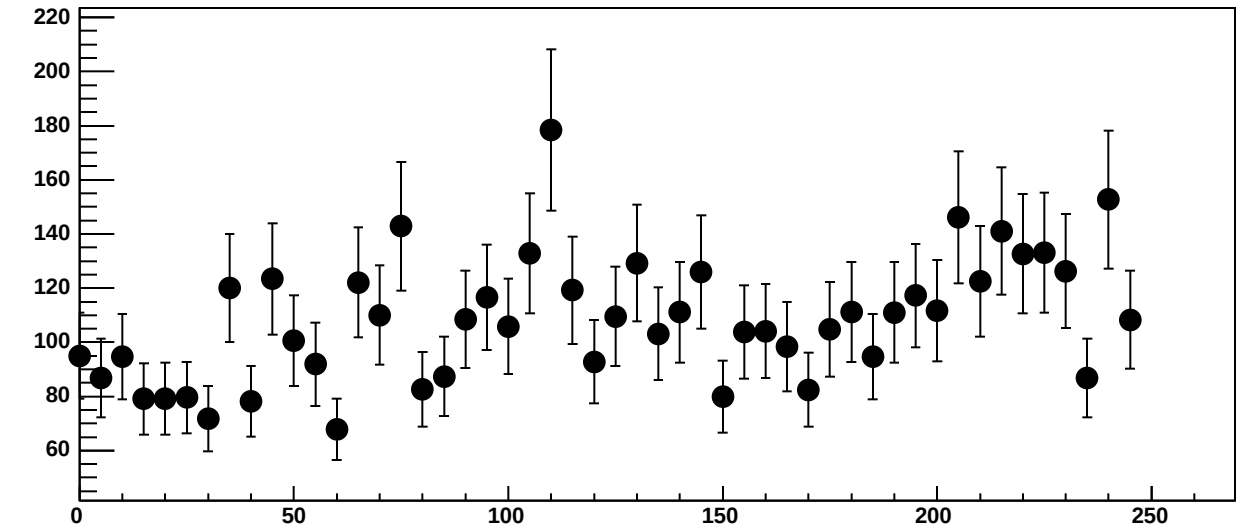

Chip 10, Channel 17, Enable 5, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 10, Channel 17, Enable 5, DAC=1600, ADC Residuals vs Hold
15000
10000
5000
0
-5000
-10000
-15000
0
50
100
150
200
250

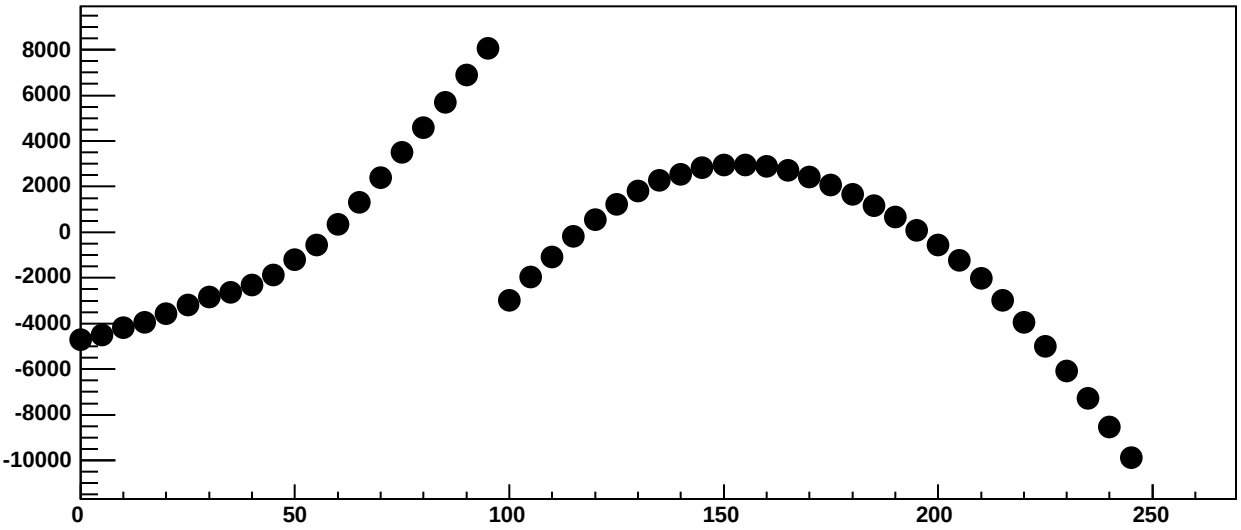
Chip 11, Channel 0, Enable 0, DAC=1600, ADC Mean vs Hold



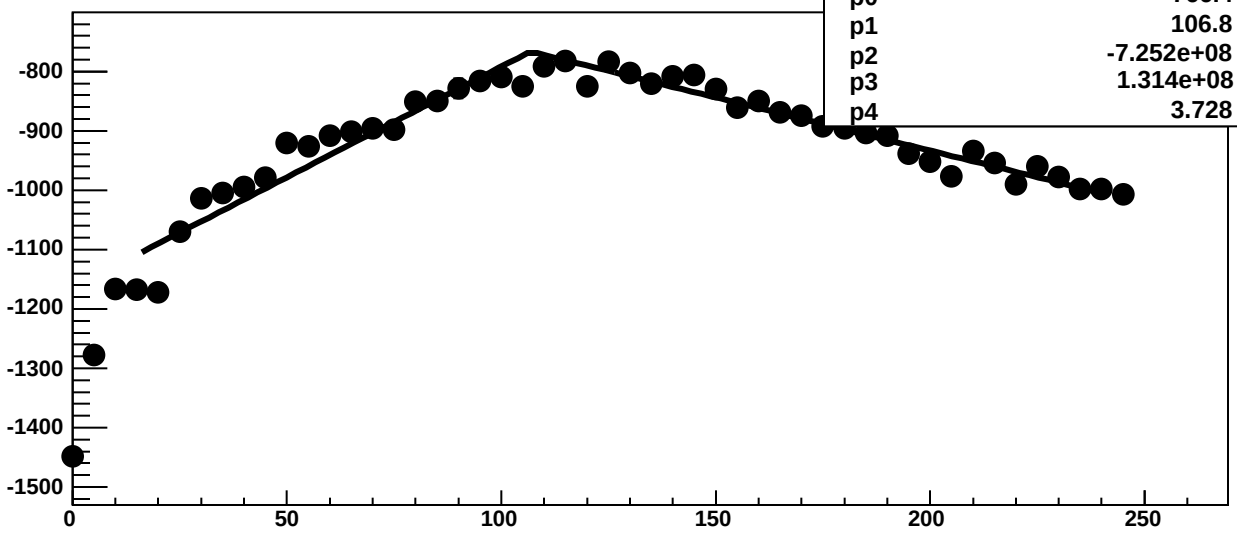
Chip 11, Channel 0, Enable 0, DAC=1600, ADC Noise vs Hold



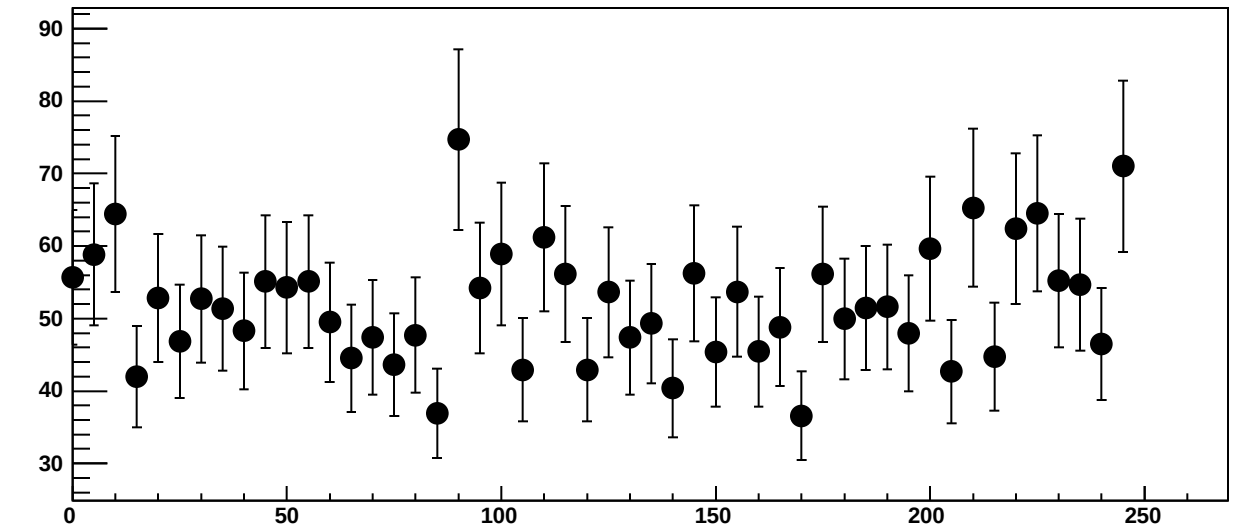
Chip 11, Channel 0, Enable 0, DAC=1600, ADC Residuals vs Hold



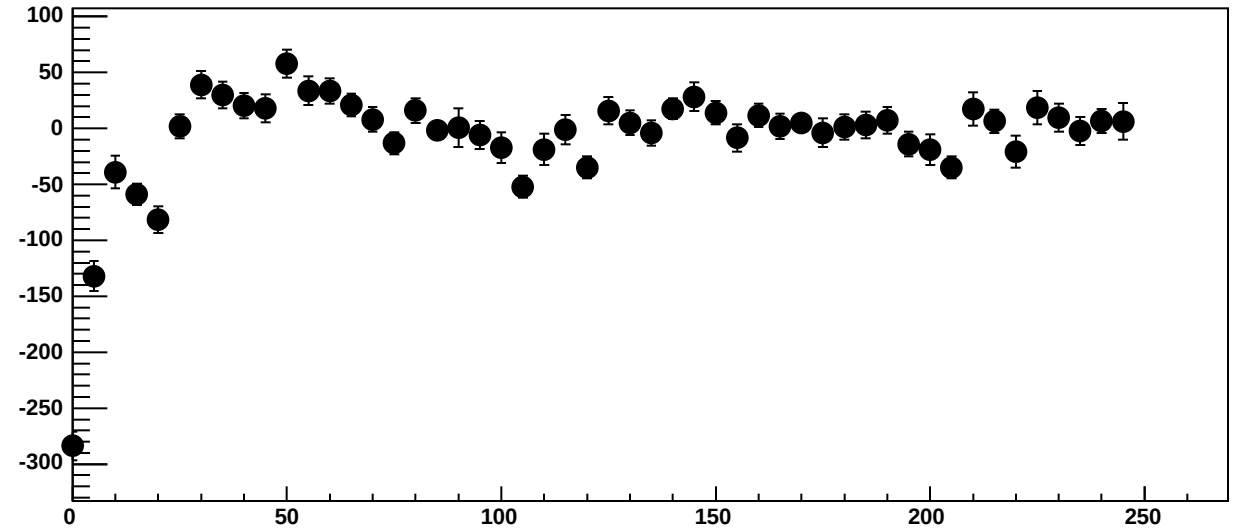
Chip 11, Channel 0, Enable 1, DAC=1600, ADC Mean vs Hold



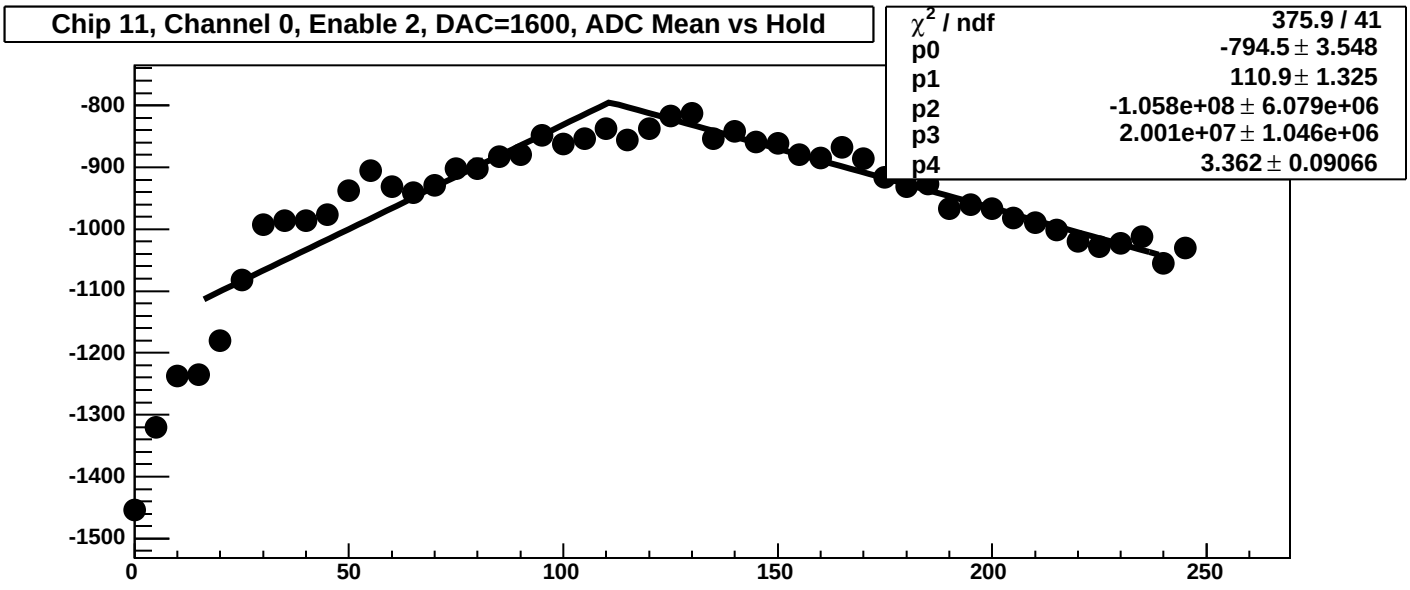
Chip 11, Channel 0, Enable 1, DAC=1600, ADC Noise vs Hold



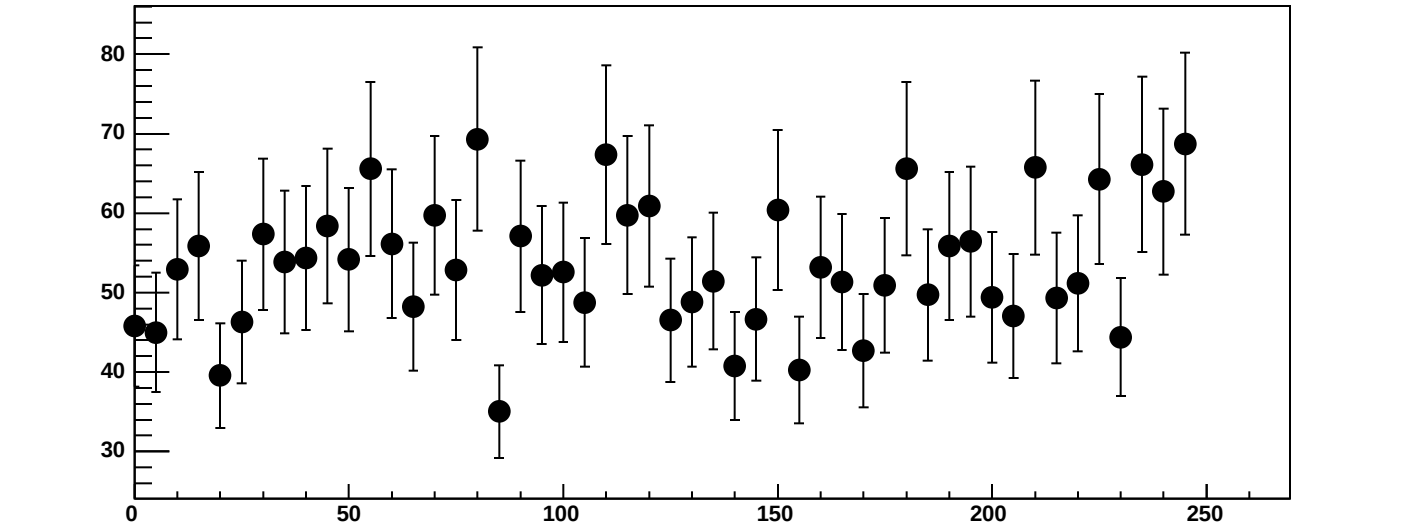
Chip 11, Channel 0, Enable 1, DAC=1600, ADC Residuals vs Hold



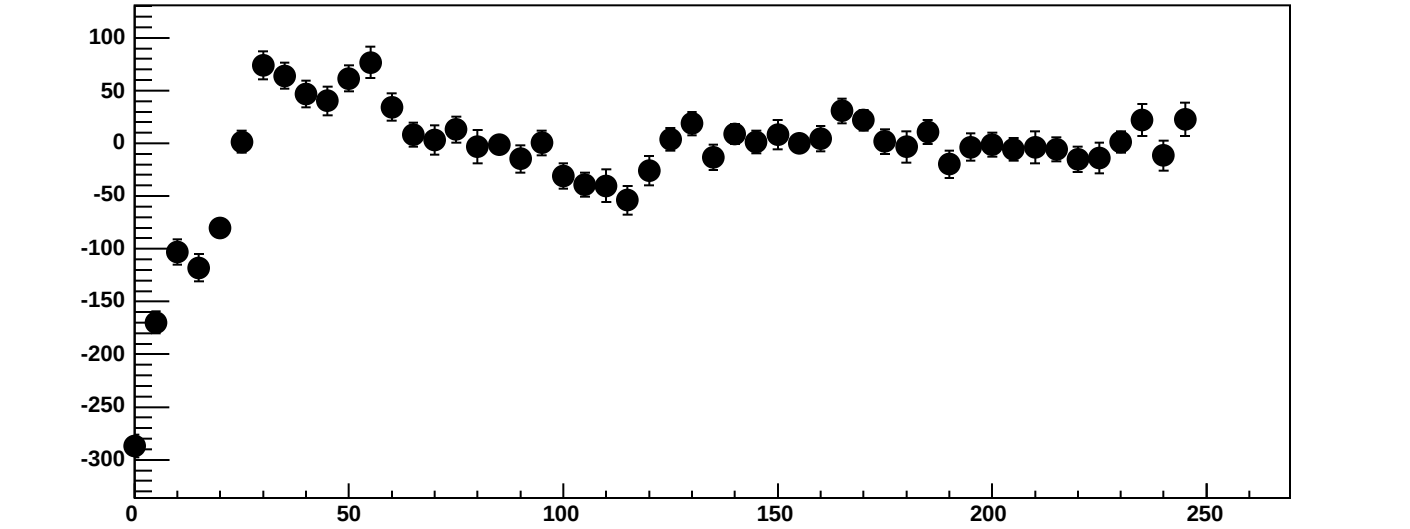
Chip 11, Channel 0, Enable 2, DAC=1600, ADC Mean vs Hold



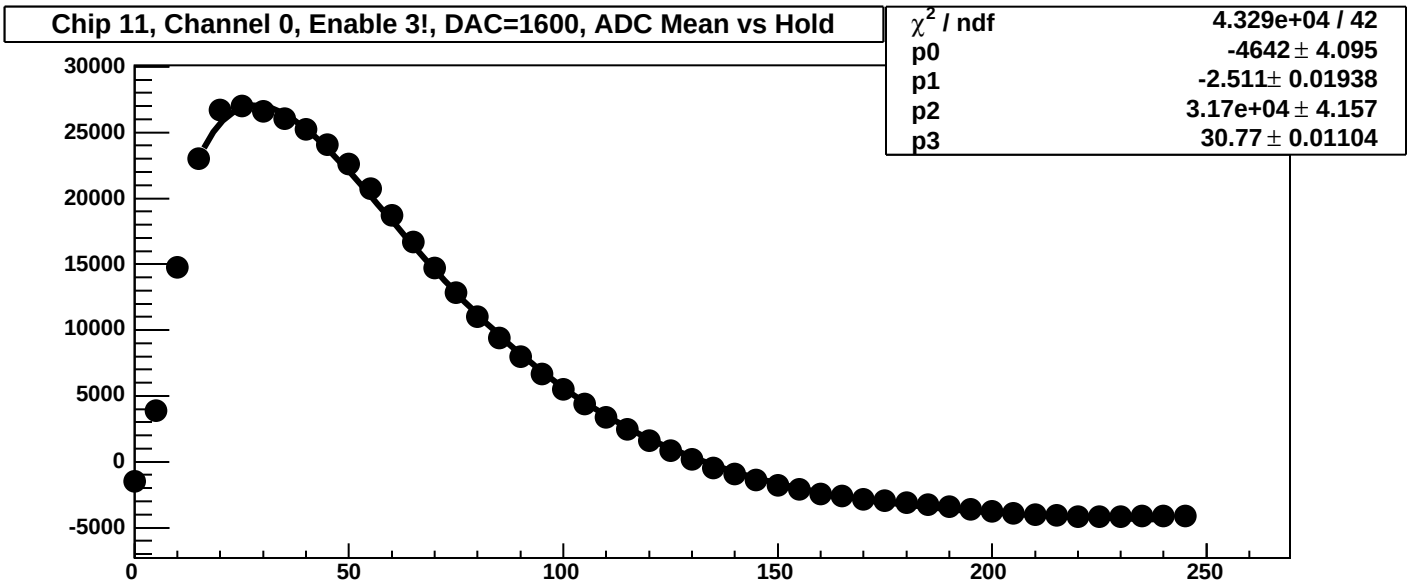
Chip 11, Channel 0, Enable 2, DAC=1600, ADC Noise vs Hold



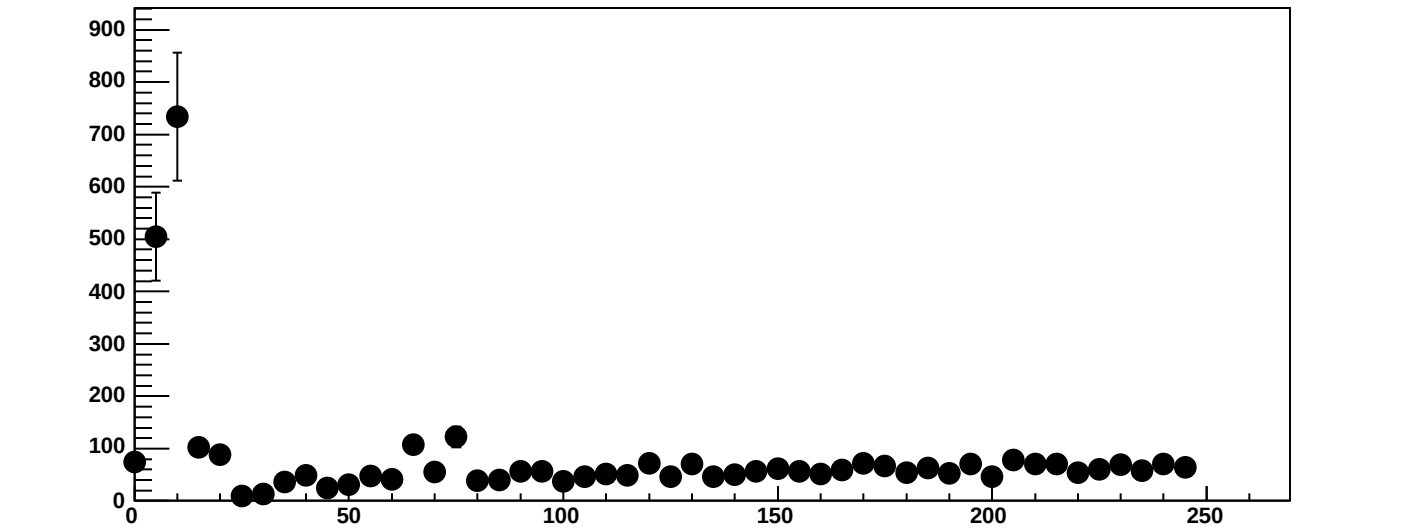
Chip 11, Channel 0, Enable 2, DAC=1600, ADC Residuals vs Hold



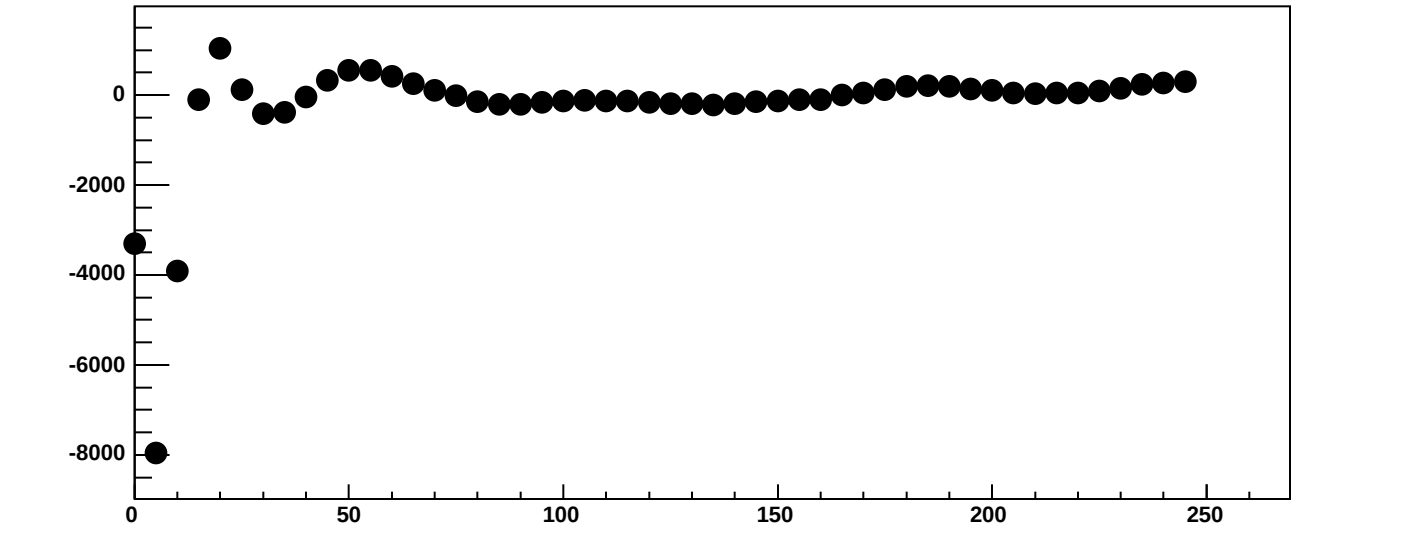
Chip 11, Channel 0, Enable 3!, DAC=1600, ADC Mean vs Hold



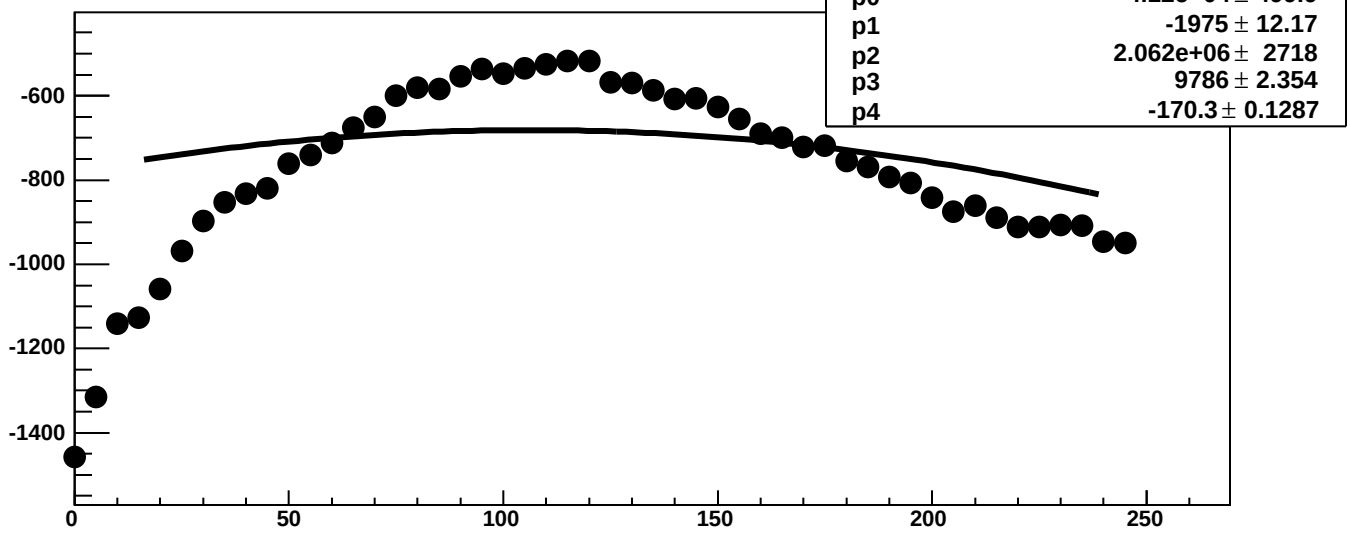
Chip 11, Channel 0, Enable 3!, DAC=1600, ADC Noise vs Hold



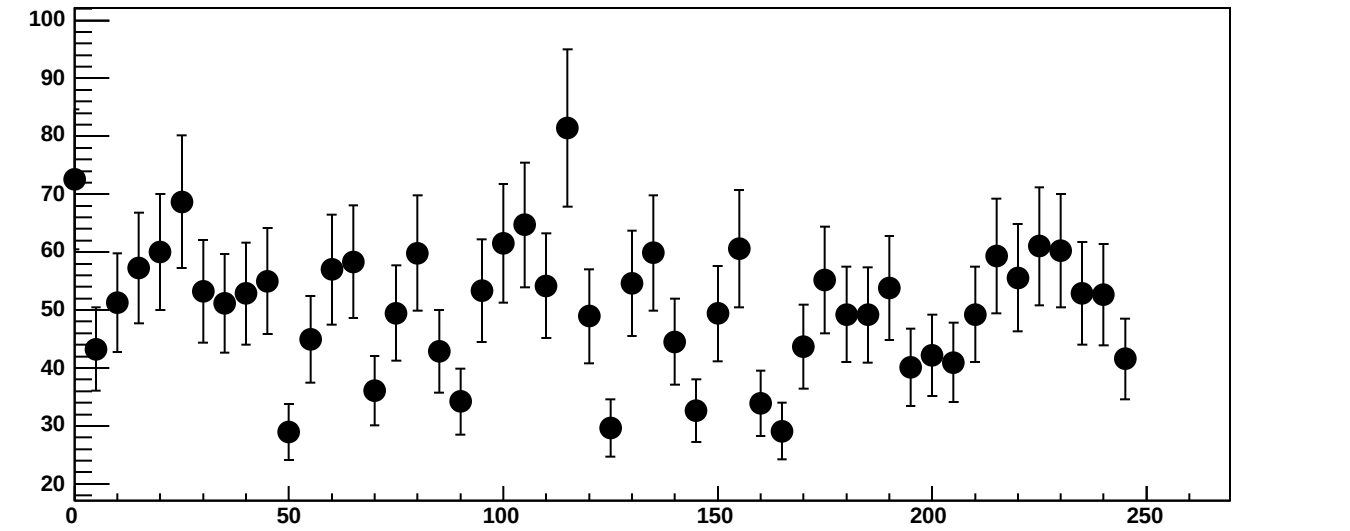
Chip 11, Channel 0, Enable 3!, DAC=1600, ADC Residuals vs Hold



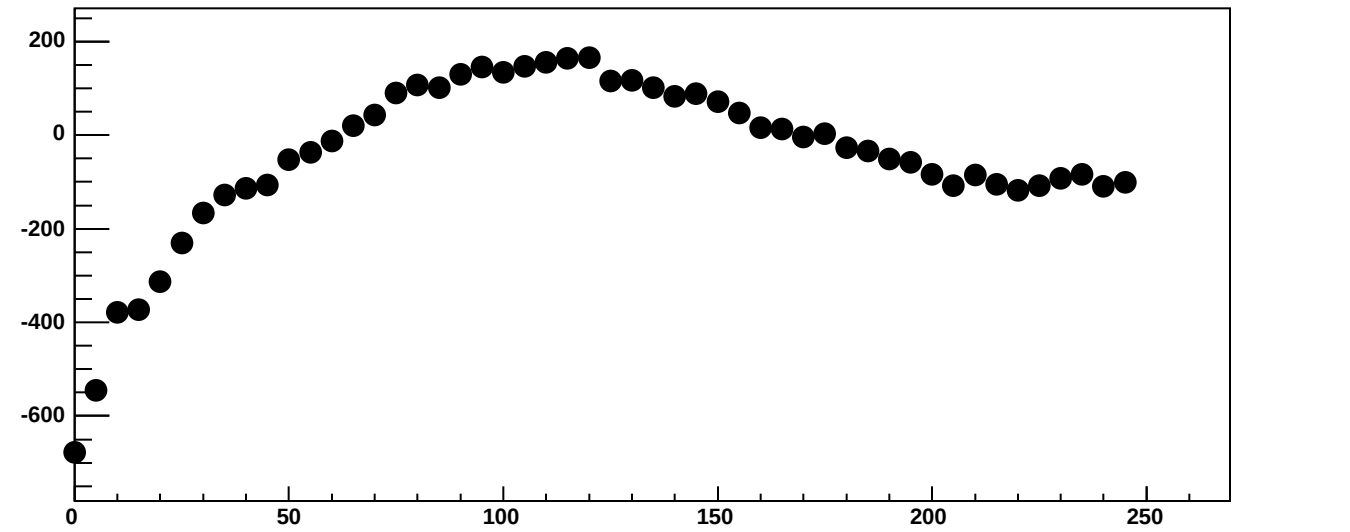
Chip 11, Channel 0, Enable 4, DAC=1600, ADC Mean vs Hold

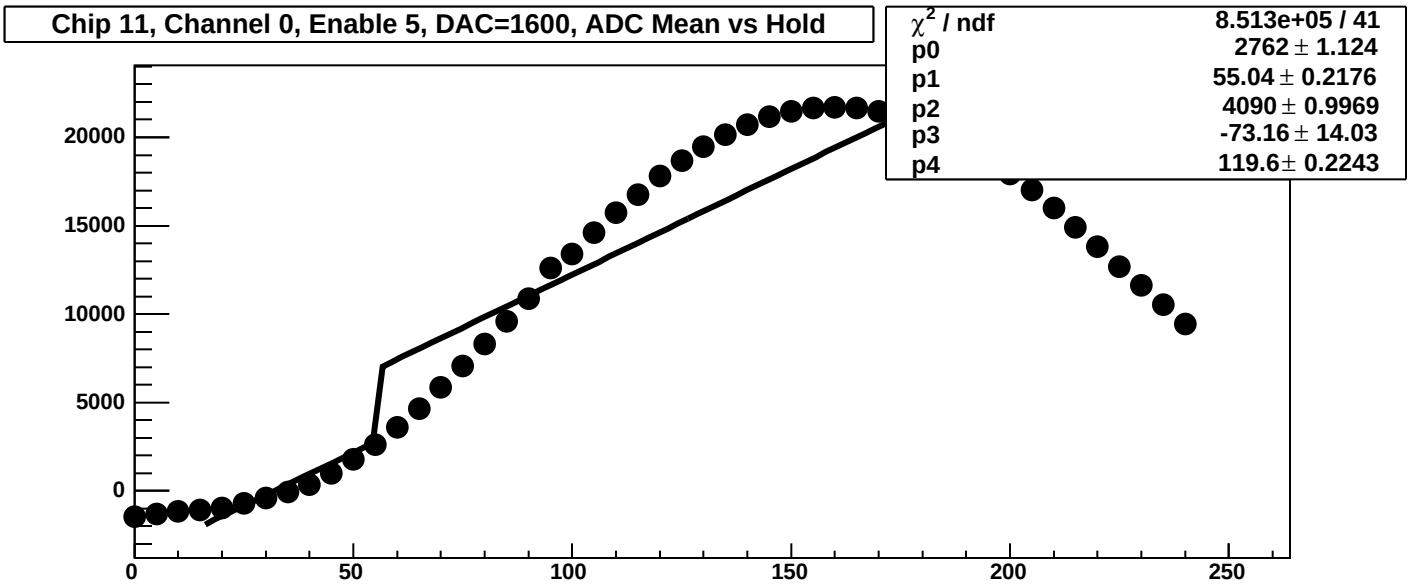


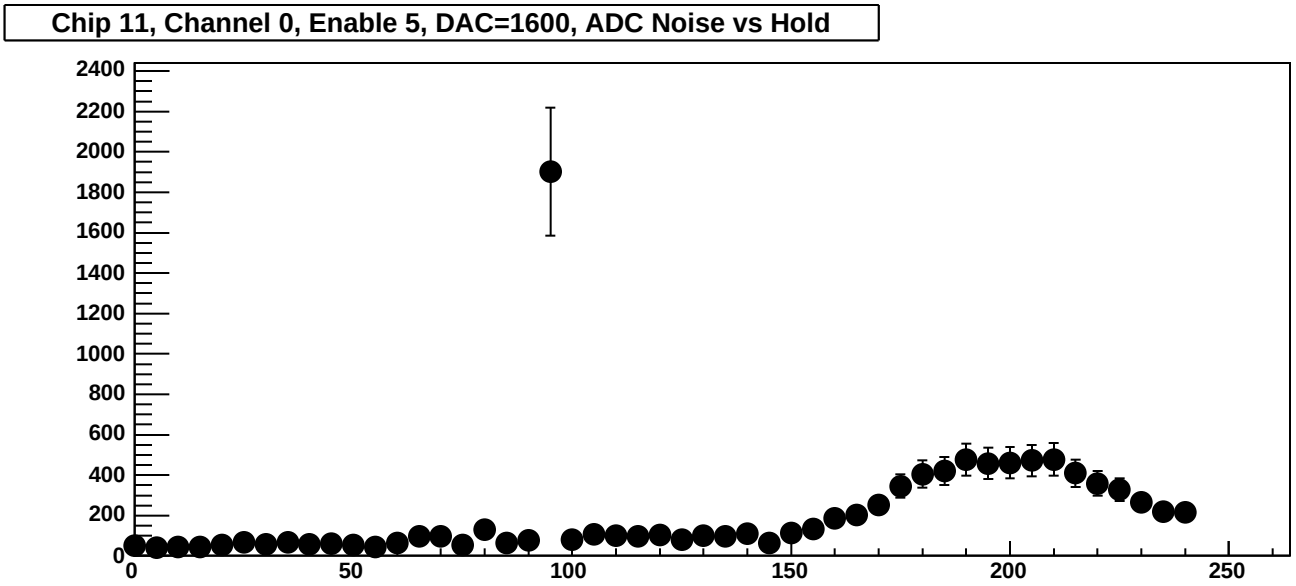
Chip 11, Channel 0, Enable 4, DAC=1600, ADC Noise vs Hold

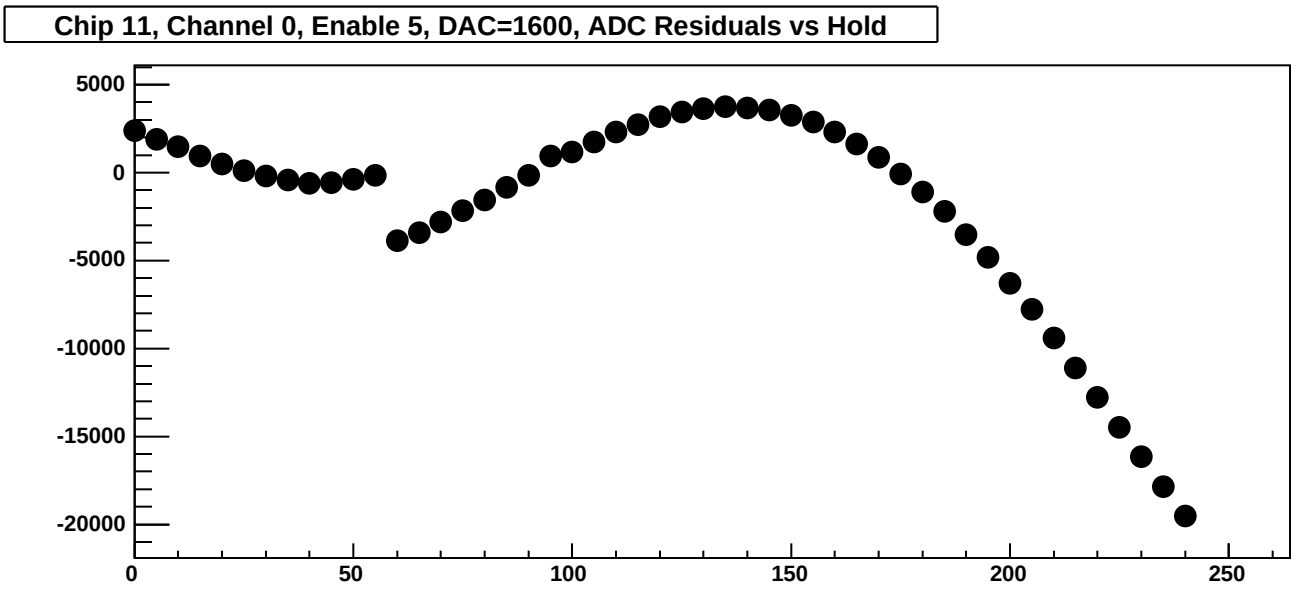


Chip 11, Channel 0, Enable 4, DAC=1600, ADC Residuals vs Hold

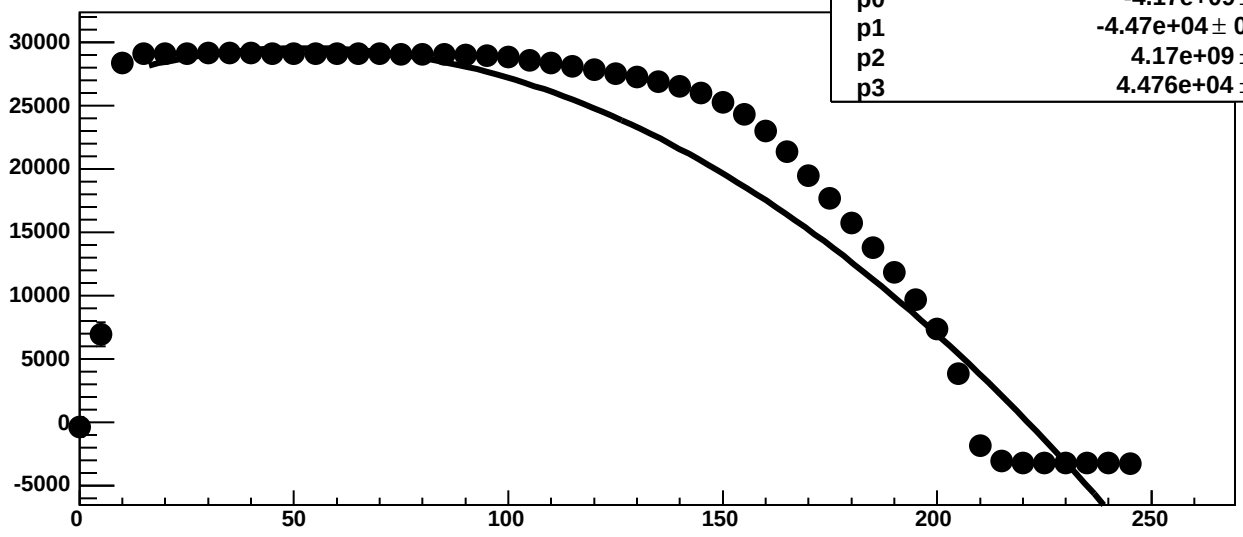



Chip 11, Channel 0, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 8.513e+05 / 41
p0 2762 ± 1.124
p1 55.04 ± 0.2176
p2 4090 ± 0.9969
p3 -73.16 ± 14.03
p4 119.6 ± 0.2243
20000
15000
10000
5000
0
0
50
100
150
200
250

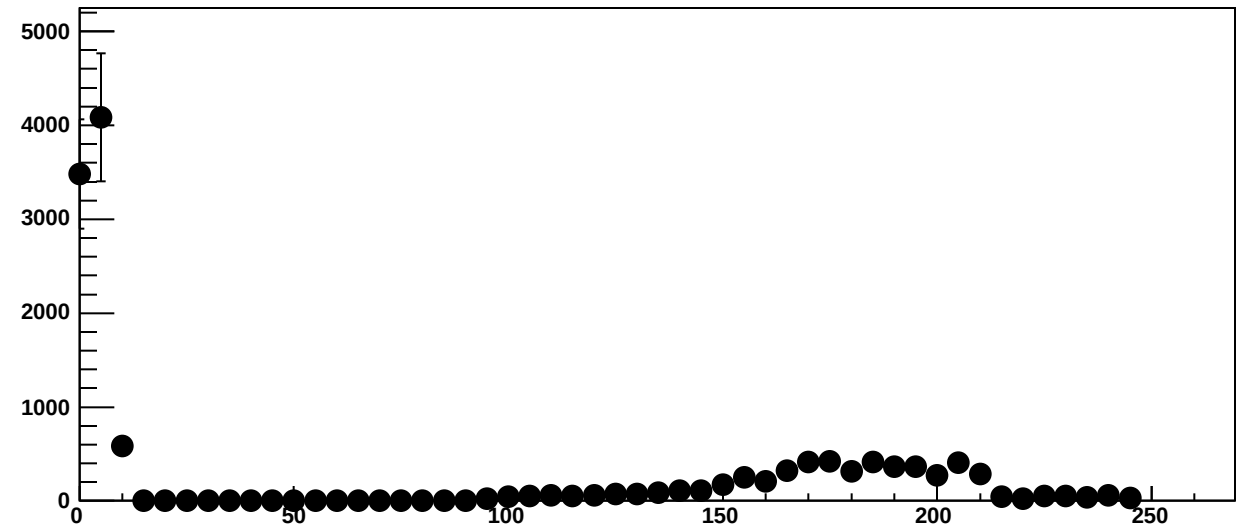

Chip 11, Channel 0, Enable 5, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 11, Channel 0, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

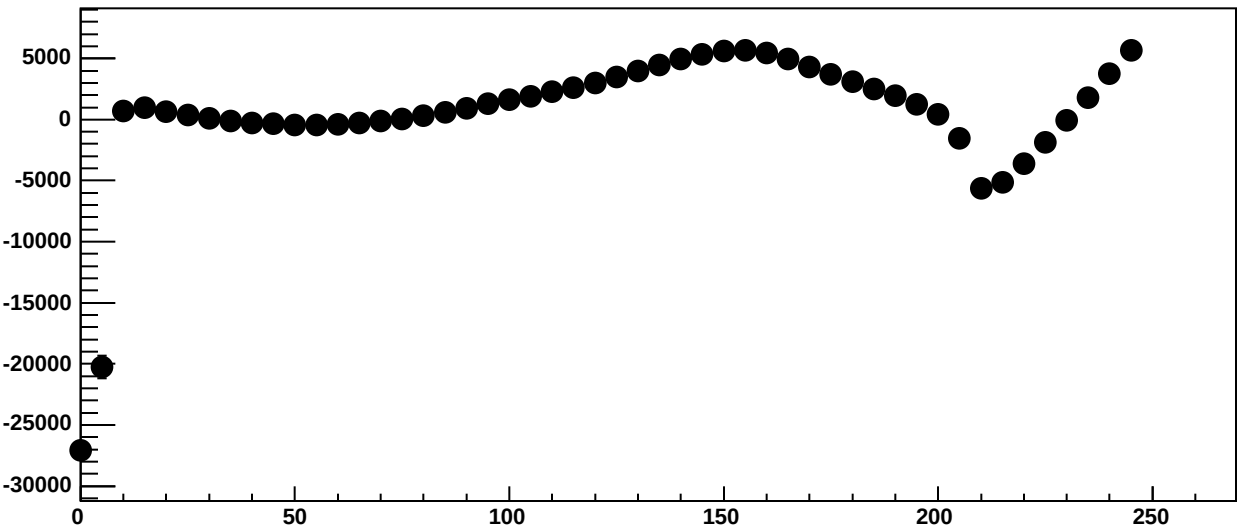
Chip 11, Channel 1, Enable 0!, DAC=1600, ADC Mean vs Hold



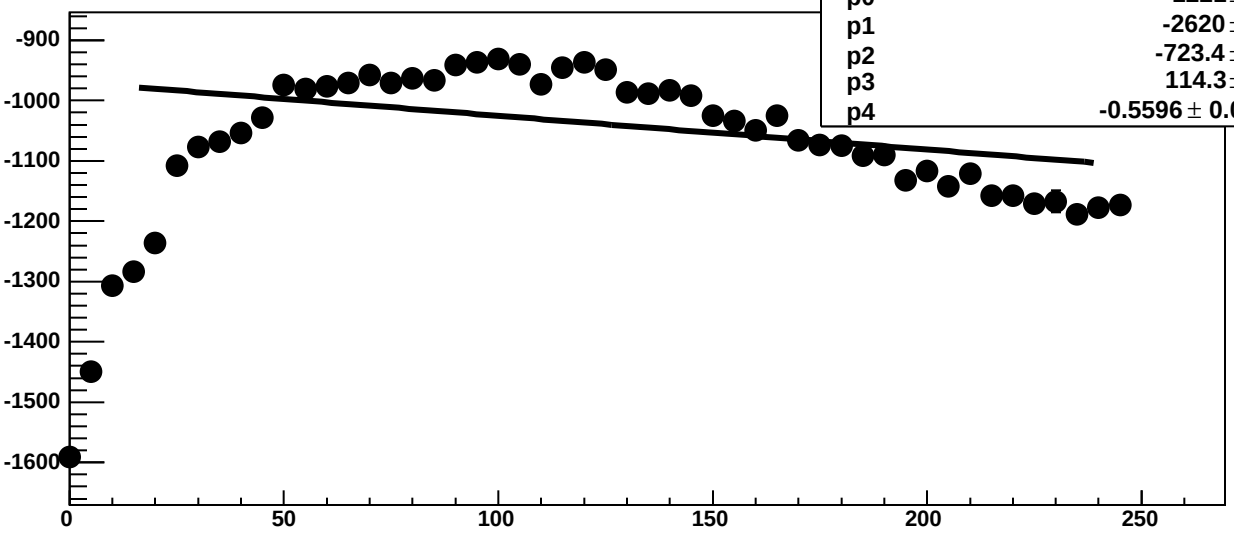
Chip 11, Channel 1, Enable 0!, DAC=1600, ADC Noise vs Hold



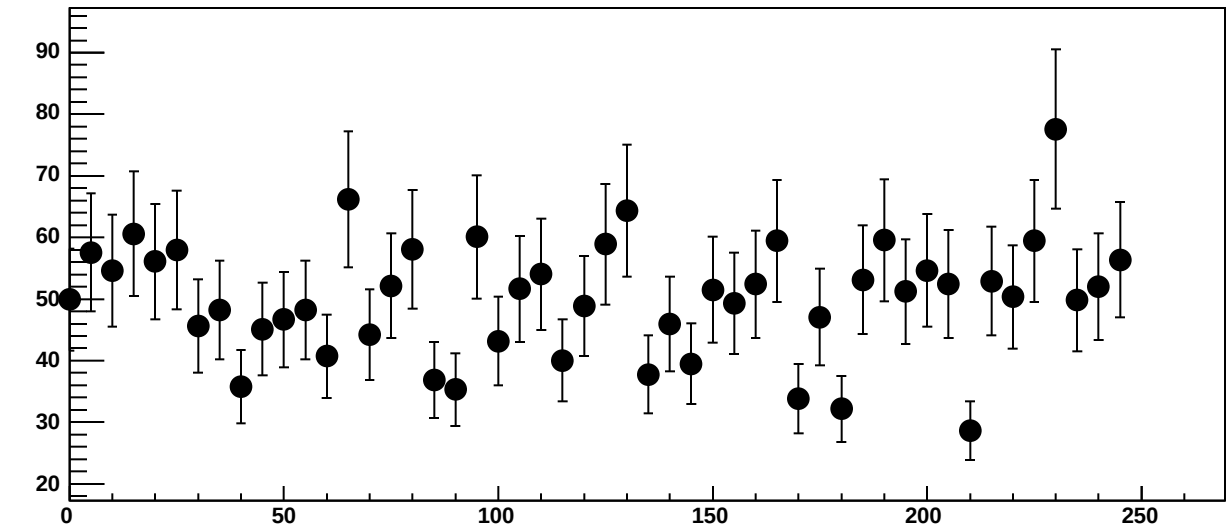
Chip 11, Channel 1, Enable 0!, DAC=1600, ADC Residuals vs Hold



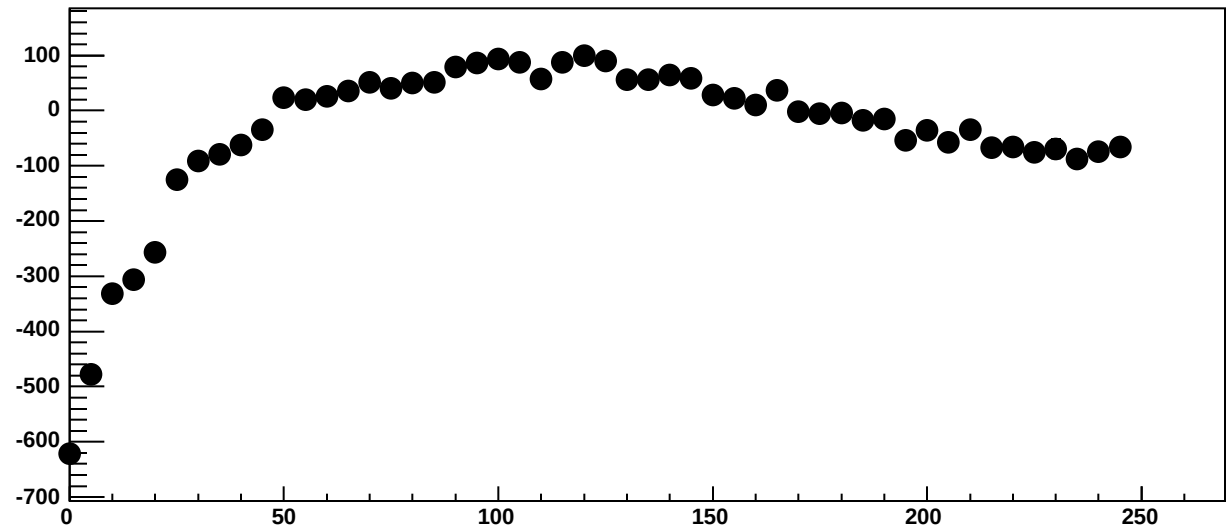
Chip 11, Channel 1, Enable 1, DAC=1600, ADC Mean vs Hold



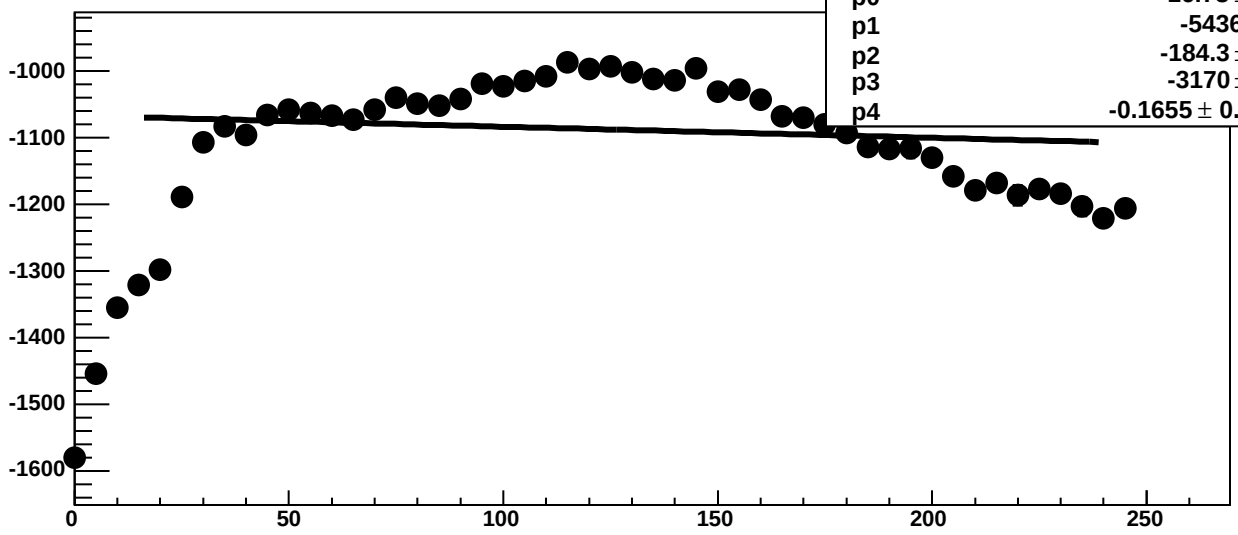
Chip 11, Channel 1, Enable 1, DAC=1600, ADC Noise vs Hold



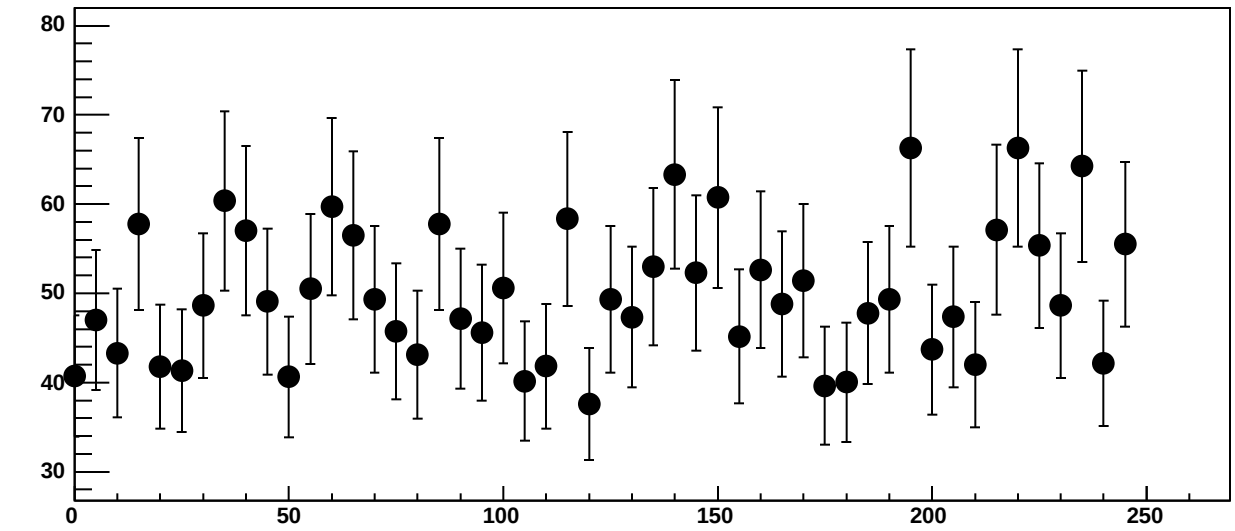
Chip 11, Channel 1, Enable 1, DAC=1600, ADC Residuals vs Hold



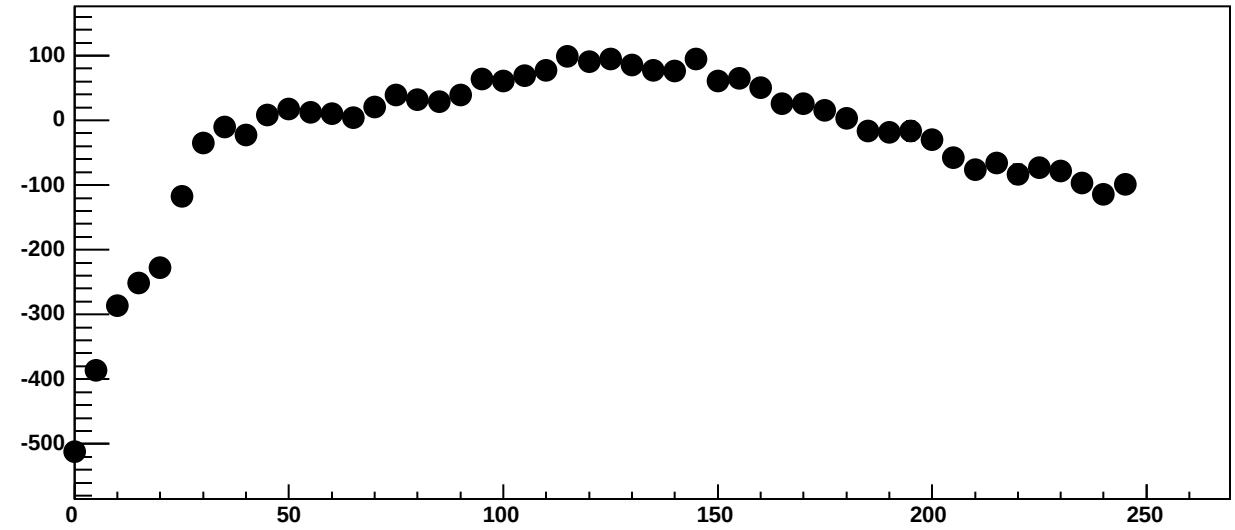
Chip 11, Channel 1, Enable 2, DAC=1600, ADC Mean vs Hold



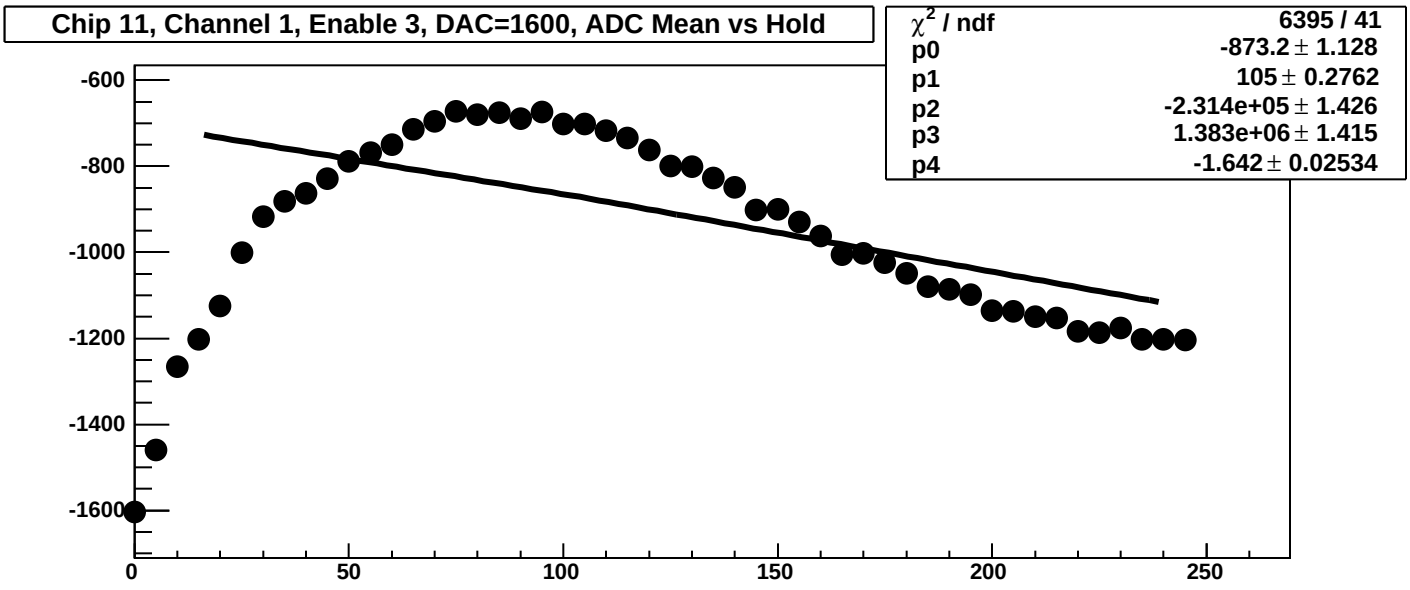
Chip 11, Channel 1, Enable 2, DAC=1600, ADC Noise vs Hold



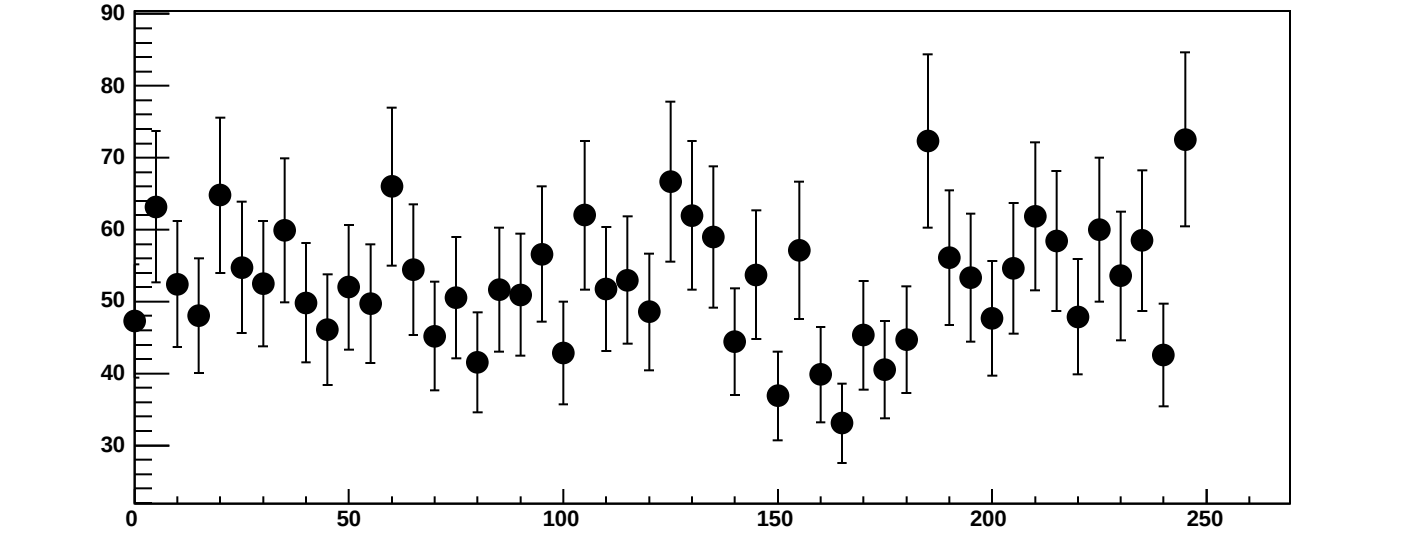
Chip 11, Channel 1, Enable 2, DAC=1600, ADC Residuals vs Hold



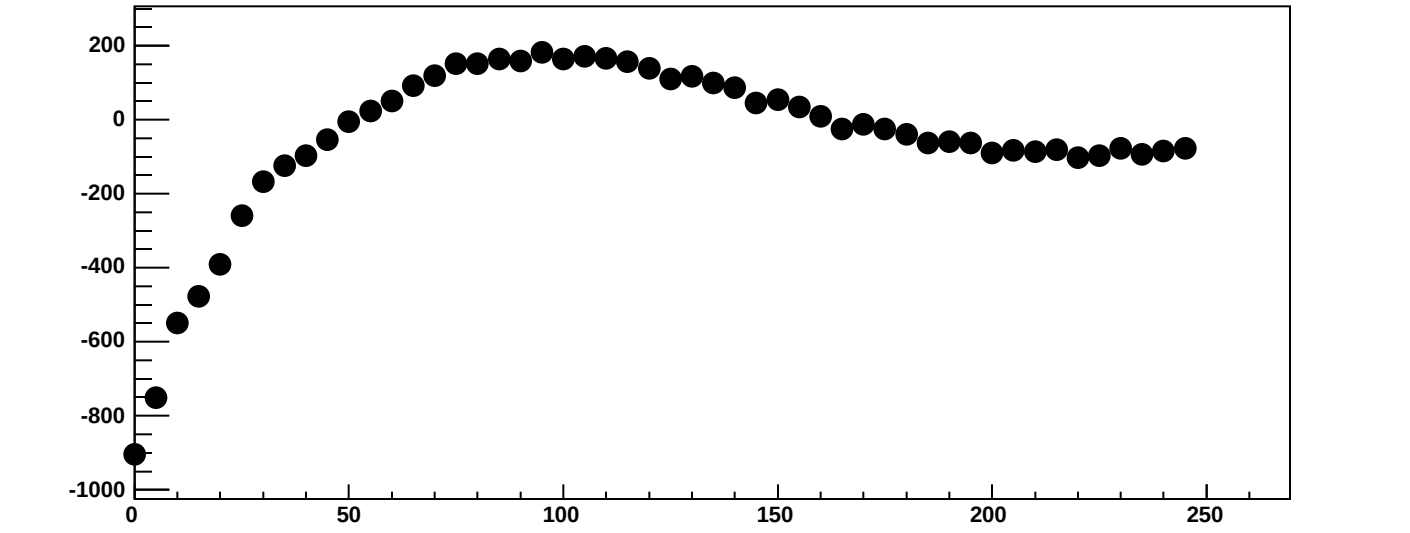
Chip 11, Channel 1, Enable 3, DAC=1600, ADC Mean vs Hold



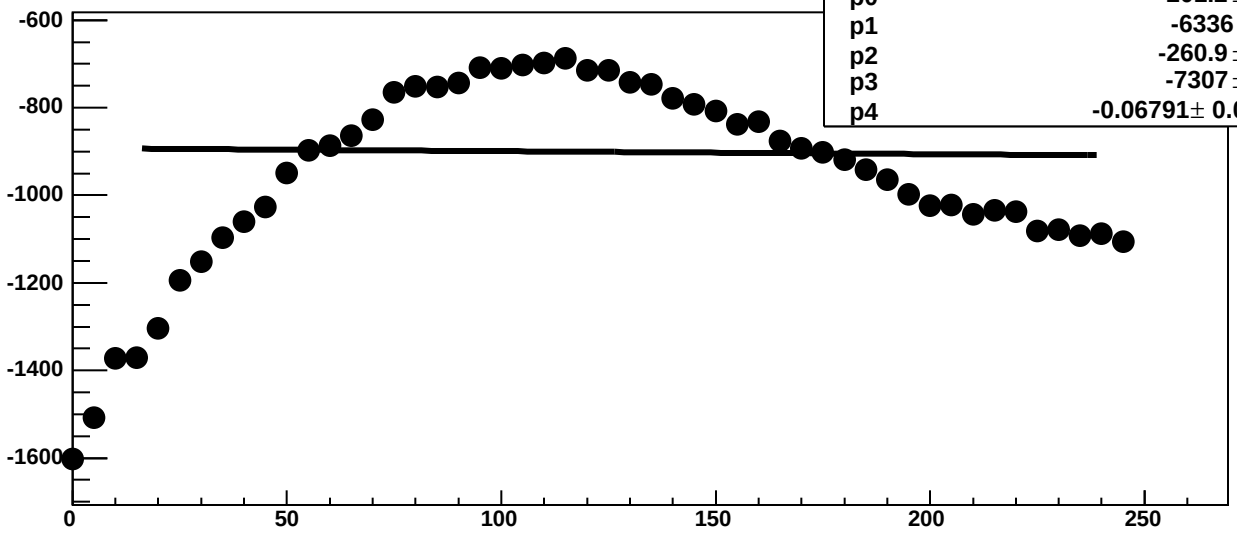
Chip 11, Channel 1, Enable 3, DAC=1600, ADC Noise vs Hold



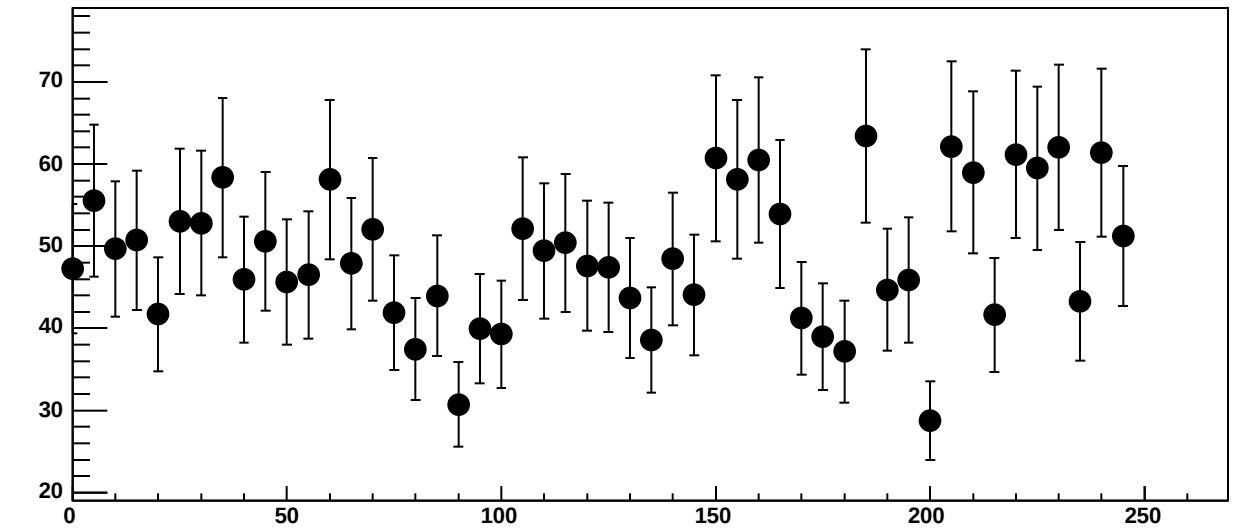
Chip 11, Channel 1, Enable 3, DAC=1600, ADC Residuals vs Hold



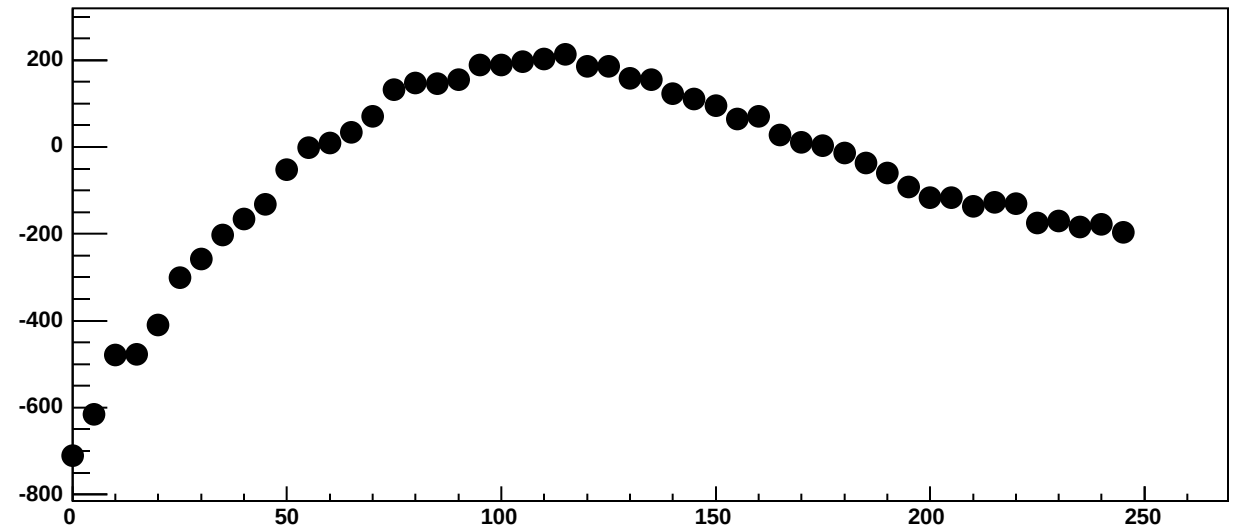
Chip 11, Channel 1, Enable 4, DAC=1600, ADC Mean vs Hold

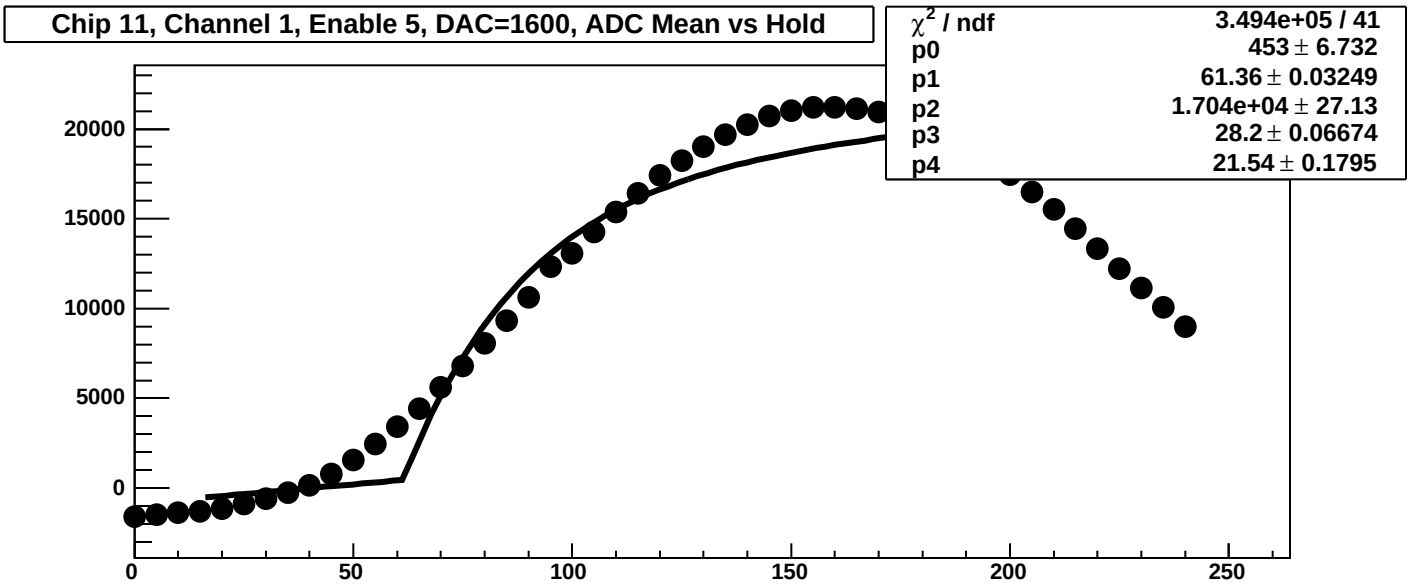


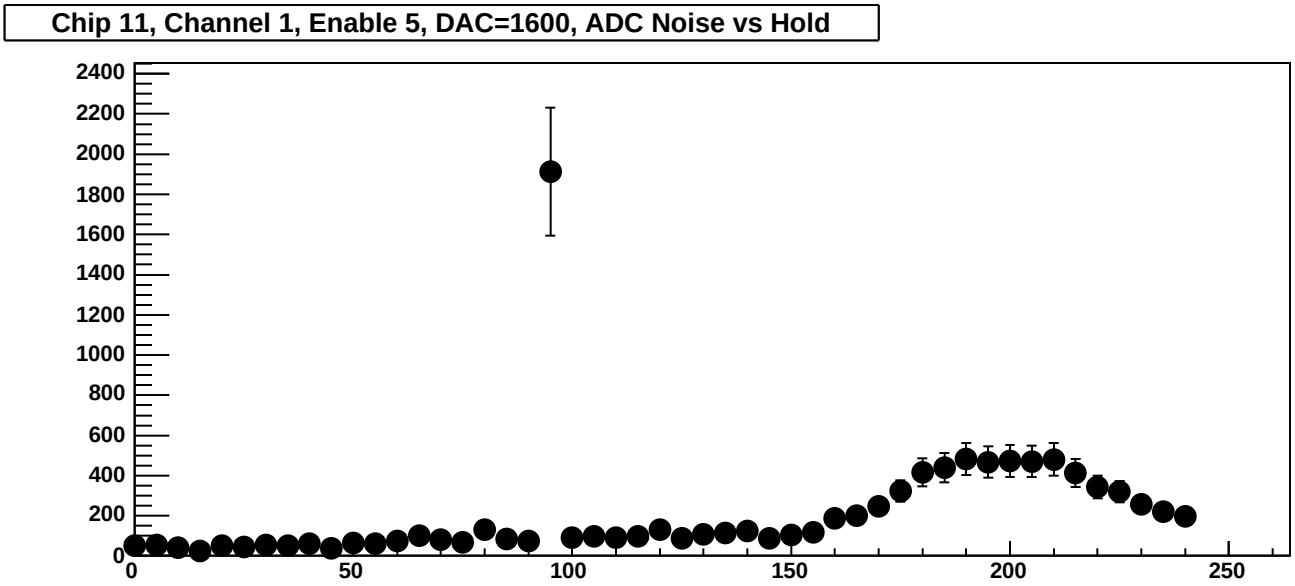
Chip 11, Channel 1, Enable 4, DAC=1600, ADC Noise vs Hold

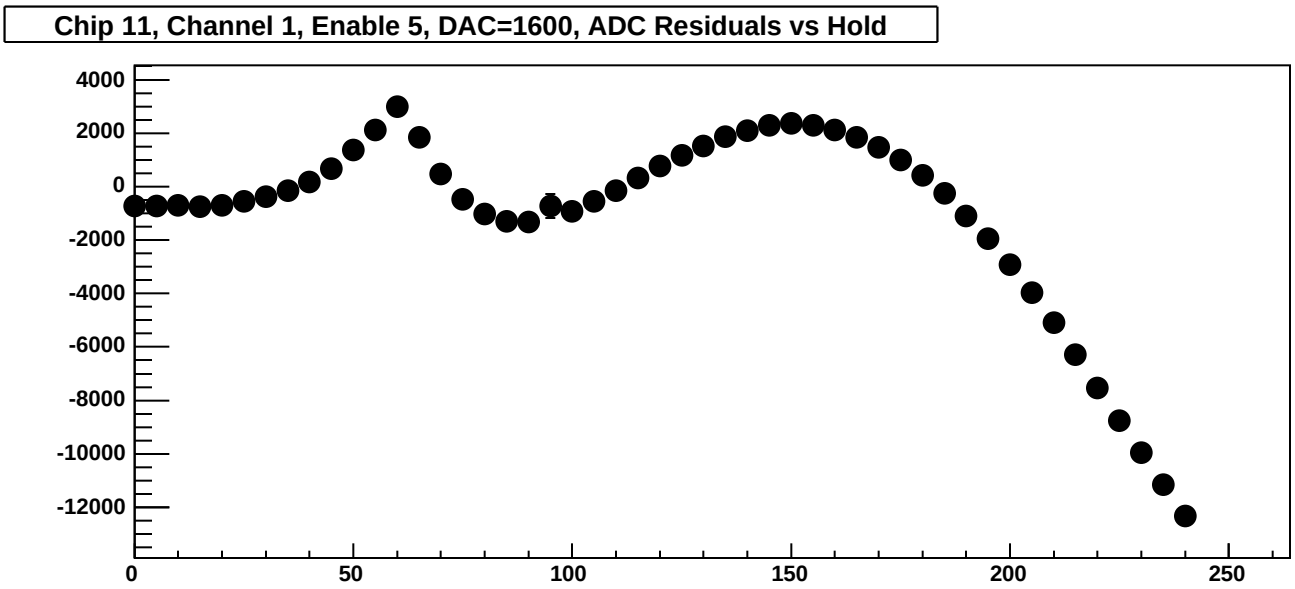


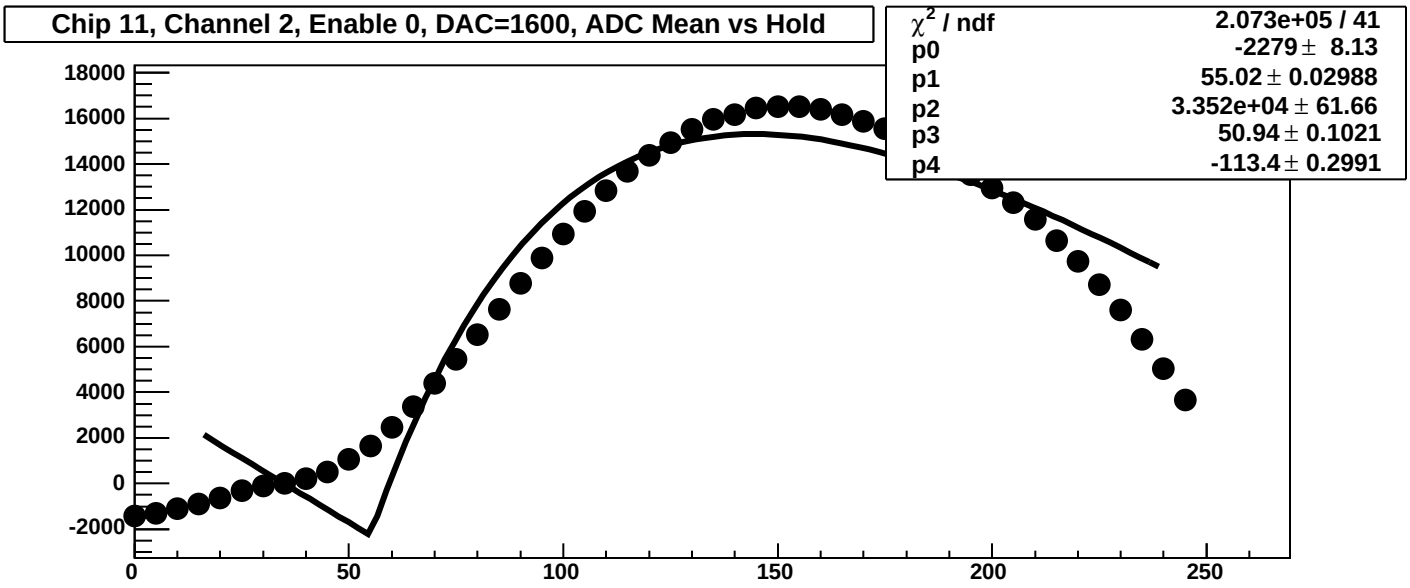
Chip 11, Channel 1, Enable 4, DAC=1600, ADC Residuals vs Hold

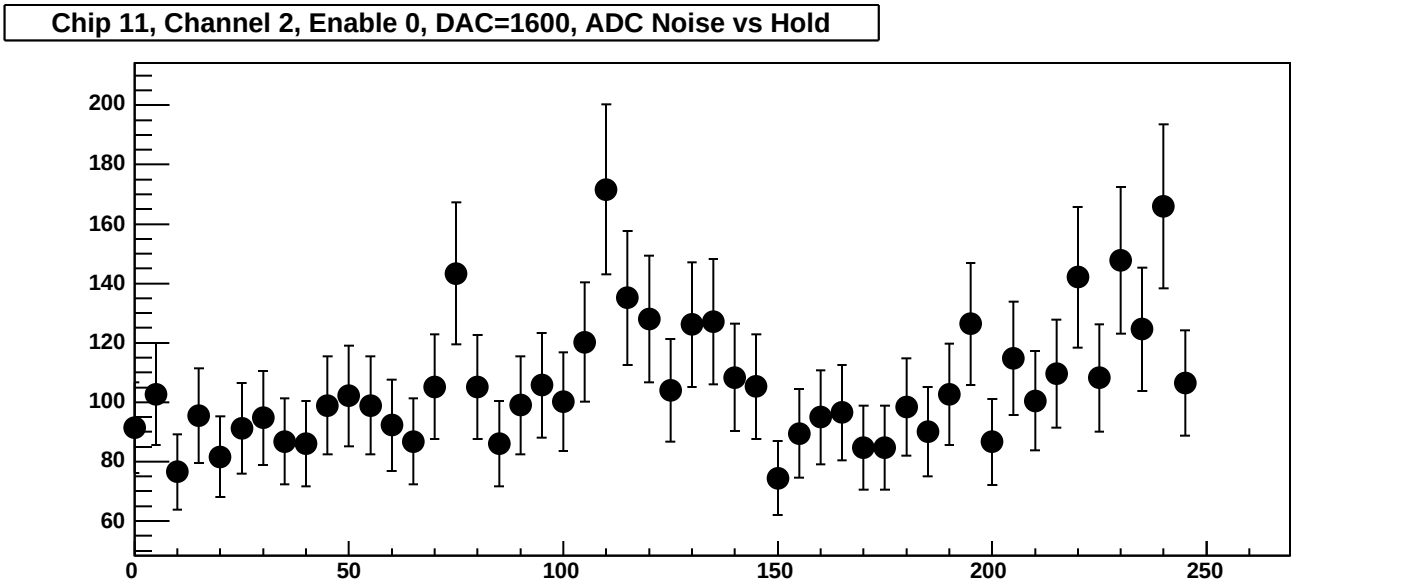


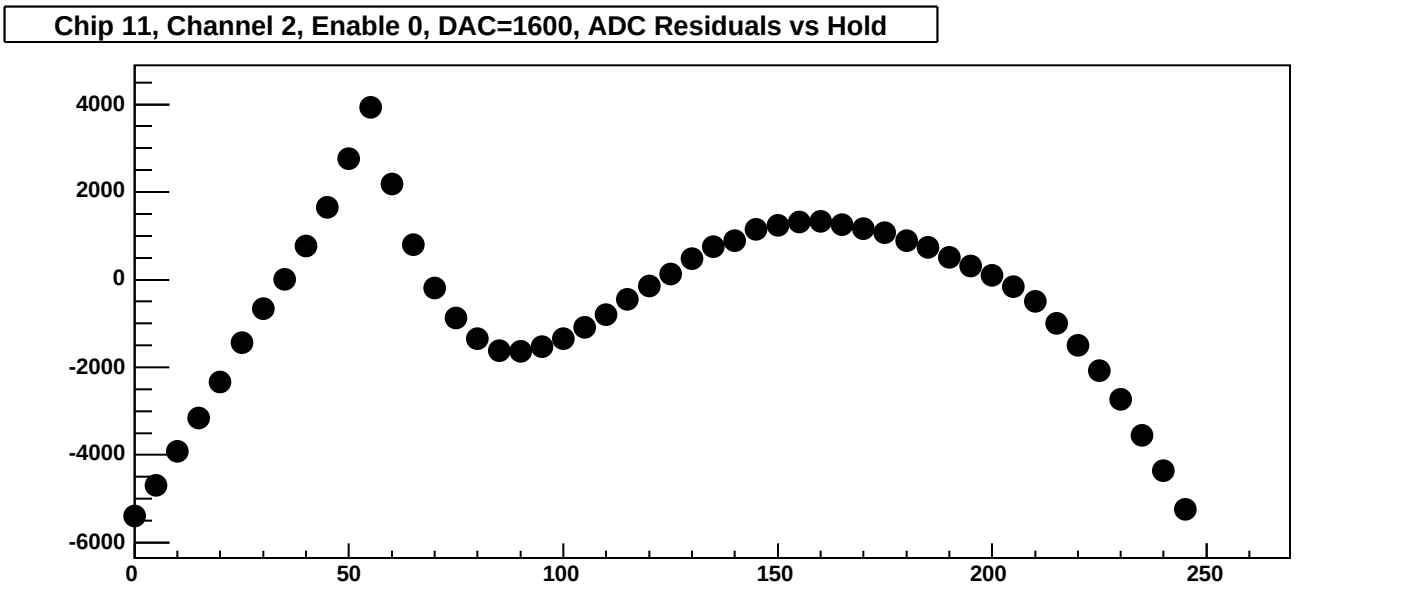

Chip 11, Channel 1, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.494e+05 / 41
p0 453 ± 6.732
p1 61.36 ± 0.03249
p2 1.704e+04 ± 27.13
p3 28.2 ± 0.06674
p4 21.54 ± 0.1795
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 11, Channel 1, Enable 5, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

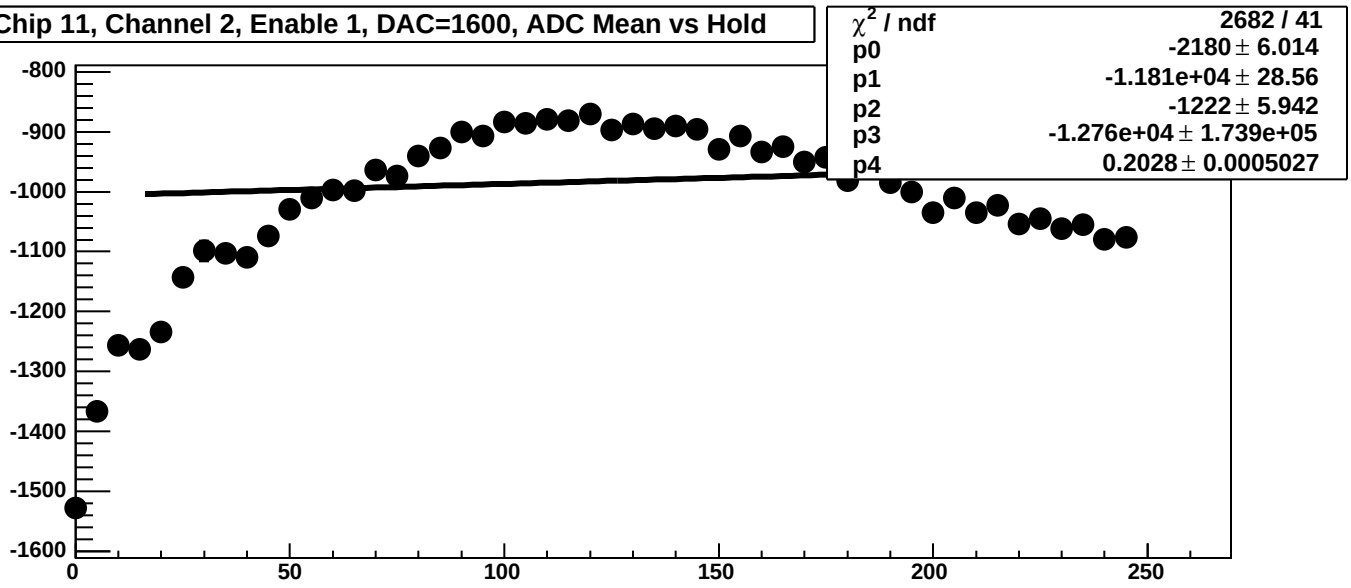

Chip 11, Channel 1, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
0
50
100
150
200
250


Chip 11, Channel 2, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.073e+05 / 41
p0 -2279 ± 8.13
p1 55.02 ± 0.02988
p2 3.352e+04 ± 61.66
p3 50.94 ± 0.1021
p4 -113.4 ± 0.2991
18000
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

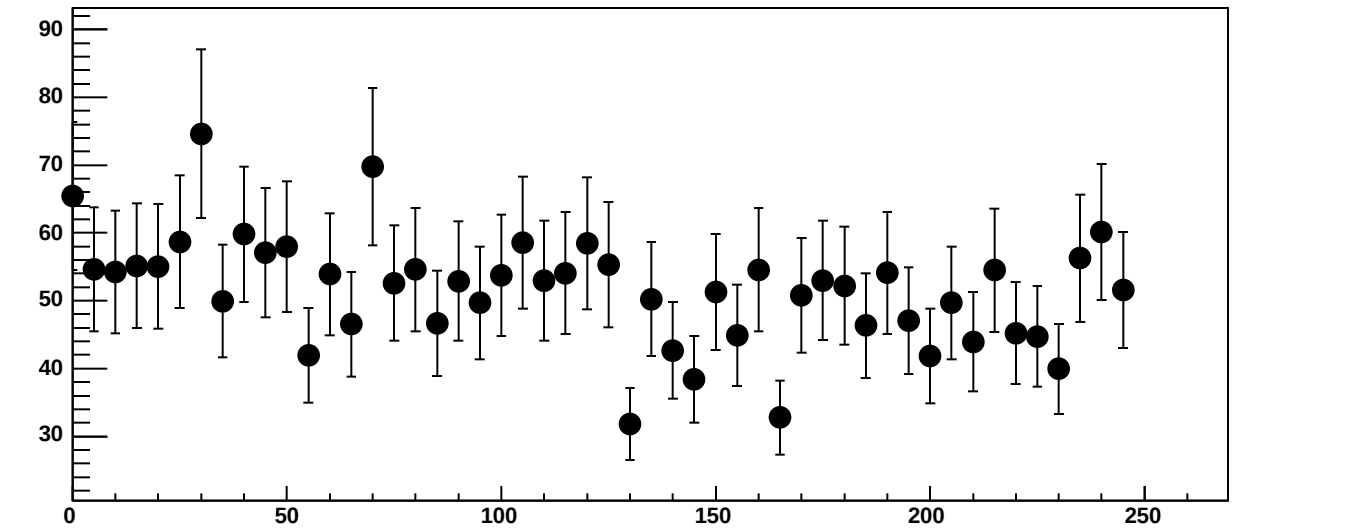

Chip 11, Channel 2, Enable 0, DAC=1600, ADC Noise vs Hold
200
180
160
140
120
100
80
60
0
50
100
150
200
250


Chip 11, Channel 2, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

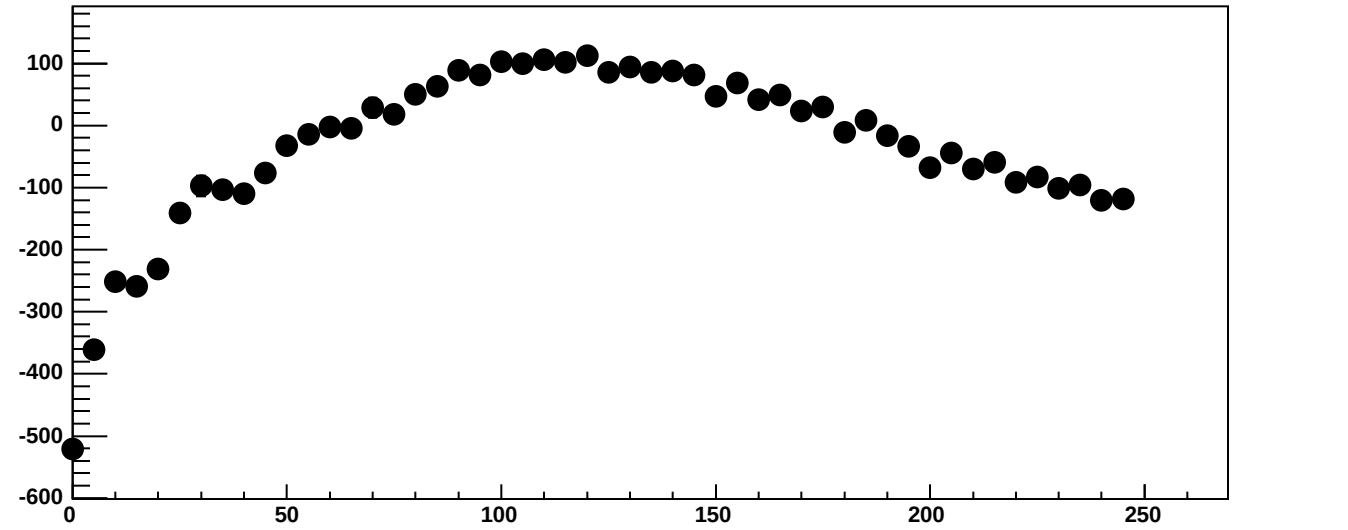
Chip 11, Channel 2, Enable 1, DAC=1600, ADC Mean vs Hold



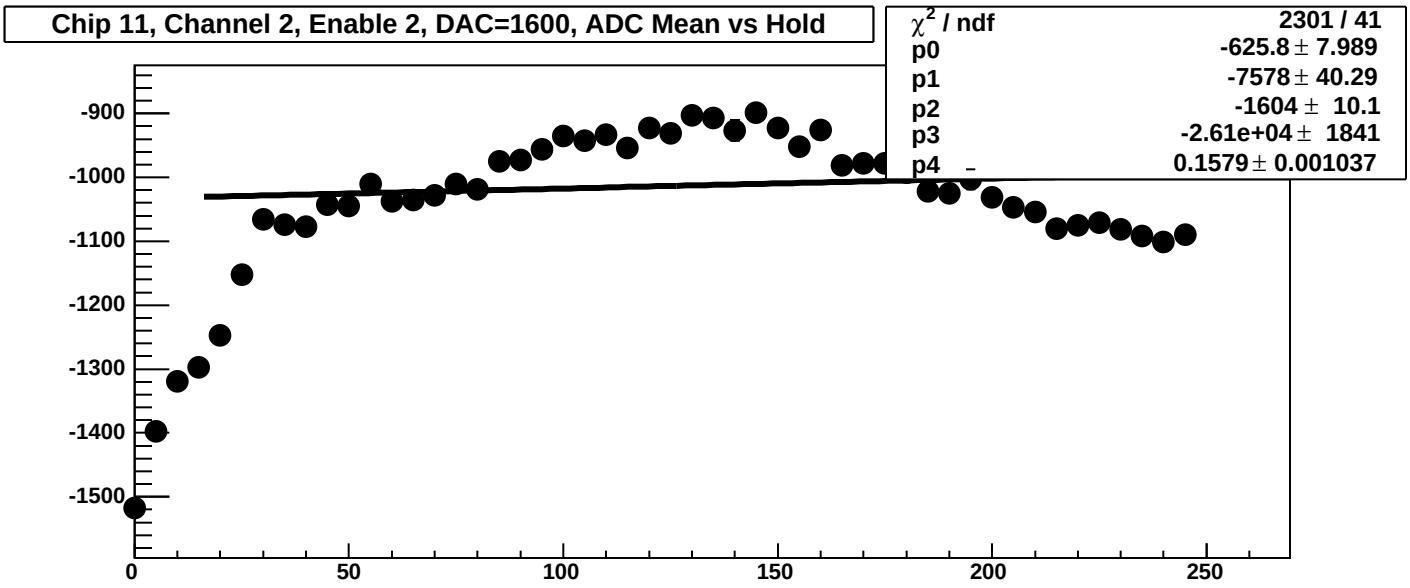
Chip 11, Channel 2, Enable 1, DAC=1600, ADC Noise vs Hold



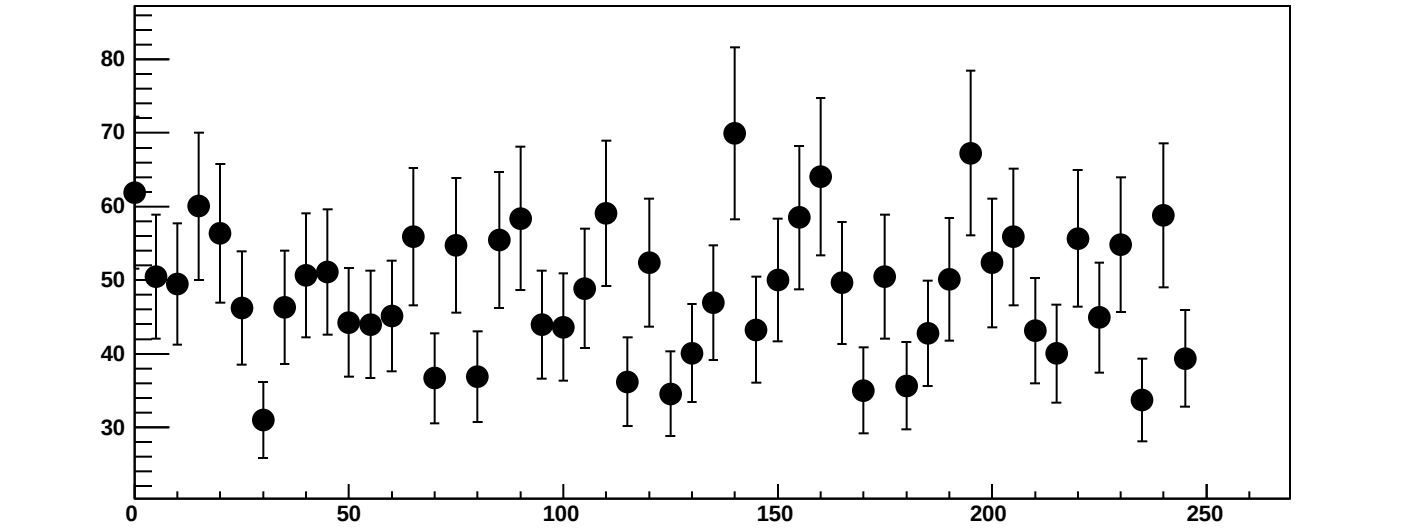
Chip 11, Channel 2, Enable 1, DAC=1600, ADC Residuals vs Hold



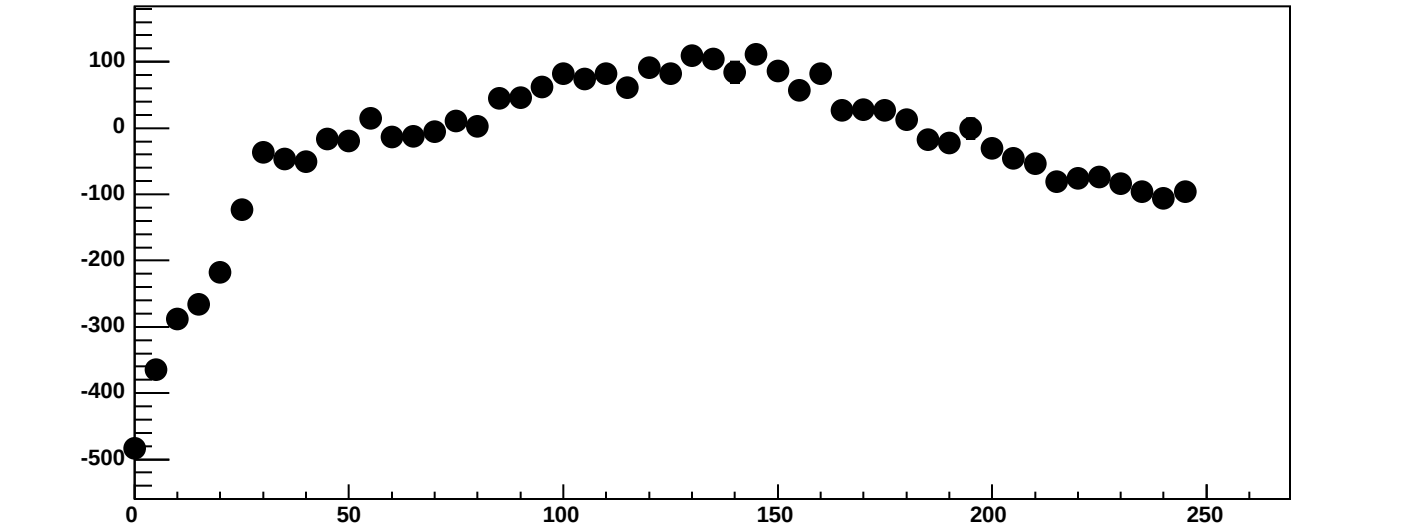
Chip 11, Channel 2, Enable 2, DAC=1600, ADC Mean vs Hold

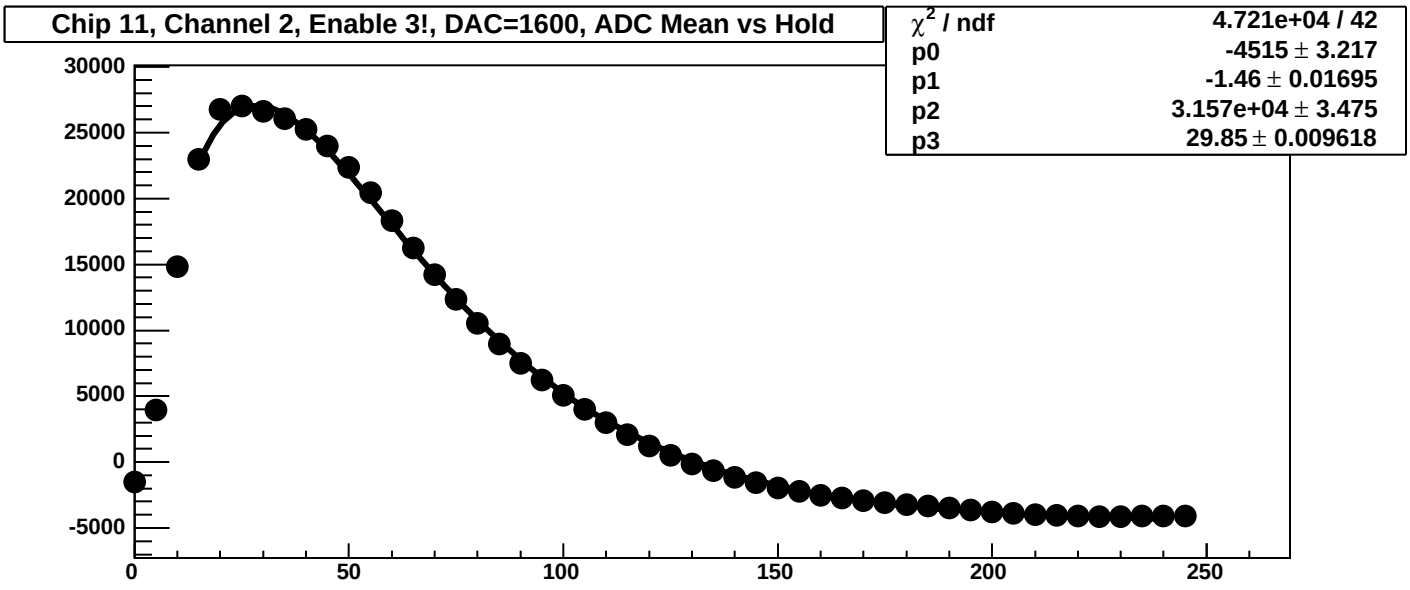


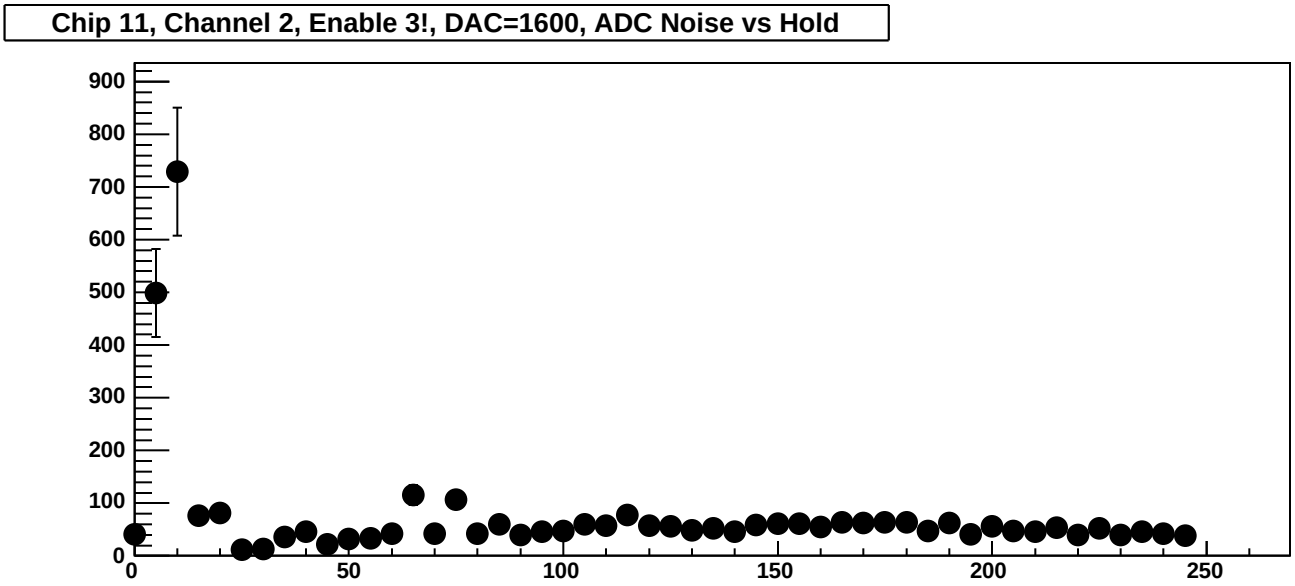
Chip 11, Channel 2, Enable 2, DAC=1600, ADC Noise vs Hold

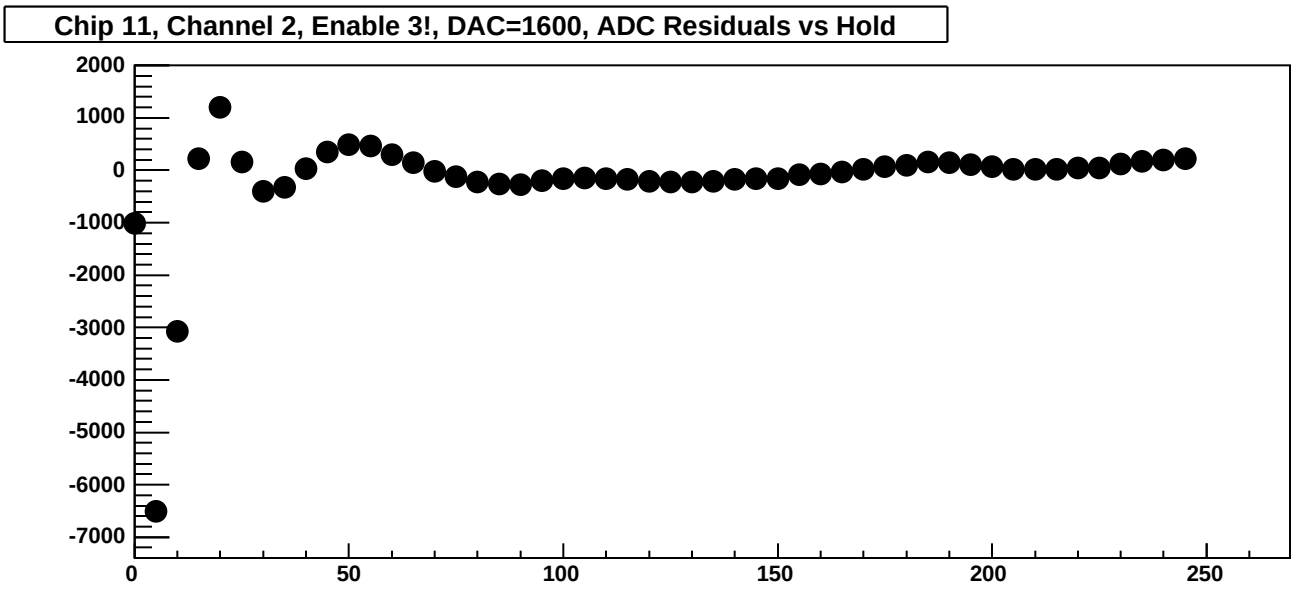


Chip 11, Channel 2, Enable 2, DAC=1600, ADC Residuals vs Hold

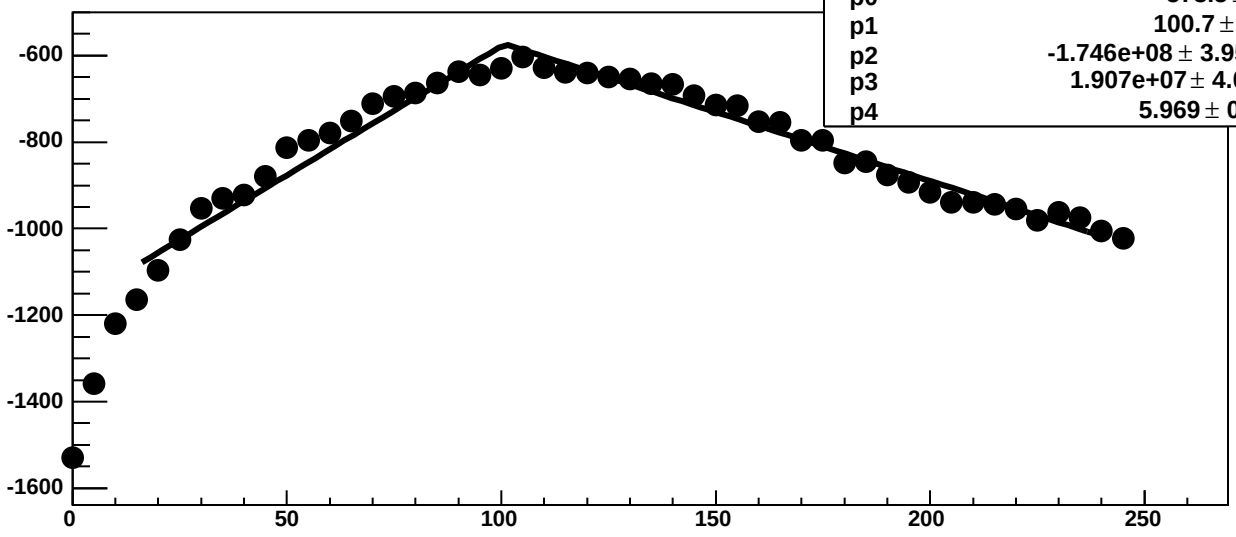



Chip 11, Channel 2, Enable 3!, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.721e+04 / 42
p0 -4515 ± 3.217
p1 -1.46 ± 0.01695
p2 3.157e+04 ± 3.475
p3 29.85 ± 0.009618
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

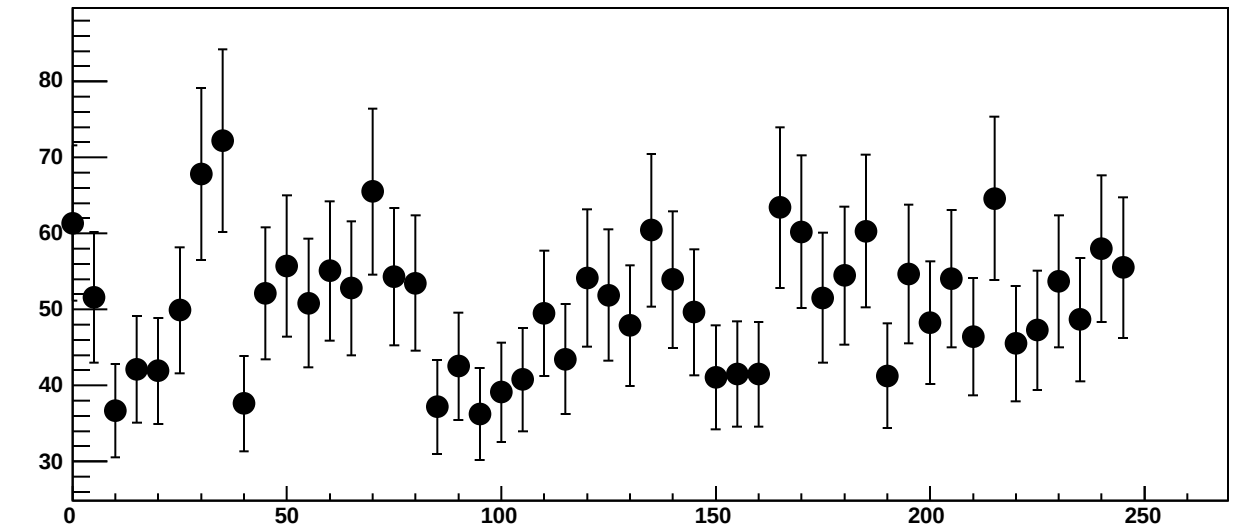

Chip 11, Channel 2, Enable 3!, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 11, Channel 2, Enable 3!, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
-7000
0
50
100
150
200
250

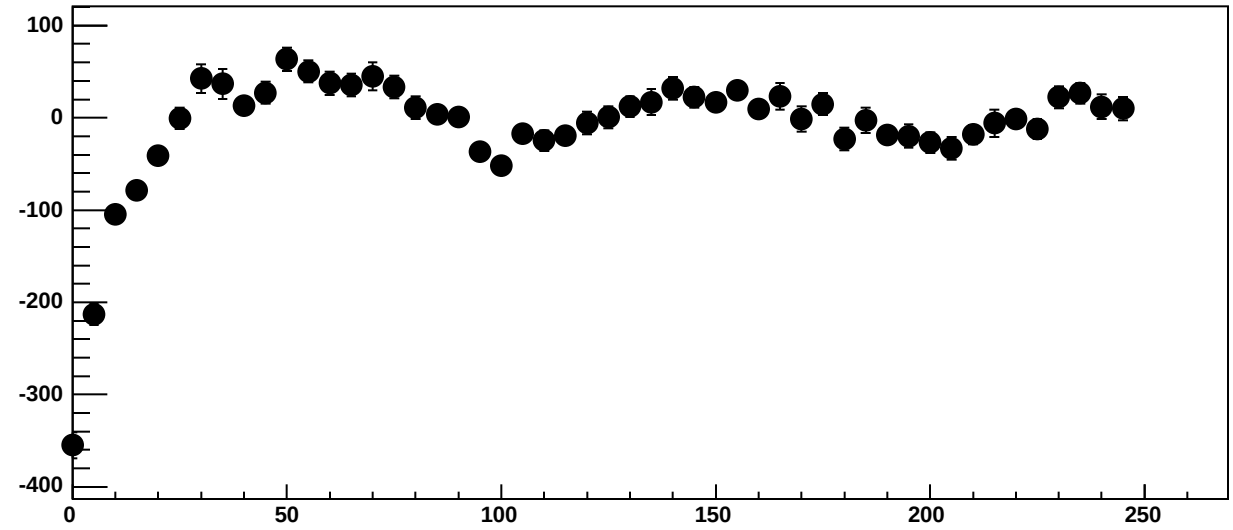
Chip 11, Channel 2, Enable 4, DAC=1600, ADC Mean vs Hold

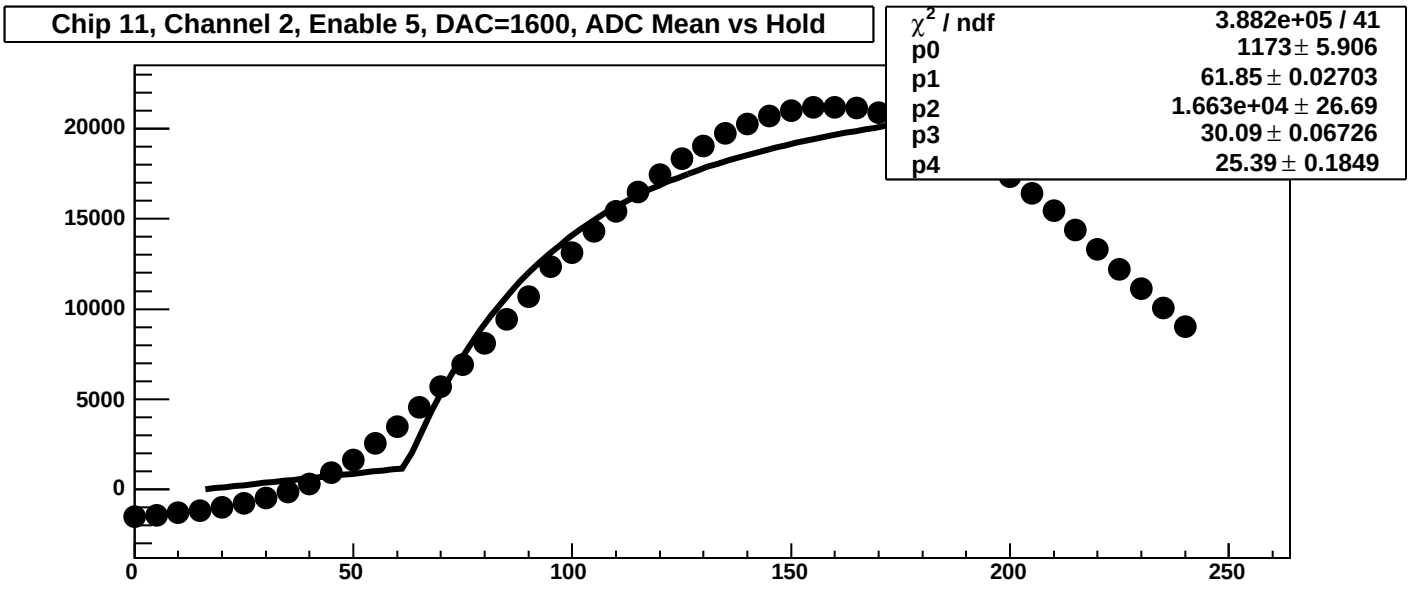


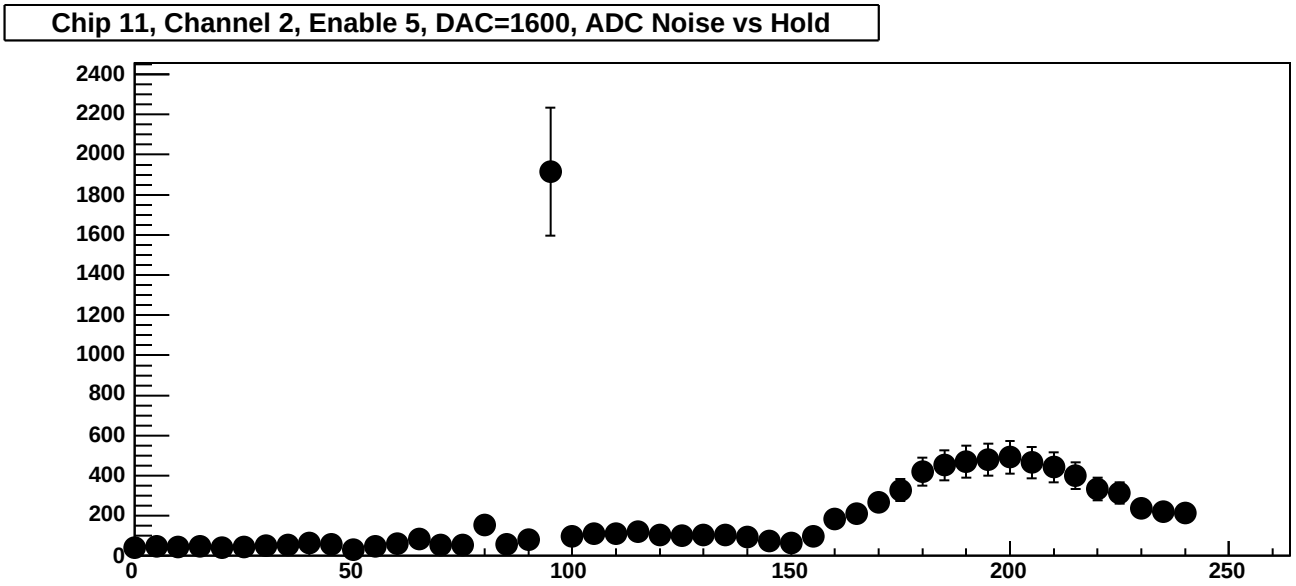
Chip 11, Channel 2, Enable 4, DAC=1600, ADC Noise vs Hold

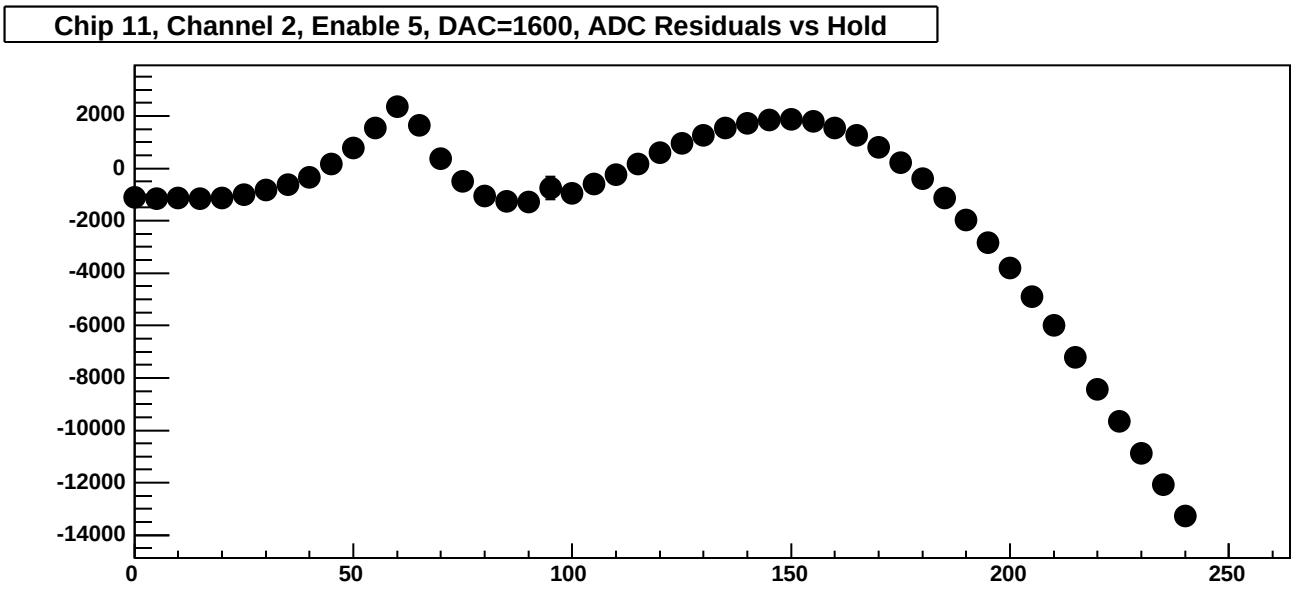


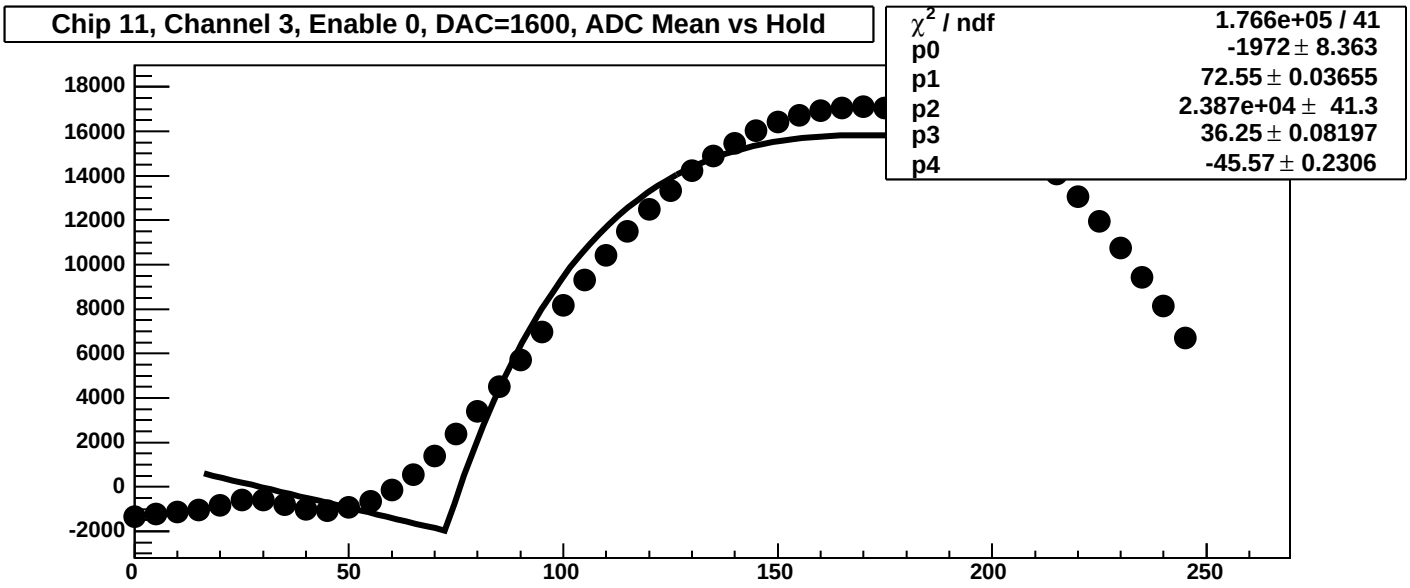
Chip 11, Channel 2, Enable 4, DAC=1600, ADC Residuals vs Hold

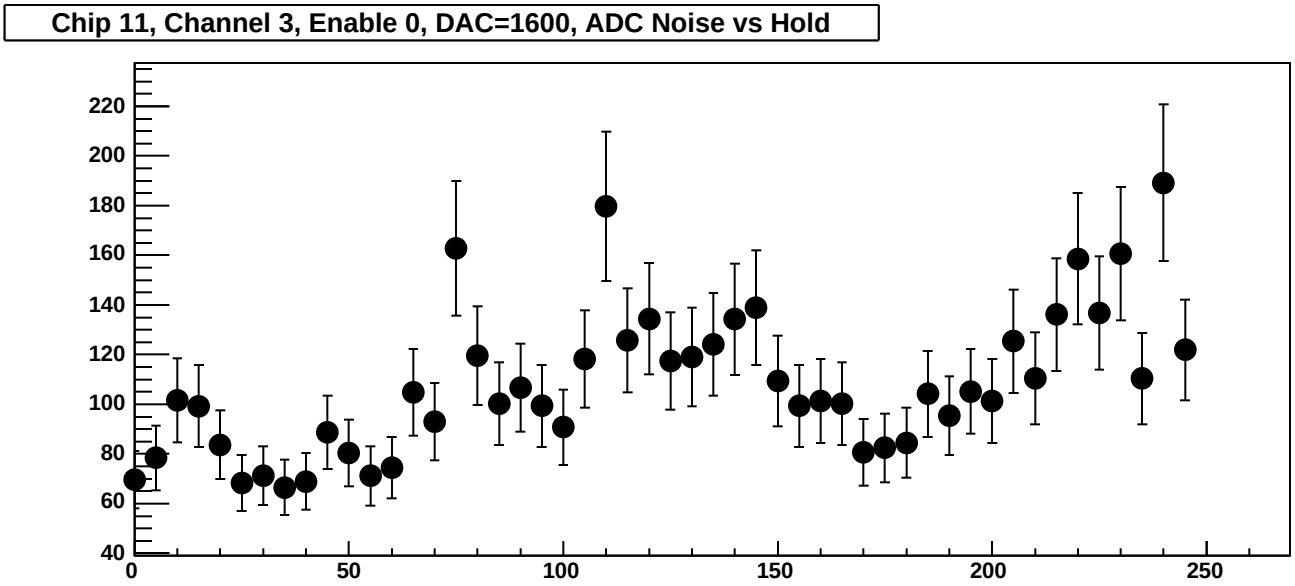


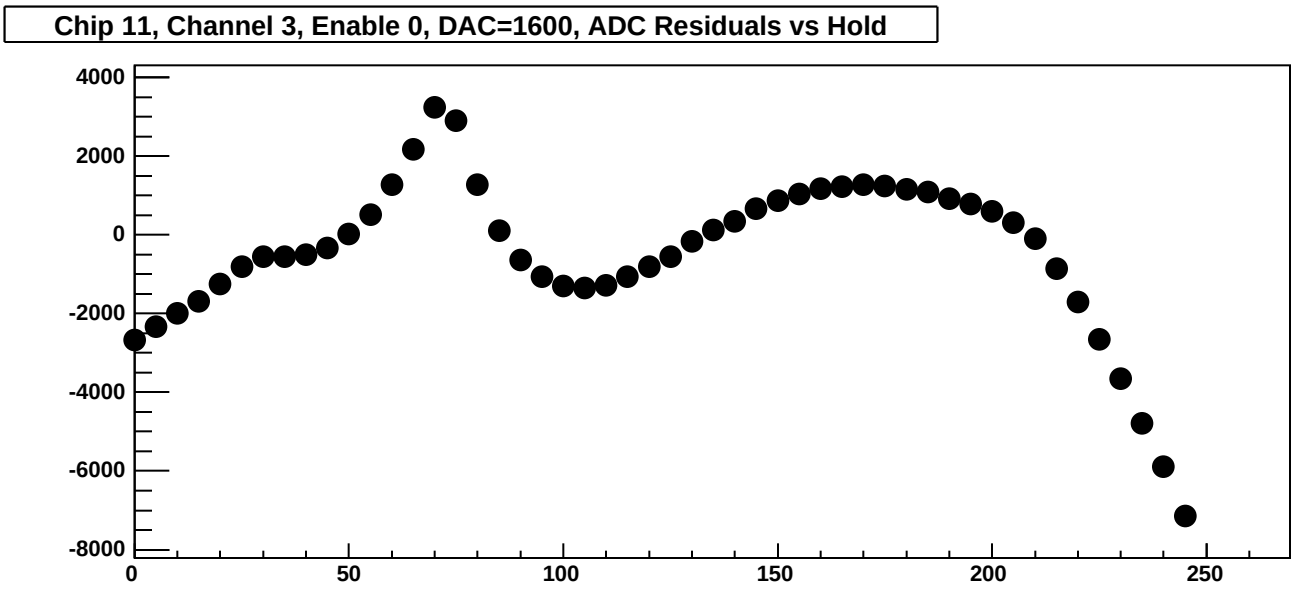

Chip 11, Channel 2, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.882e+05 / 41
p0 1173 ± 5.906
p1 61.85 ± 0.02703
p2 1.663e+04 ± 26.69
p3 30.09 ± 0.06726
p4 25.39 ± 0.1849
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 11, Channel 2, Enable 5, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

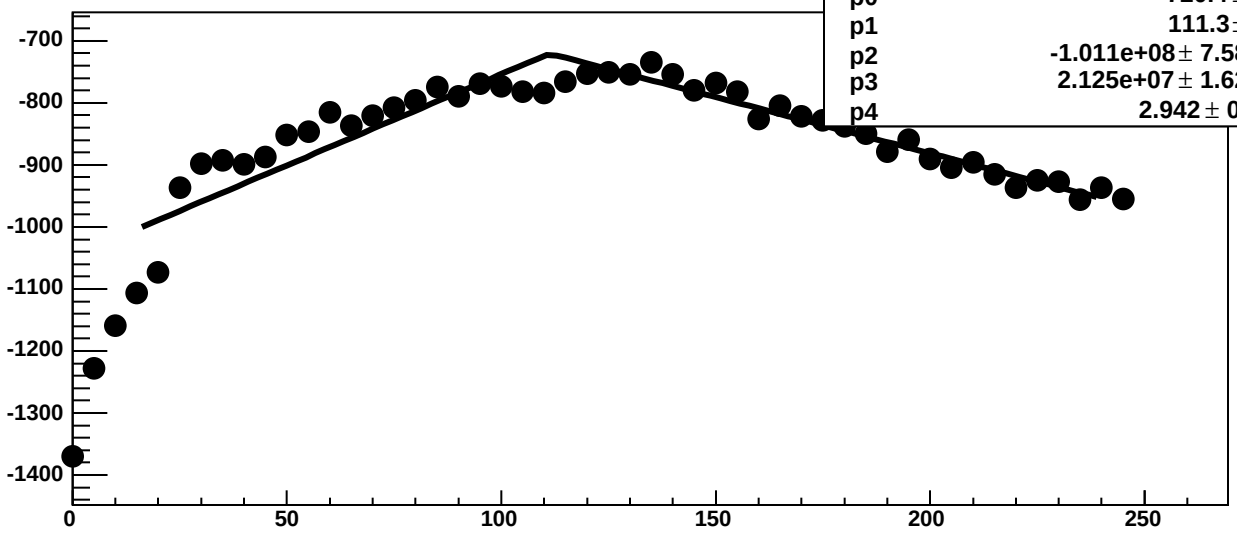

Chip 11, Channel 2, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
50
100
150
200
250


Chip 11, Channel 3, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.766e+05 / 41
p0 -1972 ± 8.363
p1 72.55 ± 0.03655
p2 2.387e+04 ± 41.3
p3 36.25 ± 0.08197
p4 -45.57 ± 0.2306
18000
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

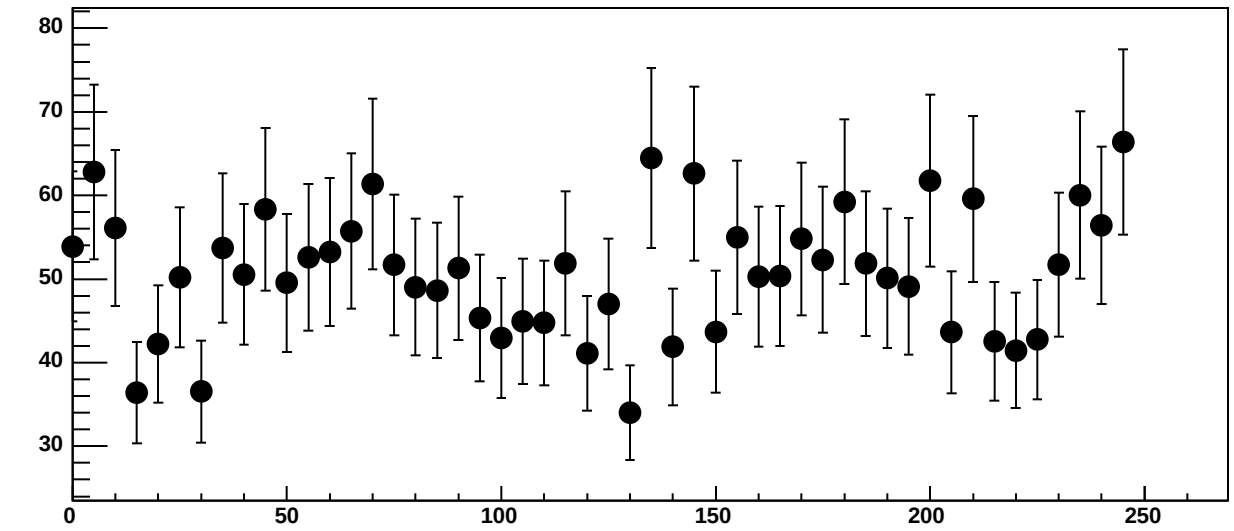

Chip 11, Channel 3, Enable 0, DAC=1600, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250


Chip 11, Channel 3, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250

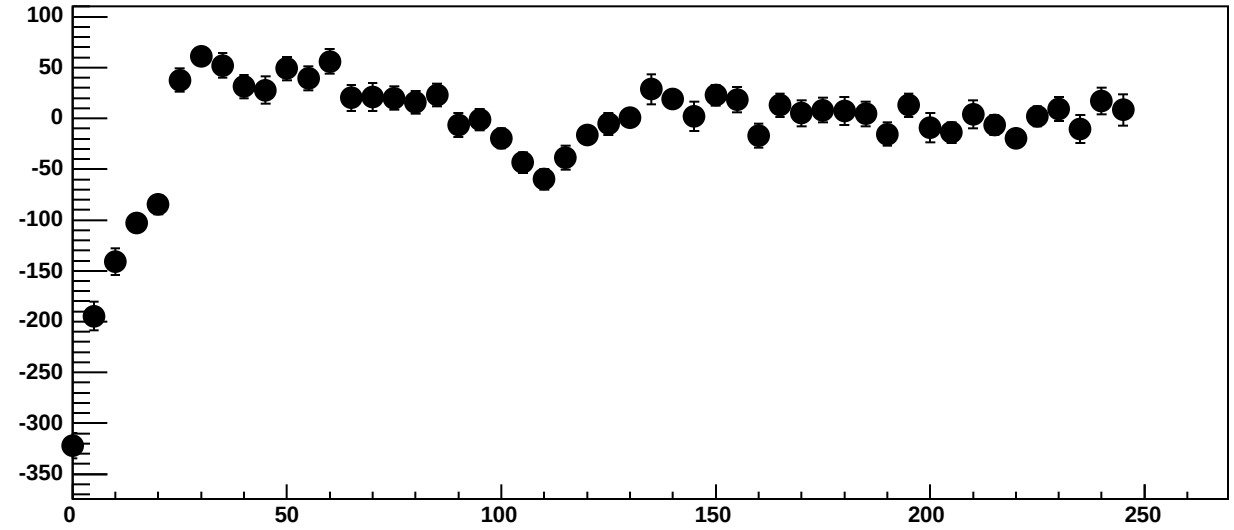
Chip 11, Channel 3, Enable 1, DAC=1600, ADC Mean vs Hold



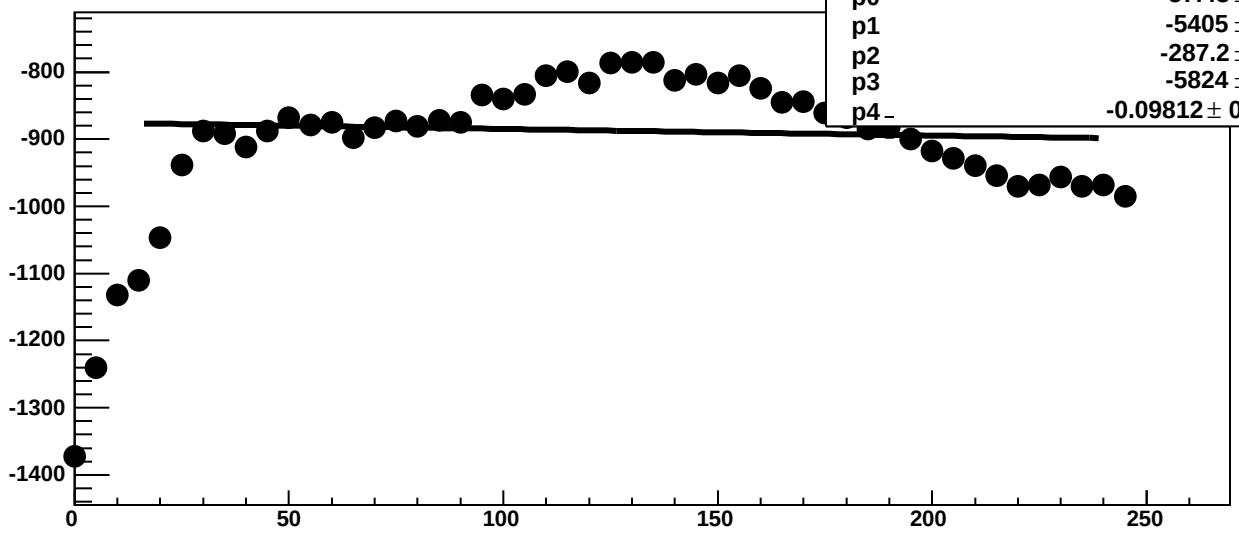
Chip 11, Channel 3, Enable 1, DAC=1600, ADC Noise vs Hold



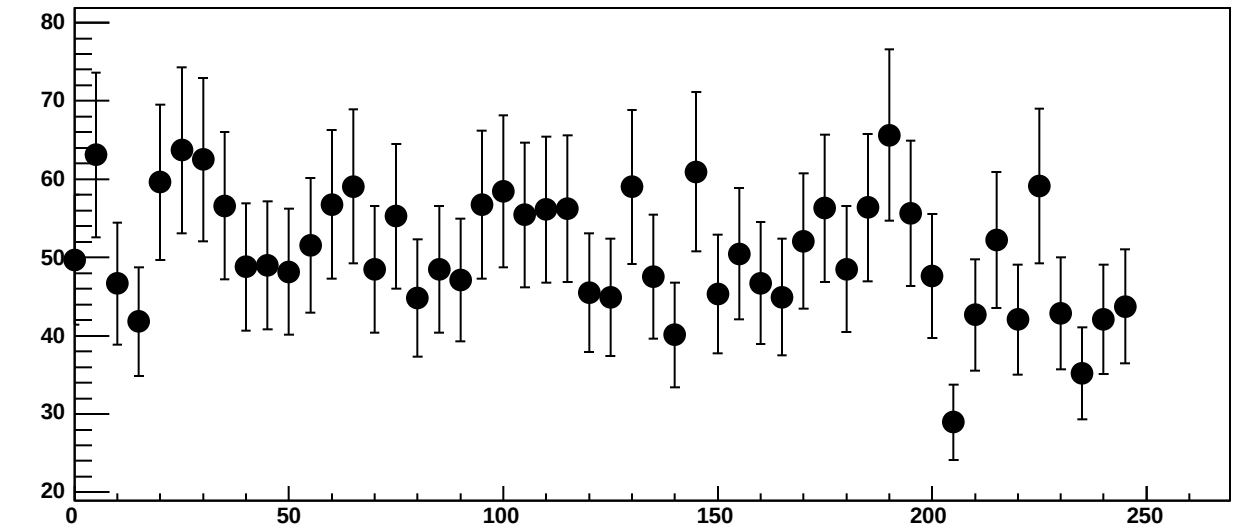
Chip 11, Channel 3, Enable 1, DAC=1600, ADC Residuals vs Hold



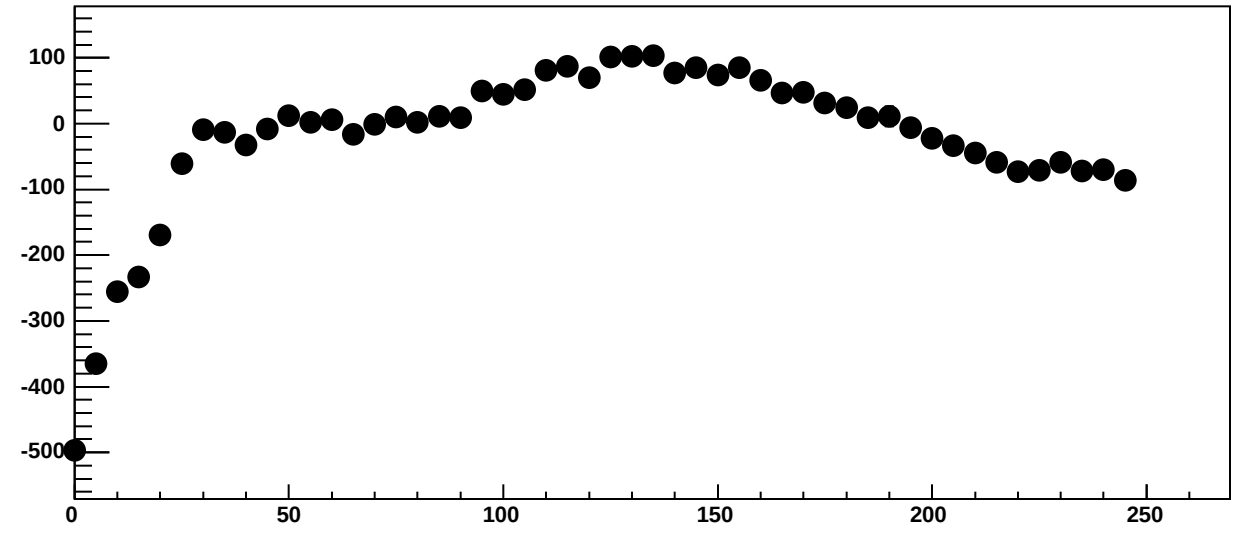
Chip 11, Channel 3, Enable 2, DAC=1600, ADC Mean vs Hold



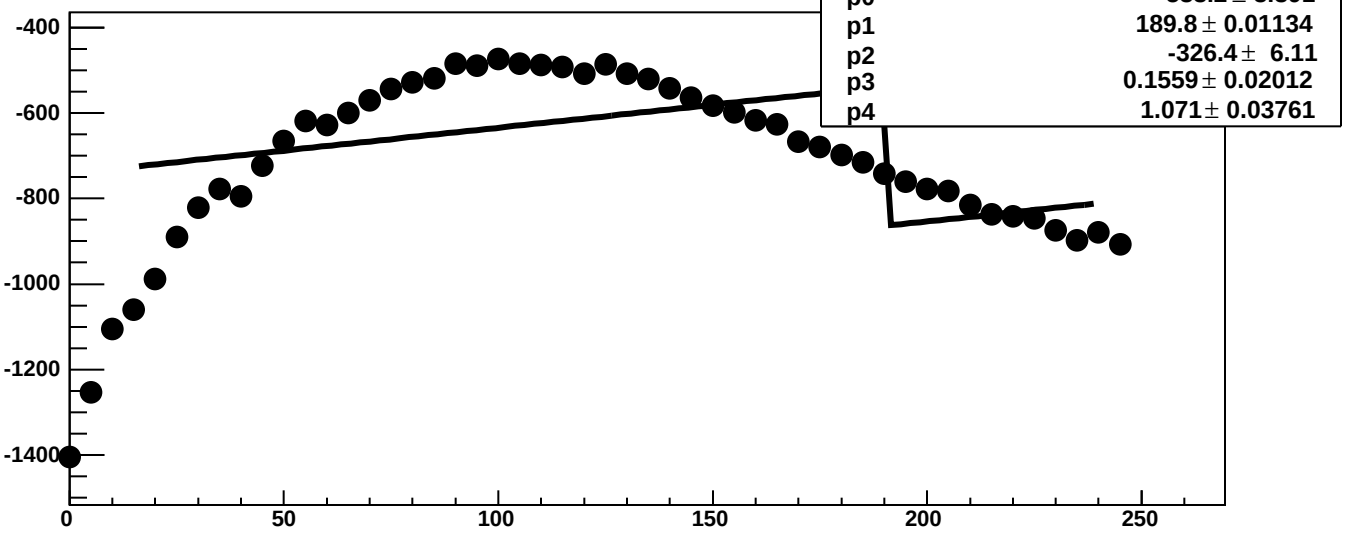
Chip 11, Channel 3, Enable 2, DAC=1600, ADC Noise vs Hold



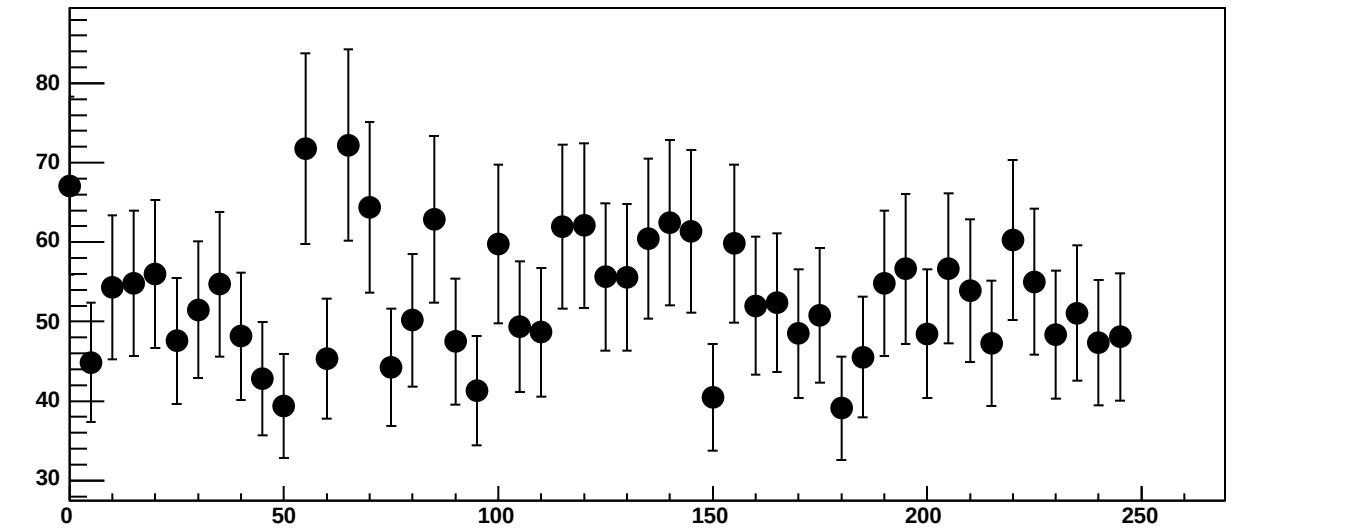
Chip 11, Channel 3, Enable 2, DAC=1600, ADC Residuals vs Hold



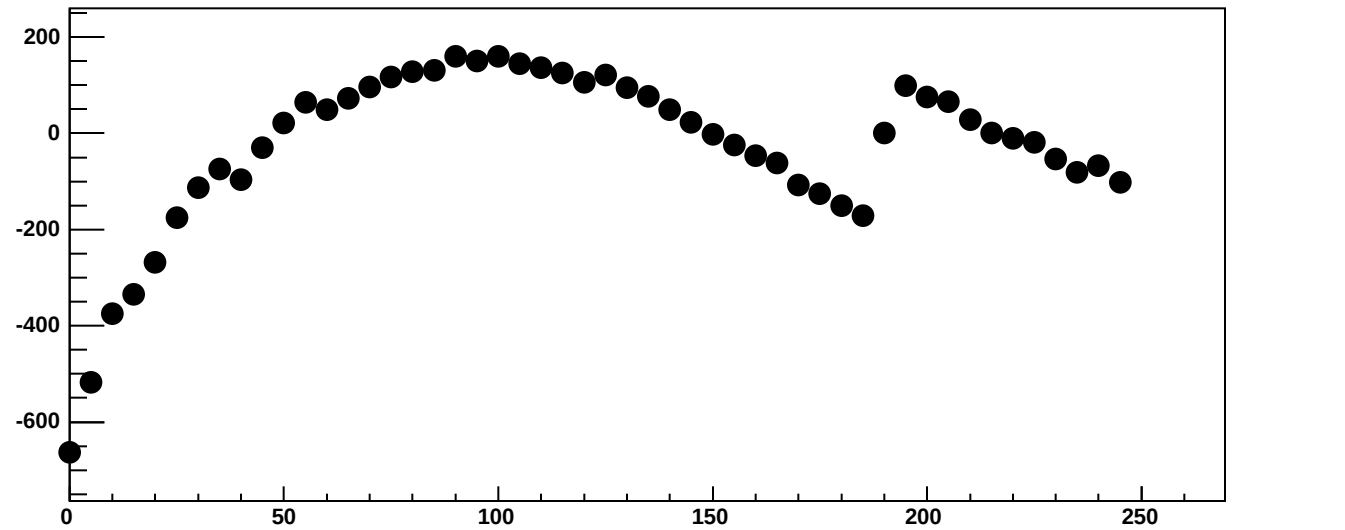
Chip 11, Channel 3, Enable 3, DAC=1600, ADC Mean vs Hold



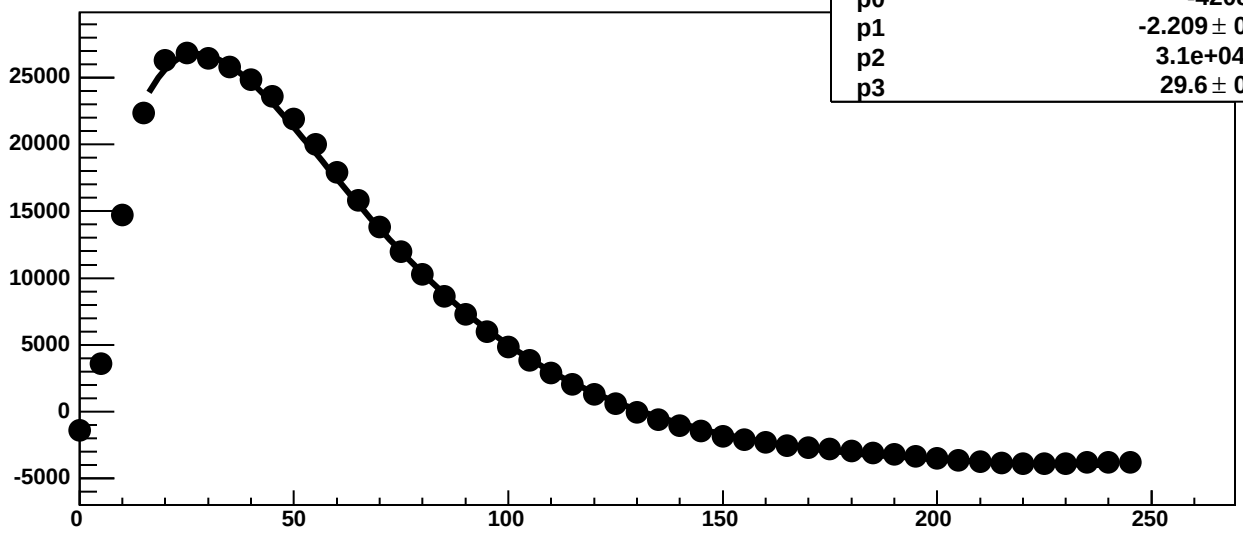
Chip 11, Channel 3, Enable 3, DAC=1600, ADC Noise vs Hold



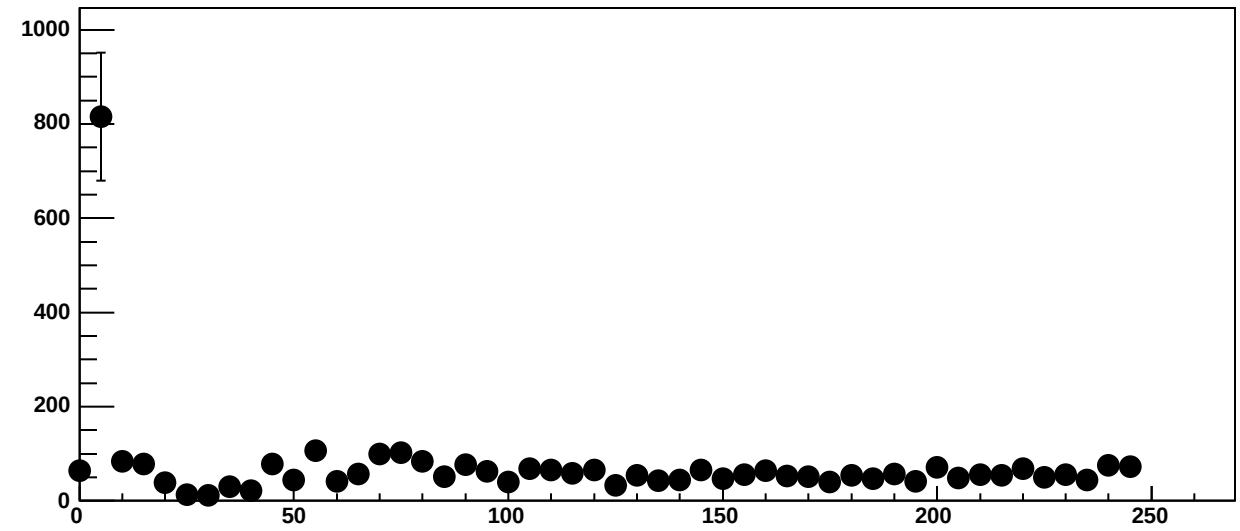
Chip 11, Channel 3, Enable 3, DAC=1600, ADC Residuals vs Hold



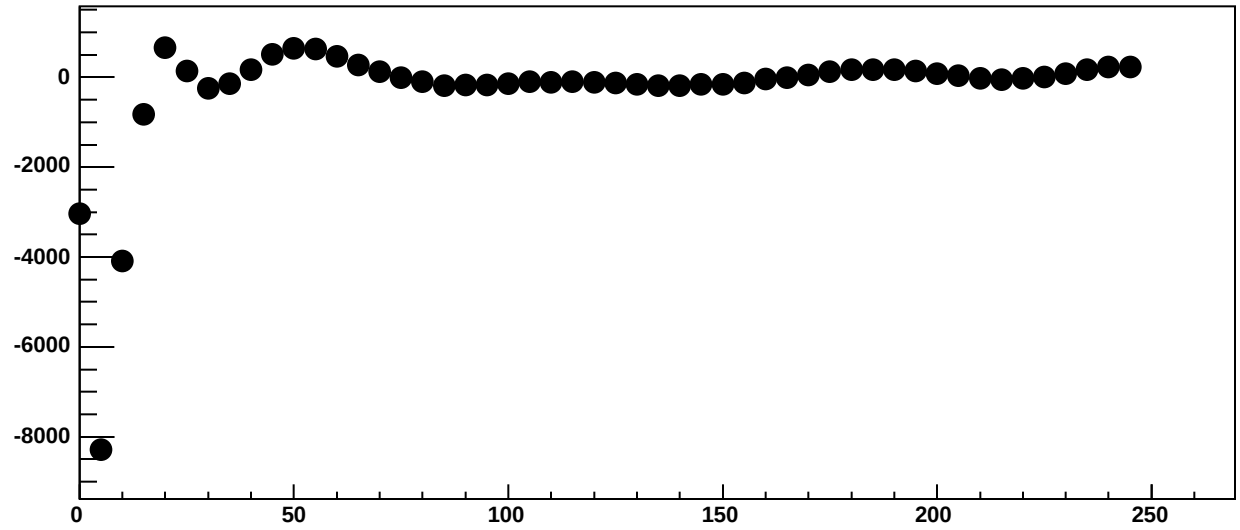
Chip 11, Channel 3, Enable 4!, DAC=1600, ADC Mean vs Hold

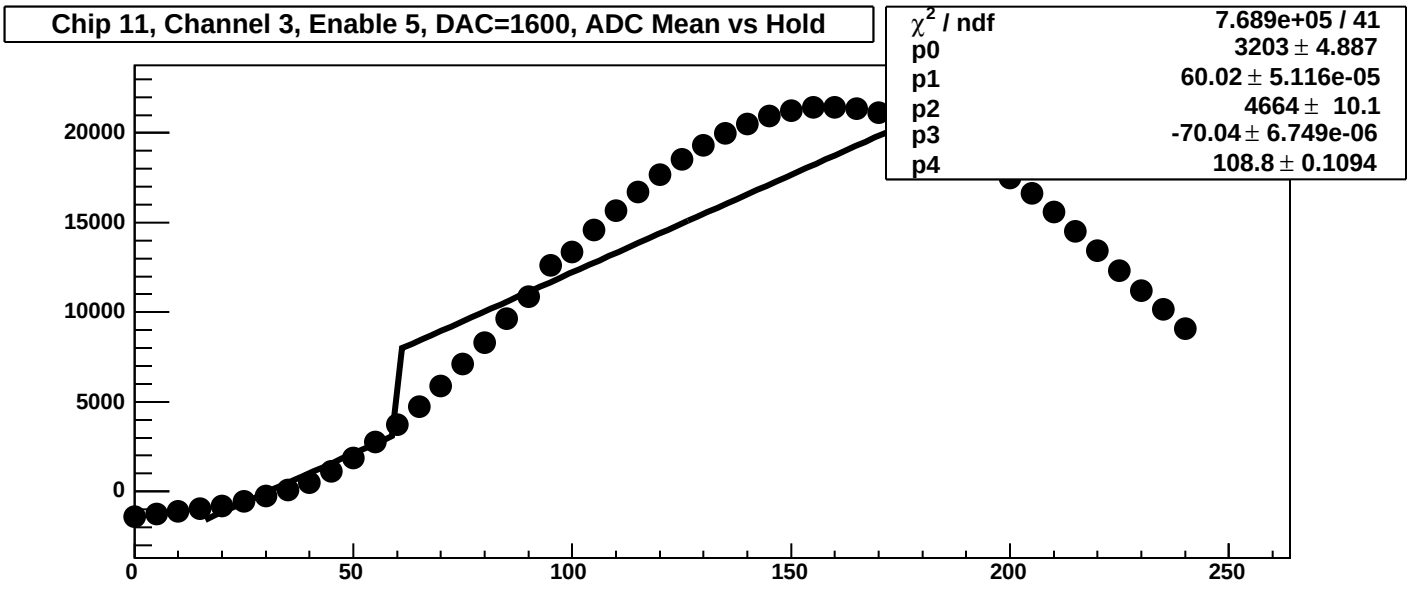


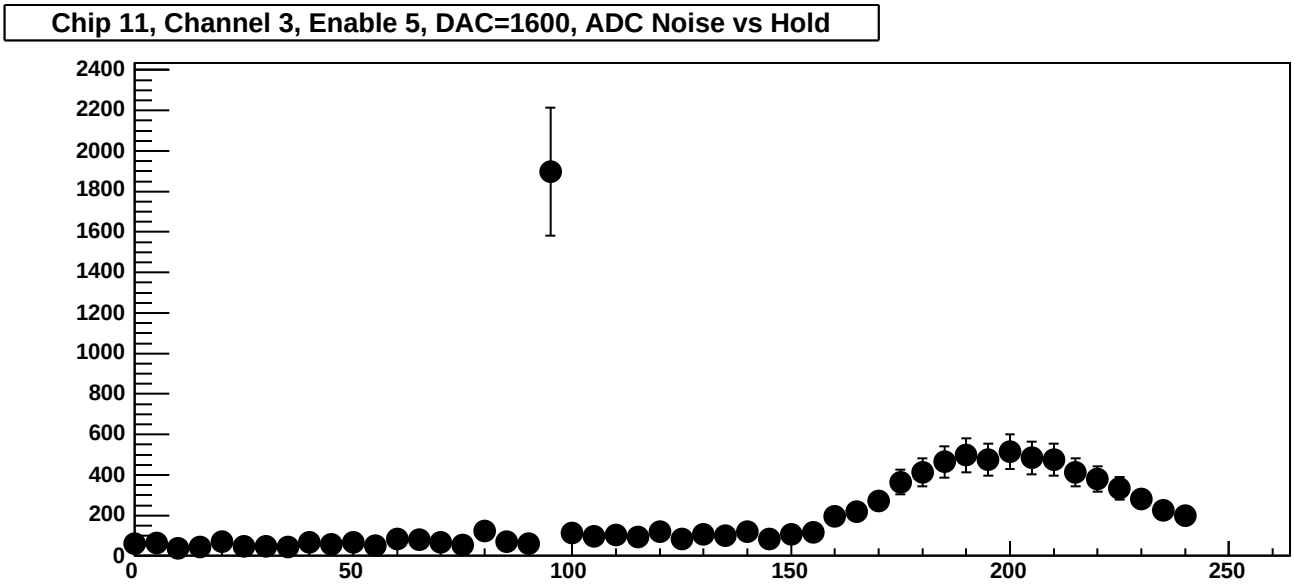
Chip 11, Channel 3, Enable 4!, DAC=1600, ADC Noise vs Hold

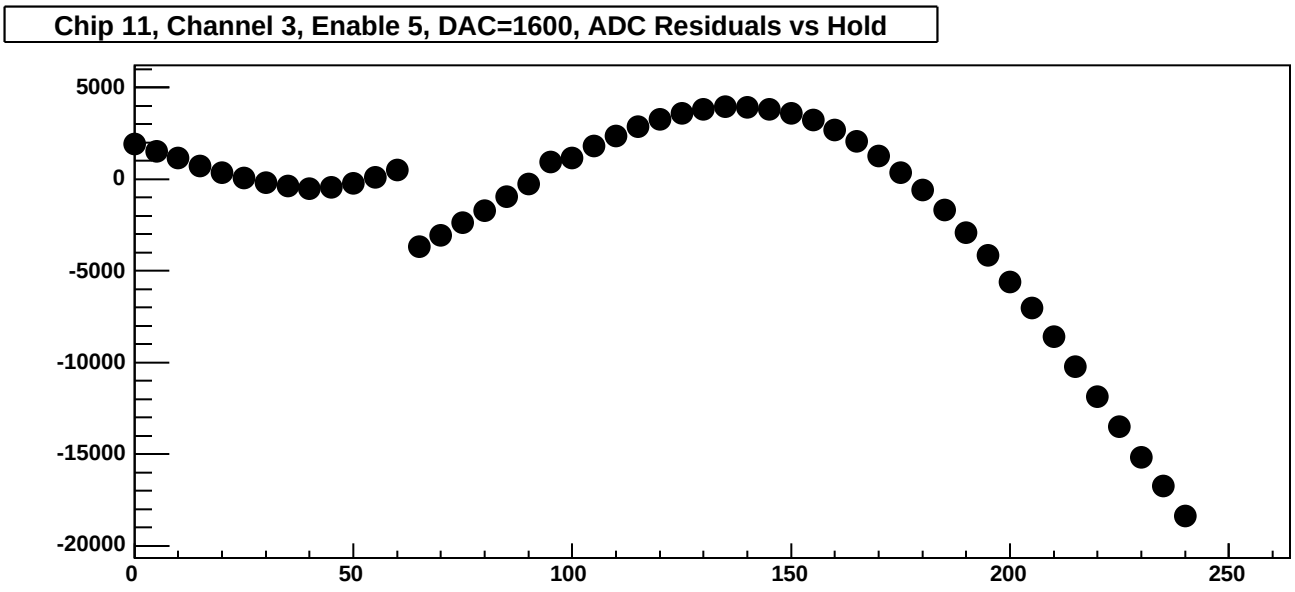


Chip 11, Channel 3, Enable 4!, DAC=1600, ADC Residuals vs Hold

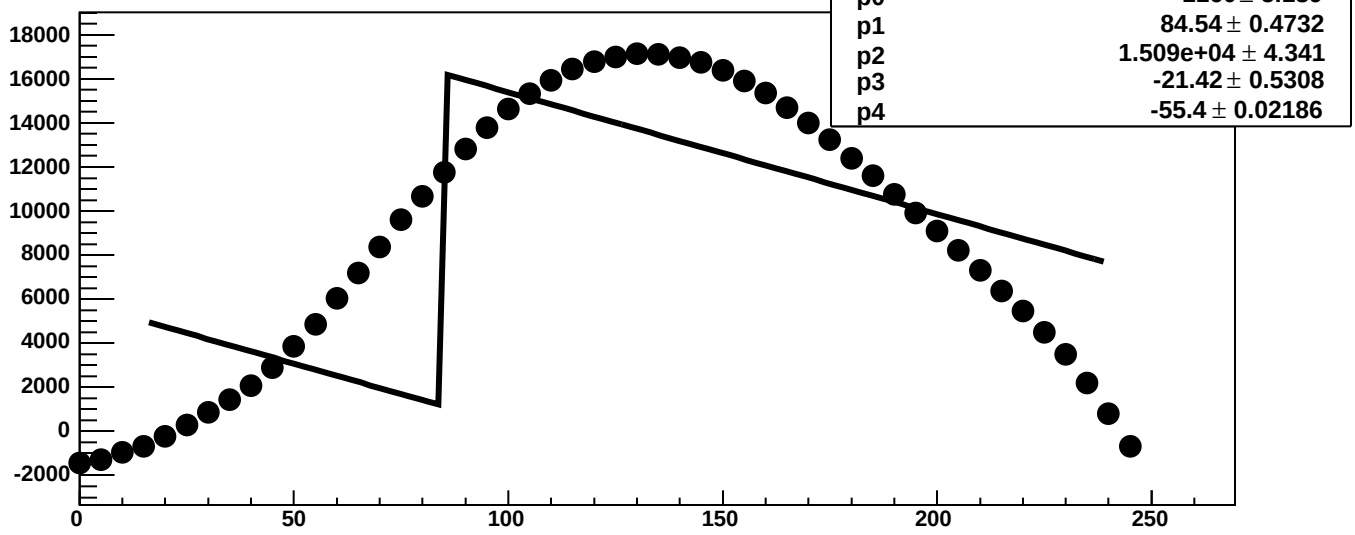



Chip 11, Channel 3, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.689e+05 / 41
p0 3203 ± 4.887
p1 60.02 ± 5.116e-05
p2 4664 ± 10.1
p3 -70.04 ± 6.749e-06
p4 108.8 ± 0.1094
20000
15000
10000
5000
0
0
50
100
150
200
250

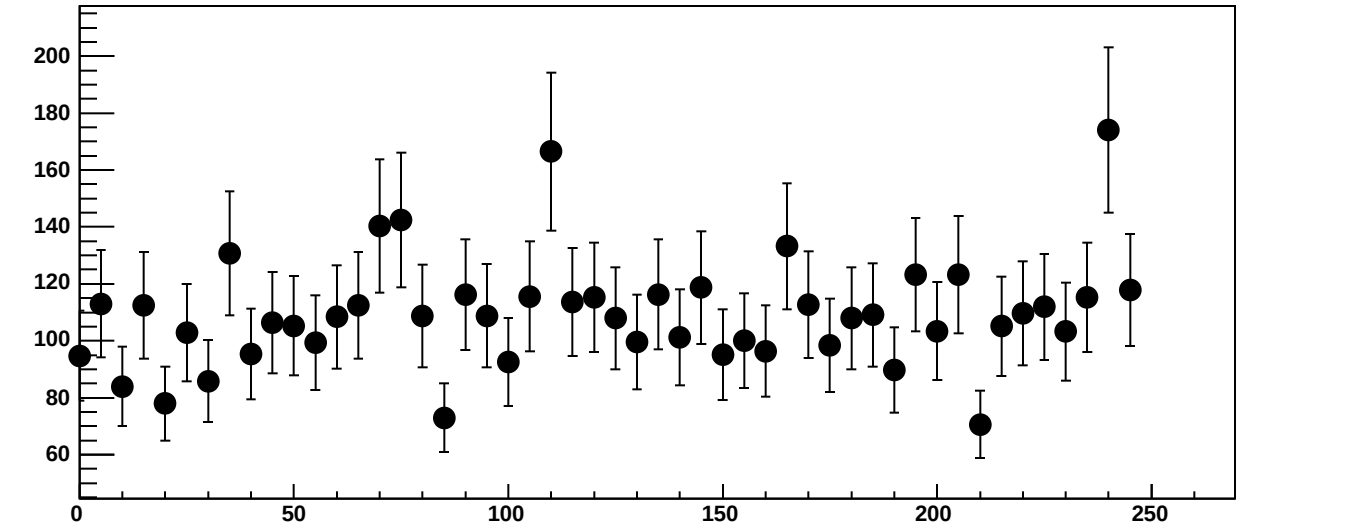

Chip 11, Channel 3, Enable 5, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 11, Channel 3, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

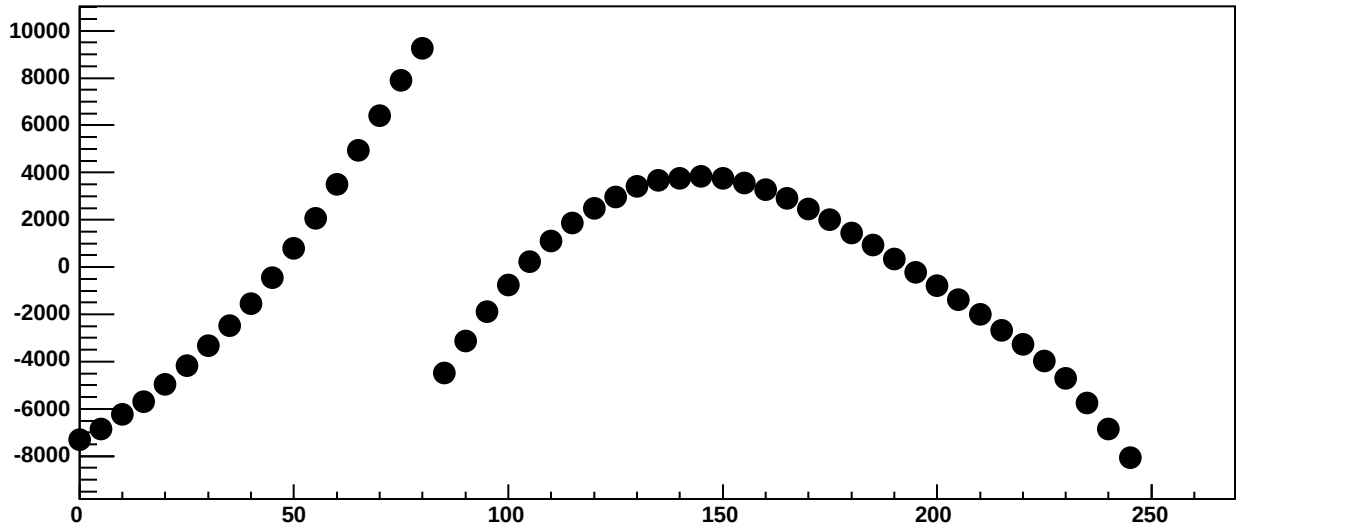
Chip 11, Channel 4, Enable 0, DAC=1600, ADC Mean vs Hold

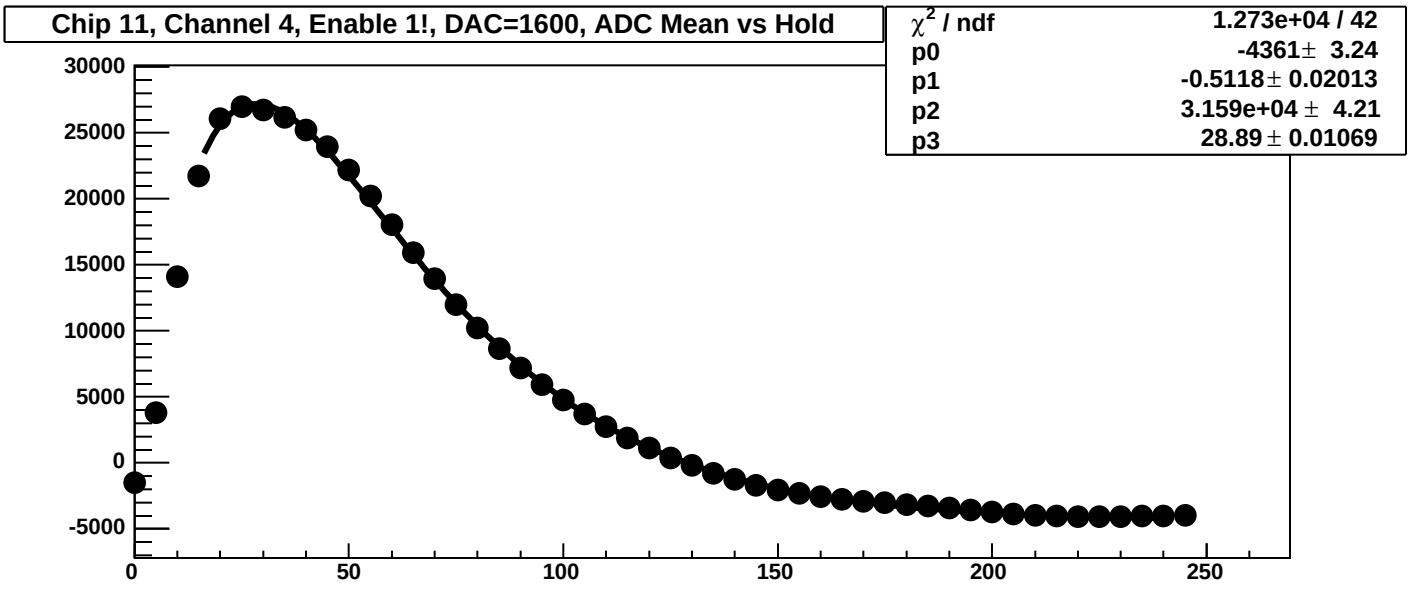


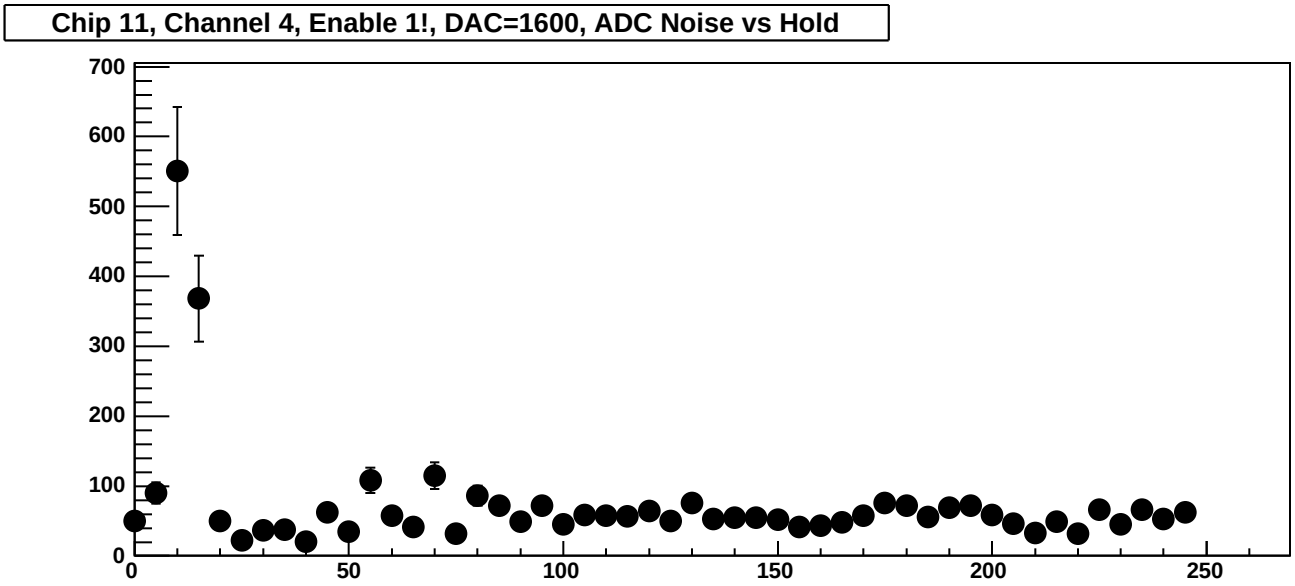
Chip 11, Channel 4, Enable 0, DAC=1600, ADC Noise vs Hold

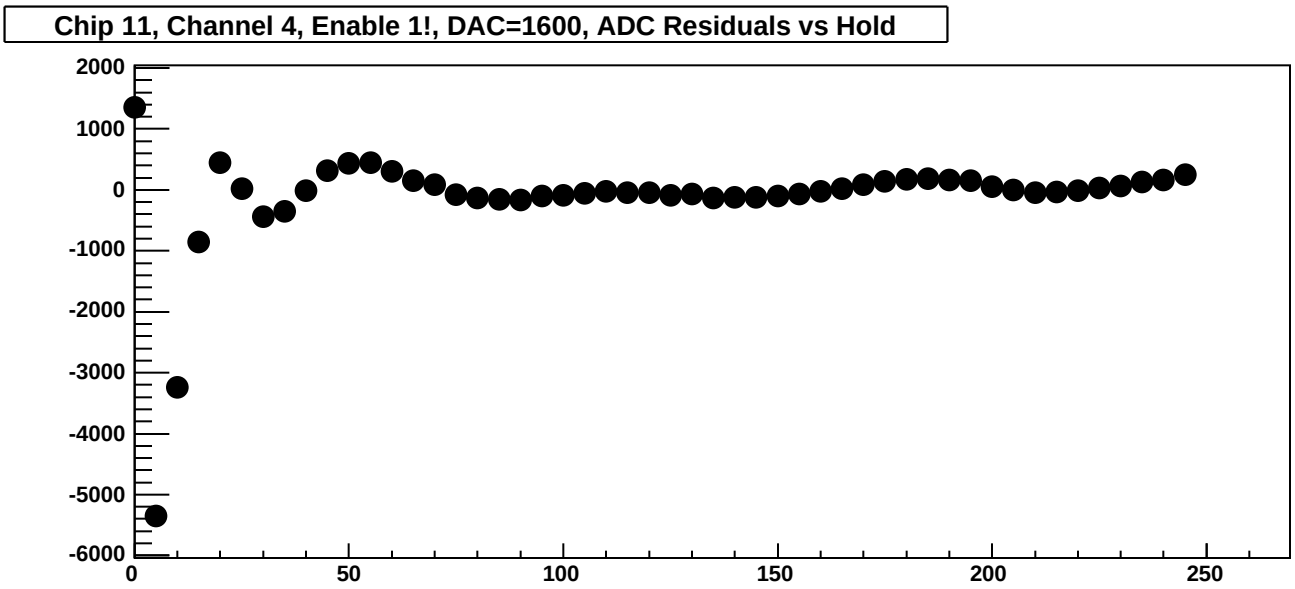


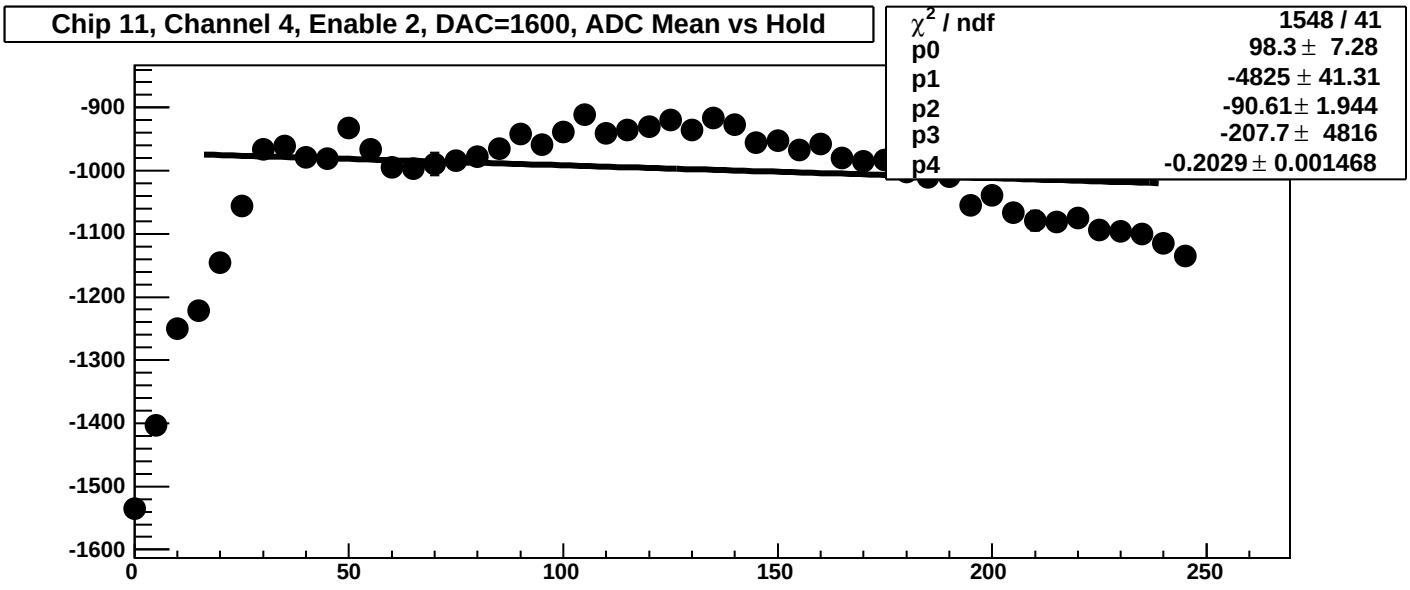
Chip 11, Channel 4, Enable 0, DAC=1600, ADC Residuals vs Hold

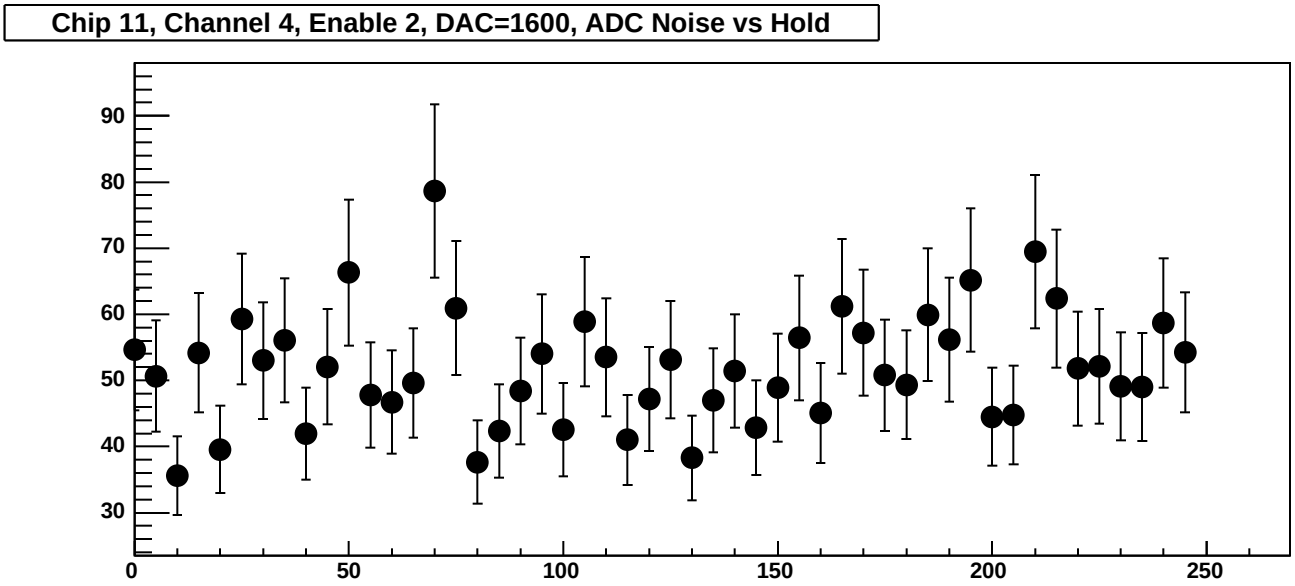


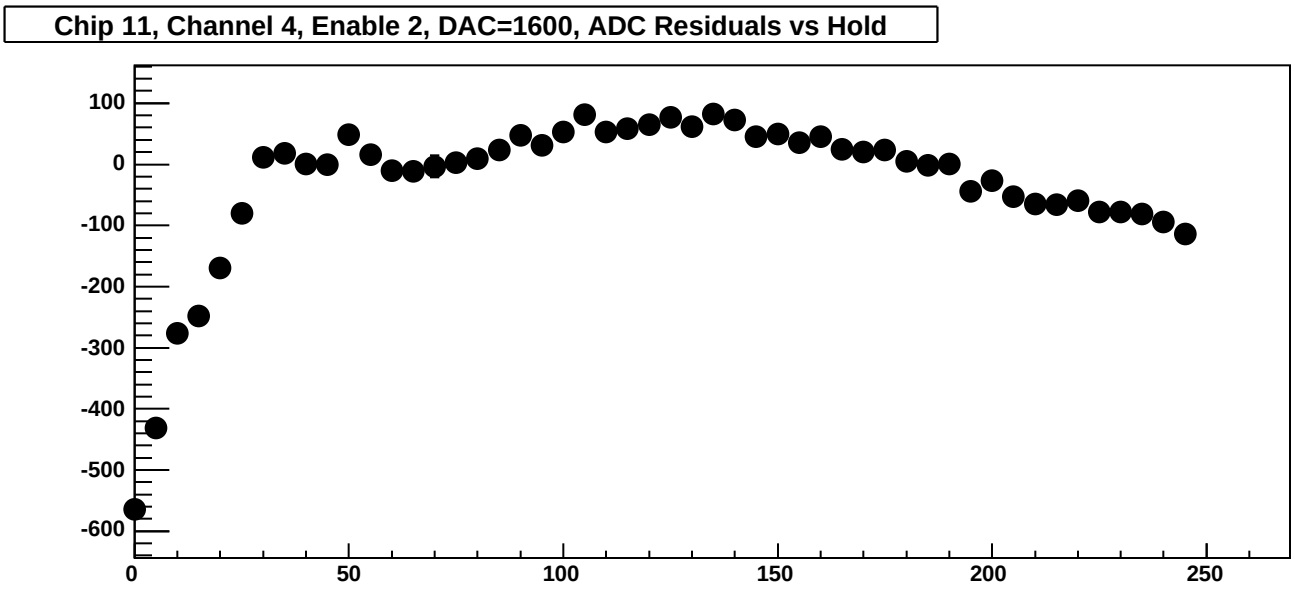

Chip 11, Channel 4, Enable 1!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.273e+04 / 42
p0 -4361± 3.24
p1 -0.5118± 0.02013
p2 3.159e+04 ± 4.21
p3 28.89 ± 0.01069
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 11, Channel 4, Enable 1!, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250

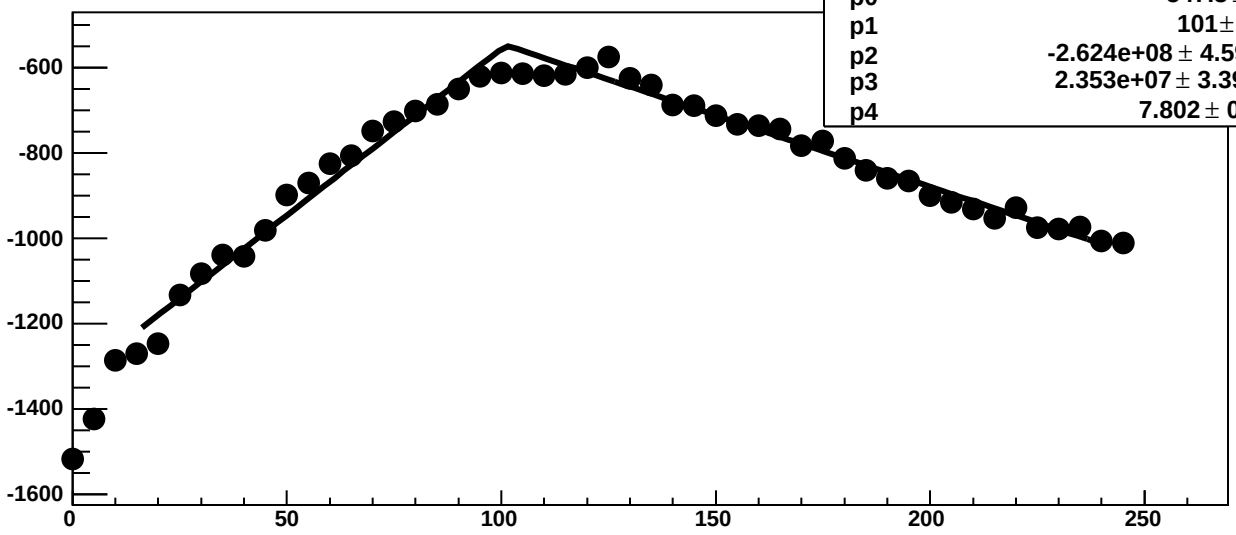

Chip 11, Channel 4, Enable 1!, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
50
100
150
200
250


Chip 11, Channel 4, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 1548 / 41
p0 98.3 ± 7.28
p1 -4825 ± 41.31
p2 -90.61 ± 1.944
p3 -207.7 ± 4816
p4 -0.2029 ± 0.001468
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
50
100
150
200
250

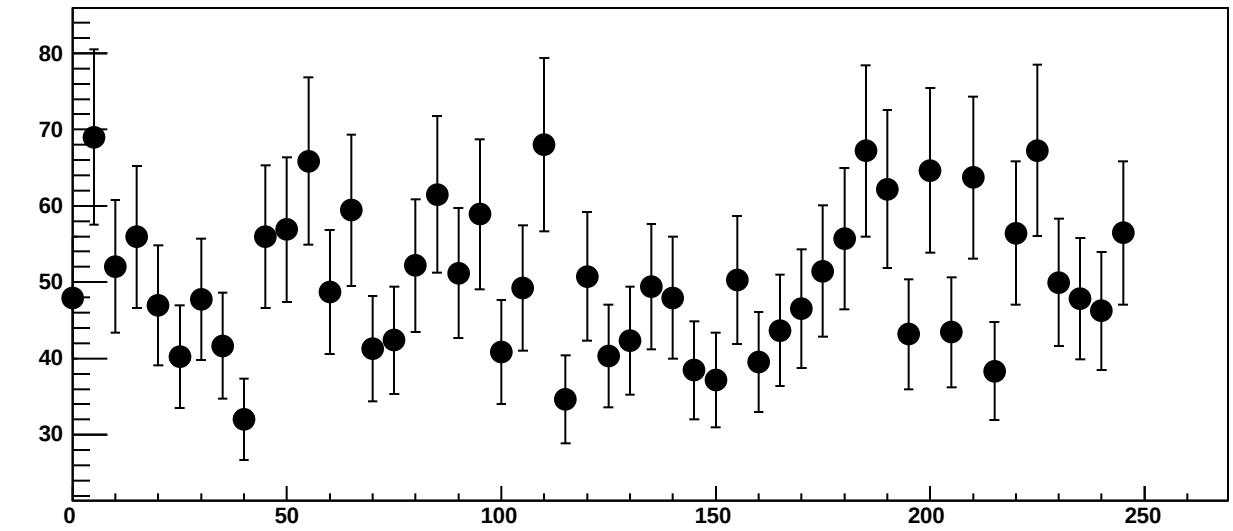

Chip 11, Channel 4, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 11, Channel 4, Enable 2, DAC=1600, ADC Residuals vs Hold
100
0
-100
-200
-300
-400
-500
-600
0
50
100
150
200
250

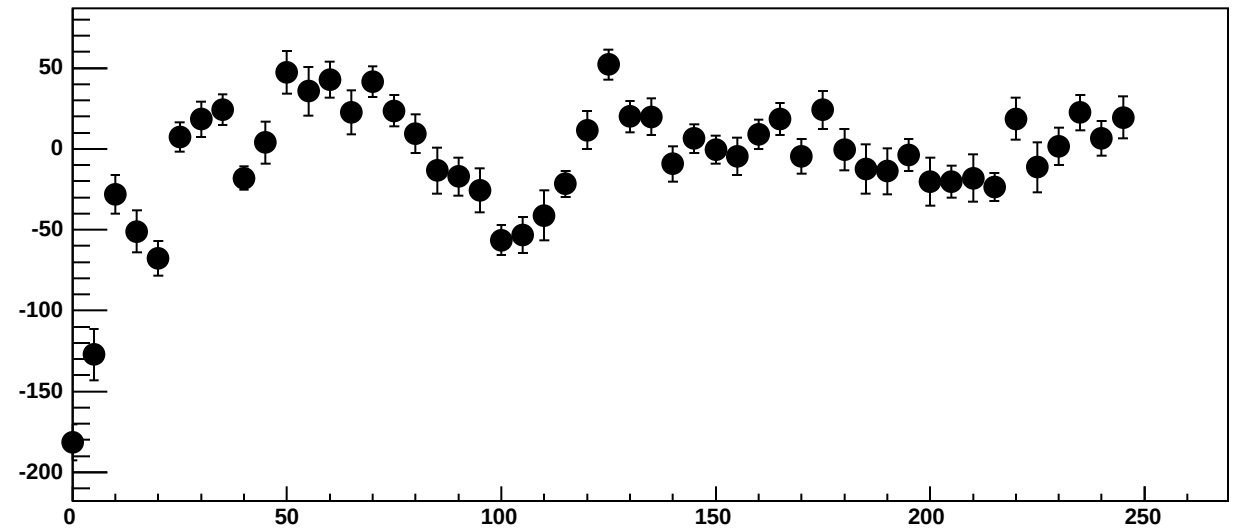
Chip 11, Channel 4, Enable 3, DAC=1600, ADC Mean vs Hold



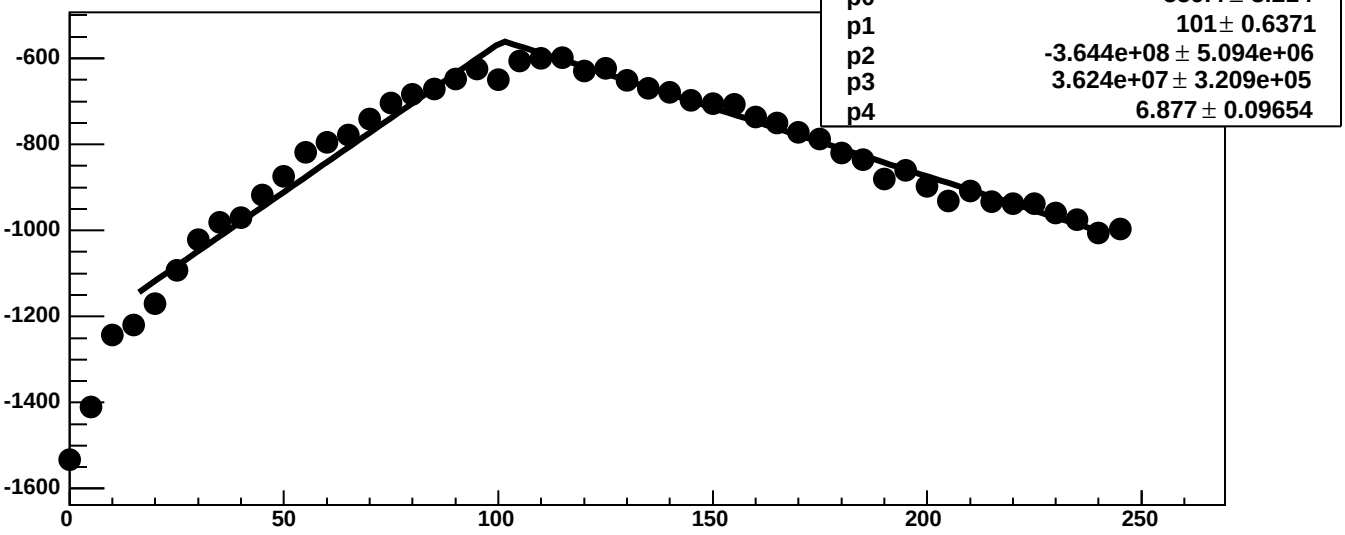
Chip 11, Channel 4, Enable 3, DAC=1600, ADC Noise vs Hold



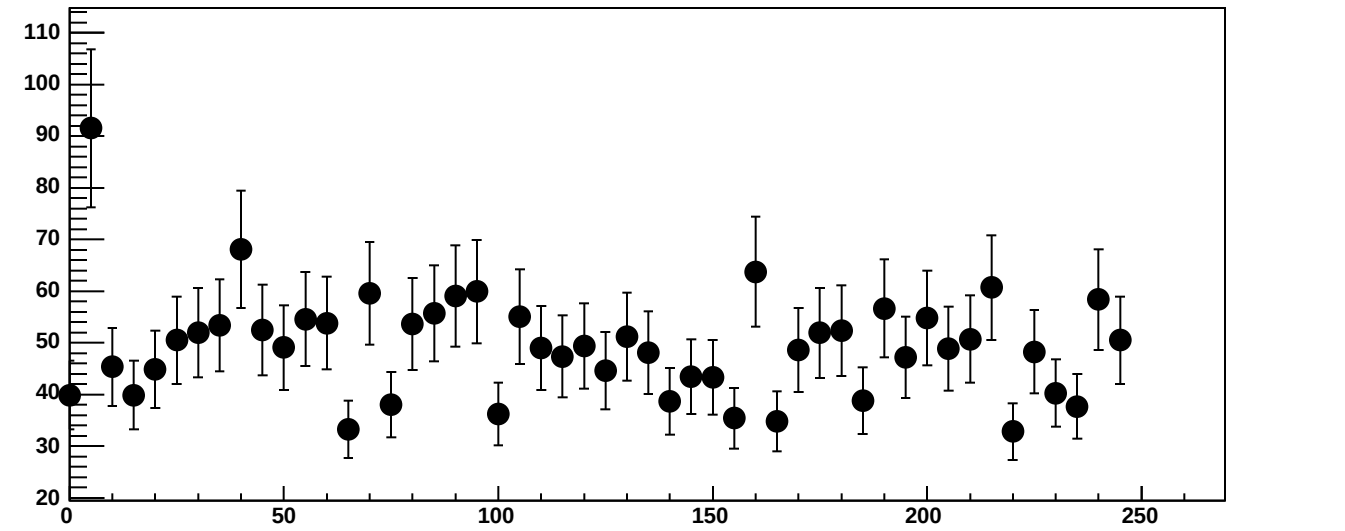
Chip 11, Channel 4, Enable 3, DAC=1600, ADC Residuals vs Hold



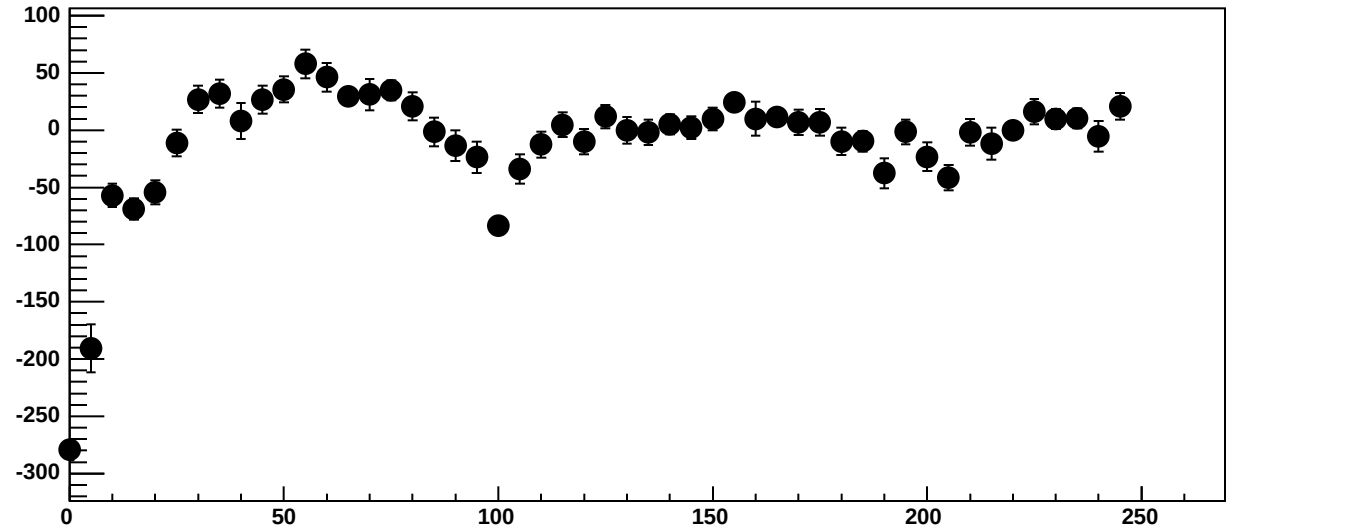
Chip 11, Channel 4, Enable 4, DAC=1600, ADC Mean vs Hold

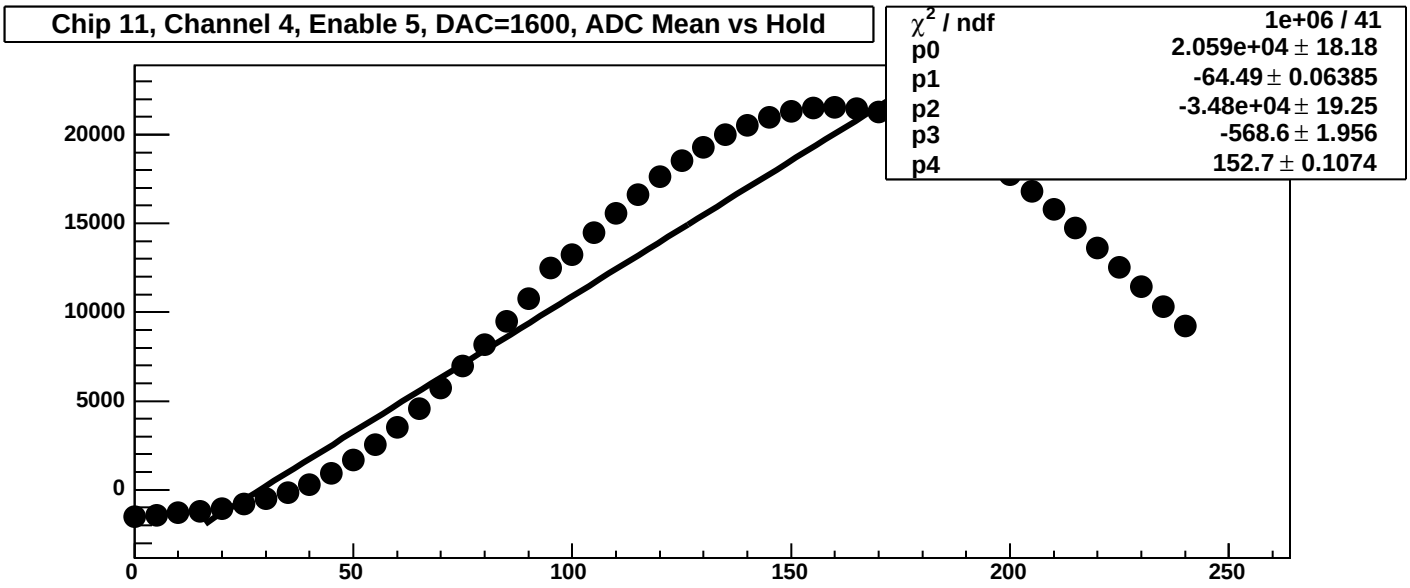


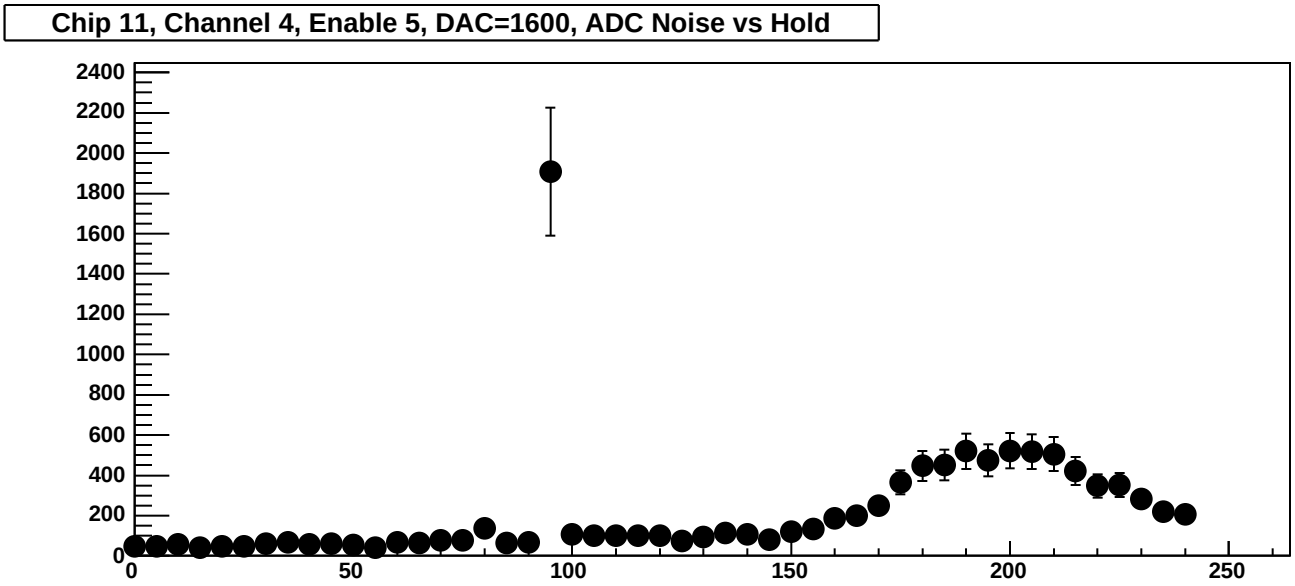
Chip 11, Channel 4, Enable 4, DAC=1600, ADC Noise vs Hold

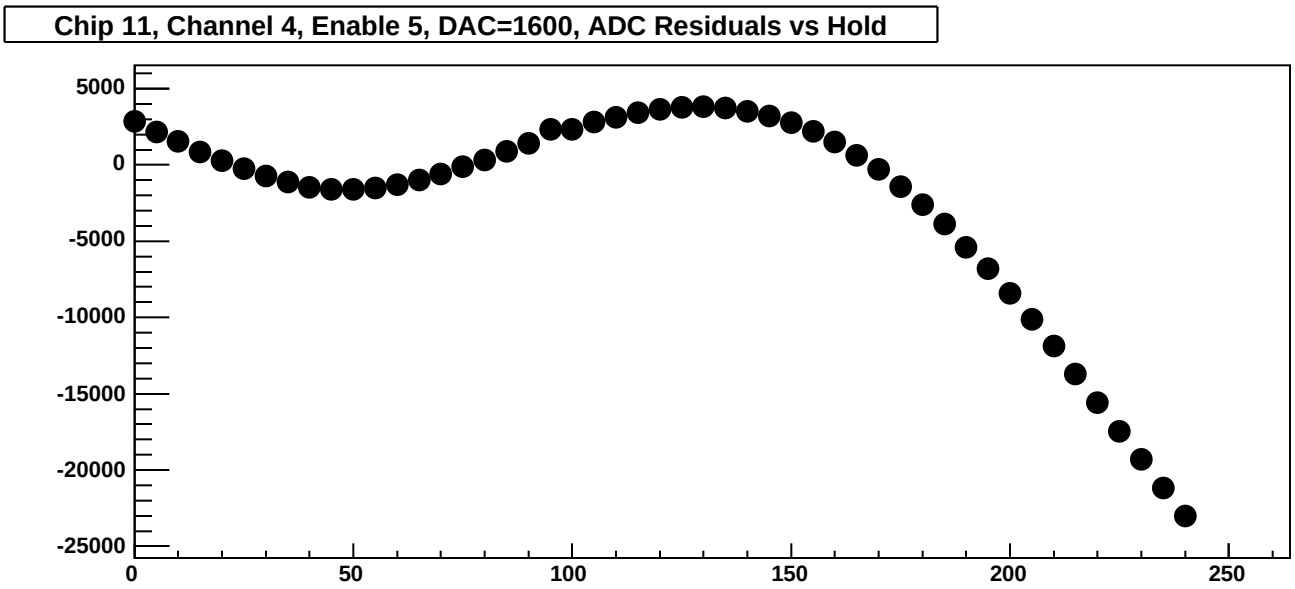


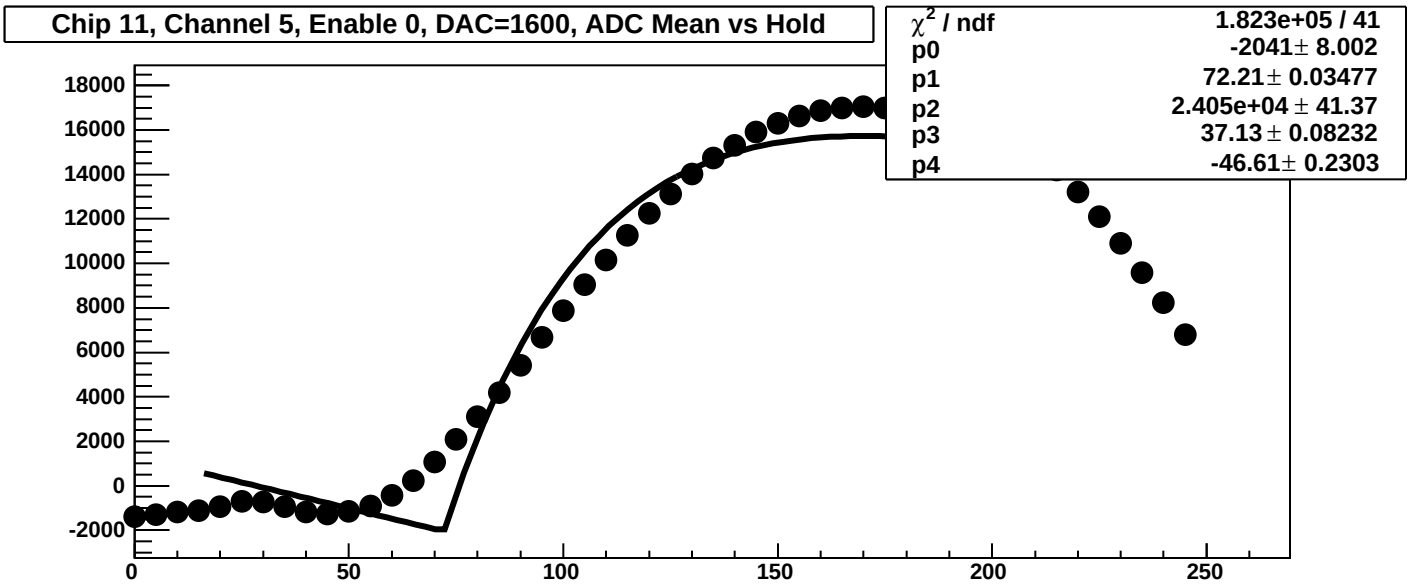
Chip 11, Channel 4, Enable 4, DAC=1600, ADC Residuals vs Hold

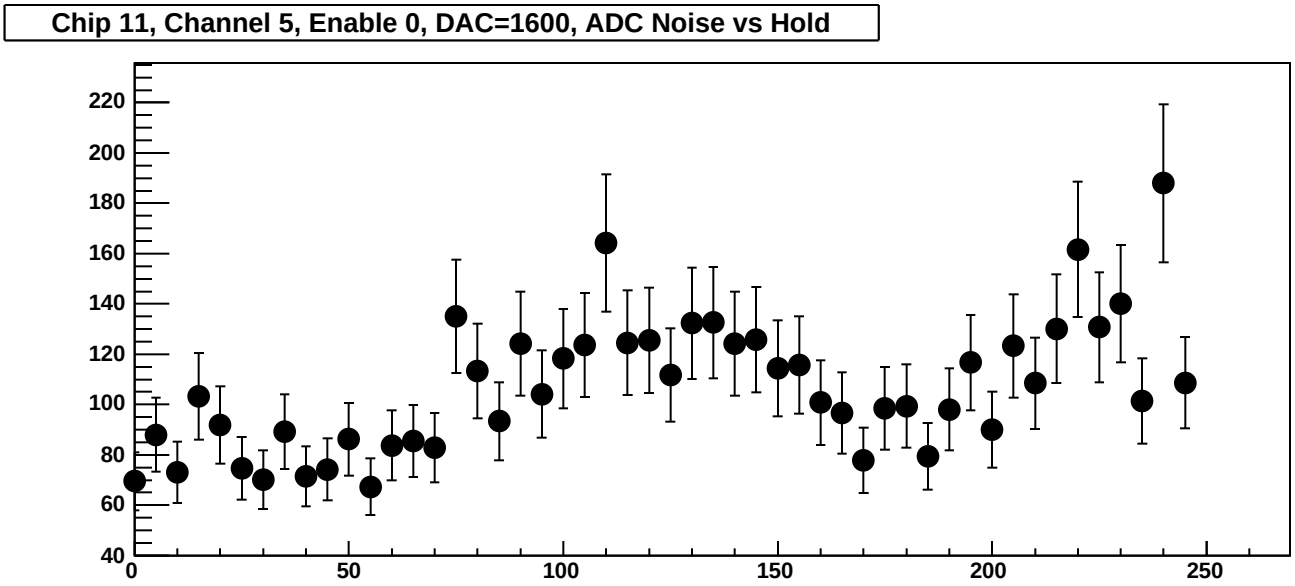


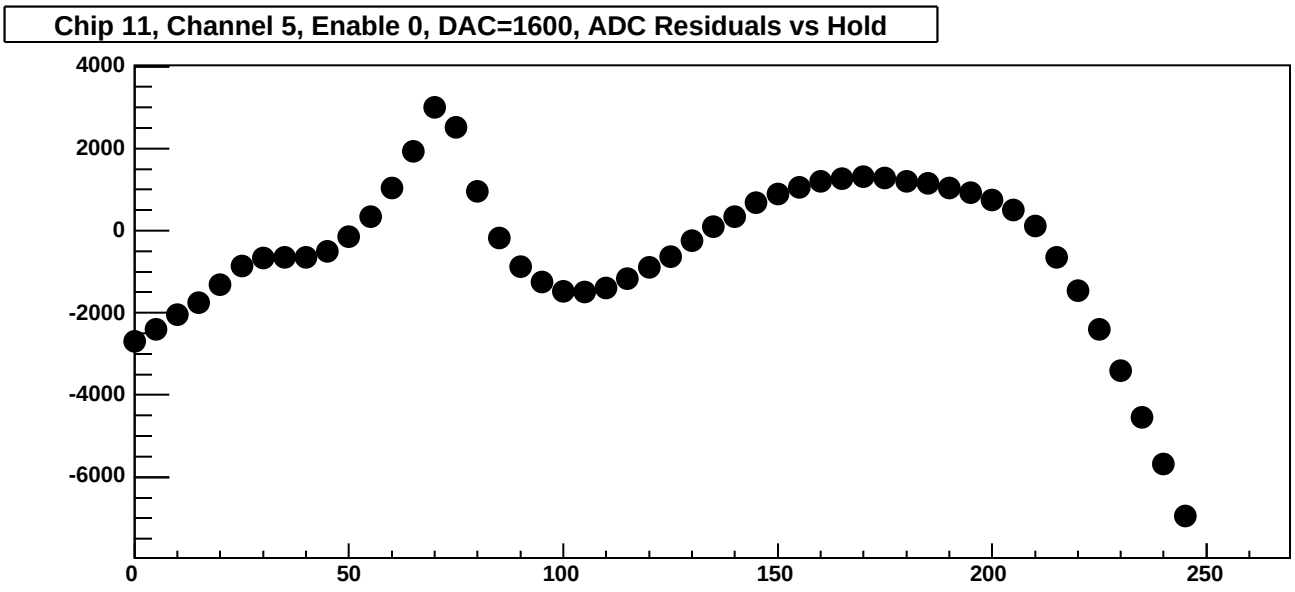

Chip 11, Channel 4, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1e+06 / 41
p0 2.059e+04 ± 18.18
p1 -64.49 ± 0.06385
p2 -3.48e+04 ± 19.25
p3 -568.6 ± 1.956
p4 152.7 ± 0.1074
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 11, Channel 4, Enable 5, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

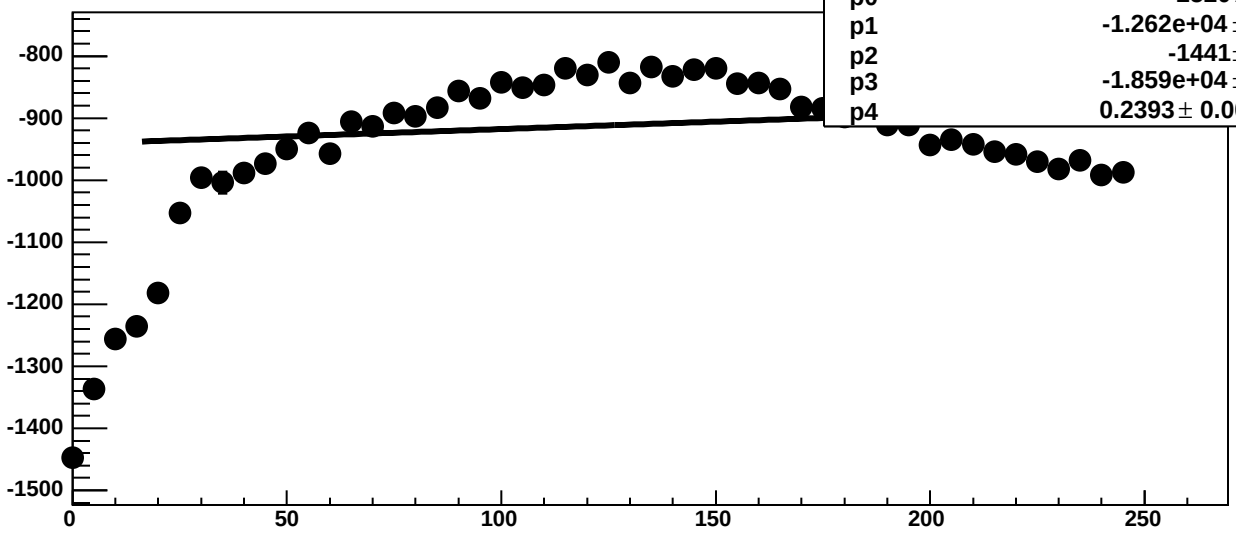

Chip 11, Channel 4, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
0
50
100
150
200
250


Chip 11, Channel 5, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.823e+05 / 41
p0 -2041± 8.002
p1 72.21± 0.03477
p2 2.405e+04 ± 41.37
p3 37.13 ± 0.08232
p4 -46.61± 0.2303
18000
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

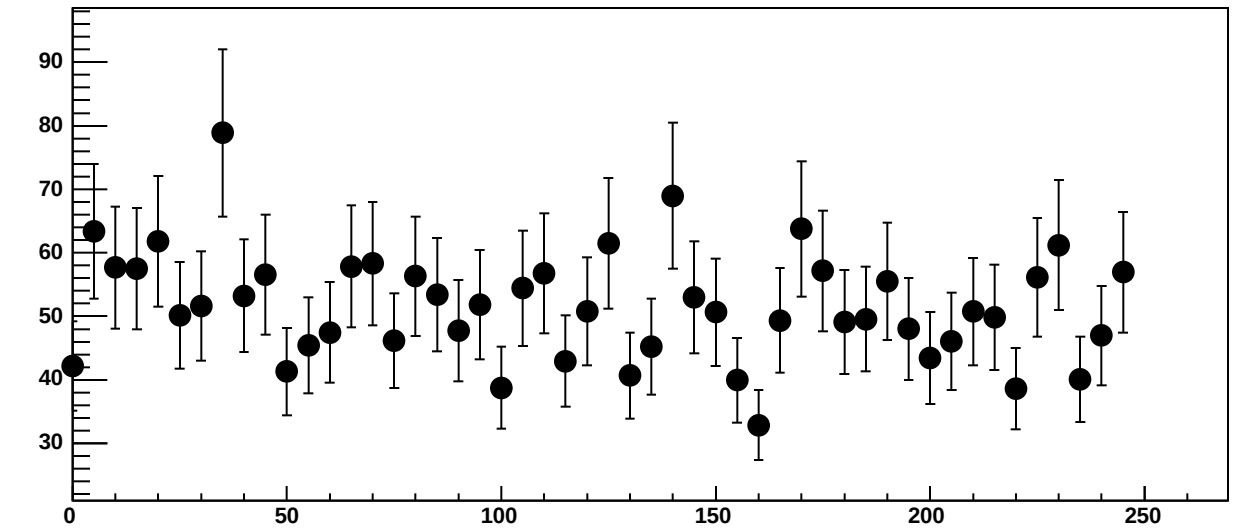

Chip 11, Channel 5, Enable 0, DAC=1600, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250


Chip 11, Channel 5, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

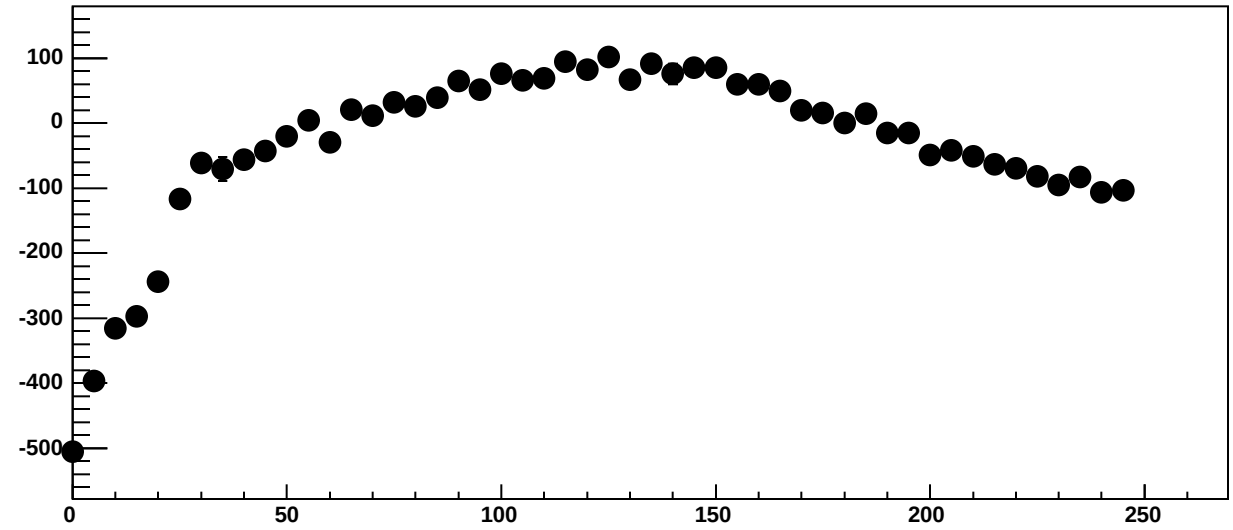
Chip 11, Channel 5, Enable 1, DAC=1600, ADC Mean vs Hold

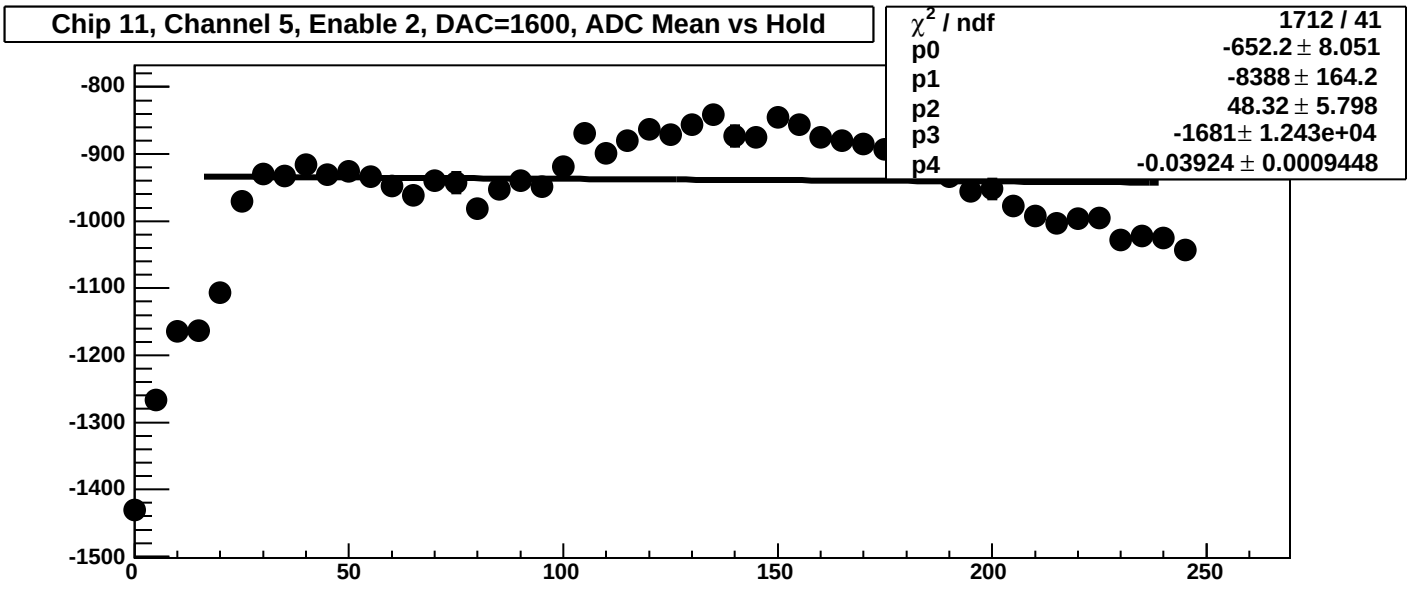


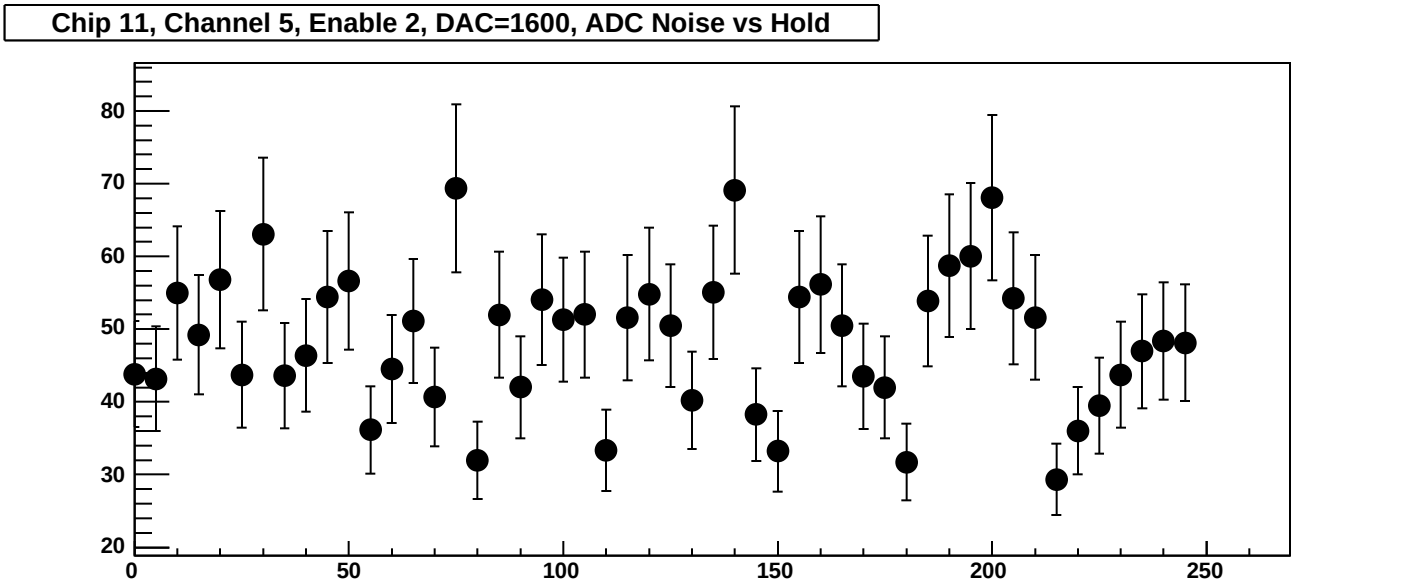
Chip 11, Channel 5, Enable 1, DAC=1600, ADC Noise vs Hold

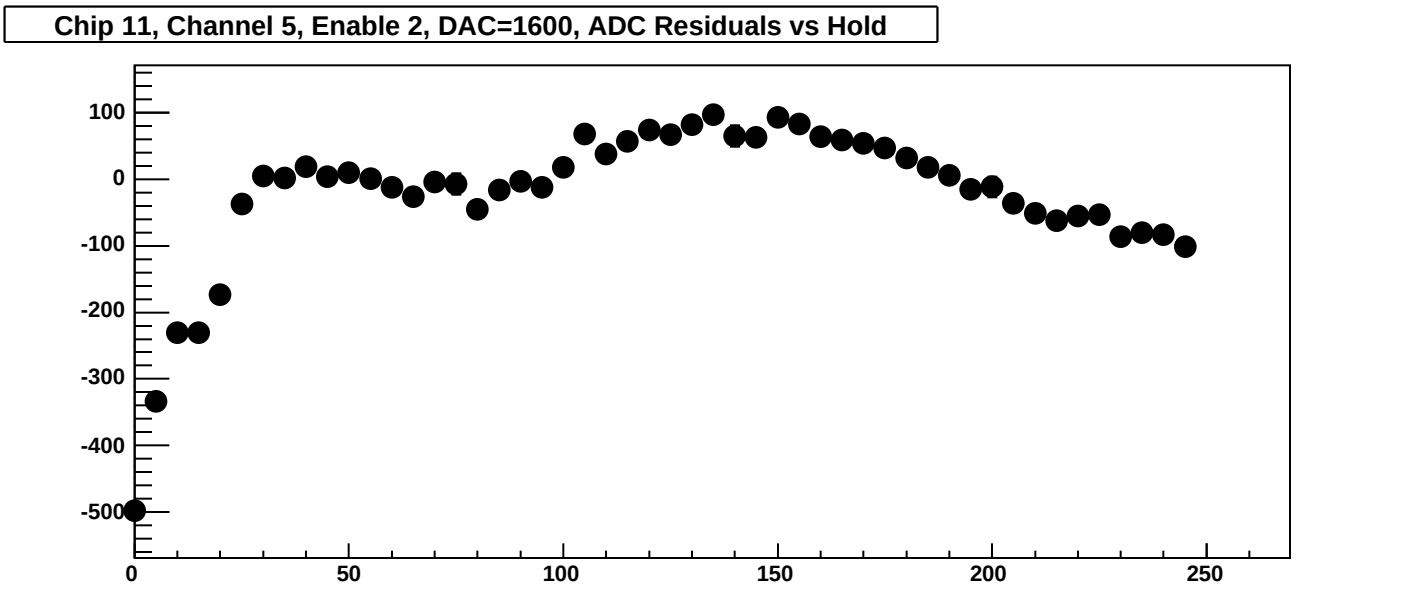


Chip 11, Channel 5, Enable 1, DAC=1600, ADC Residuals vs Hold

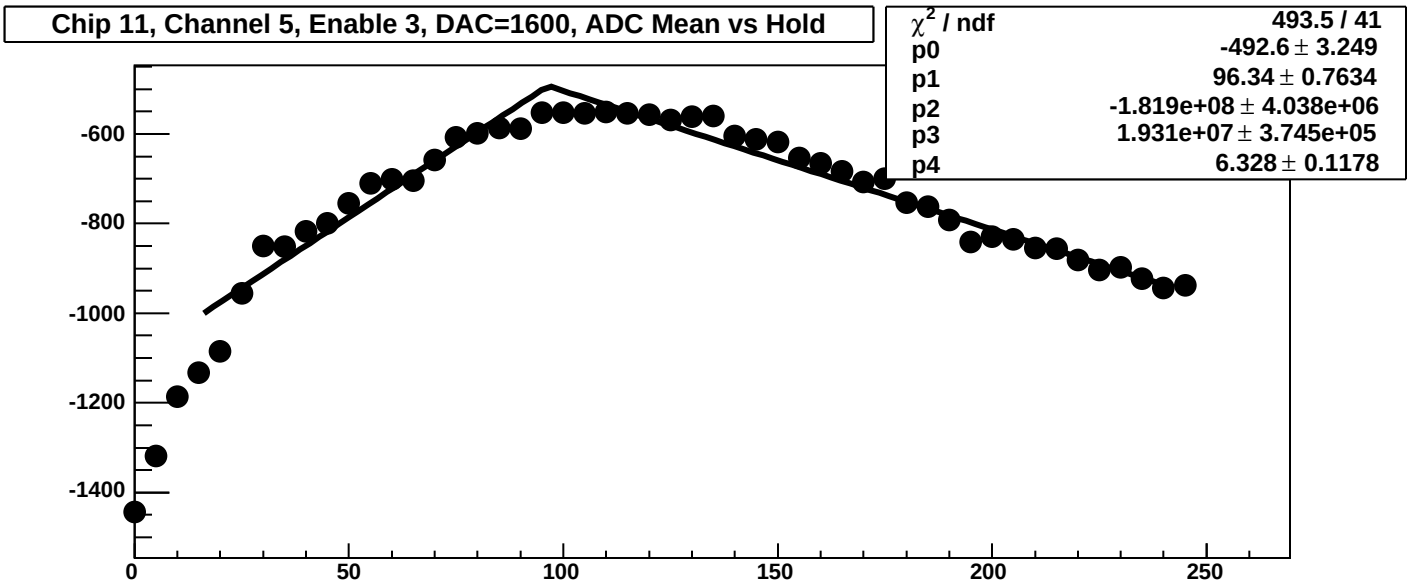



Chip 11, Channel 5, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 1712 / 41
p0 -652.2 ± 8.051
p1 -8388 ± 164.2
p2 48.32 ± 5.798
p3 -1681 ± 1.243e+04
p4 -0.03924 ± 0.0009448
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250

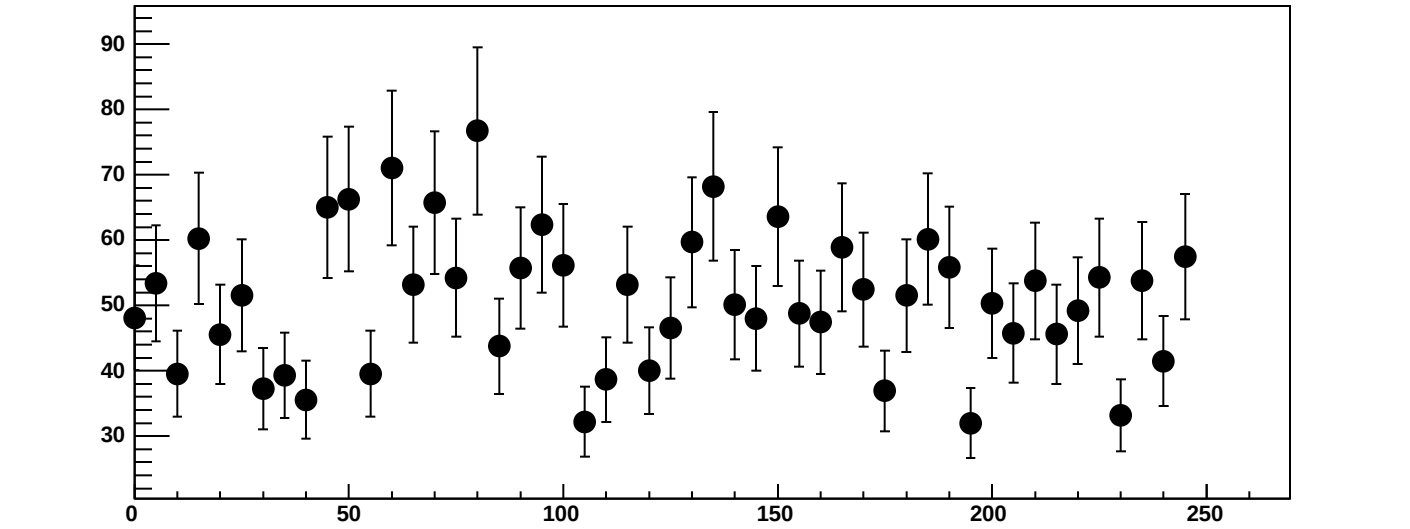

Chip 11, Channel 5, Enable 2, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 11, Channel 5, Enable 2, DAC=1600, ADC Residuals vs Hold
100
0
-100
-200
-300
-400
-500
0
50
100
150
200
250

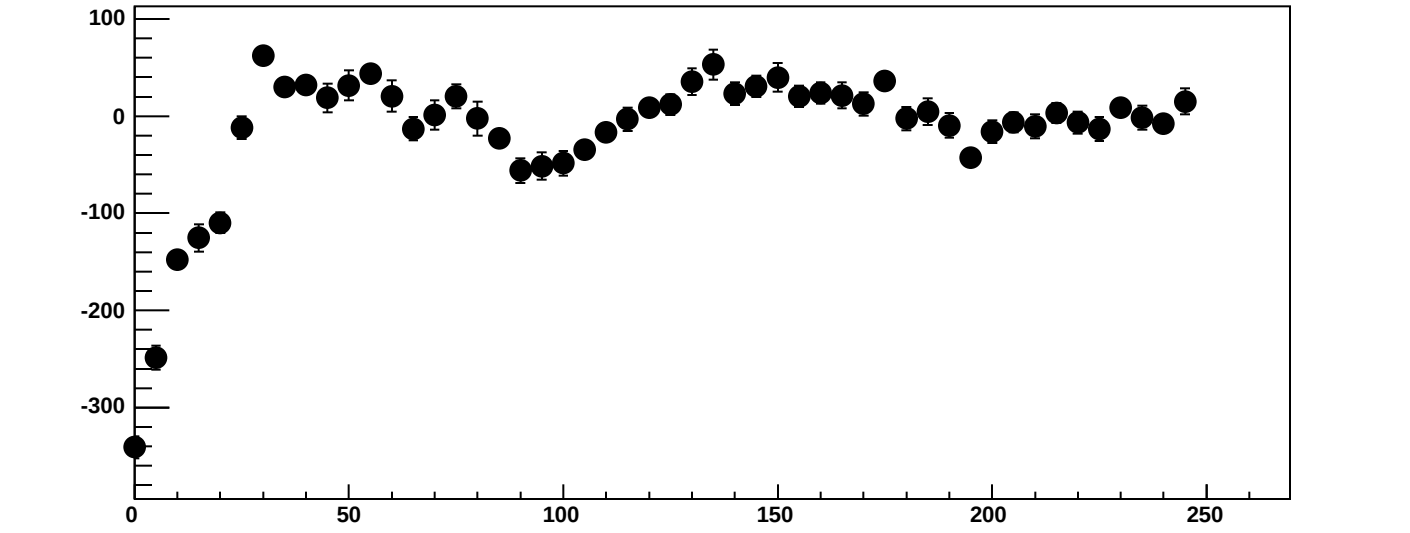
Chip 11, Channel 5, Enable 3, DAC=1600, ADC Mean vs Hold

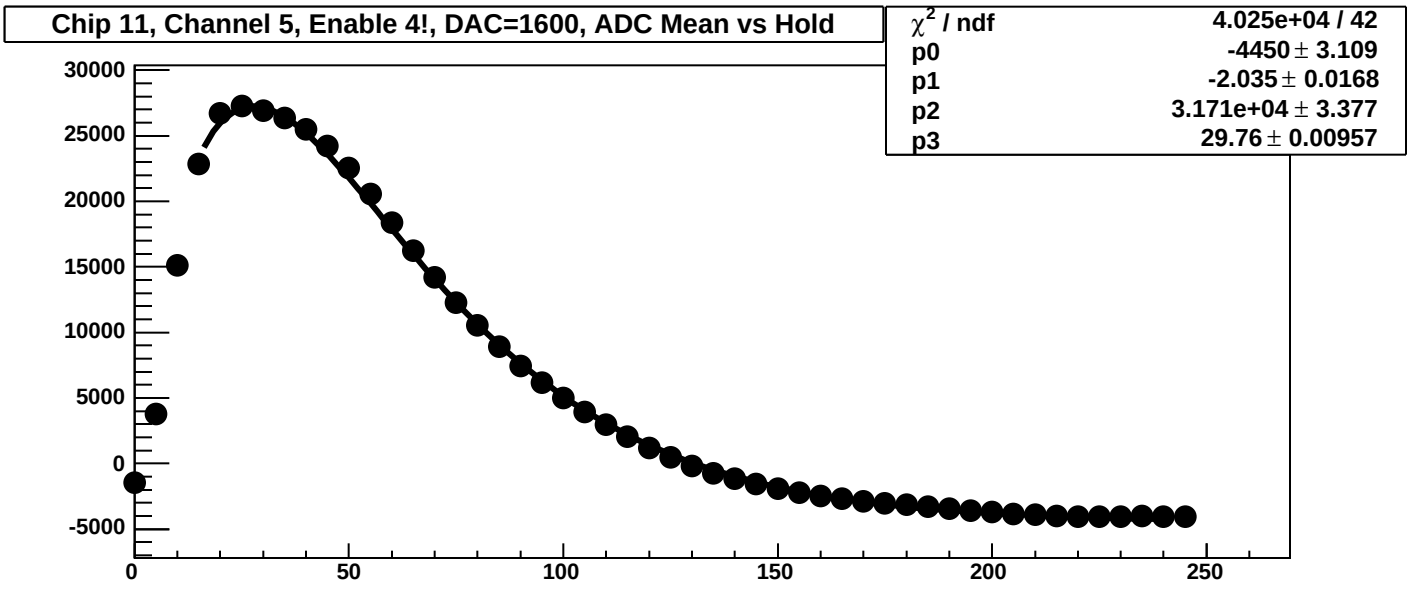


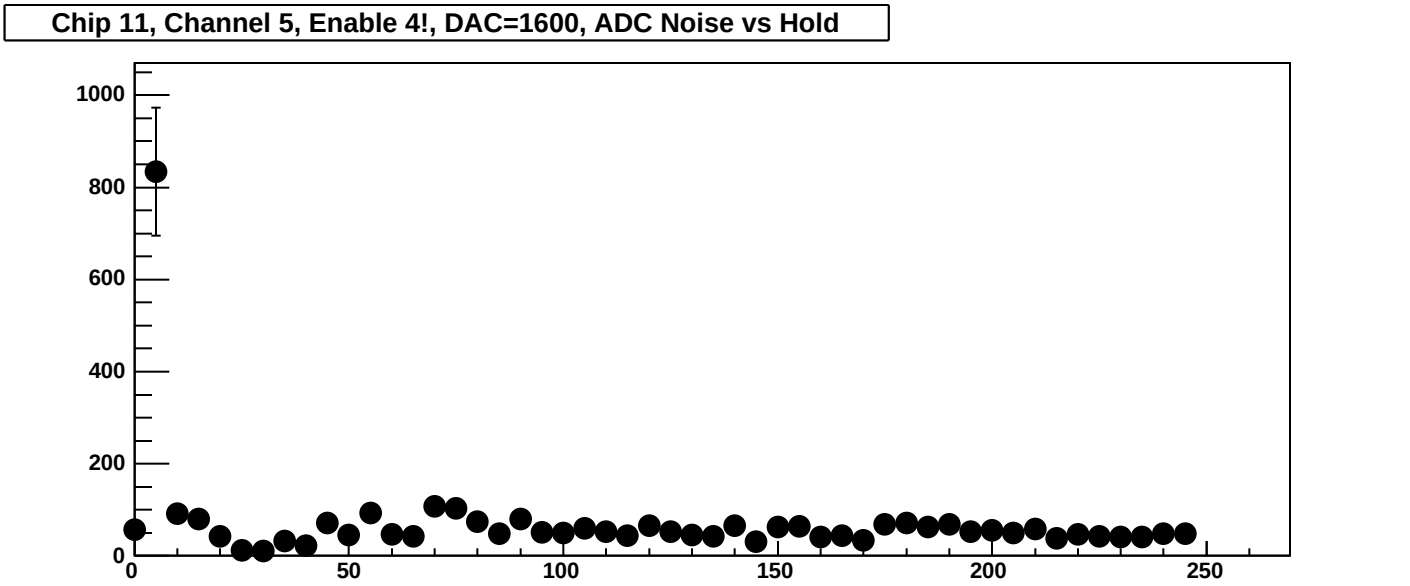
Chip 11, Channel 5, Enable 3, DAC=1600, ADC Noise vs Hold

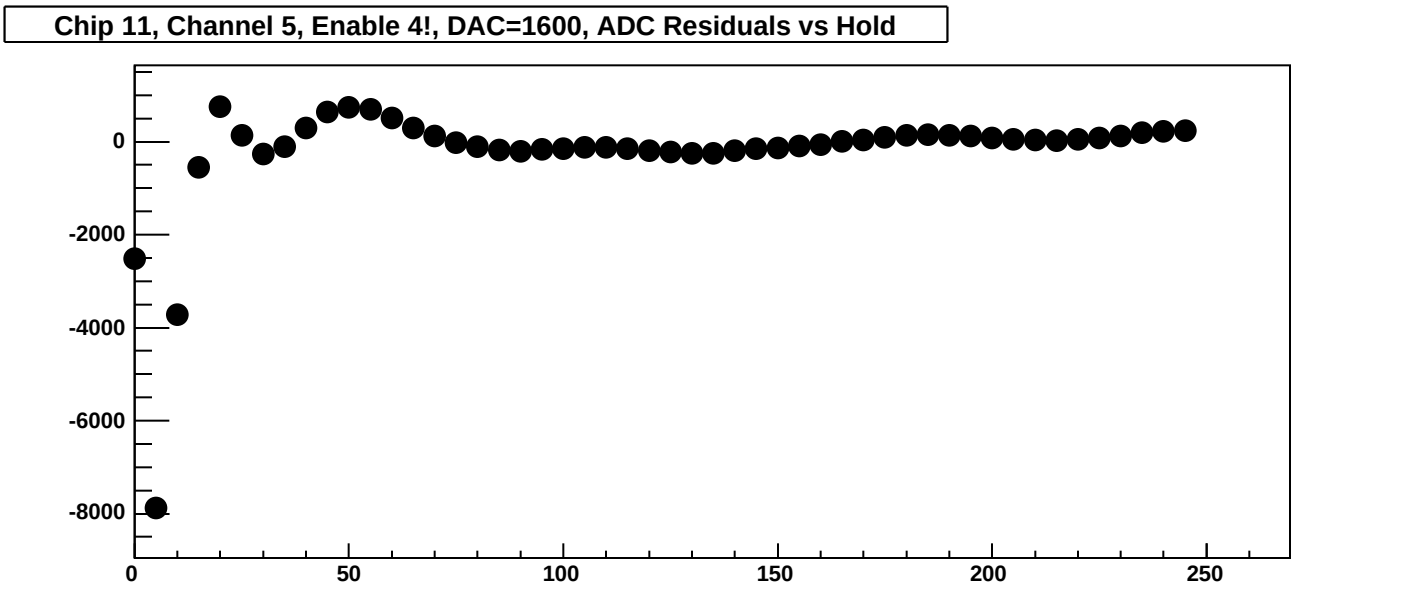


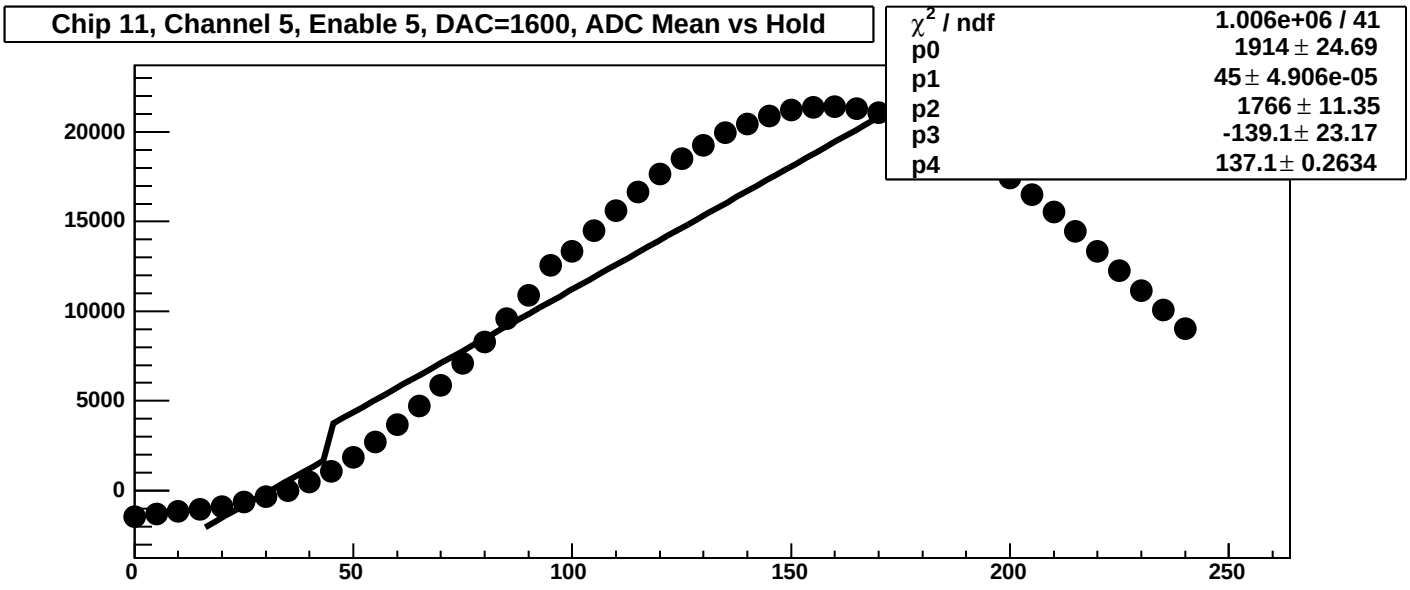
Chip 11, Channel 5, Enable 3, DAC=1600, ADC Residuals vs Hold

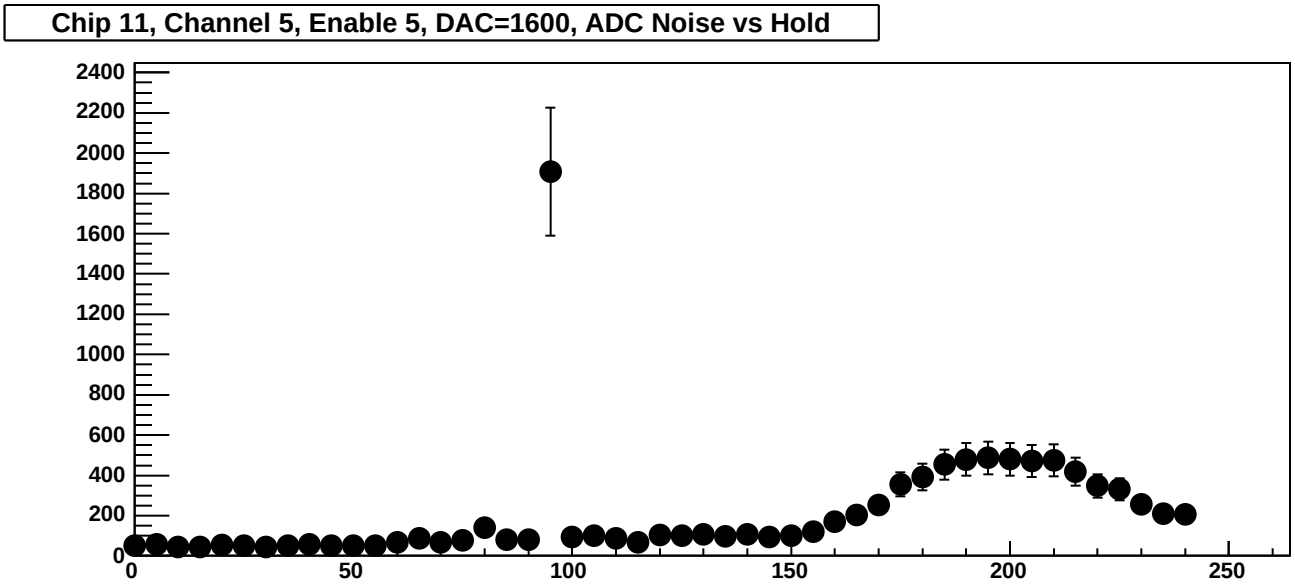


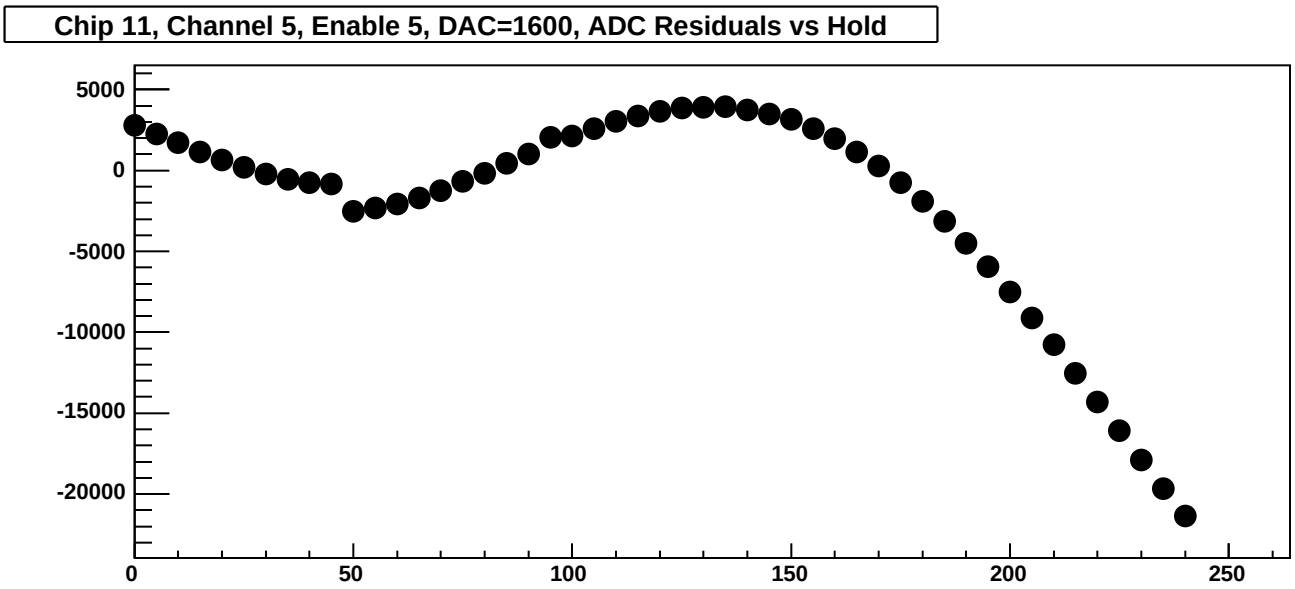

Chip 11, Channel 5, Enable 4!, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.025e+04 / 42
p0 -4450 ± 3.109
p1 -2.035 ± 0.0168
p2 3.171e+04 ± 3.377
p3 29.76 ± 0.00957
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

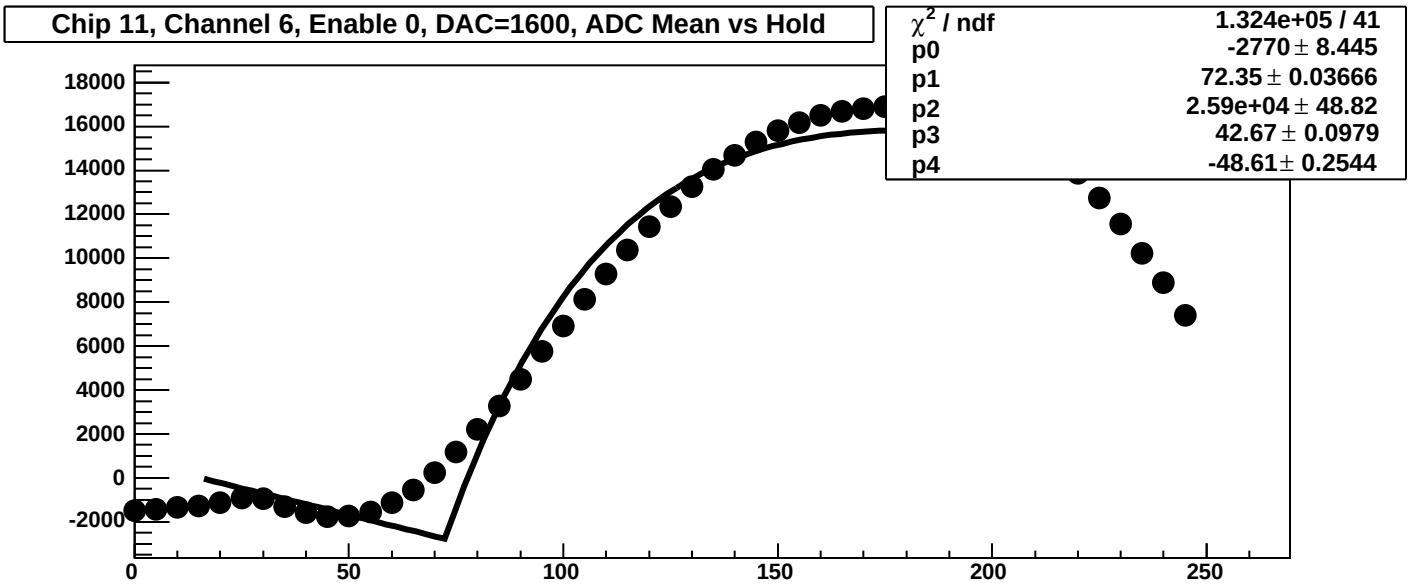

Chip 11, Channel 5, Enable 4!, DAC=1600, ADC Noise vs Hold
1000
800
600
400
200
0
0
50
100
150
200
250

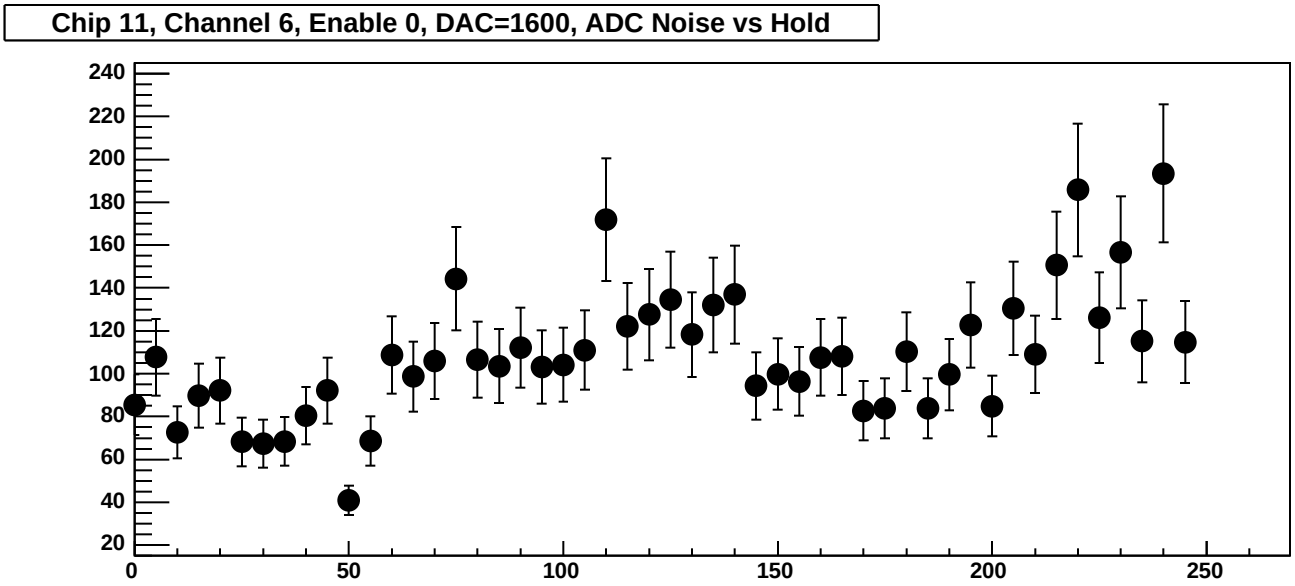

Chip 11, Channel 5, Enable 4!, DAC=1600, ADC Residuals vs Hold
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250

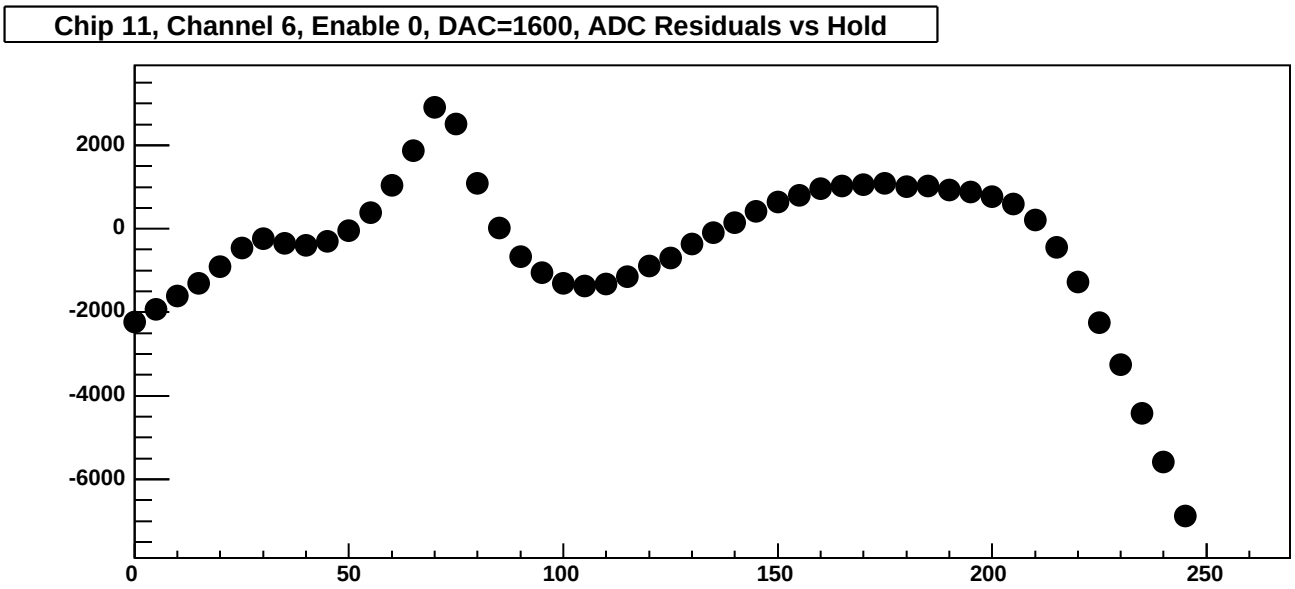

Chip 11, Channel 5, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.006e+06 / 41
p0 1914 ± 24.69
p1 45 ± 4.906e-05
p2 1766 ± 11.35
p3 -139.1 ± 23.17
p4 137.1 ± 0.2634
20000
15000
10000
5000
0
0
50
100
150
200
250

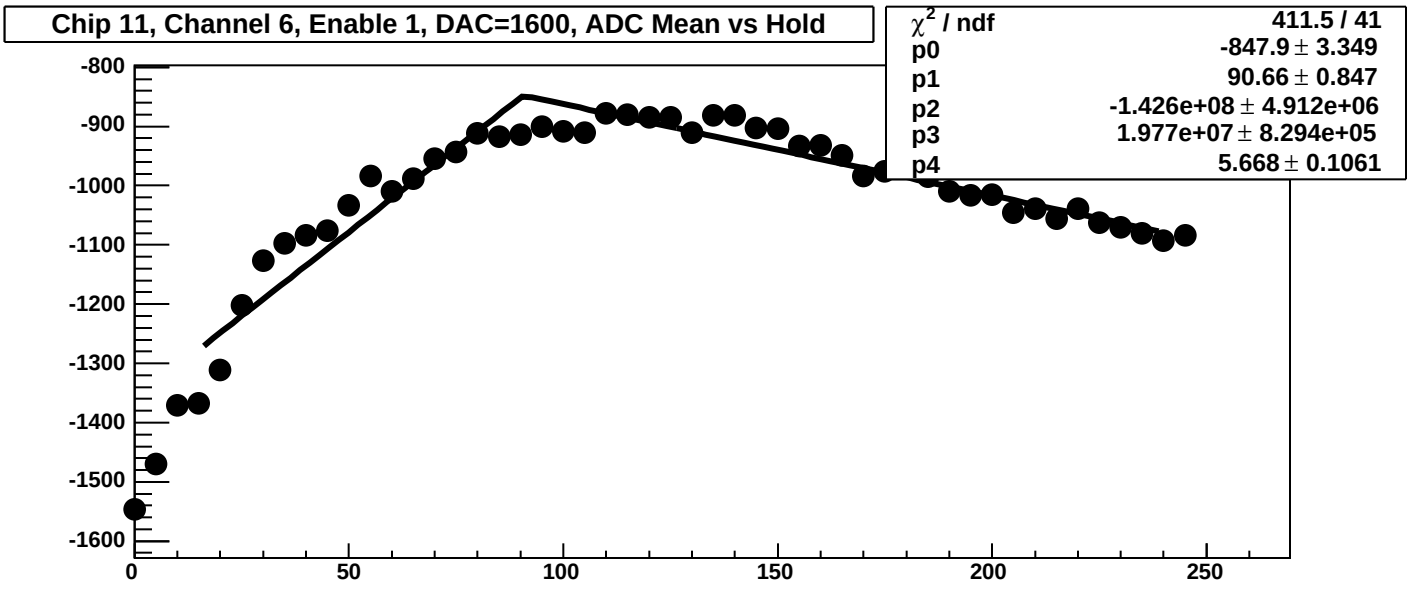

Chip 11, Channel 5, Enable 5, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

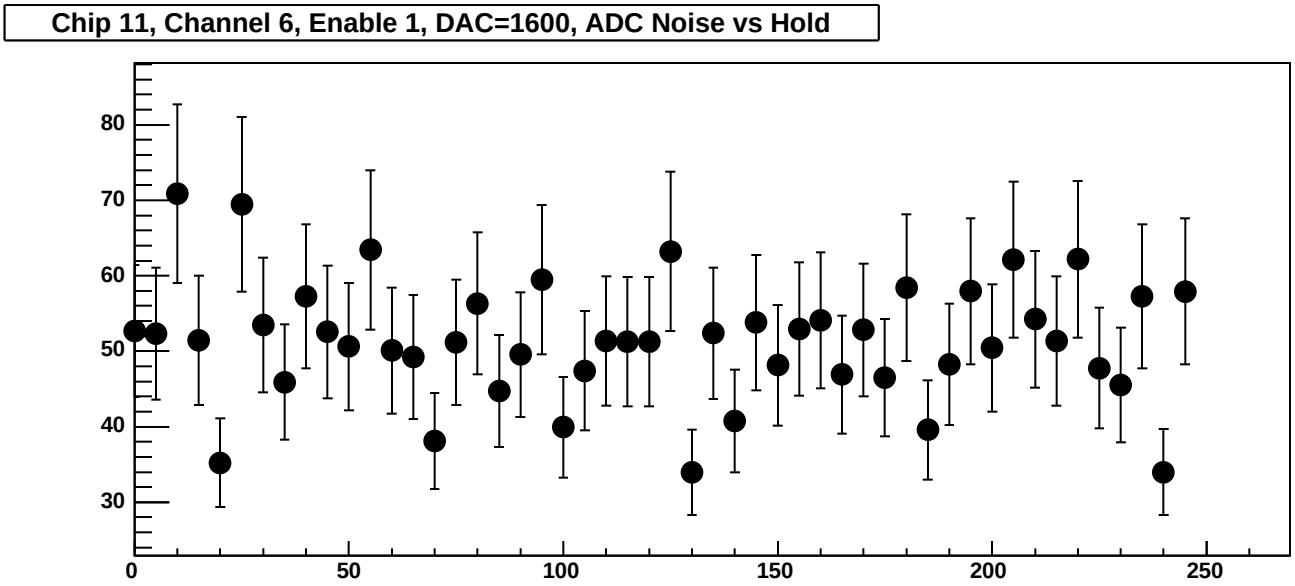

Chip 11, Channel 5, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

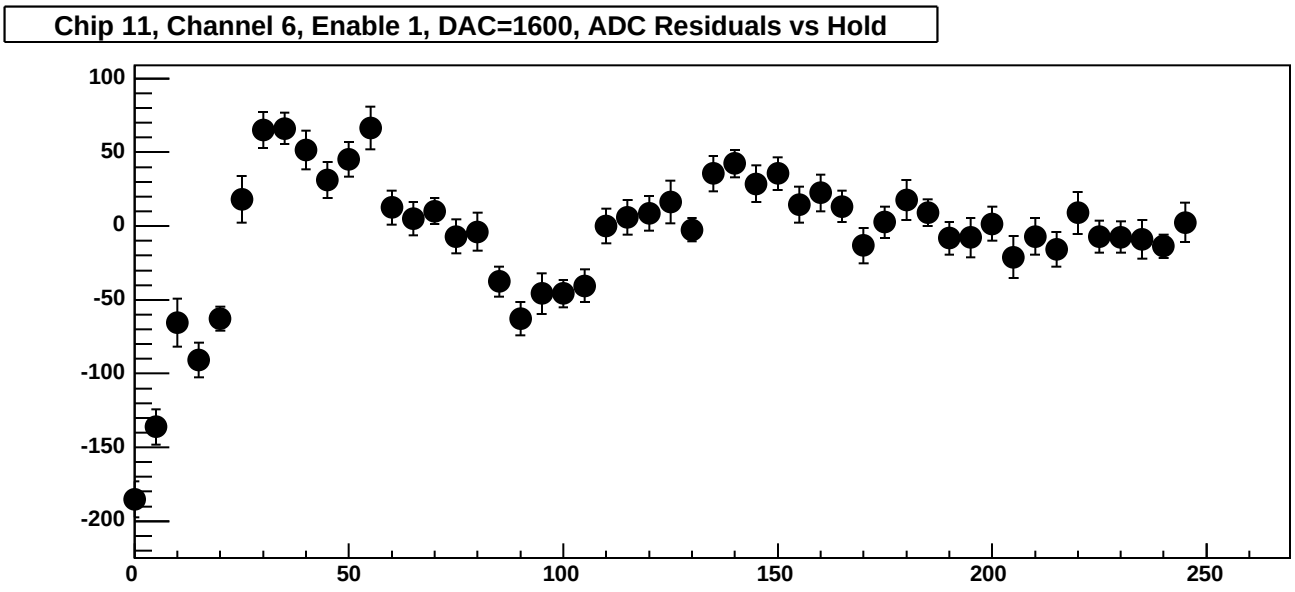

Chip 11, Channel 6, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.324e+05 / 41
p0 -2770 ± 8.445
p1 72.35 ± 0.03666
p2 2.59e+04 ± 48.82
p3 42.67 ± 0.0979
p4 -48.61 ± 0.2544
18000
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

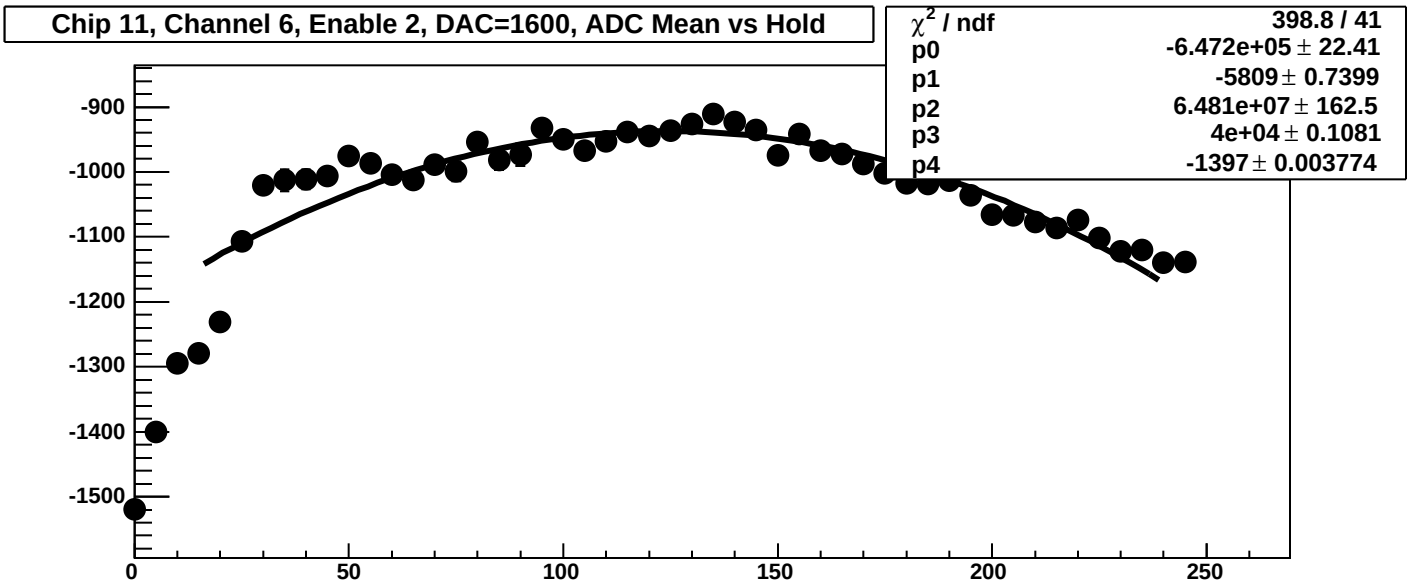

Chip 11, Channel 6, Enable 0, DAC=1600, ADC Noise vs Hold
240
220
200
180
160
140
120
100
80
60
40
20
0
50
100
150
200
250

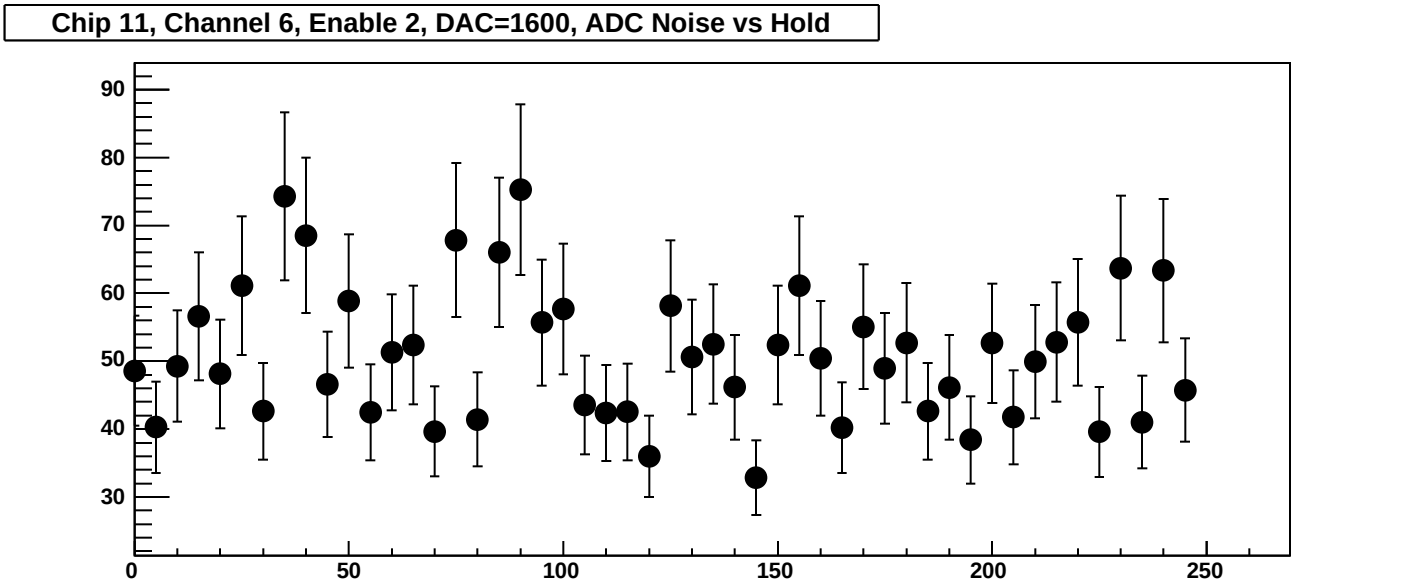

Chip 11, Channel 6, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

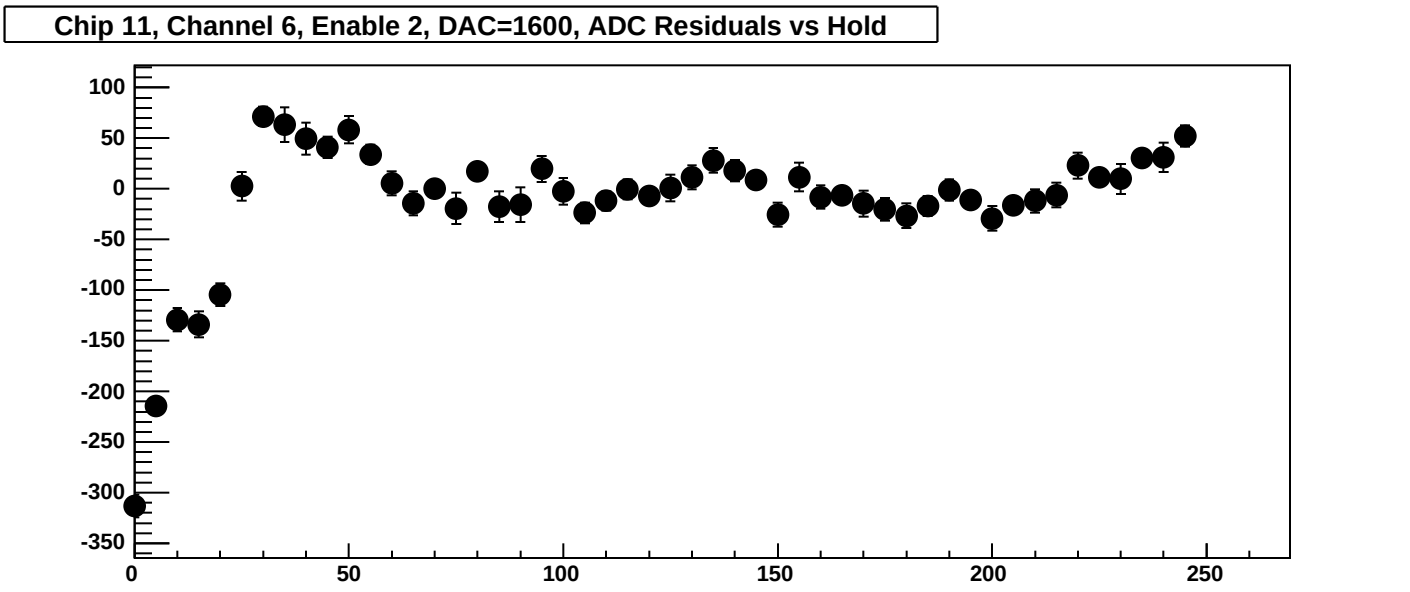

Chip 11, Channel 6, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 411.5 / 41
p0 -847.9 ± 3.349
p1 90.66 ± 0.847
p2 -1.426e+08 ± 4.912e+06
p3 1.977e+07 ± 8.294e+05
p4 5.668 ± 0.1061
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
50
100
150
200
250


Chip 11, Channel 6, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

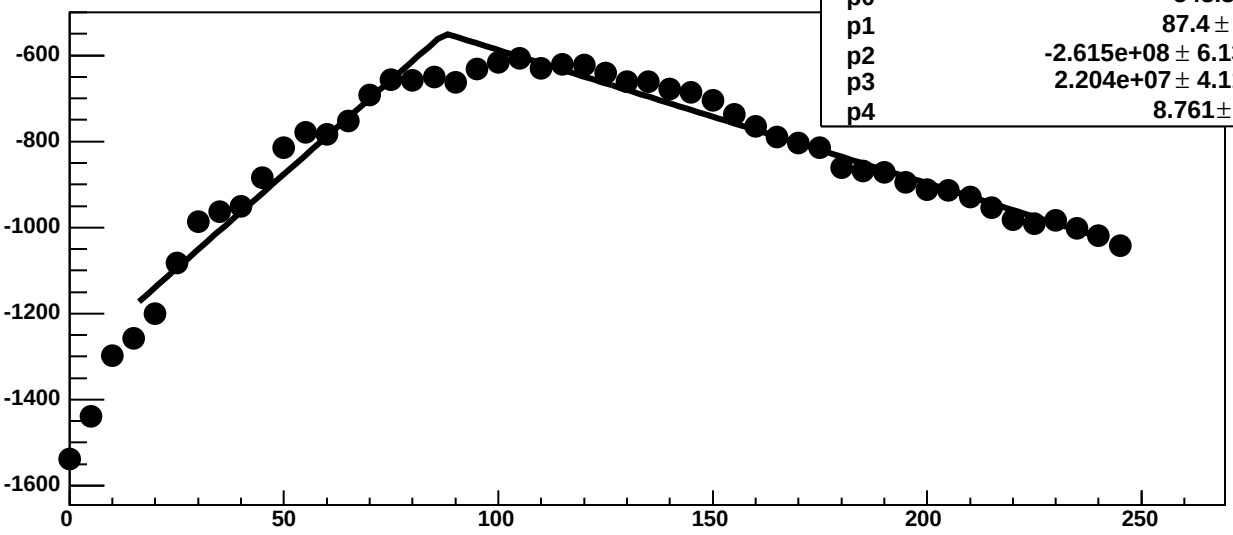

Chip 11, Channel 6, Enable 1, DAC=1600, ADC Residuals vs Hold
100
50
0
-50
-100
-150
-200
0
50
100
150
200
250


Chip 11, Channel 6, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 398.8 / 41
p0 -6.472e+05 ± 22.41
p1 -5809 ± 0.7399
p2 6.481e+07 ± 162.5
p3 4e+04 ± 0.1081
p4 -1397 ± 0.003774
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250

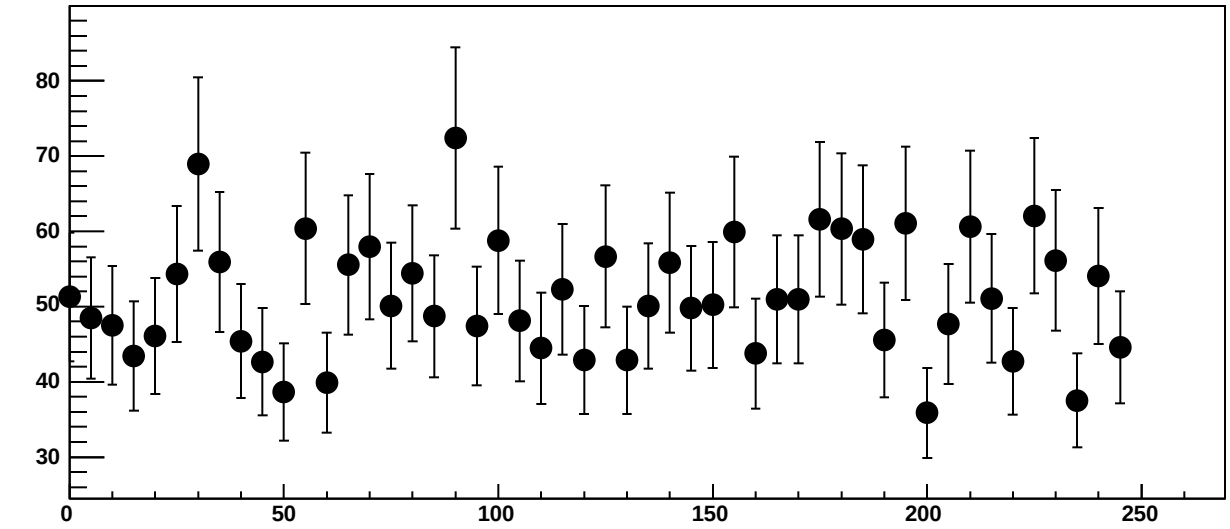

Chip 11, Channel 6, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250


Chip 11, Channel 6, Enable 2, DAC=1600, ADC Residuals vs Hold
100
50
0
-50
-100
-150
-200
-250
-300
-350
0
50
100
150
200
250

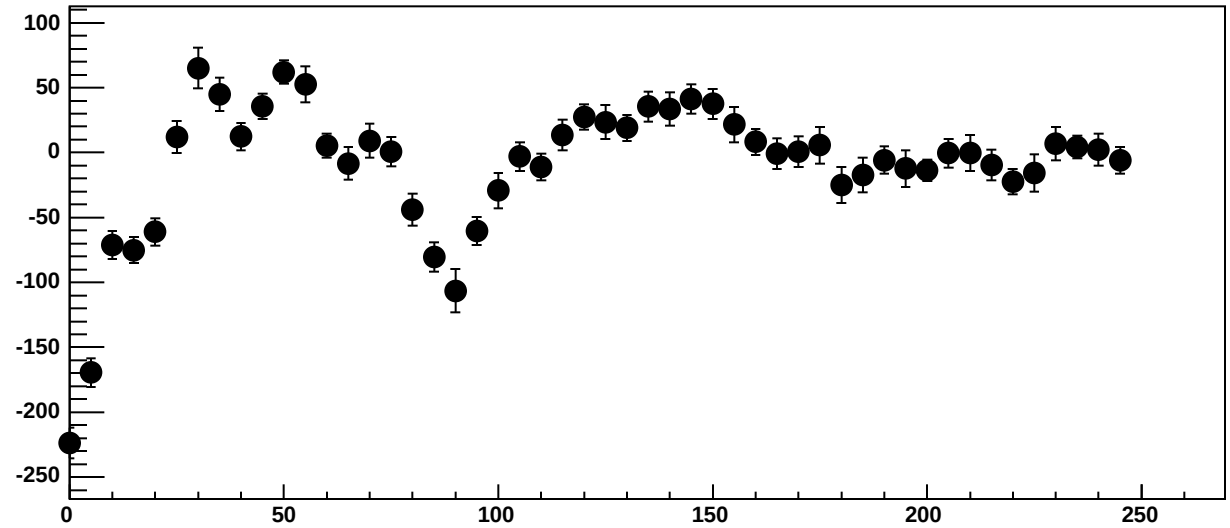
Chip 11, Channel 6, Enable 3, DAC=1600, ADC Mean vs Hold



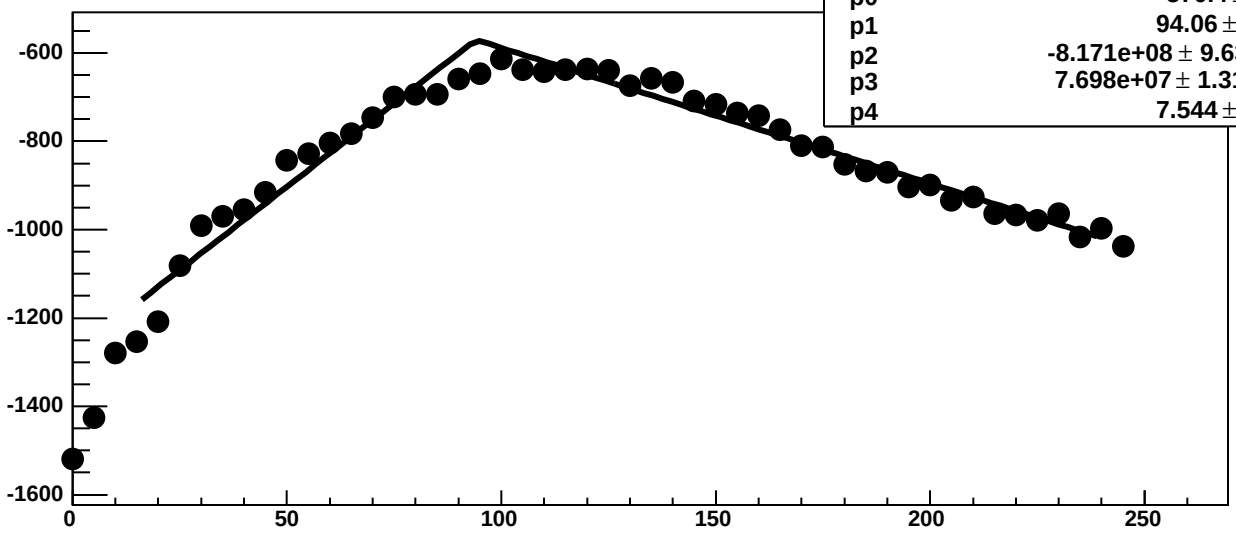
Chip 11, Channel 6, Enable 3, DAC=1600, ADC Noise vs Hold



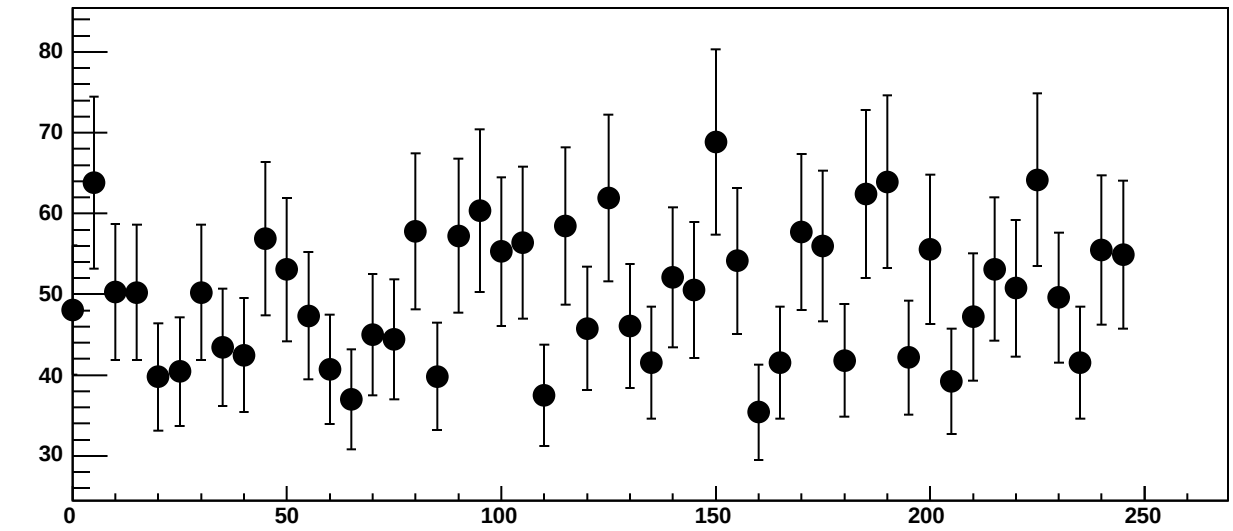
Chip 11, Channel 6, Enable 3, DAC=1600, ADC Residuals vs Hold



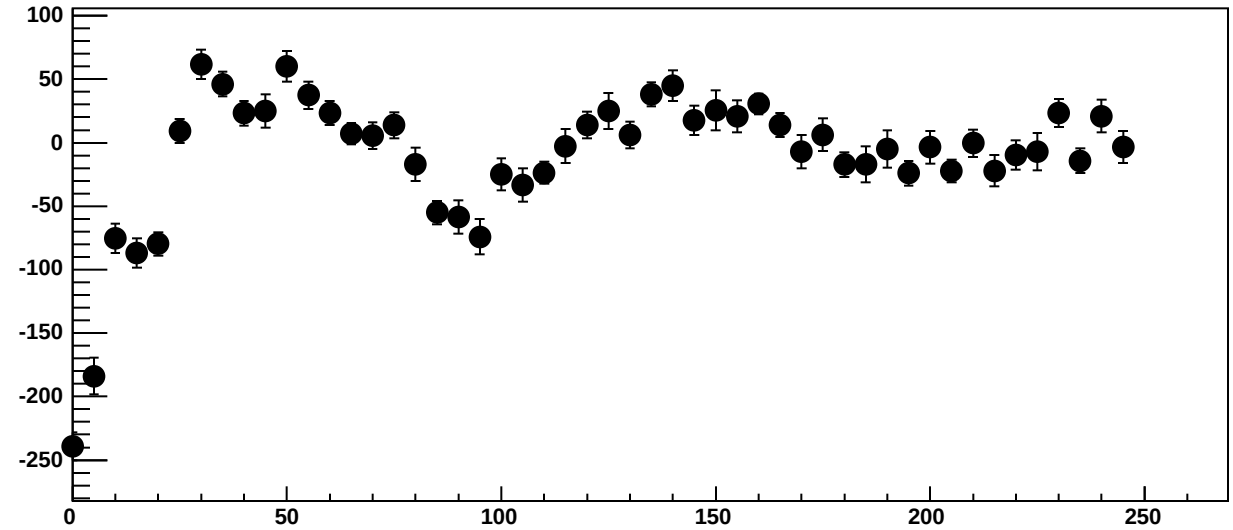
Chip 11, Channel 6, Enable 4, DAC=1600, ADC Mean vs Hold

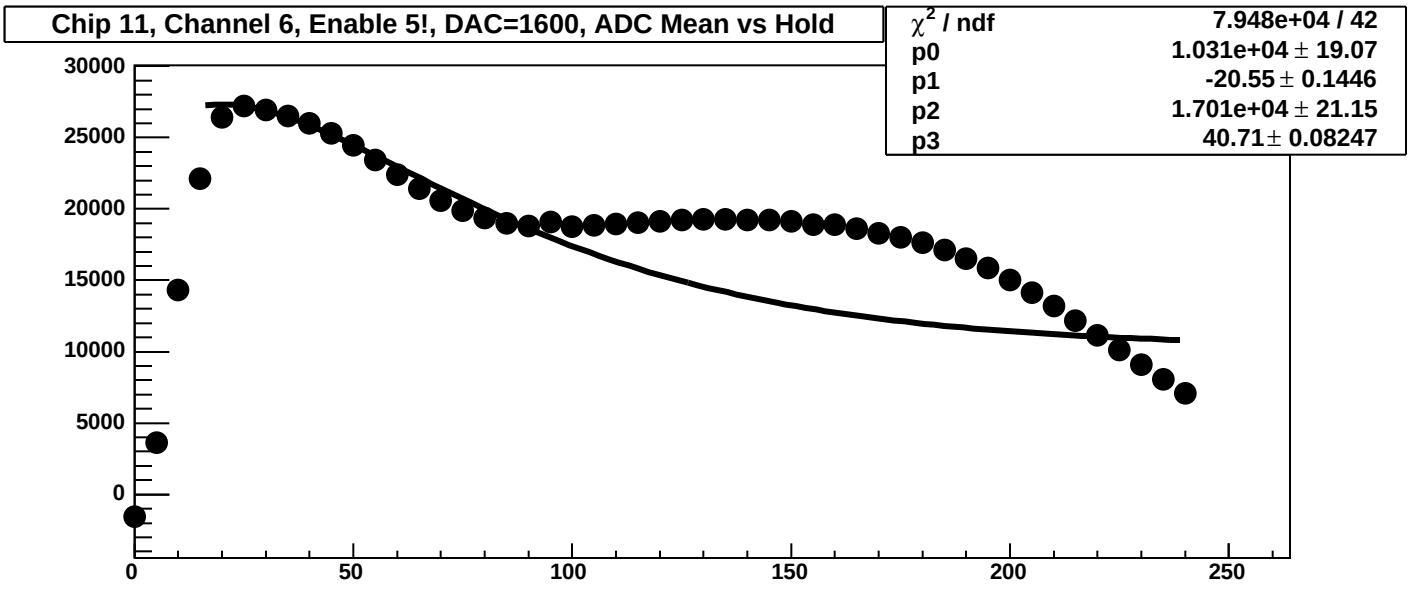


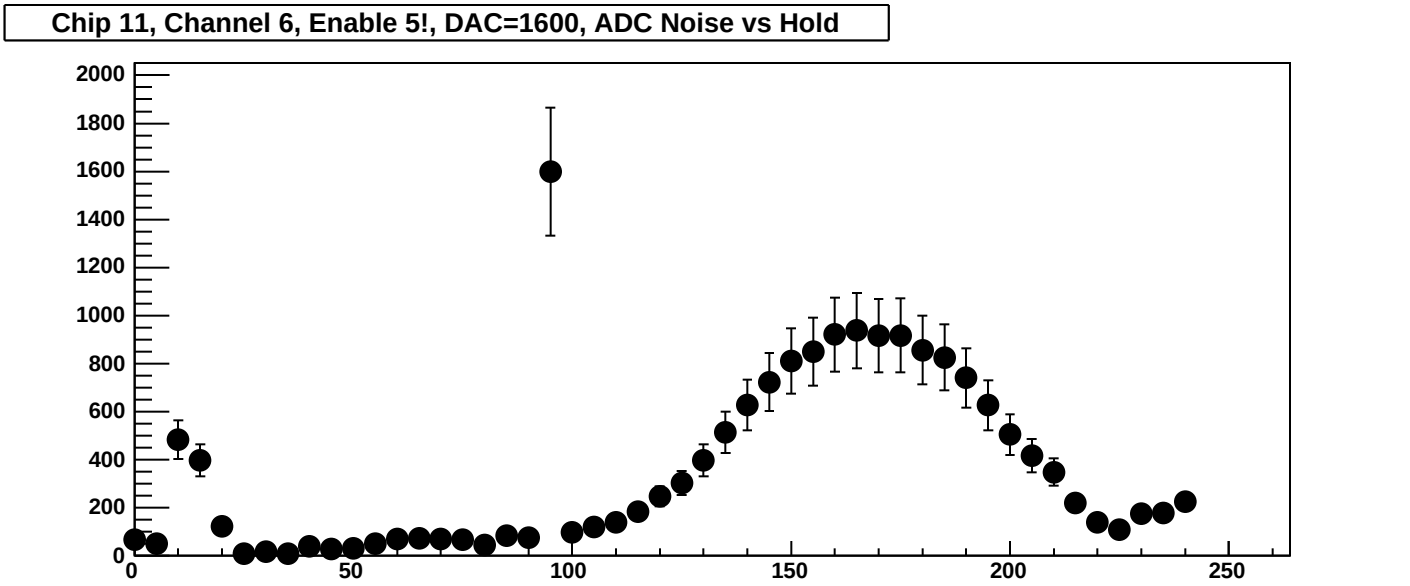
Chip 11, Channel 6, Enable 4, DAC=1600, ADC Noise vs Hold

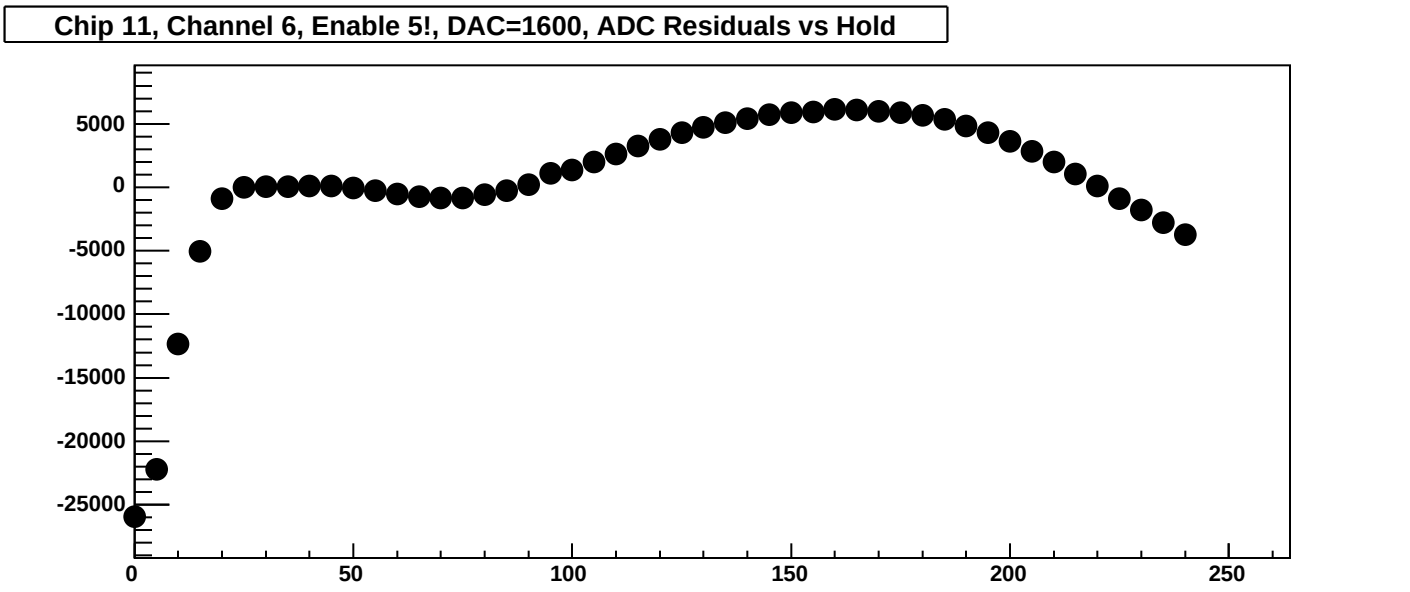


Chip 11, Channel 6, Enable 4, DAC=1600, ADC Residuals vs Hold

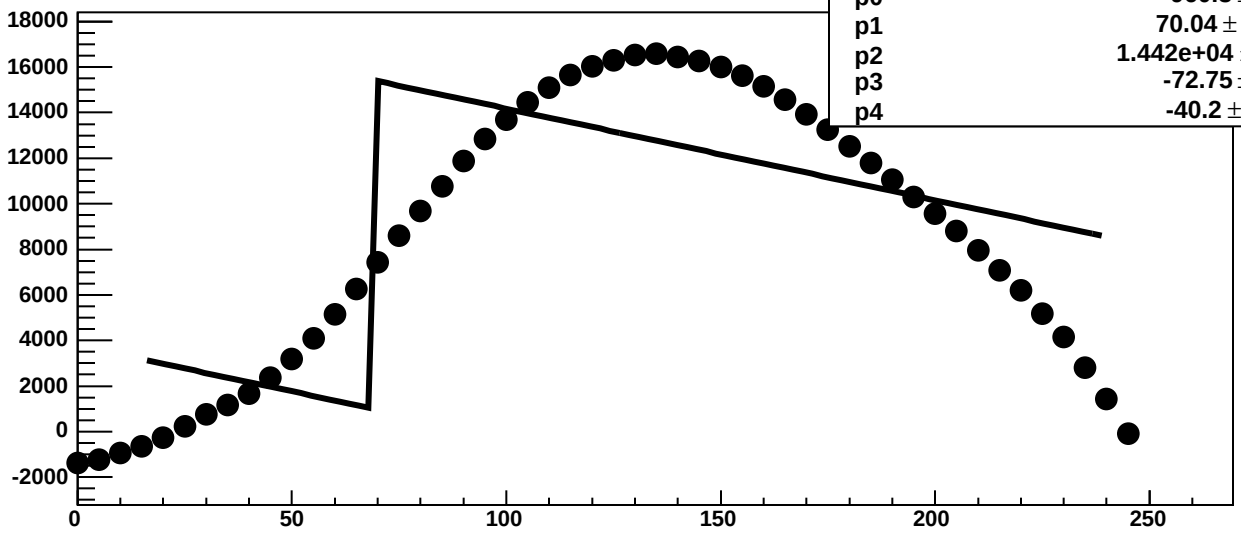



Chip 11, Channel 6, Enable 5!, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.948e+04 / 42
p0 1.031e+04 ± 19.07
p1 -20.55 ± 0.1446
p2 1.701e+04 ± 21.15
p3 40.71 ± 0.08247
30000
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

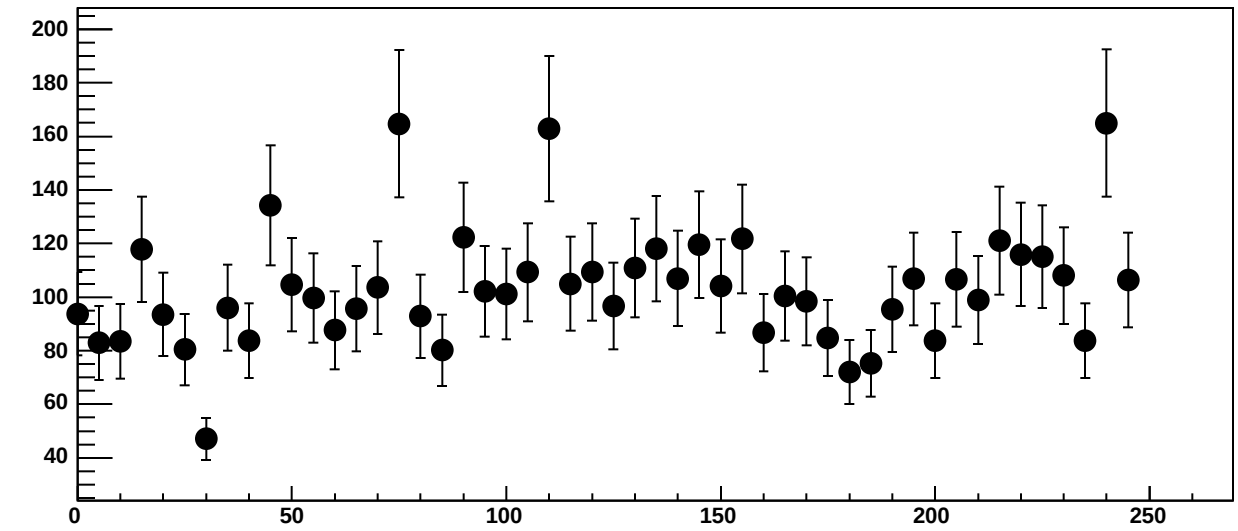

Chip 11, Channel 6, Enable 5!, DAC=1600, ADC Noise vs Hold
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 11, Channel 6, Enable 5!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
-25000
0
50
100
150
200
250

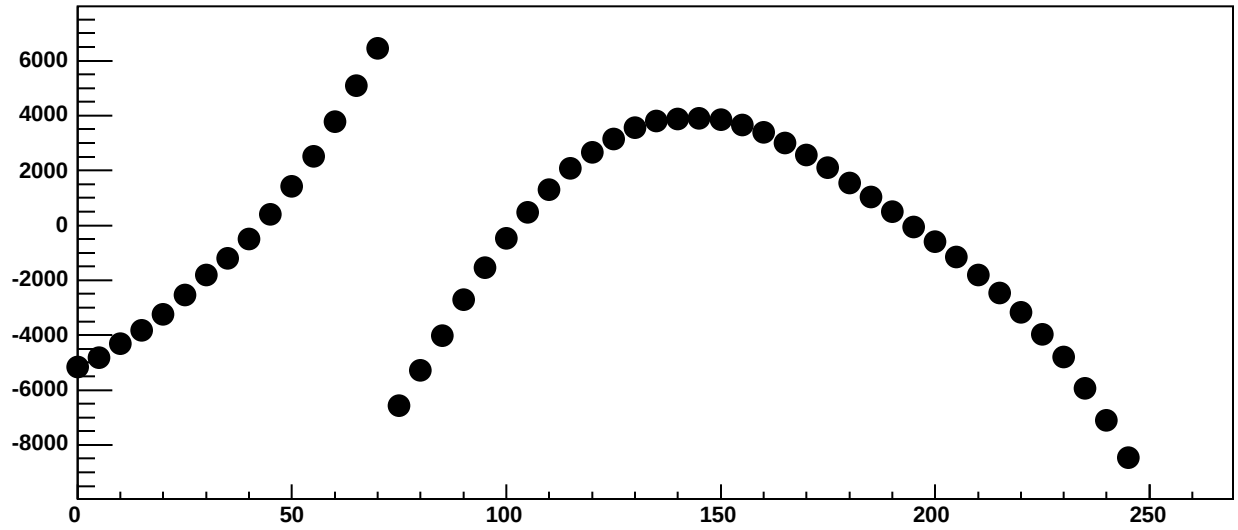
Chip 11, Channel 7, Enable 0, DAC=1600, ADC Mean vs Hold



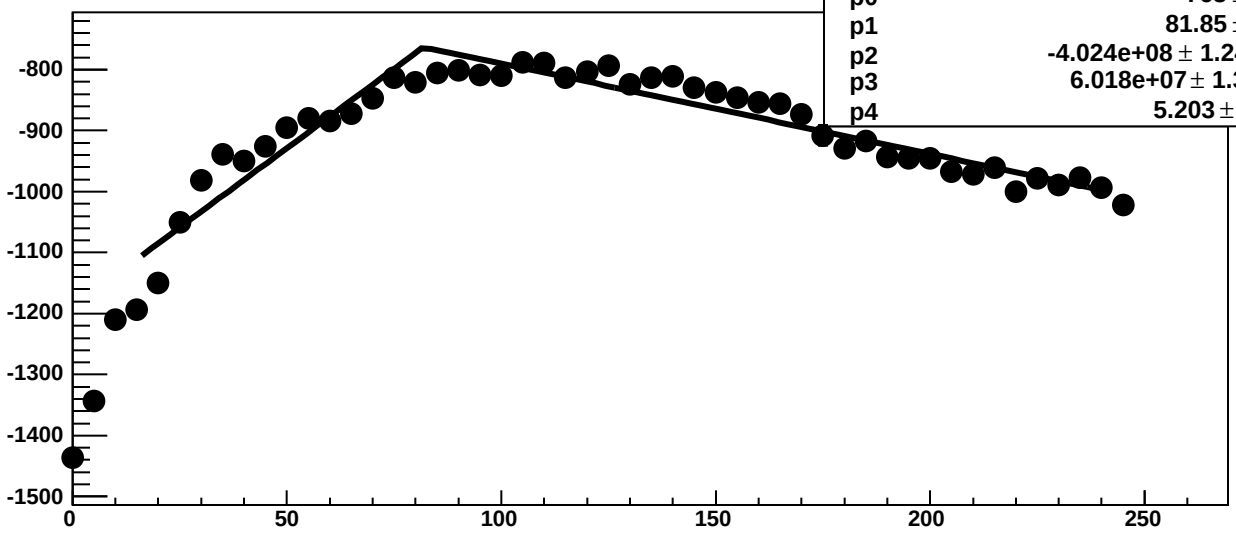
Chip 11, Channel 7, Enable 0, DAC=1600, ADC Noise vs Hold



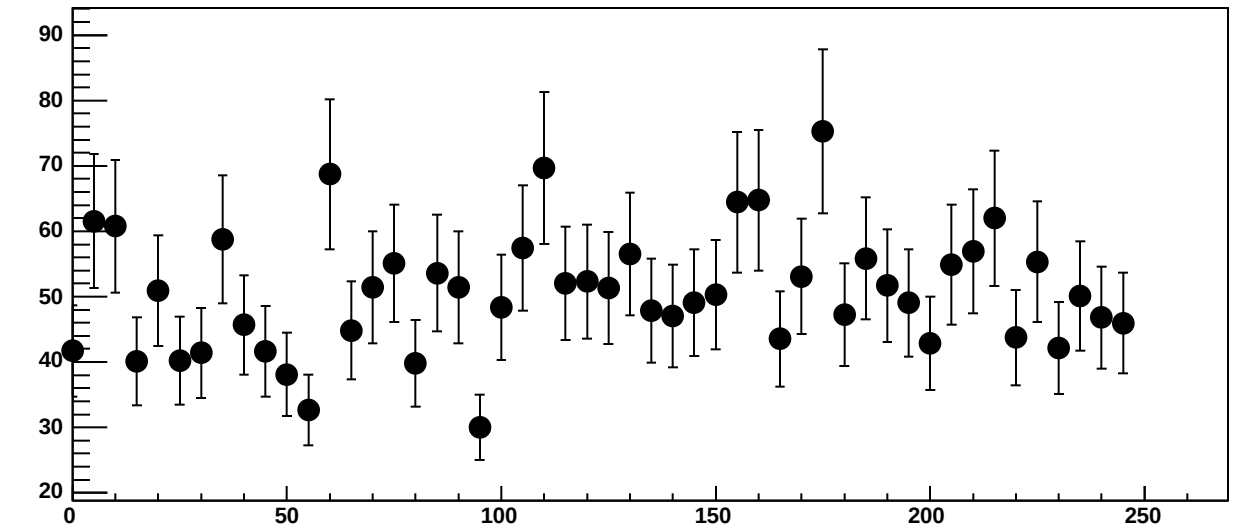
Chip 11, Channel 7, Enable 0, DAC=1600, ADC Residuals vs Hold



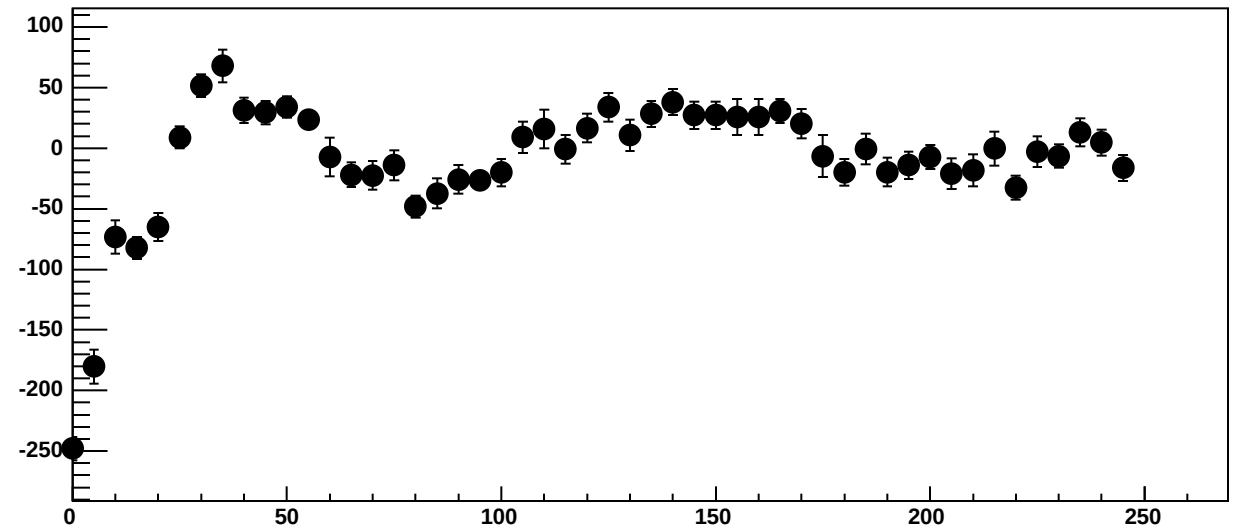
Chip 11, Channel 7, Enable 1, DAC=1600, ADC Mean vs Hold

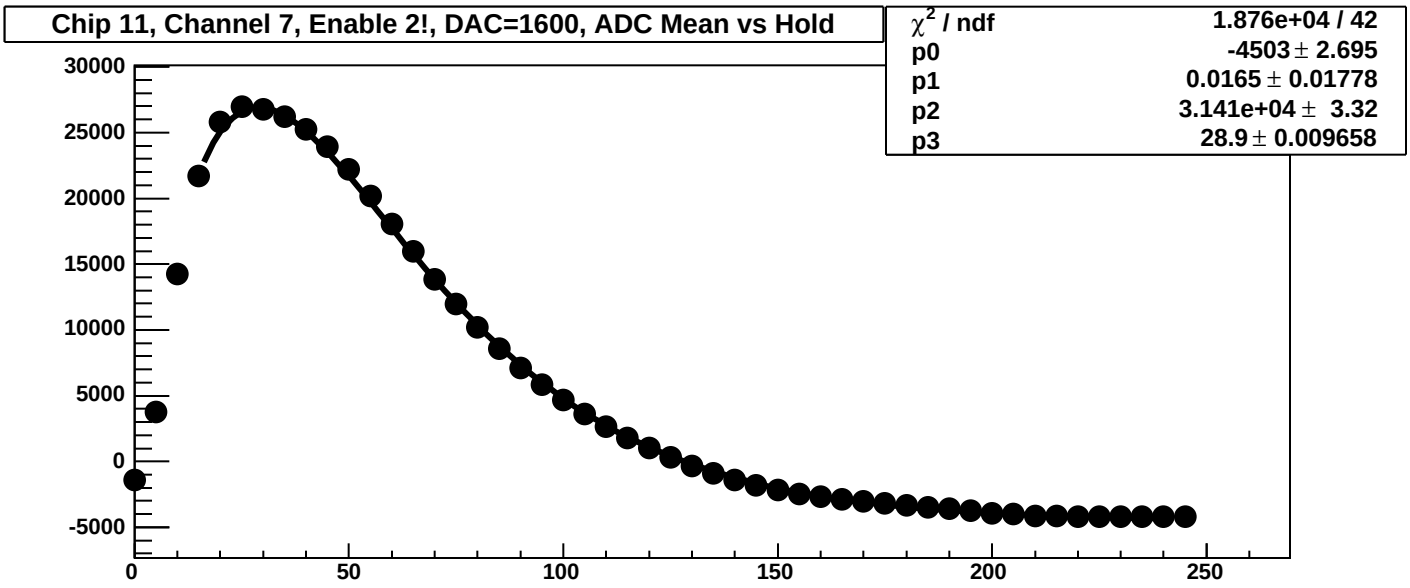


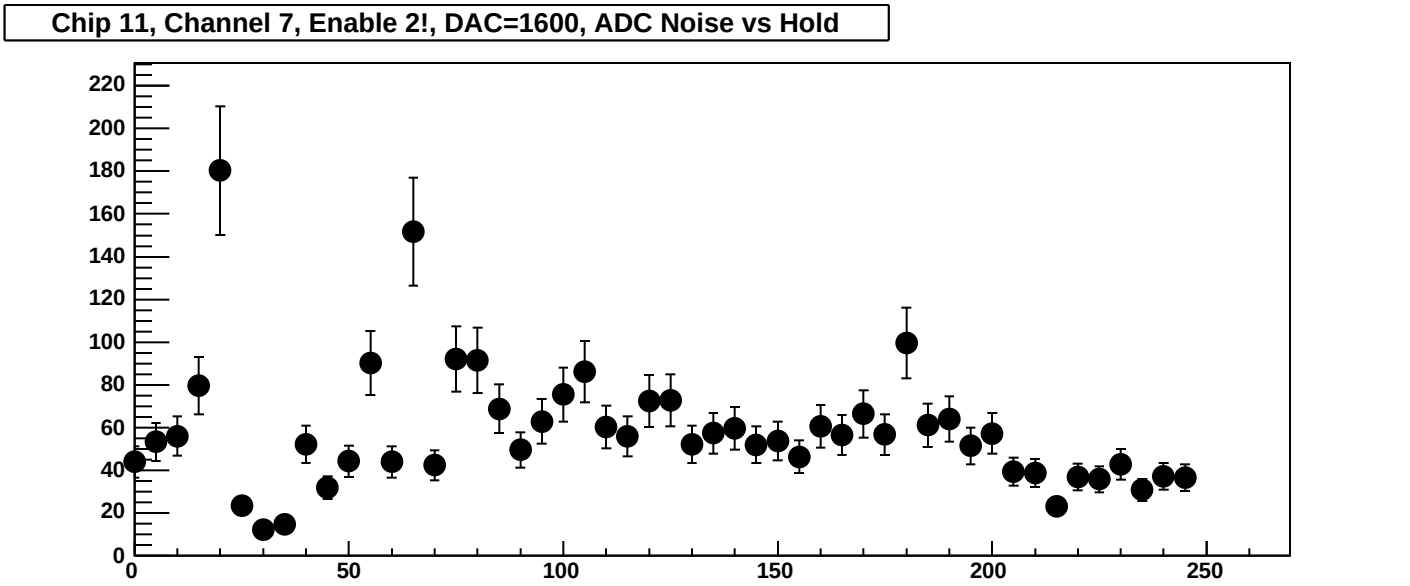
Chip 11, Channel 7, Enable 1, DAC=1600, ADC Noise vs Hold

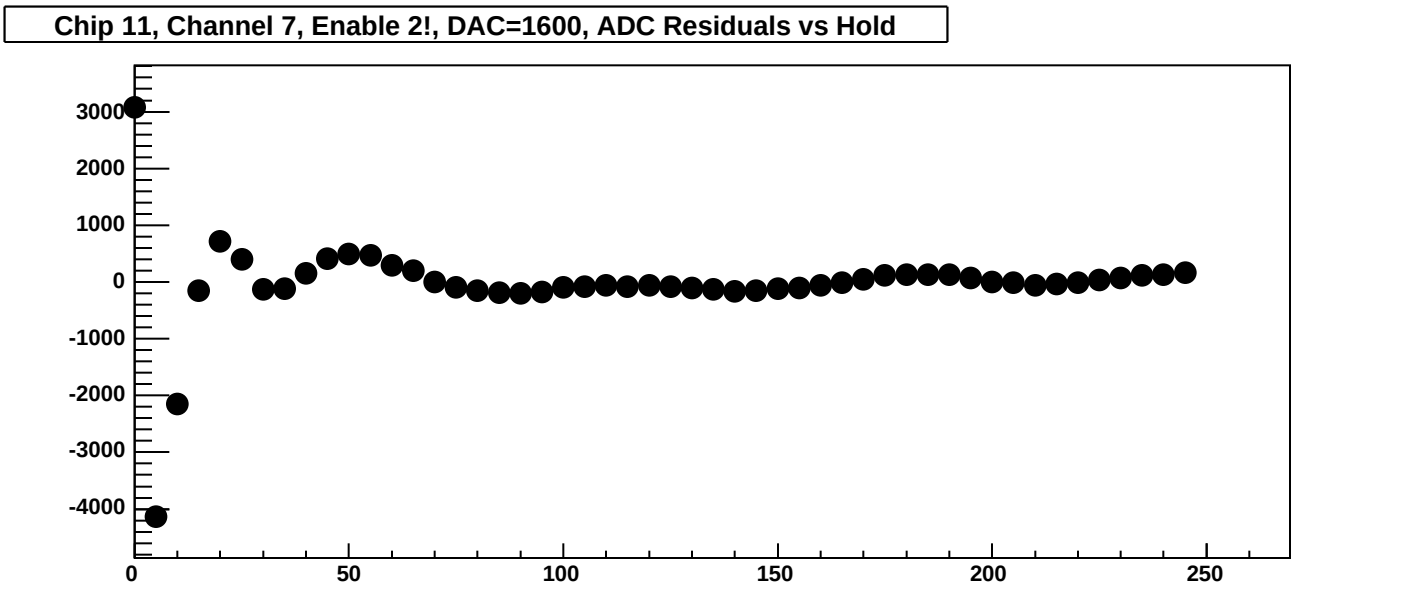


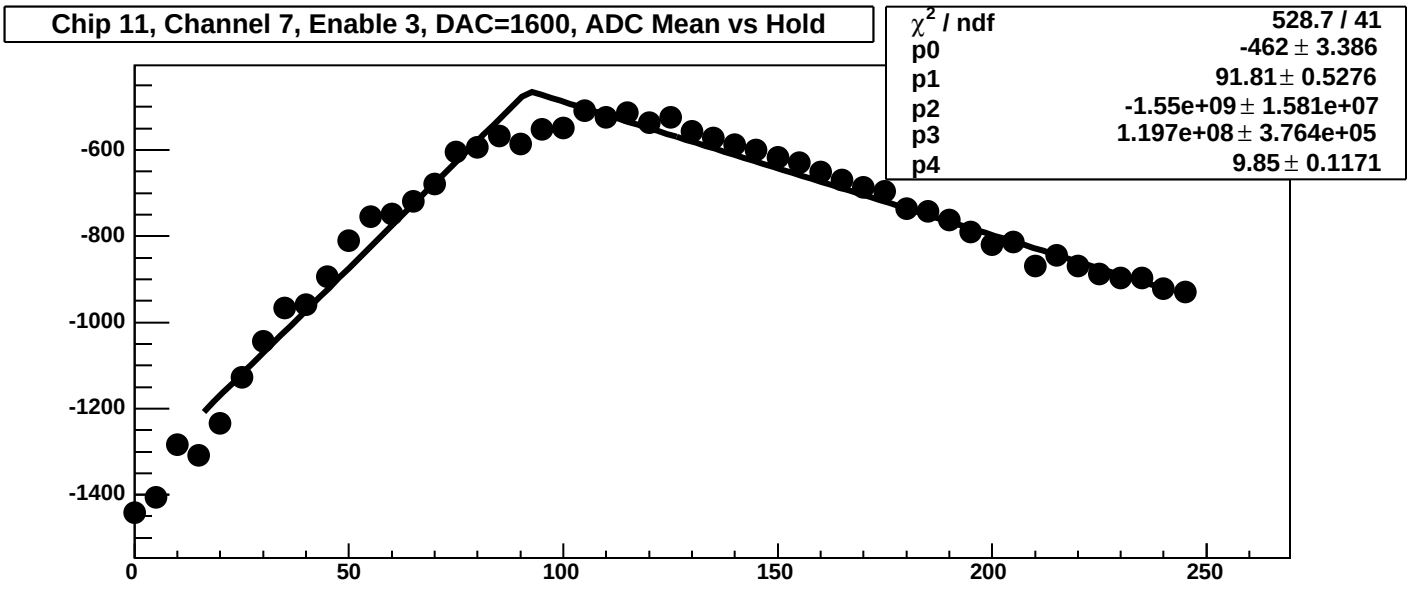
Chip 11, Channel 7, Enable 1, DAC=1600, ADC Residuals vs Hold

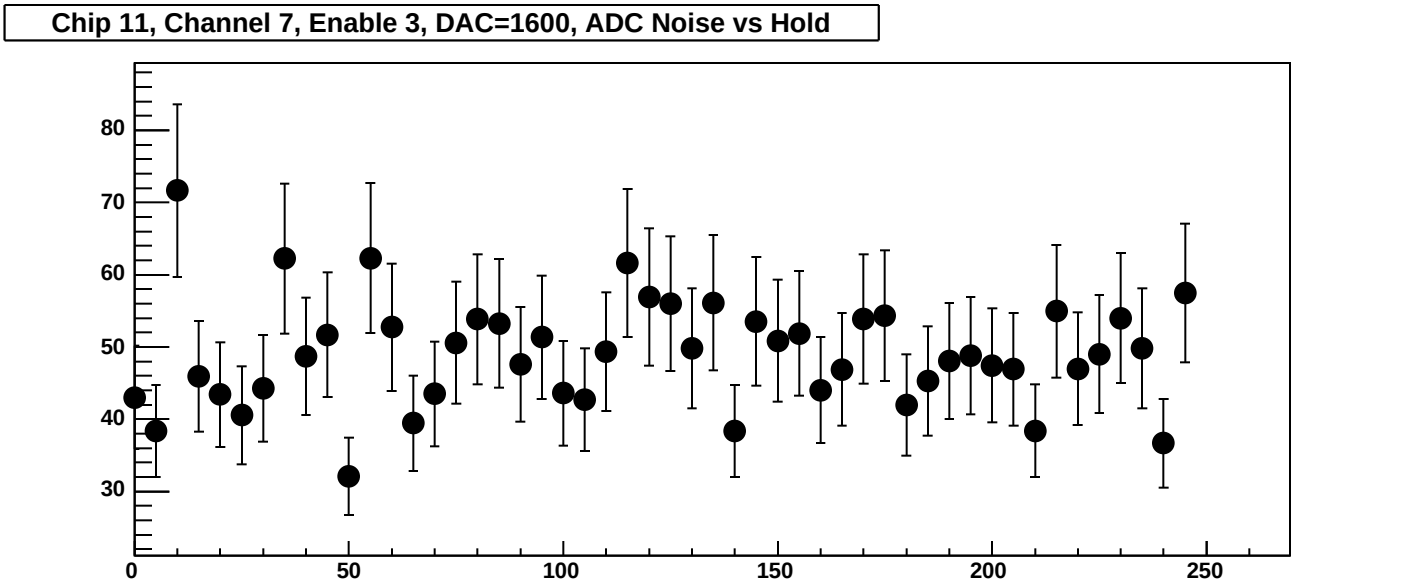


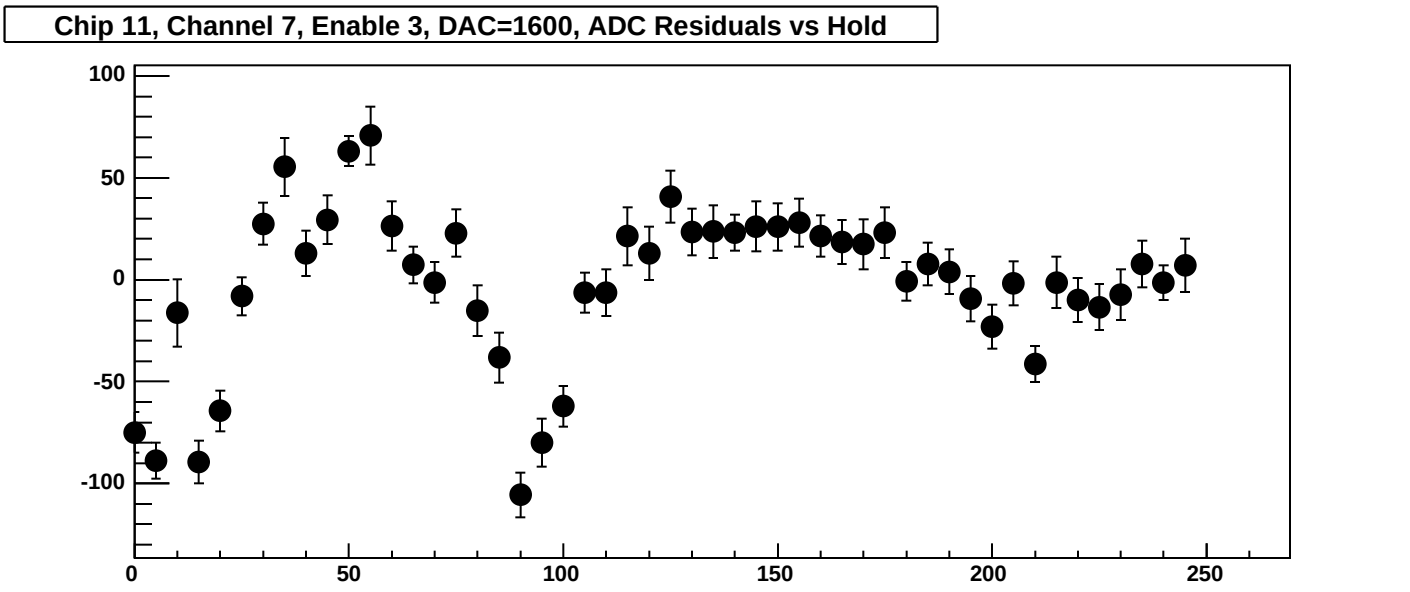

Chip 11, Channel 7, Enable 2!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.876e+04 / 42
p0 -4503 ± 2.695
p1 0.0165 ± 0.01778
p2 3.141e+04 ± 3.32
p3 28.9 ± 0.009658
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 11, Channel 7, Enable 2!, DAC=1600, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

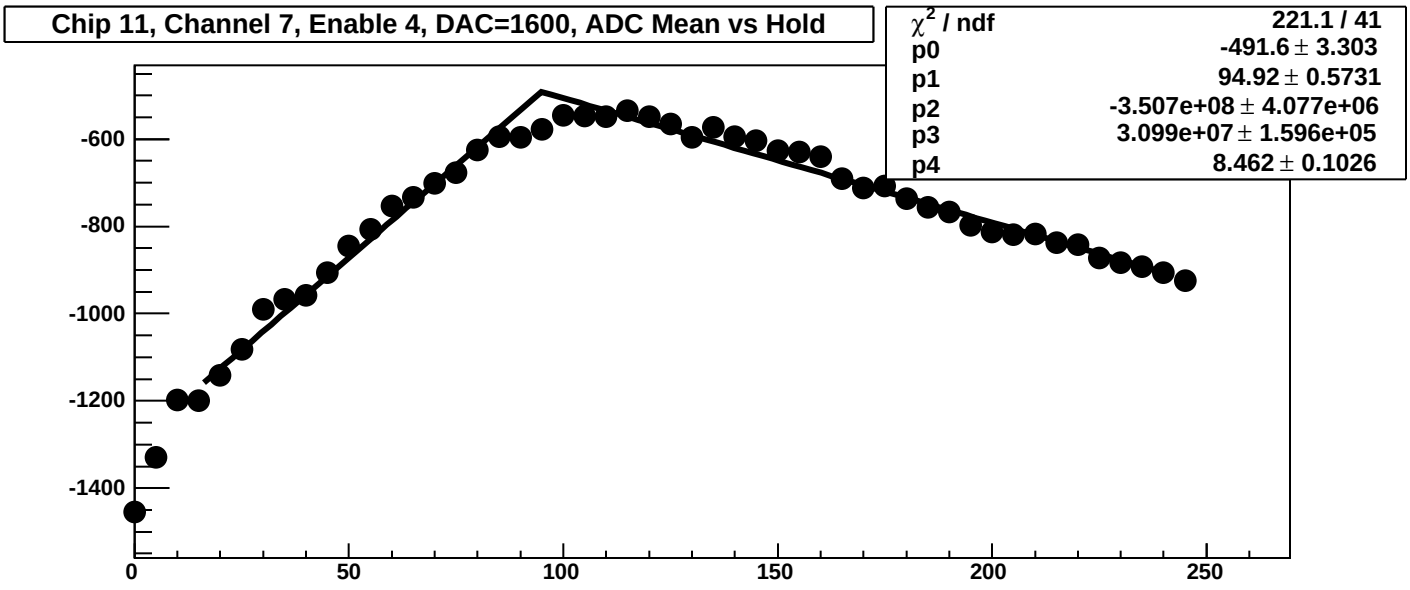

Chip 11, Channel 7, Enable 2!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250


Chip 11, Channel 7, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 528.7 / 41
p0 -462 ± 3.386
p1 91.81 ± 0.5276
p2 -1.55e+09 ± 1.581e+07
p3 1.197e+08 ± 3.764e+05
p4 9.85 ± 0.1171
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

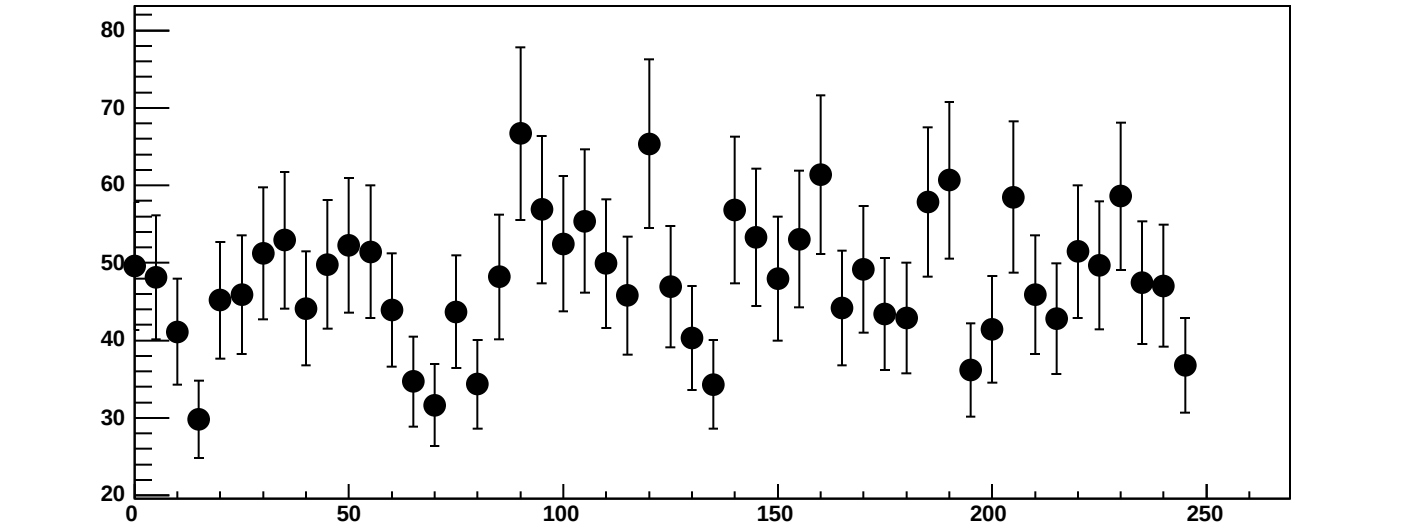

Chip 11, Channel 7, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 11, Channel 7, Enable 3, DAC=1600, ADC Residuals vs Hold
100
50
0
-50
-100
0
50
100
150
200
250

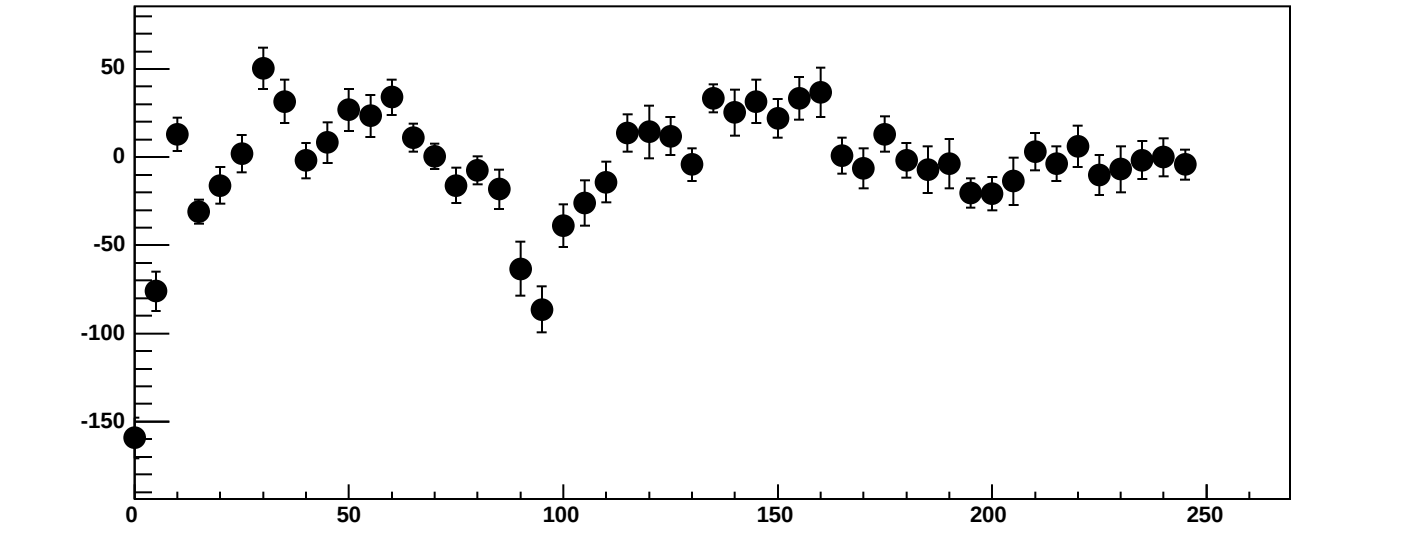
Chip 11, Channel 7, Enable 4, DAC=1600, ADC Mean vs Hold

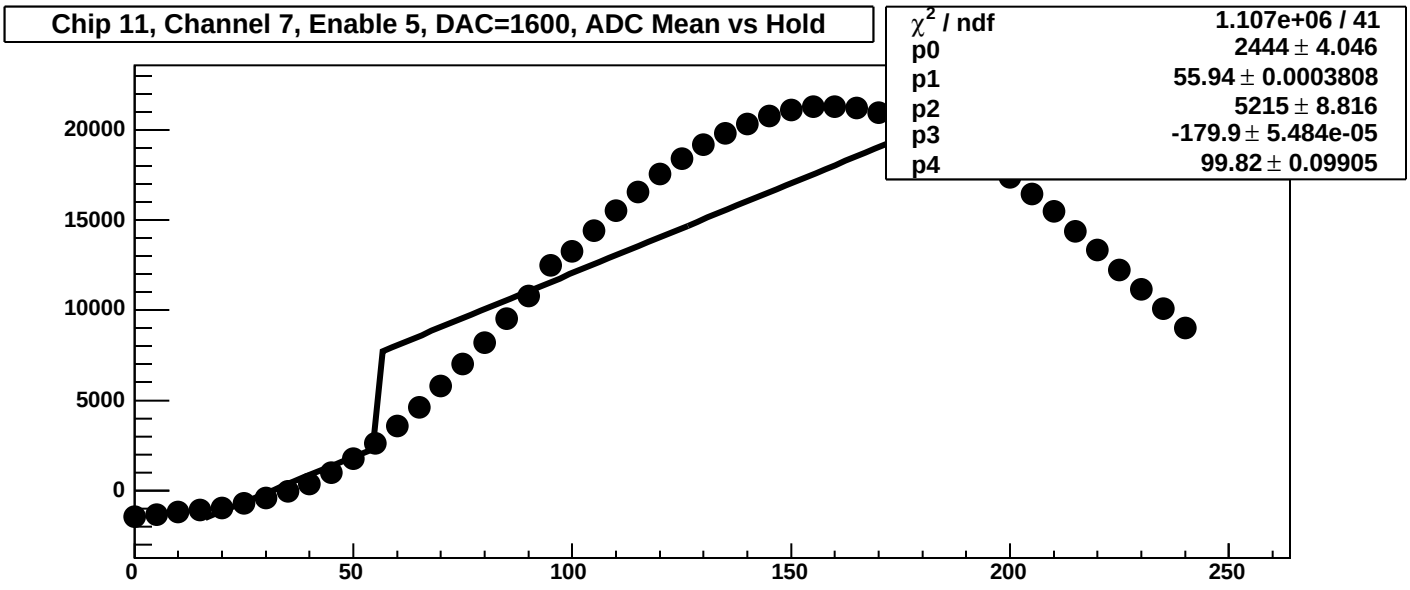


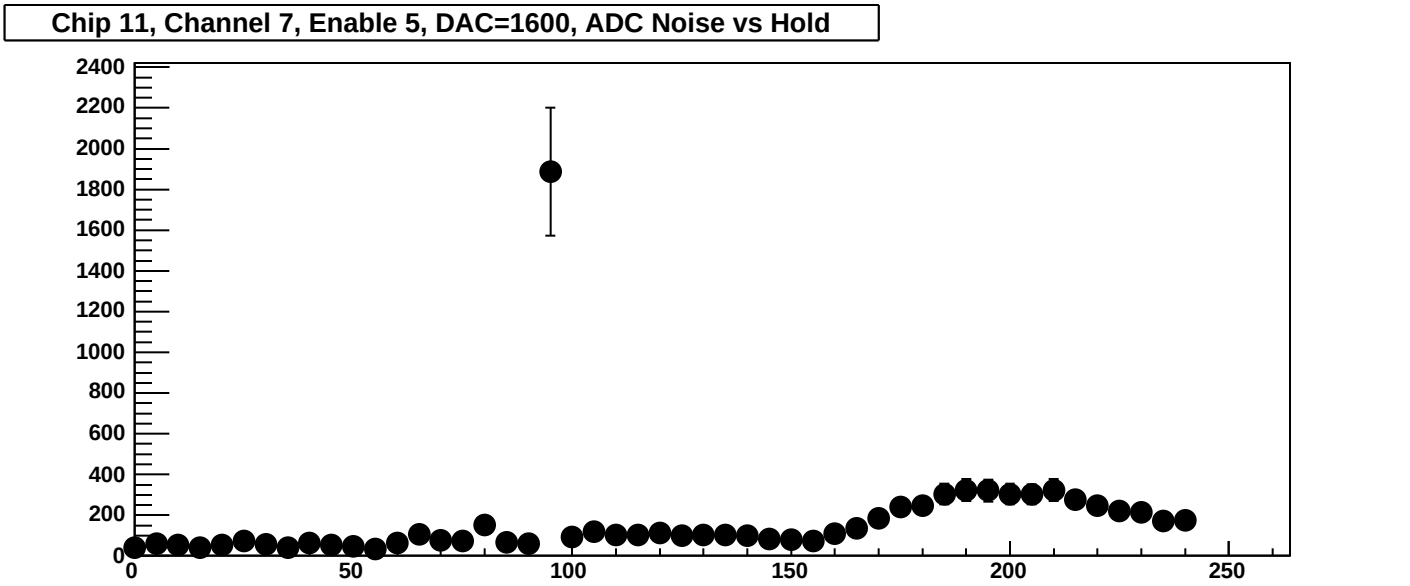
Chip 11, Channel 7, Enable 4, DAC=1600, ADC Noise vs Hold

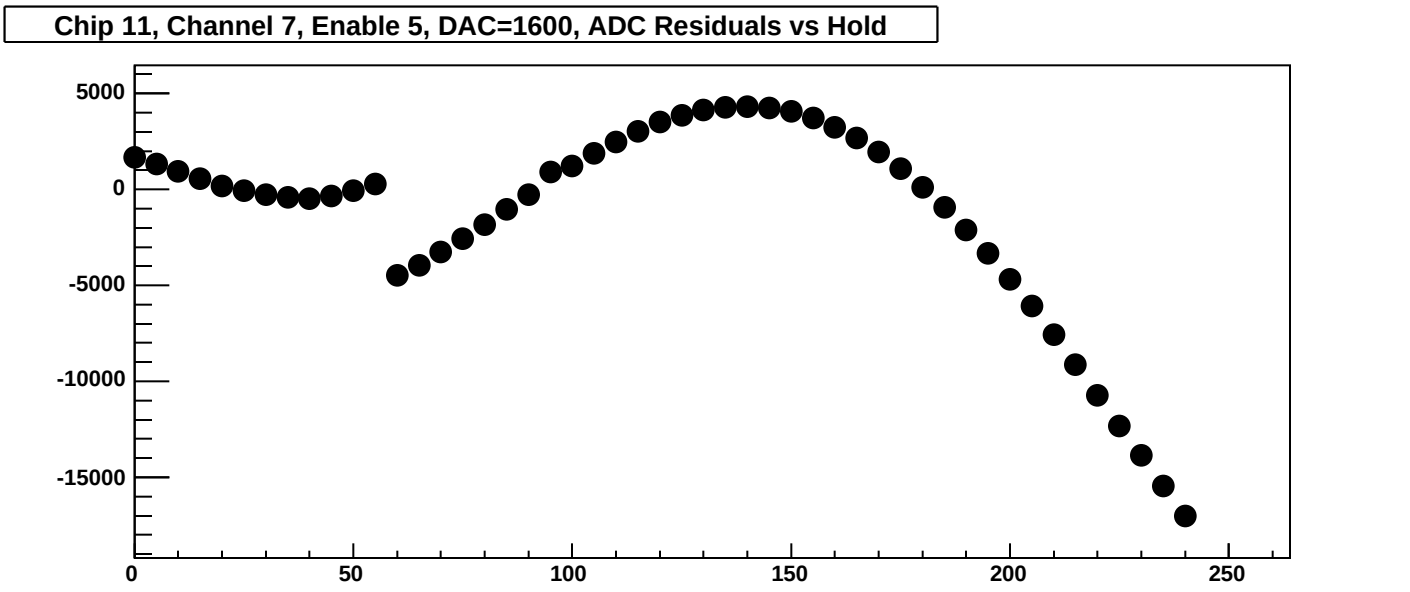


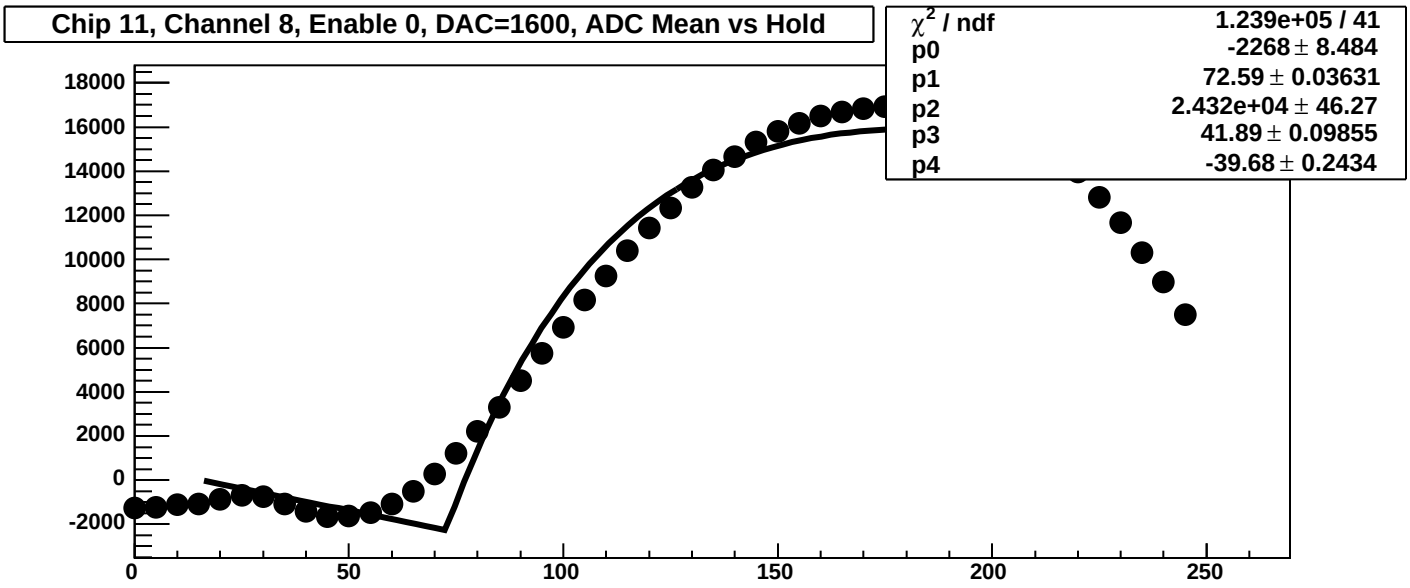
Chip 11, Channel 7, Enable 4, DAC=1600, ADC Residuals vs Hold

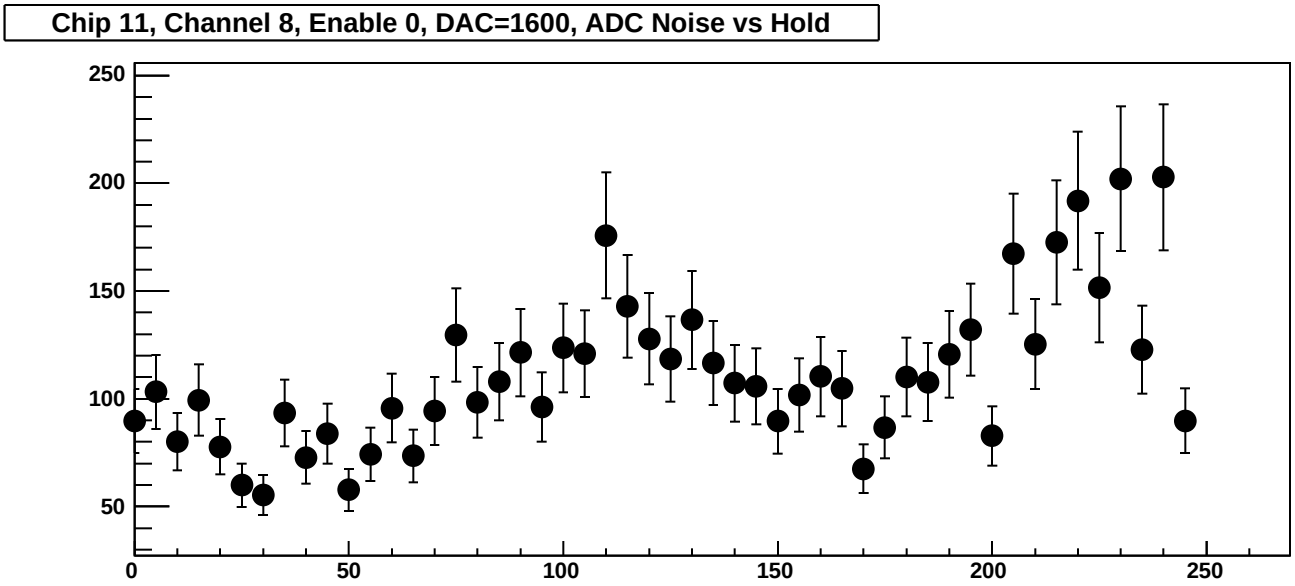


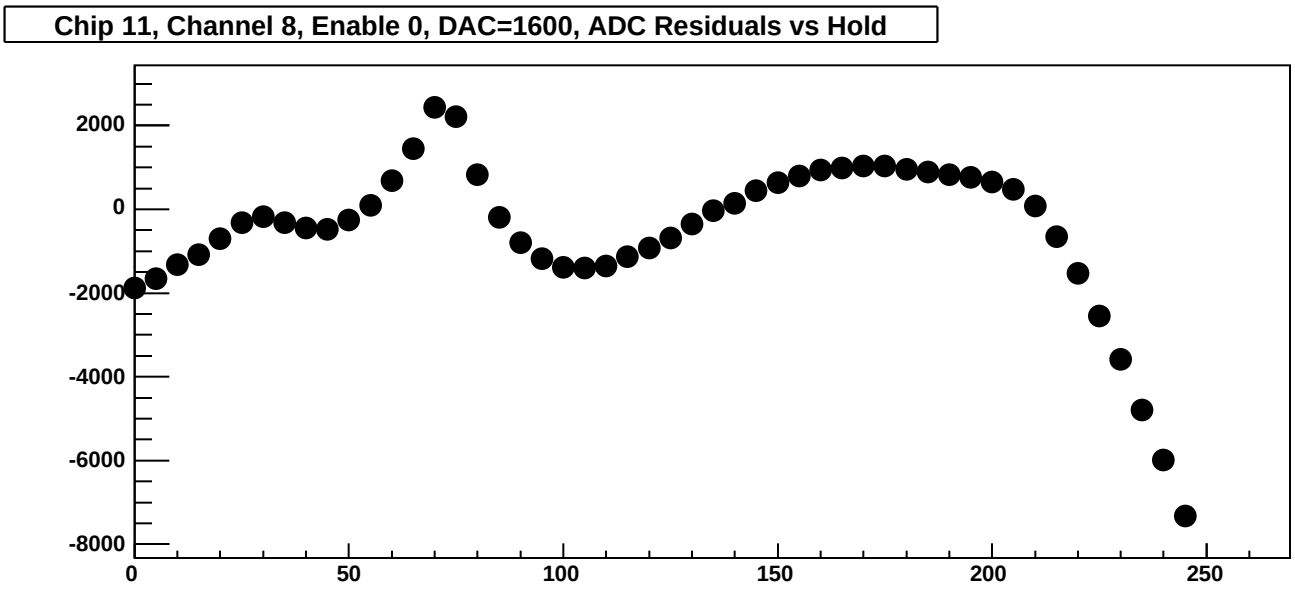

Chip 11, Channel 7, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.107e+06 / 41
p0 2444 ± 4.046
p1 55.94 ± 0.0003808
p2 5215 ± 8.816
p3 -179.9 ± 5.484e-05
p4 99.82 ± 0.09905
20000
15000
10000
5000
0
0
50
100
150
200
250

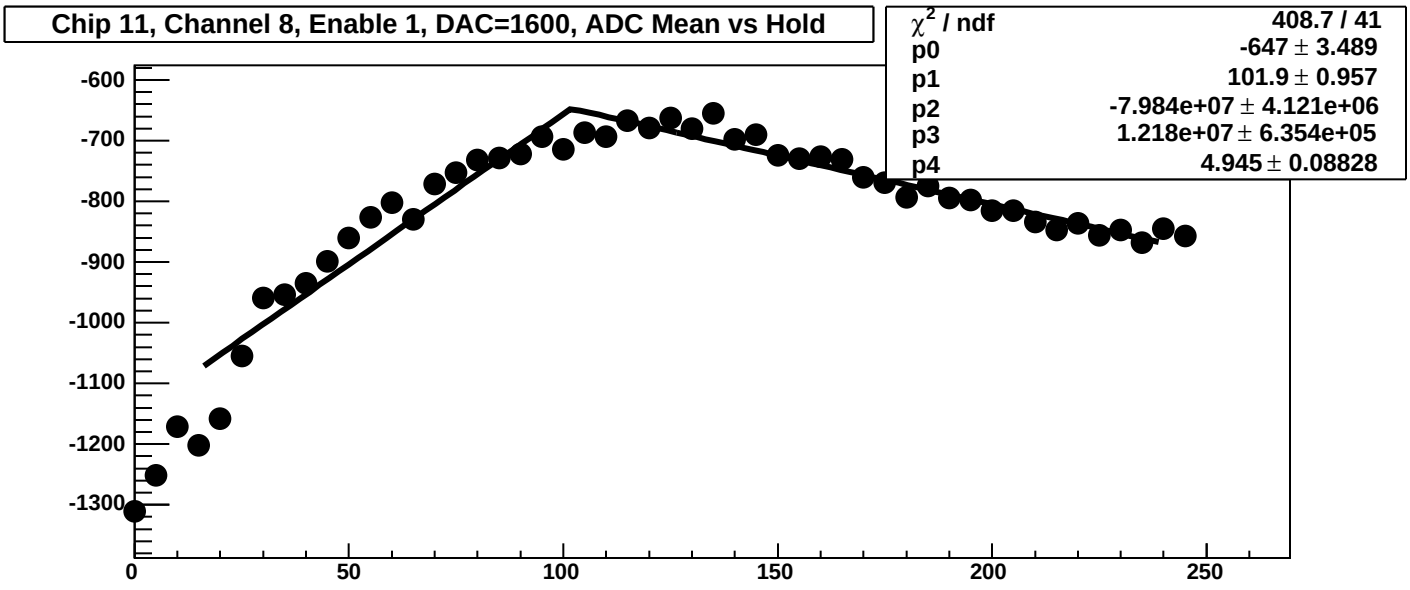

Chip 11, Channel 7, Enable 5, DAC=1600, ADC Noise vs Hold
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

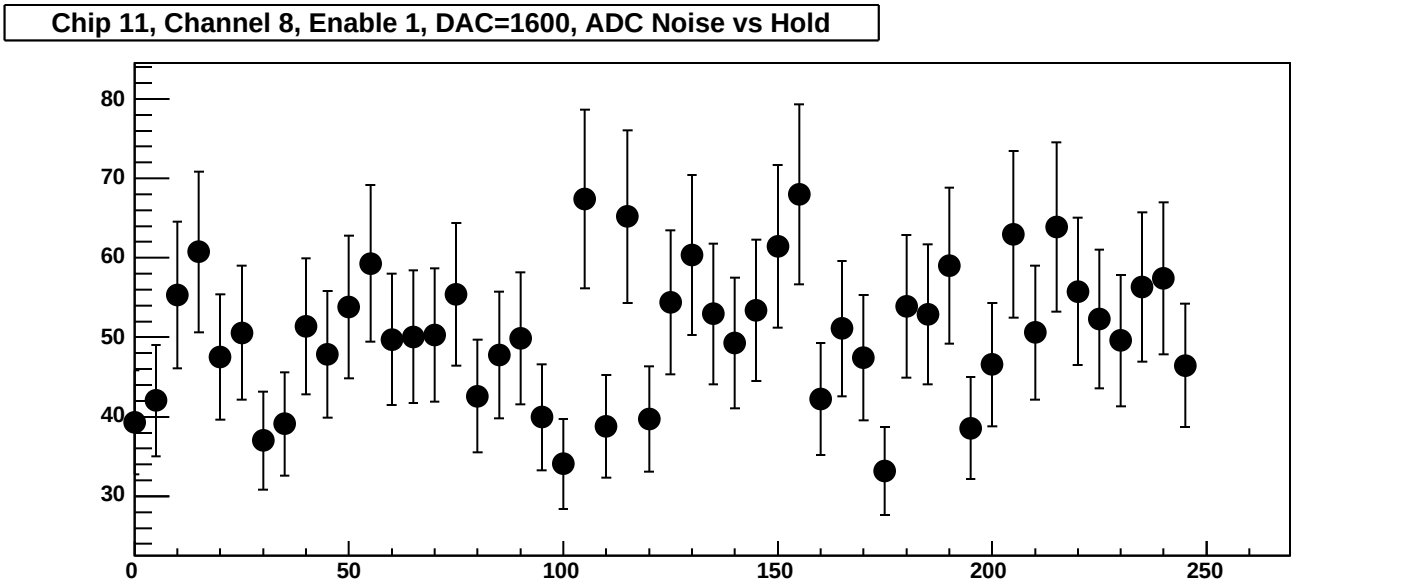

Chip 11, Channel 7, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
0
50
100
150
200
250

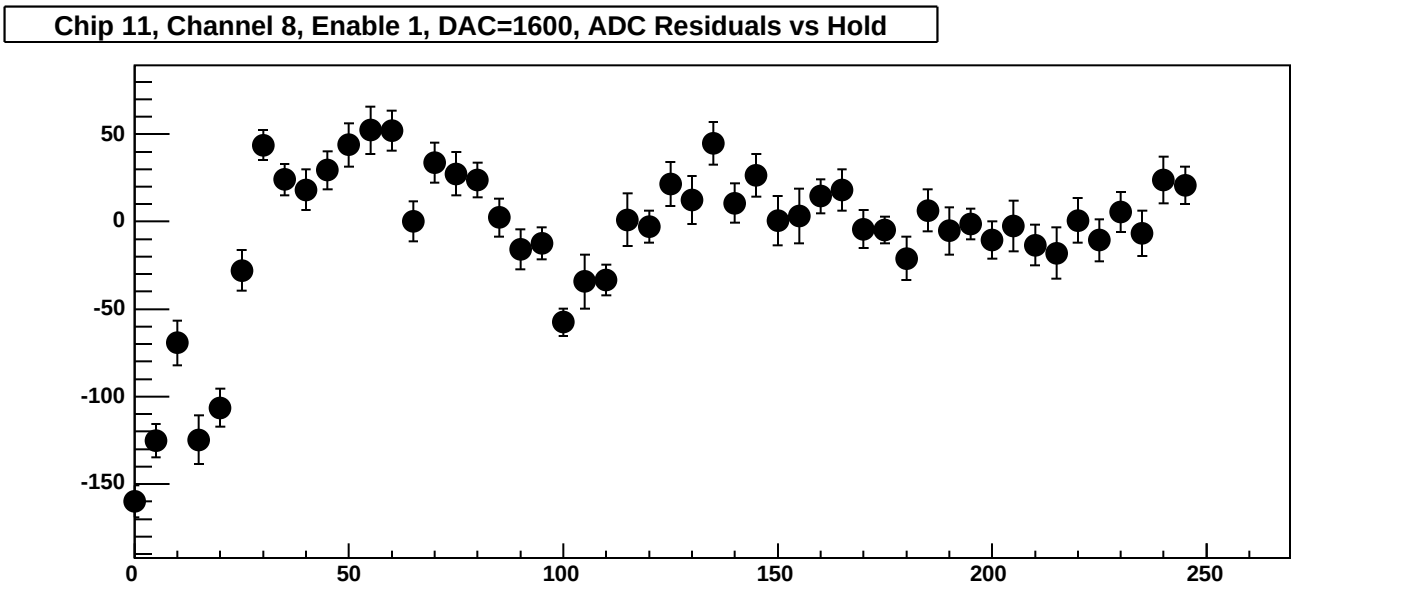

Chip 11, Channel 8, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.239e+05 / 41
p0 -2268 ± 8.484
p1 72.59 ± 0.03631
p2 2.432e+04 ± 46.27
p3 41.89 ± 0.09855
p4 -39.68 ± 0.2434
18000
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250


Chip 11, Channel 8, Enable 0, DAC=1600, ADC Noise vs Hold
250
200
150
100
50
0
50
100
150
200
250

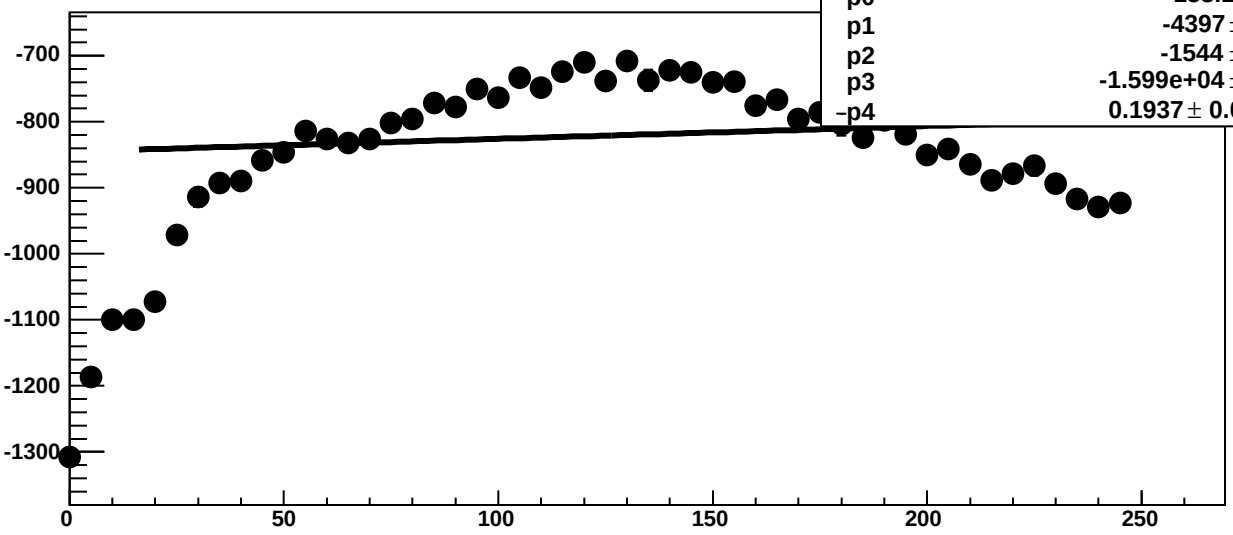

Chip 11, Channel 8, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250


Chip 11, Channel 8, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 408.7 / 41
p0 -647 ± 3.489
p1 101.9 ± 0.957
p2 -7.984e+07 ± 4.121e+06
p3 1.218e+07 ± 6.354e+05
p4 4.945 ± 0.08828
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
50
100
150
200
250

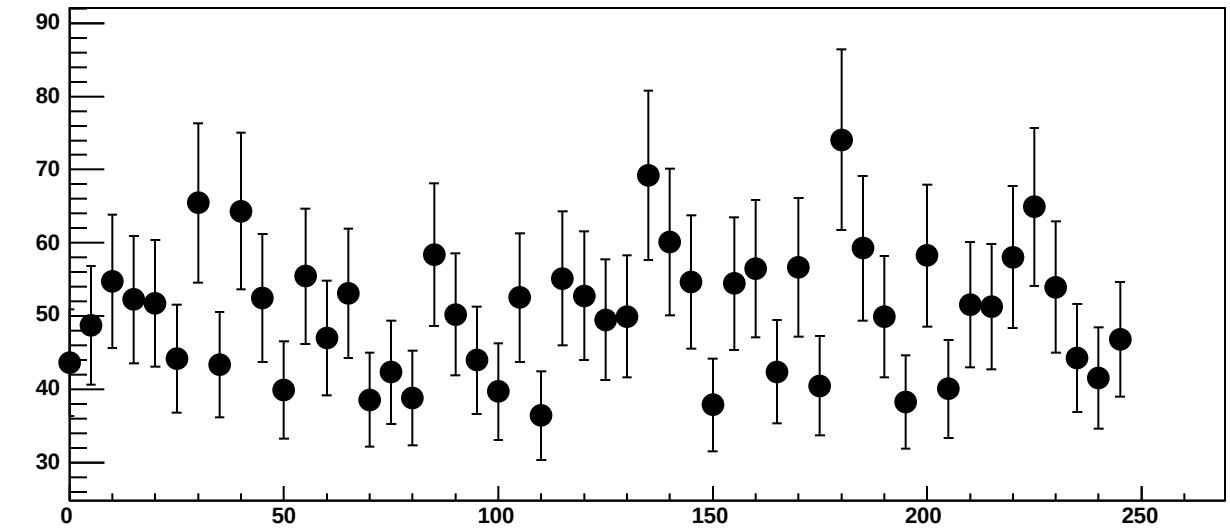

Chip 11, Channel 8, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 11, Channel 8, Enable 1, DAC=1600, ADC Residuals vs Hold
50
0
-50
-100
-150
0
50
100
150
200
250

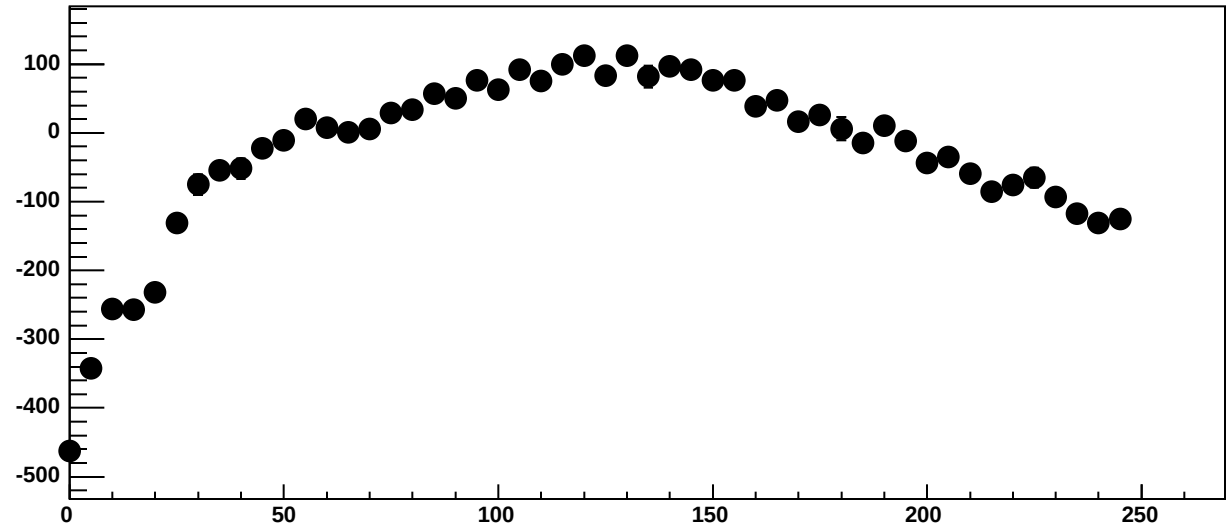
Chip 11, Channel 8, Enable 2, DAC=1600, ADC Mean vs Hold



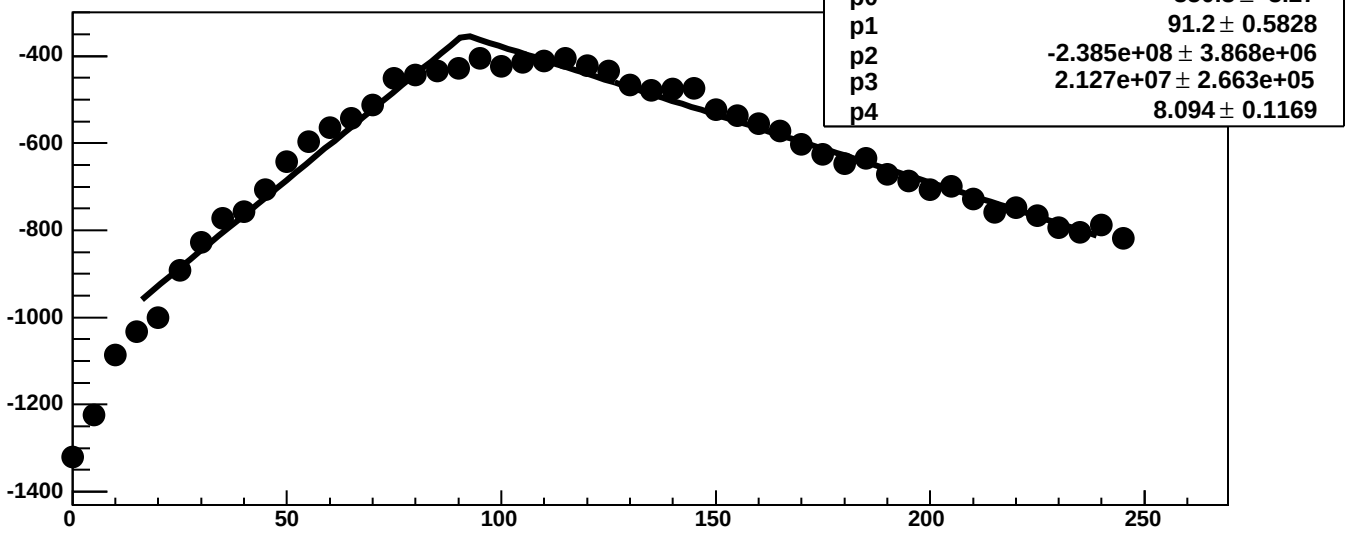
Chip 11, Channel 8, Enable 2, DAC=1600, ADC Noise vs Hold



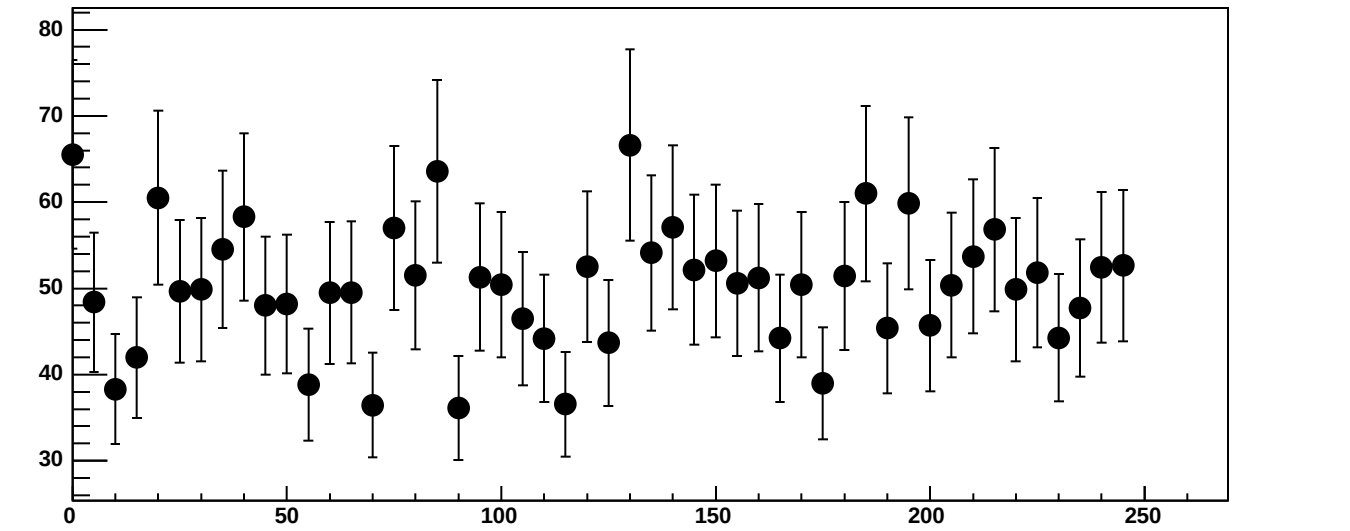
Chip 11, Channel 8, Enable 2, DAC=1600, ADC Residuals vs Hold



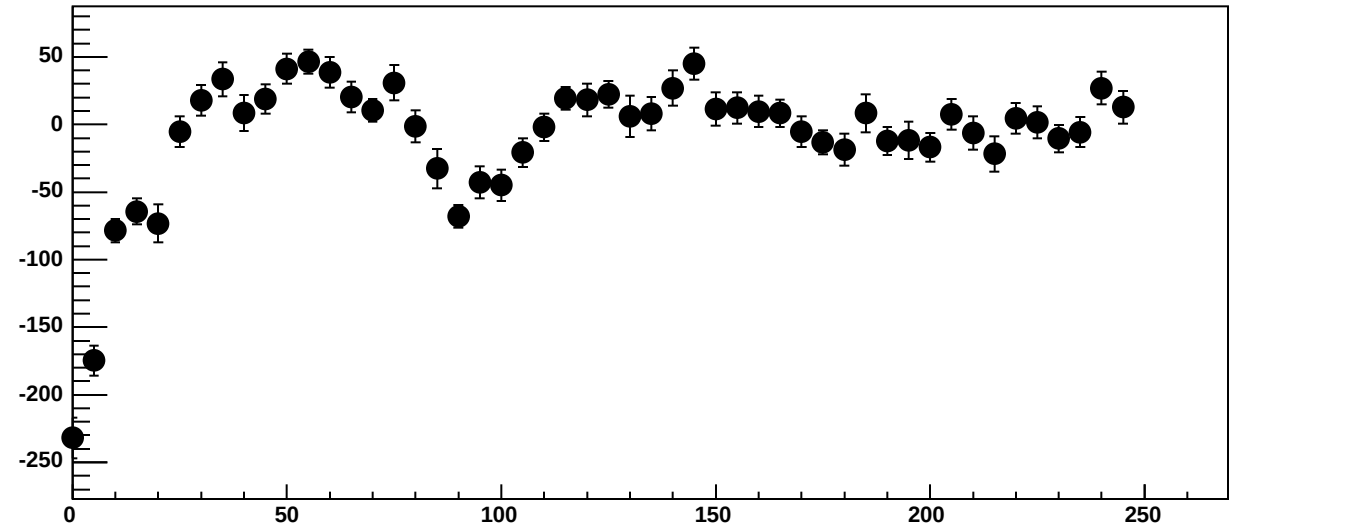
Chip 11, Channel 8, Enable 3, DAC=1600, ADC Mean vs Hold



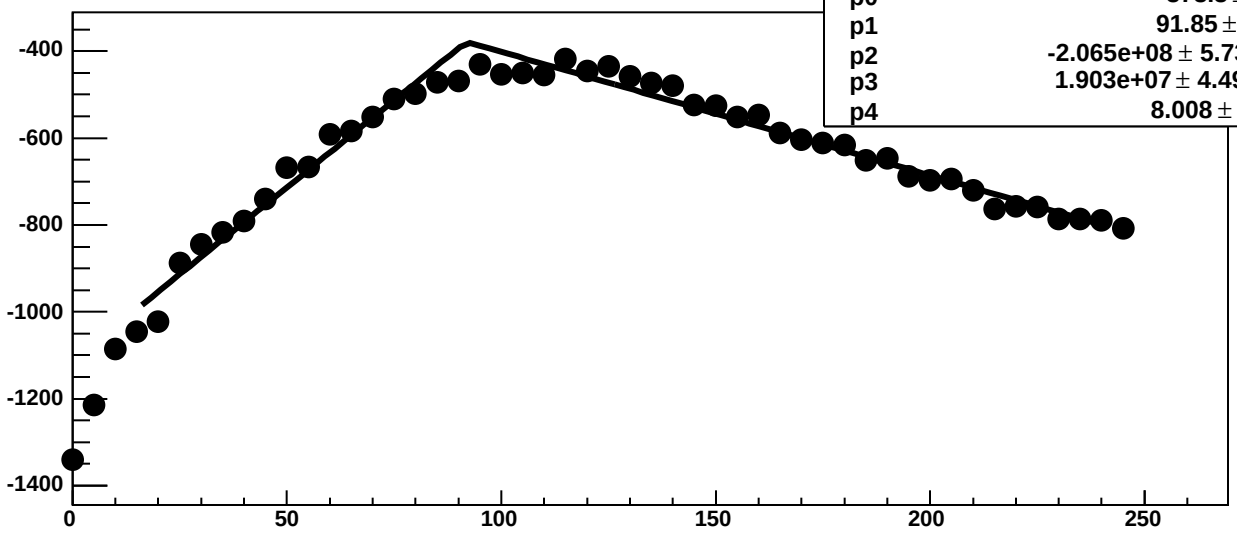
Chip 11, Channel 8, Enable 3, DAC=1600, ADC Noise vs Hold



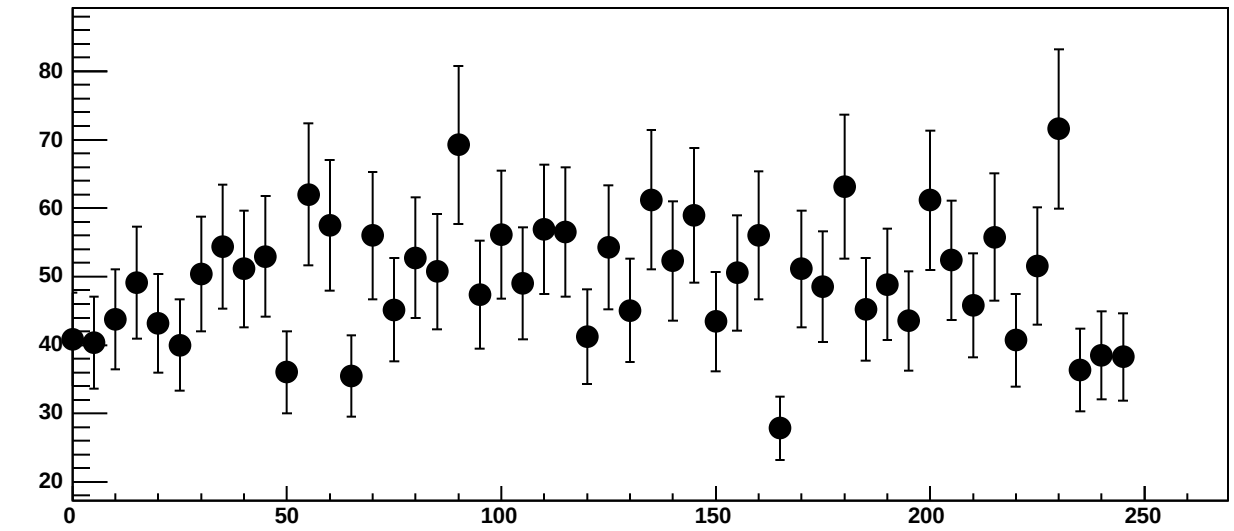
Chip 11, Channel 8, Enable 3, DAC=1600, ADC Residuals vs Hold



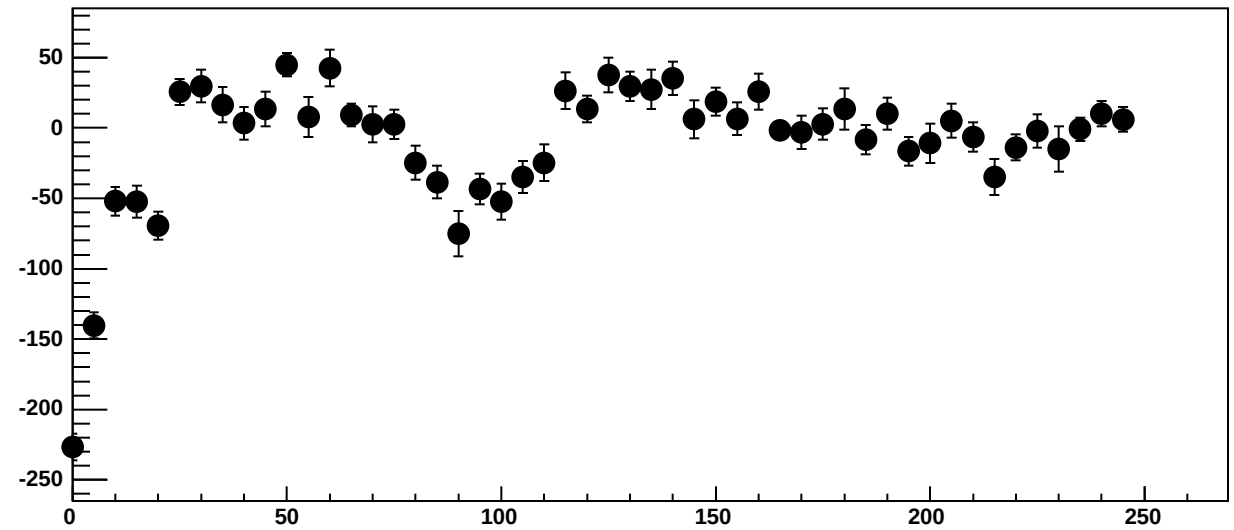
Chip 11, Channel 8, Enable 4, DAC=1600, ADC Mean vs Hold



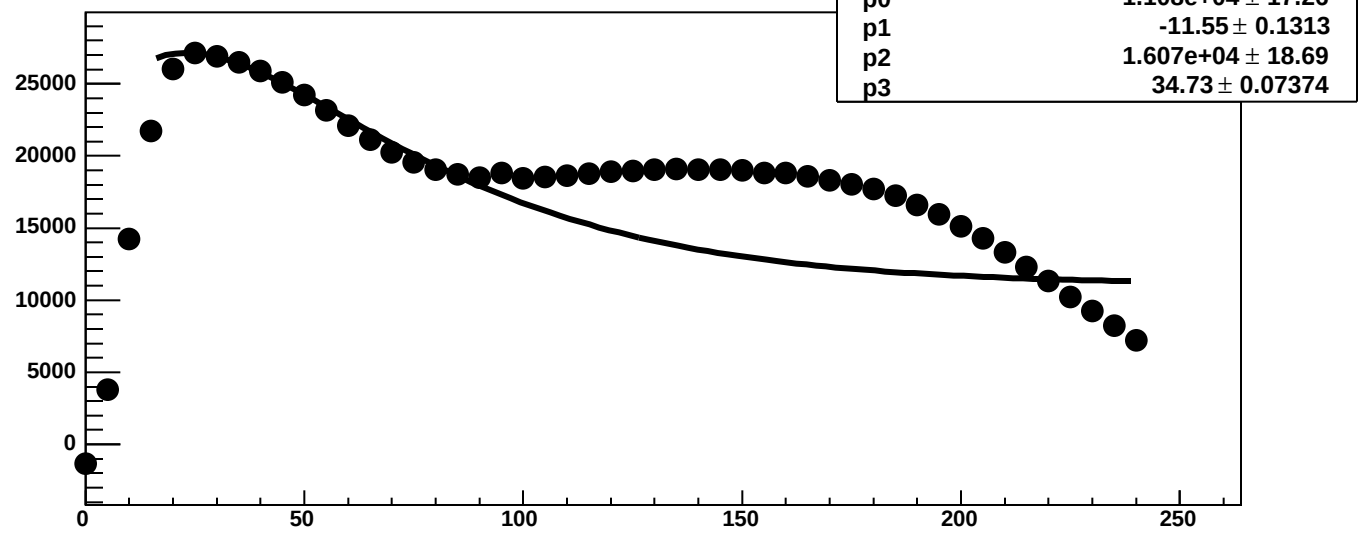
Chip 11, Channel 8, Enable 4, DAC=1600, ADC Noise vs Hold



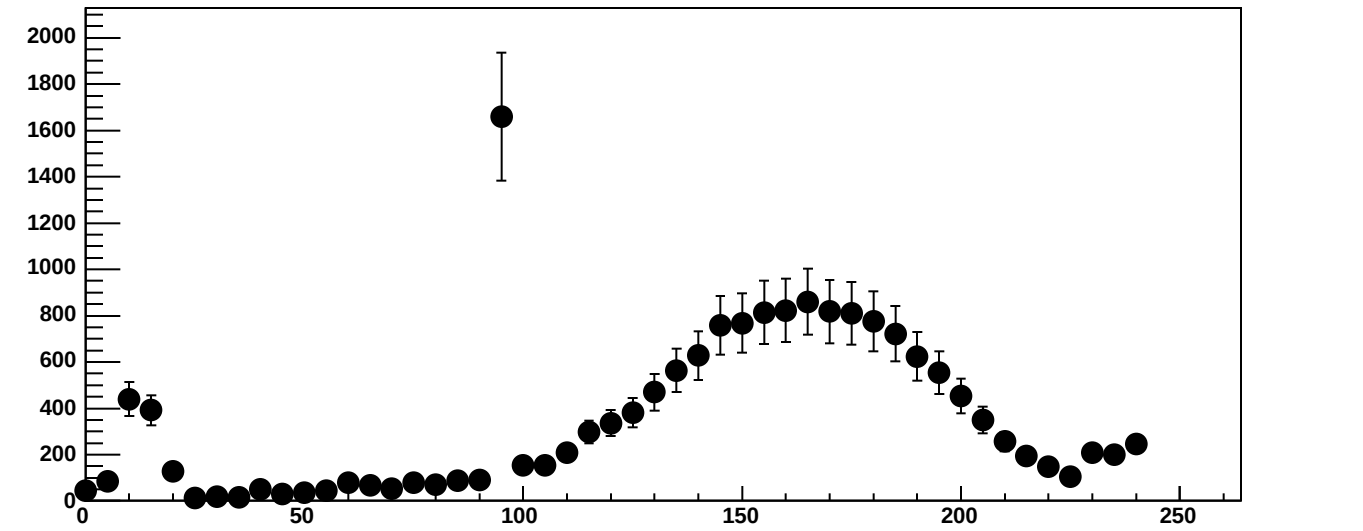
Chip 11, Channel 8, Enable 4, DAC=1600, ADC Residuals vs Hold



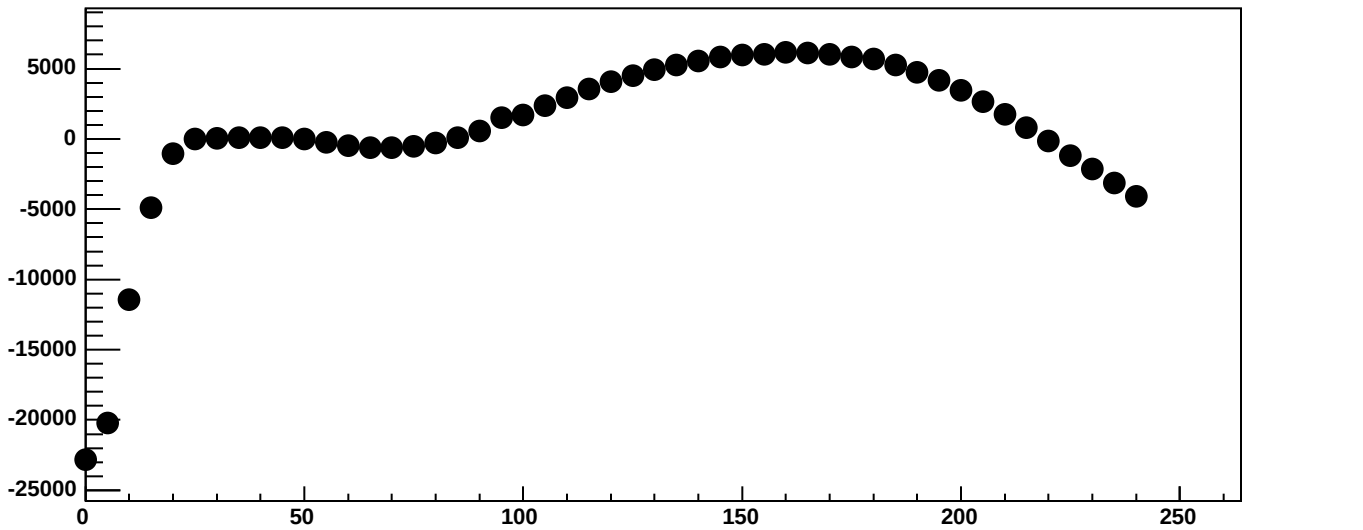
Chip 11, Channel 8, Enable 5!, DAC=1600, ADC Mean vs Hold



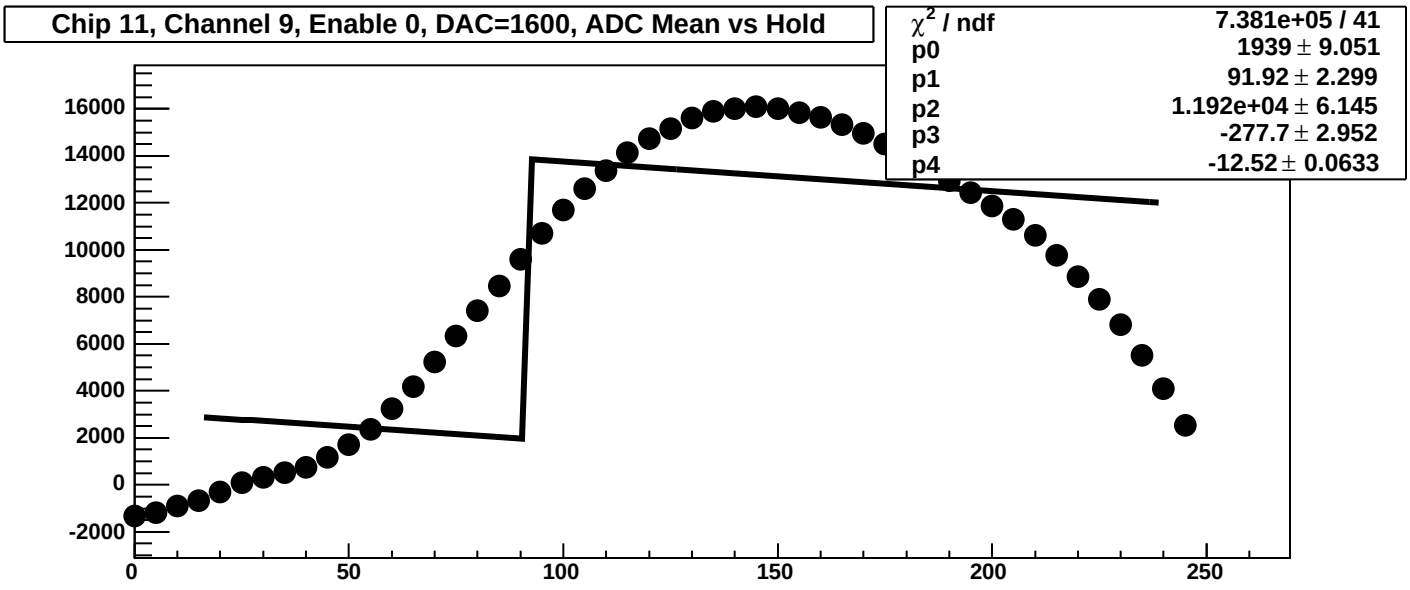
Chip 11, Channel 8, Enable 5!, DAC=1600, ADC Noise vs Hold



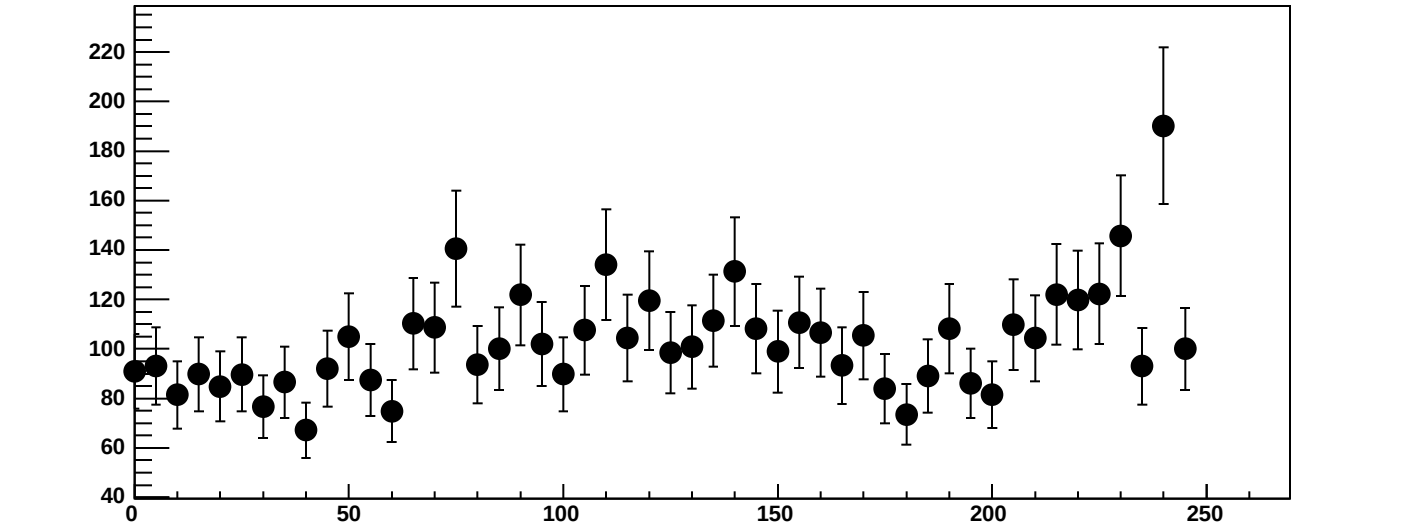
Chip 11, Channel 8, Enable 5!, DAC=1600, ADC Residuals vs Hold



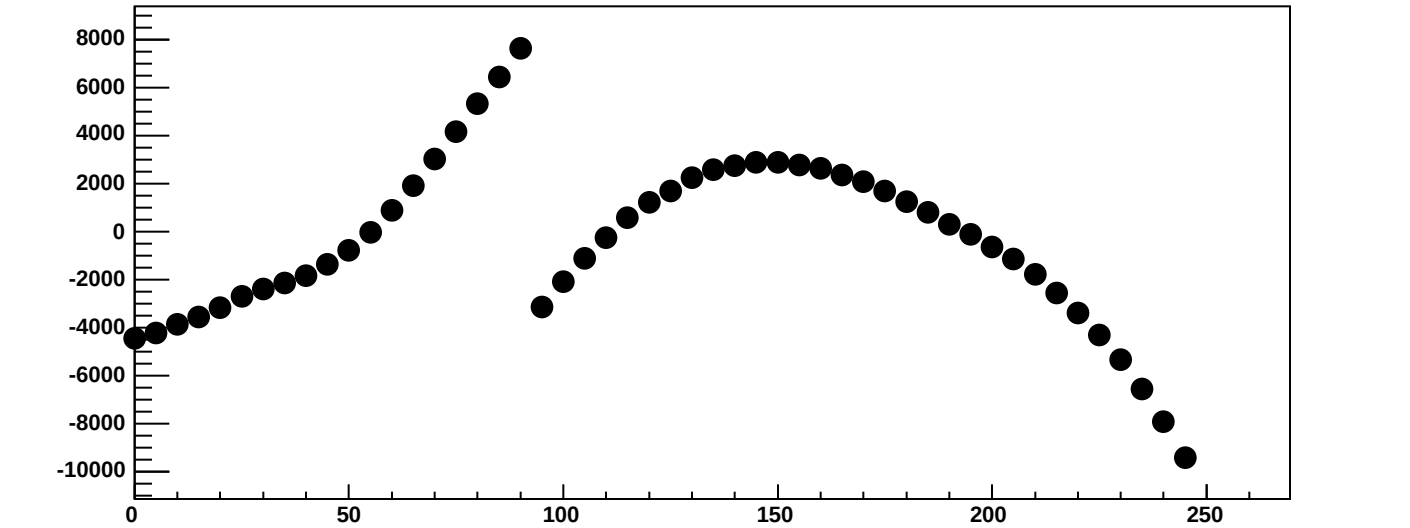
Chip 11, Channel 9, Enable 0, DAC=1600, ADC Mean vs Hold



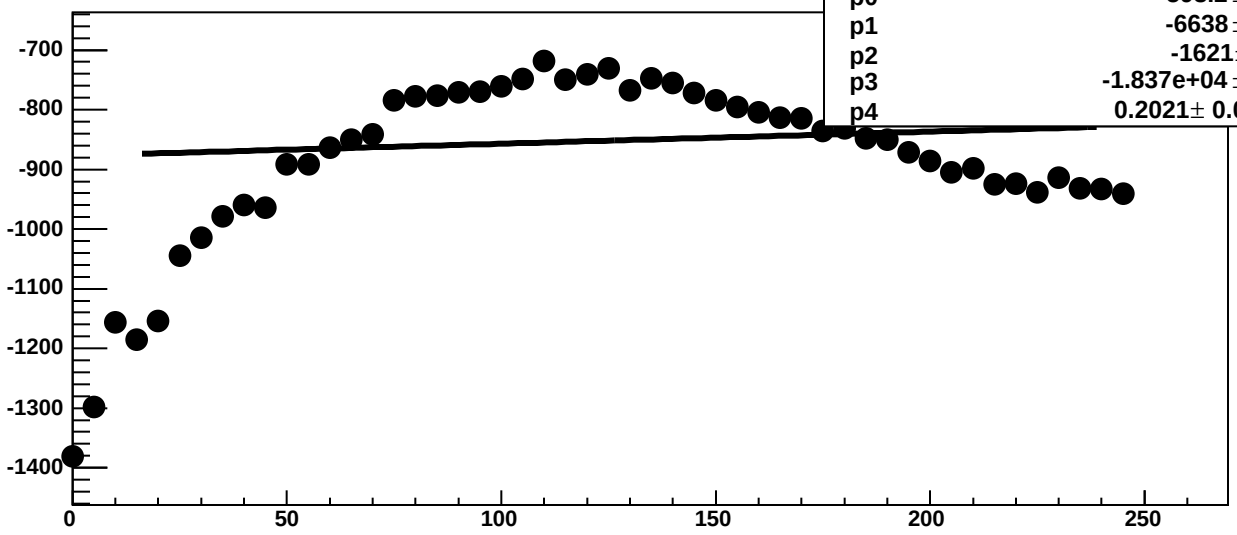
Chip 11, Channel 9, Enable 0, DAC=1600, ADC Noise vs Hold



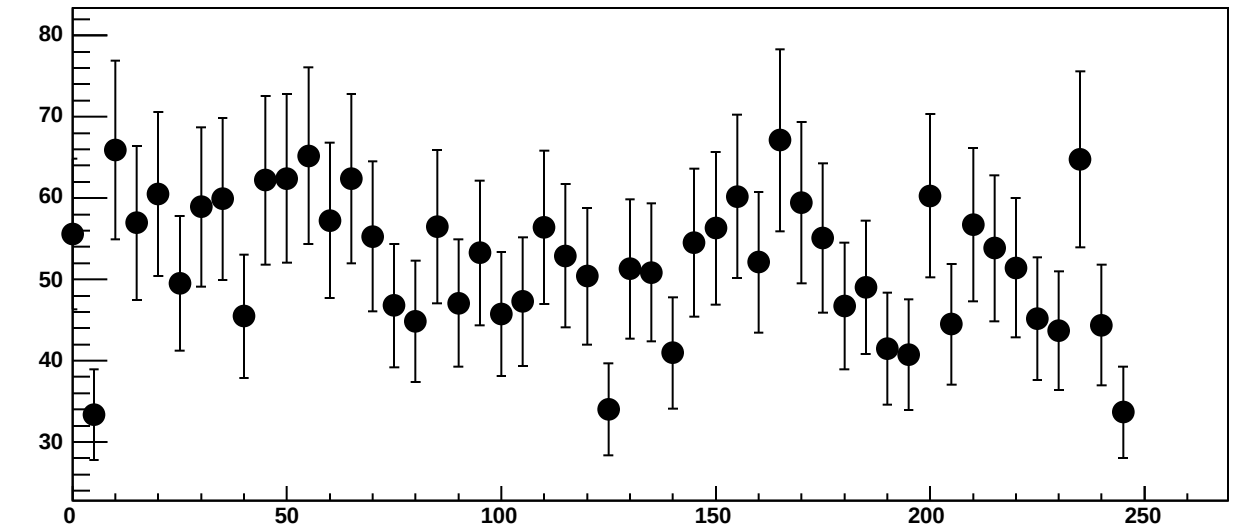
Chip 11, Channel 9, Enable 0, DAC=1600, ADC Residuals vs Hold



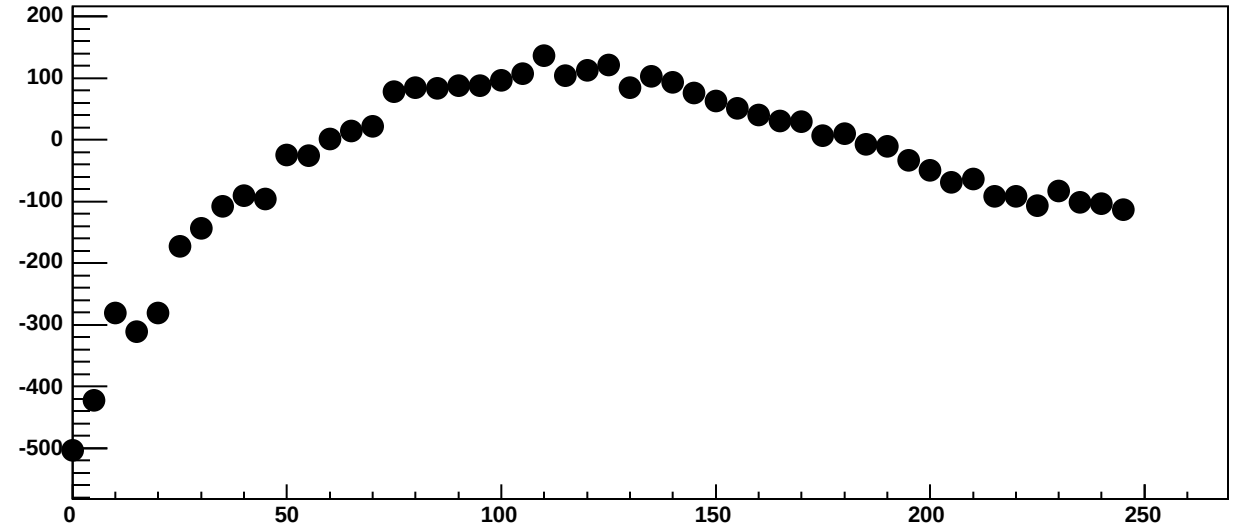
Chip 11, Channel 9, Enable 1, DAC=1600, ADC Mean vs Hold



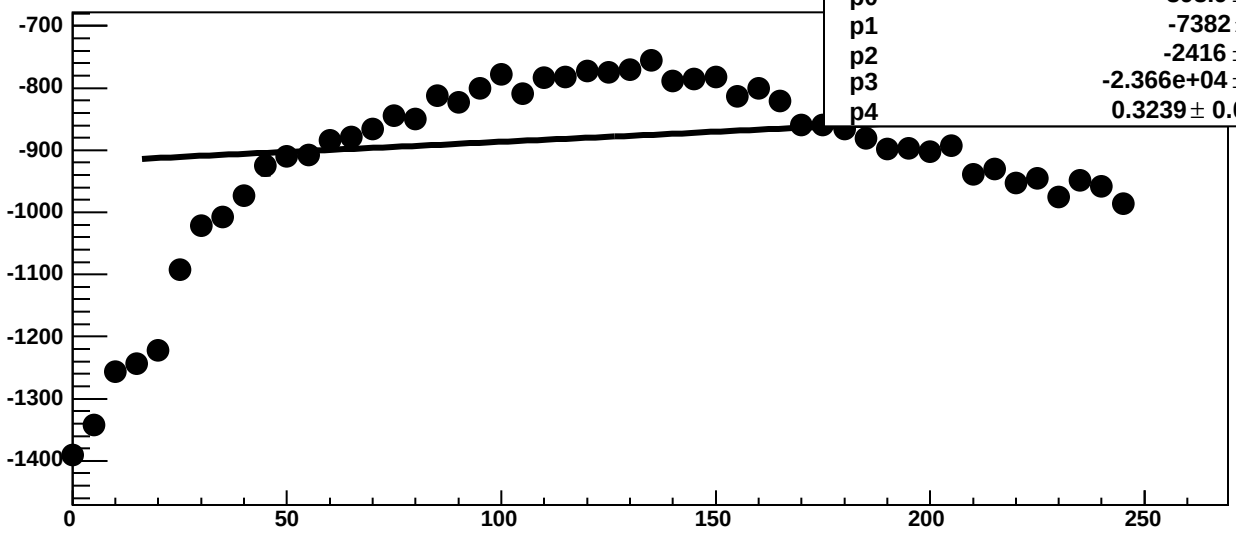
Chip 11, Channel 9, Enable 1, DAC=1600, ADC Noise vs Hold



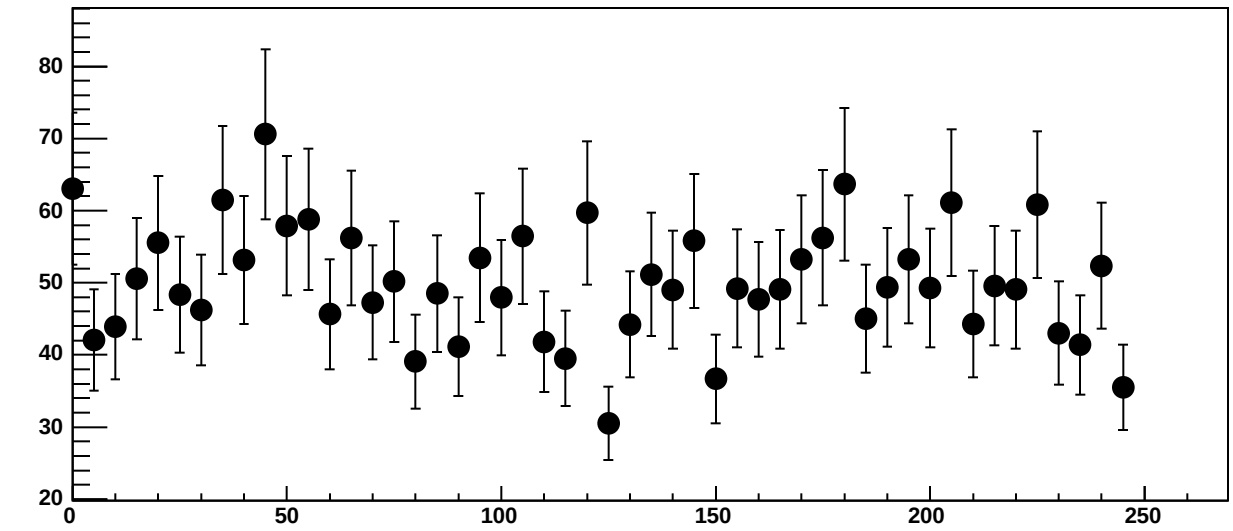
Chip 11, Channel 9, Enable 1, DAC=1600, ADC Residuals vs Hold



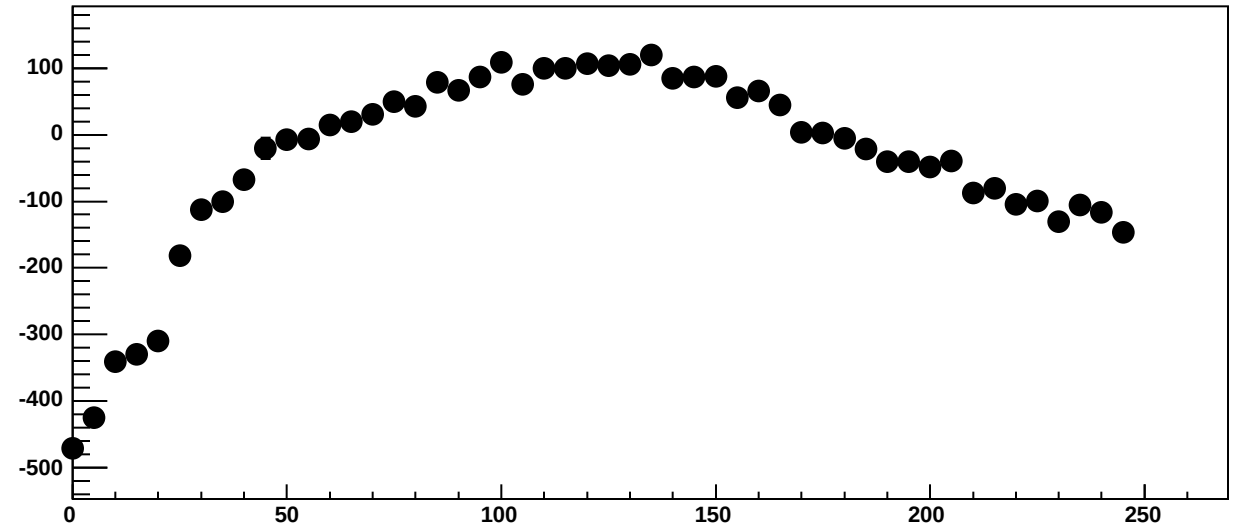
Chip 11, Channel 9, Enable 2, DAC=1600, ADC Mean vs Hold

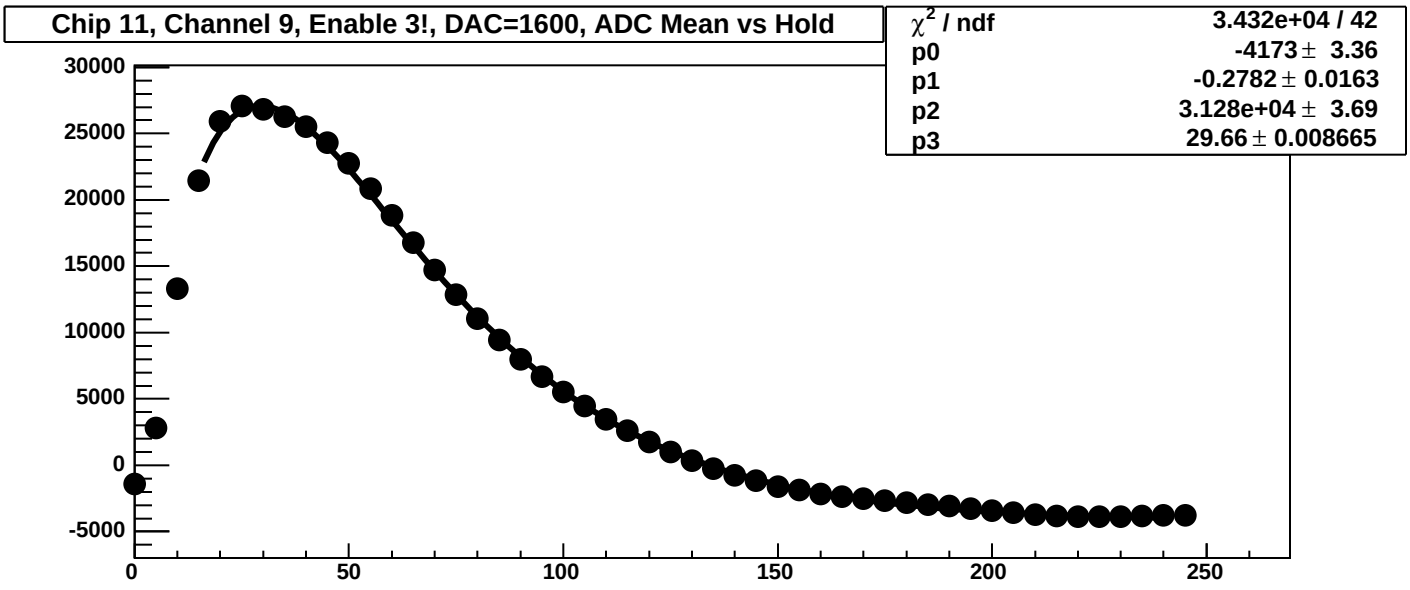


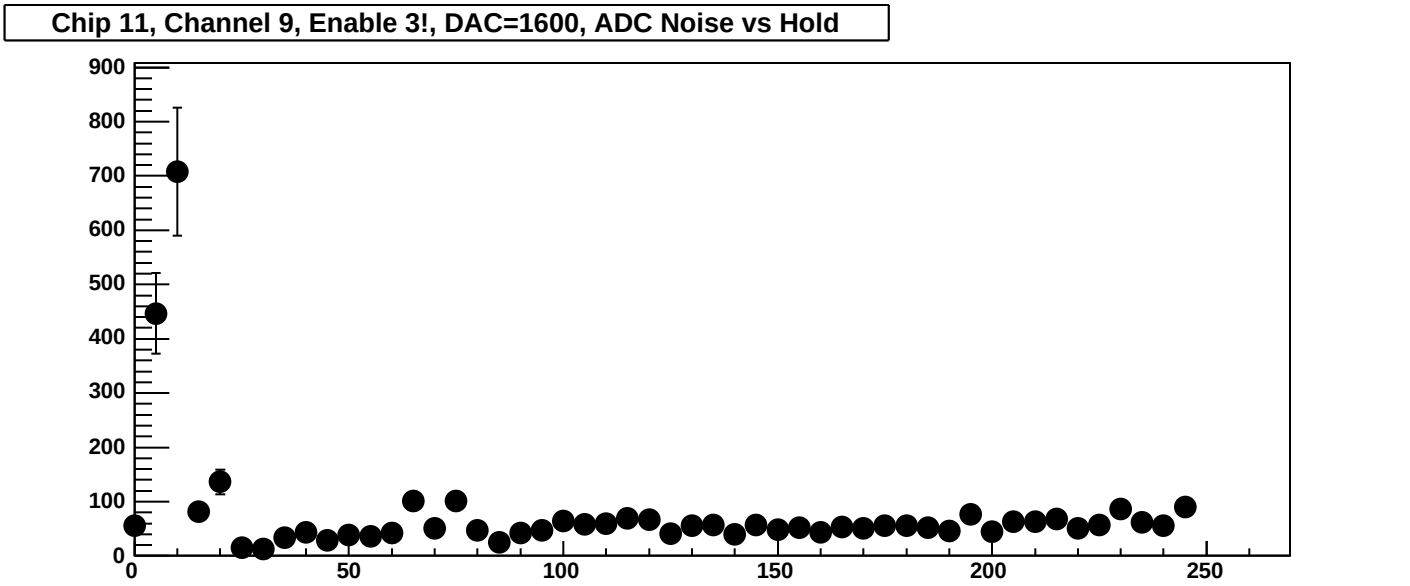
Chip 11, Channel 9, Enable 2, DAC=1600, ADC Noise vs Hold

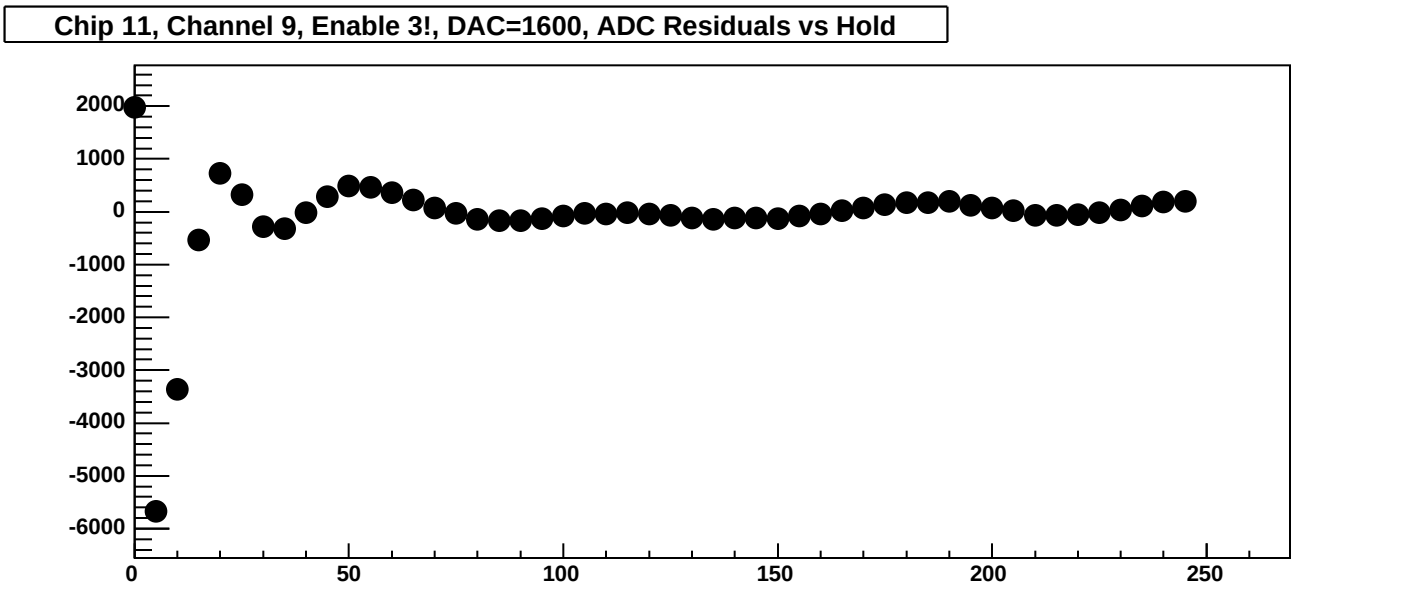


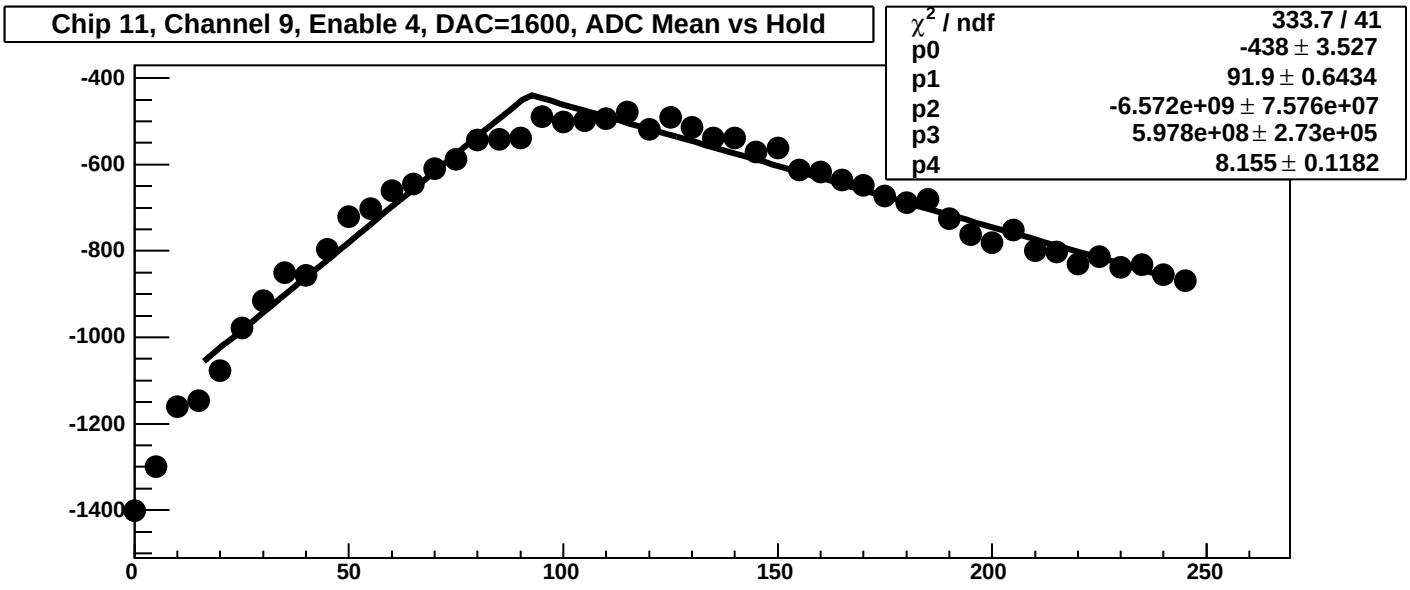
Chip 11, Channel 9, Enable 2, DAC=1600, ADC Residuals vs Hold

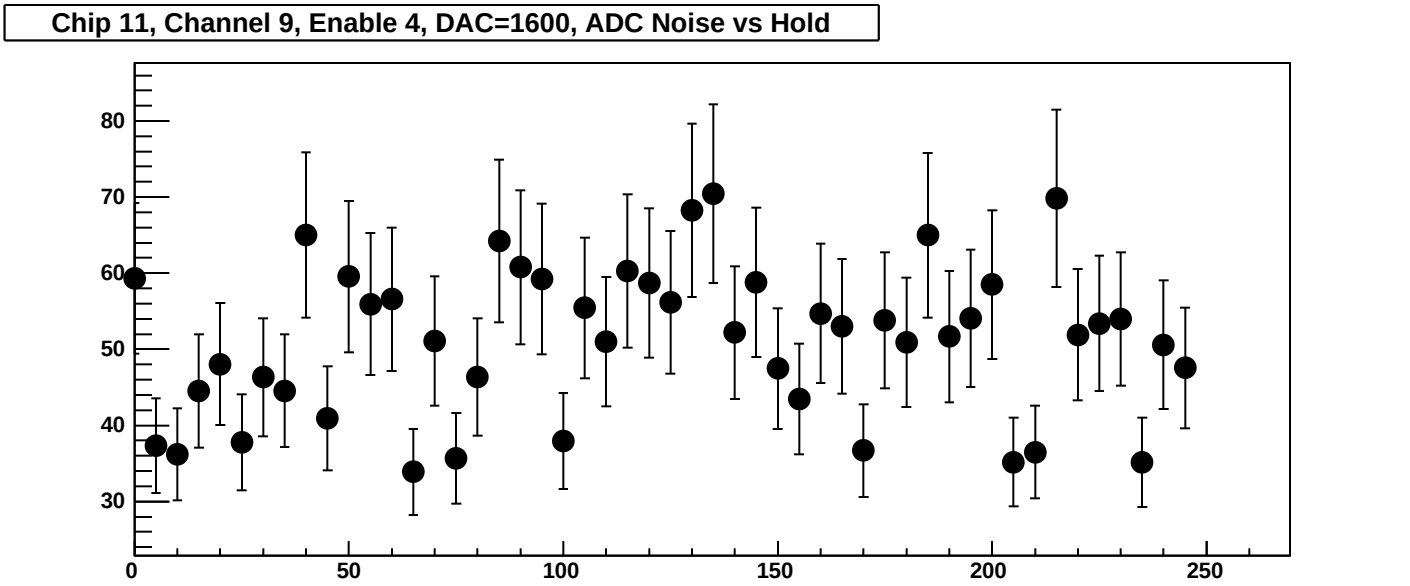


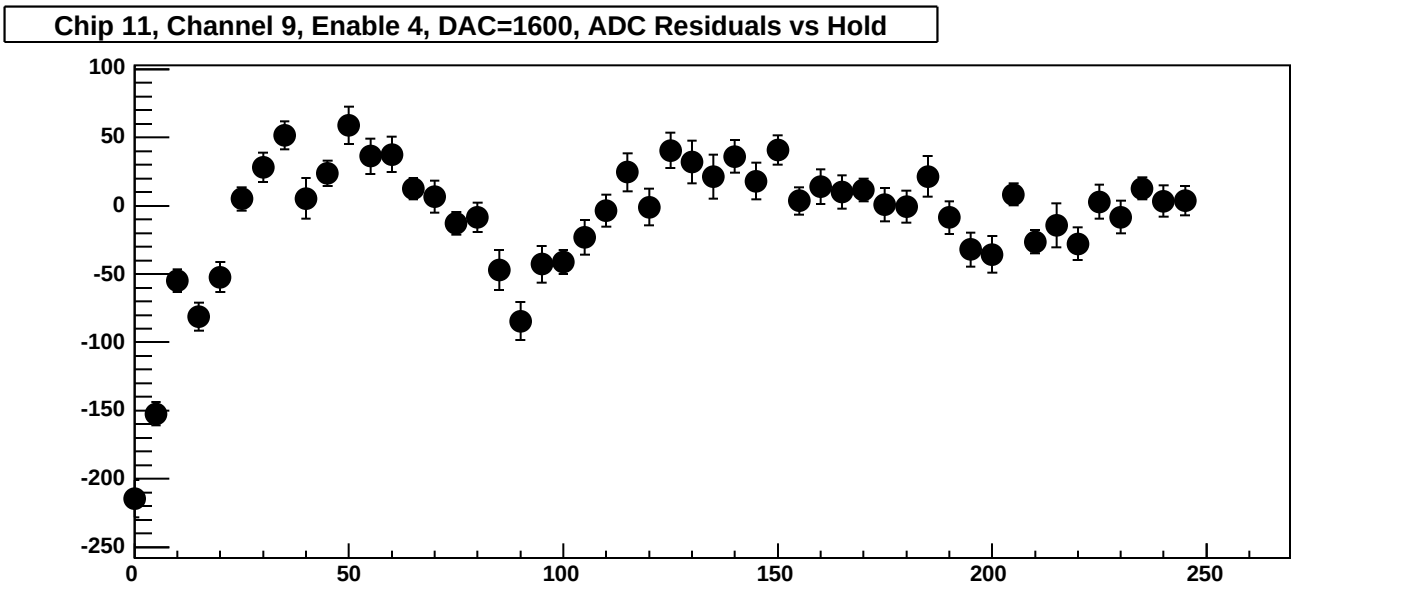

Chip 11, Channel 9, Enable 3!, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.432e+04 / 42
p0 -4173 ± 3.36
p1 -0.2782 ± 0.0163
p2 3.128e+04 ± 3.69
p3 29.66 ± 0.008665
30000
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

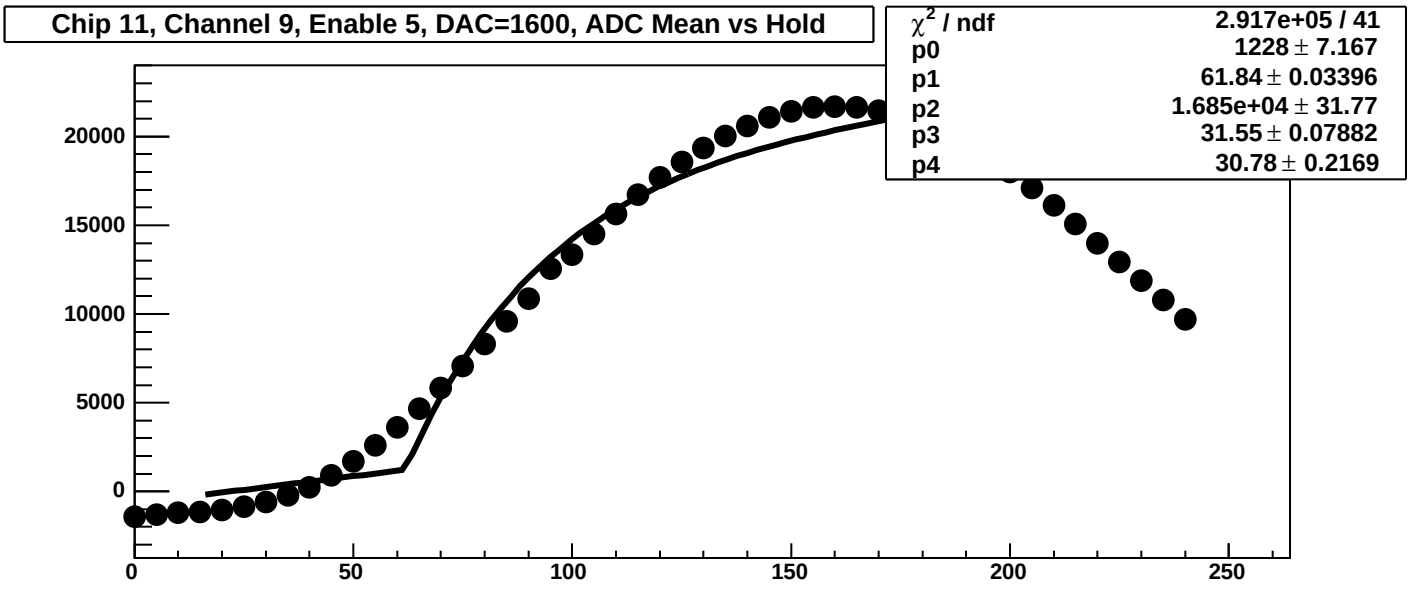

Chip 11, Channel 9, Enable 3!, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250

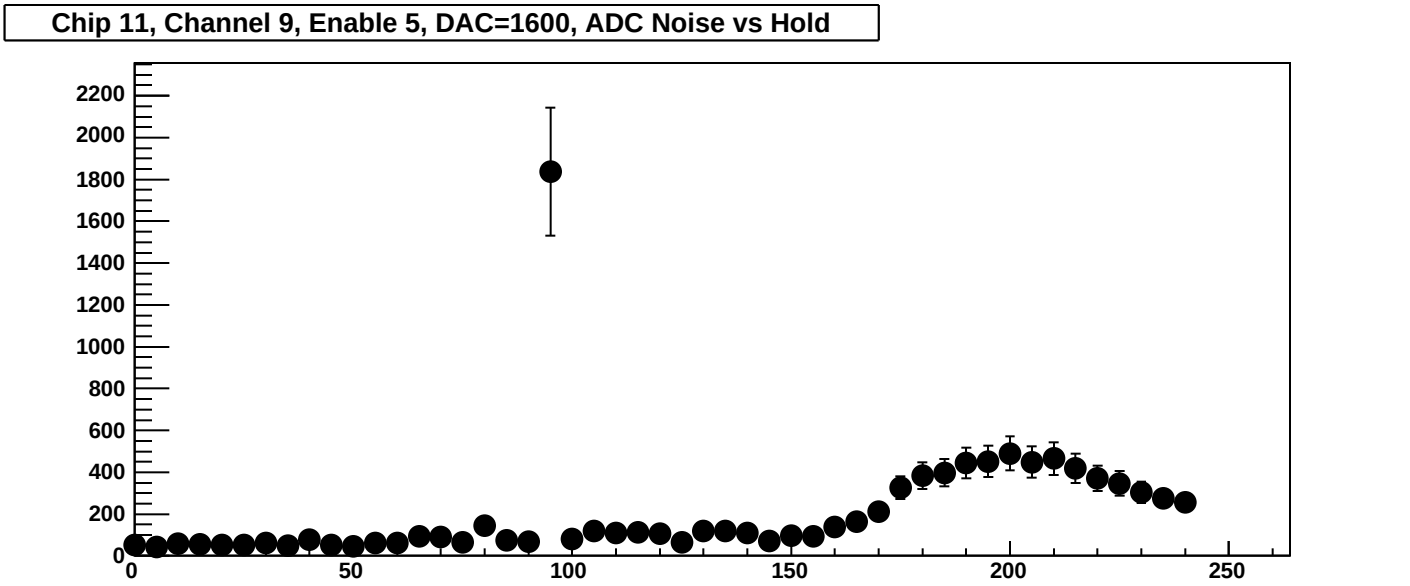

Chip 11, Channel 9, Enable 3!, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
50
100
150
200
250

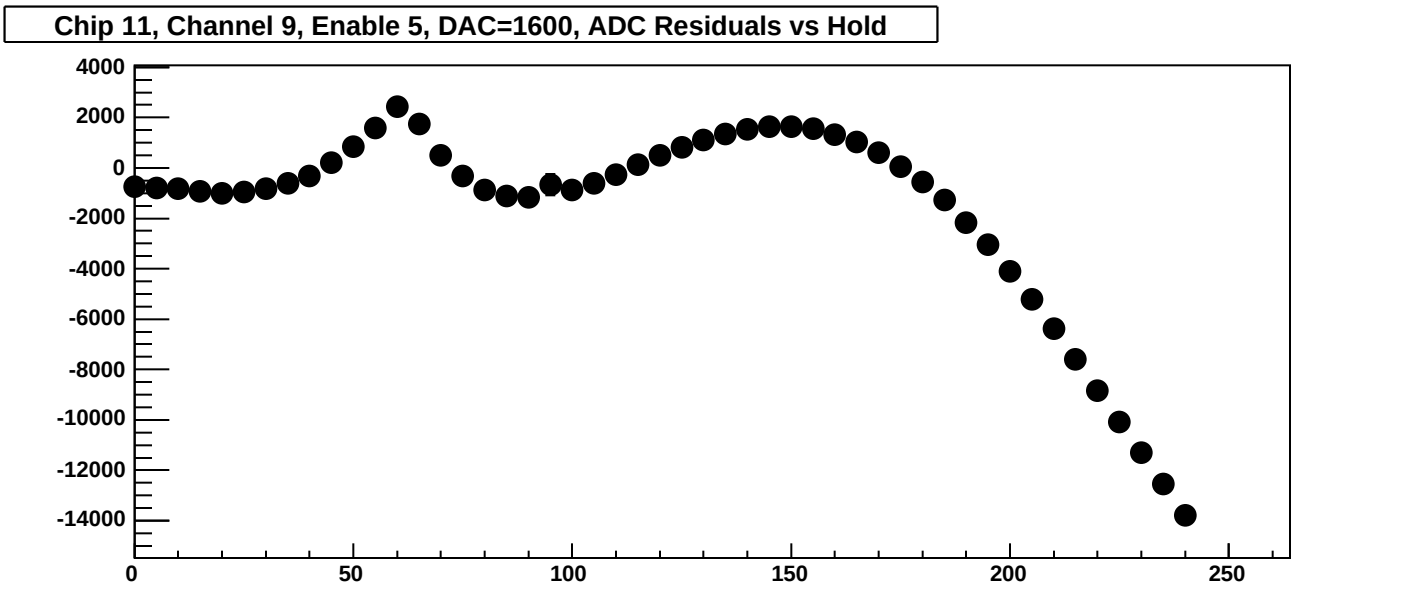

Chip 11, Channel 9, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 333.7 / 41
p0 -438 ± 3.527
p1 91.9 ± 0.6434
p2 -6.572e+09 ± 7.576e+07
p3 5.978e+08 ± 2.73e+05
p4 8.155 ± 0.1182
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250


Chip 11, Channel 9, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

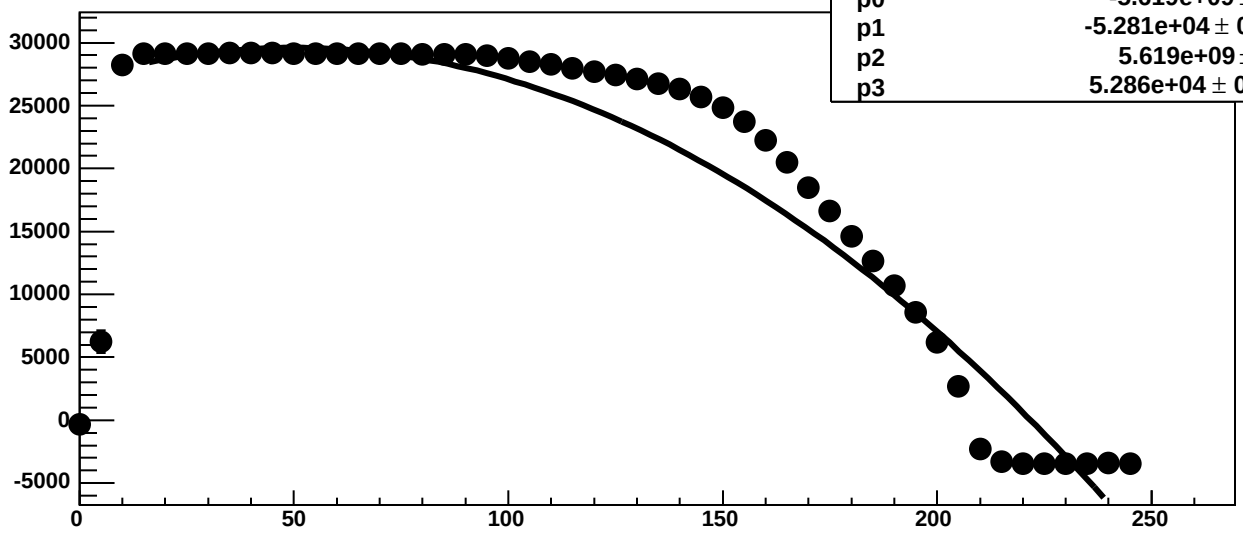

Chip 11, Channel 9, Enable 4, DAC=1600, ADC Residuals vs Hold
100
50
0
-50
-100
-150
-200
-250
0
50
100
150
200
250


Chip 11, Channel 9, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 2.917e+05 / 41
p0 1228 ± 7.167
p1 61.84 ± 0.03396
p2 1.685e+04 ± 31.77
p3 31.55 ± 0.07882
p4 30.78 ± 0.2169
20000
15000
10000
5000
0
0
50
100
150
200
250

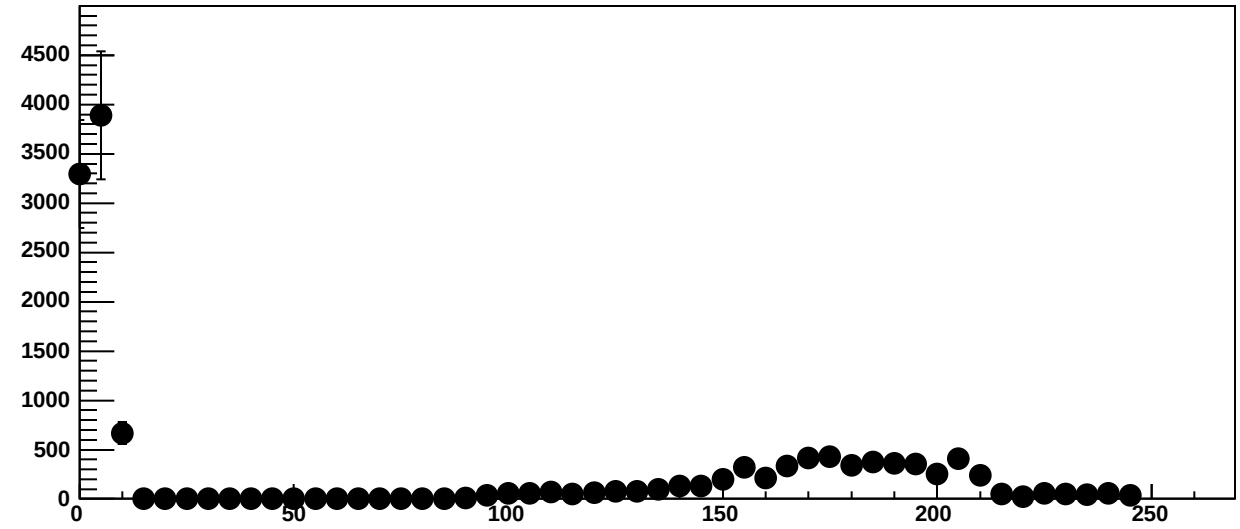

Chip 11, Channel 9, Enable 5, DAC=1600, ADC Noise vs Hold
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 11, Channel 9, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
50
100
150
200
250

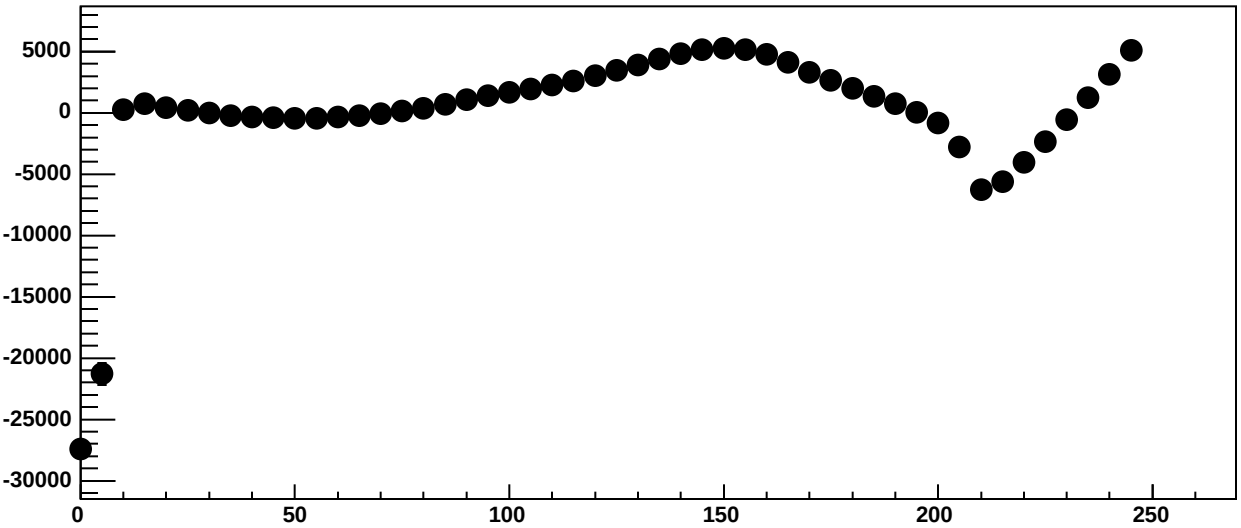
Chip 11, Channel 10, Enable 0!, DAC=1600, ADC Mean vs Hold

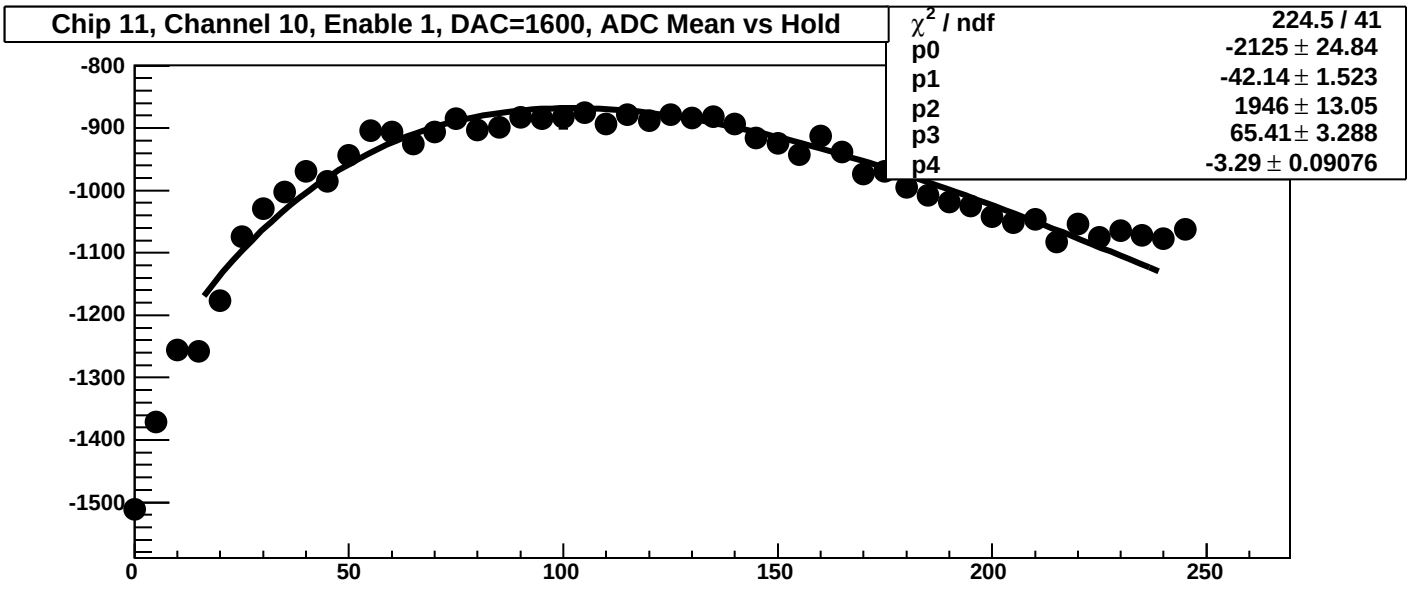


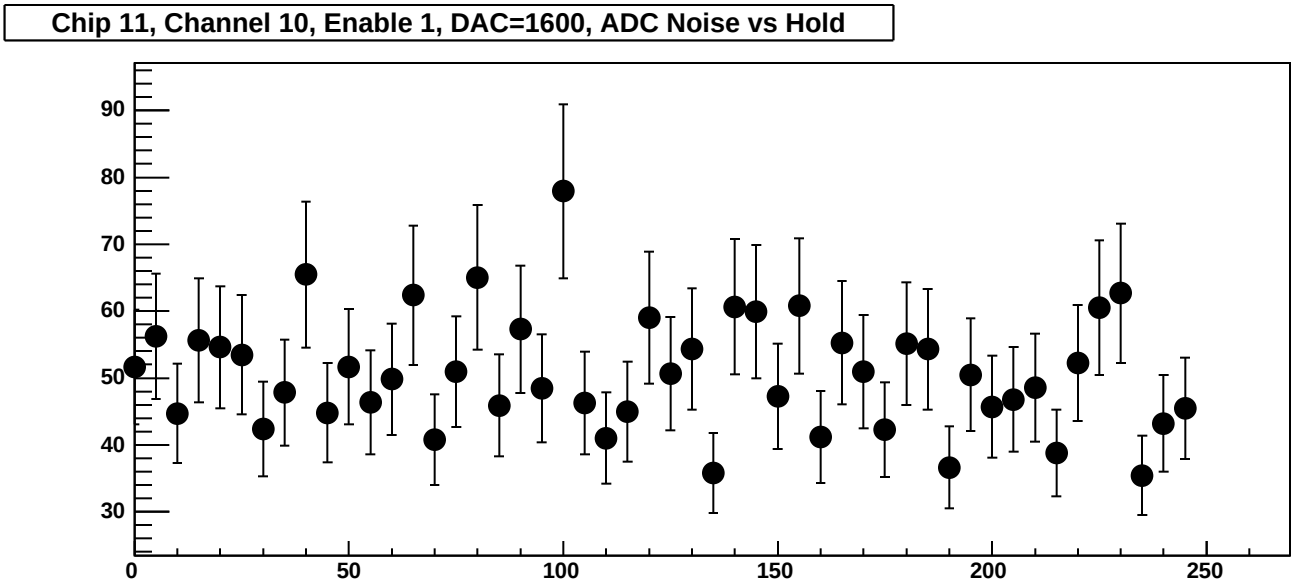
Chip 11, Channel 10, Enable 0!, DAC=1600, ADC Noise vs Hold

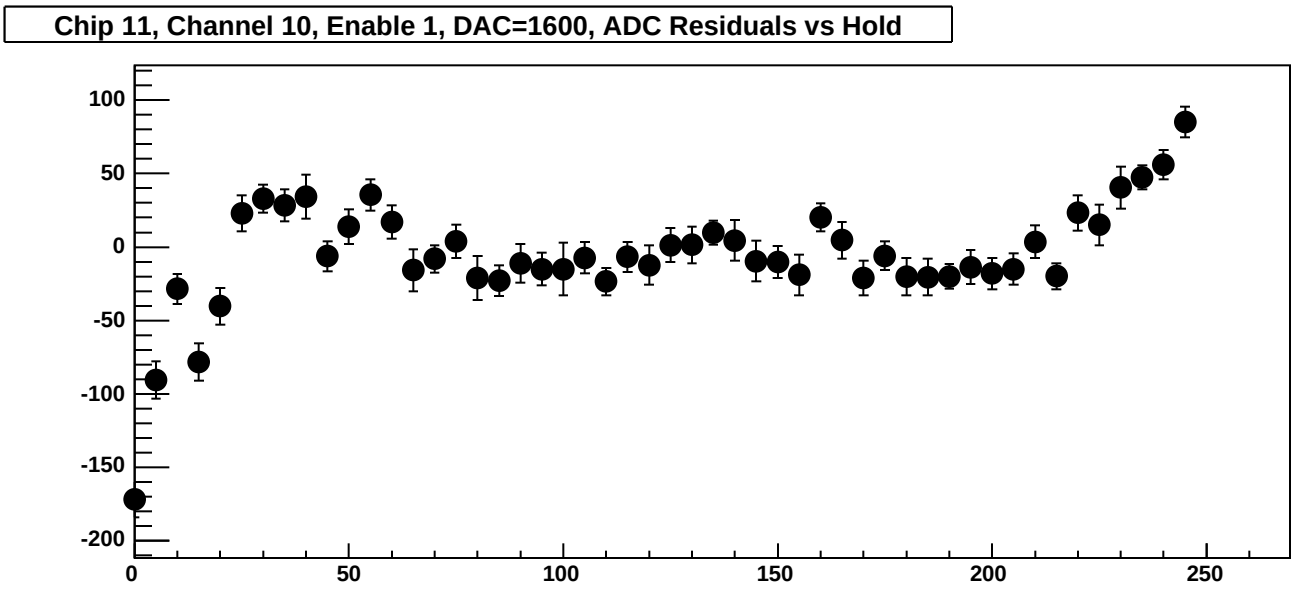


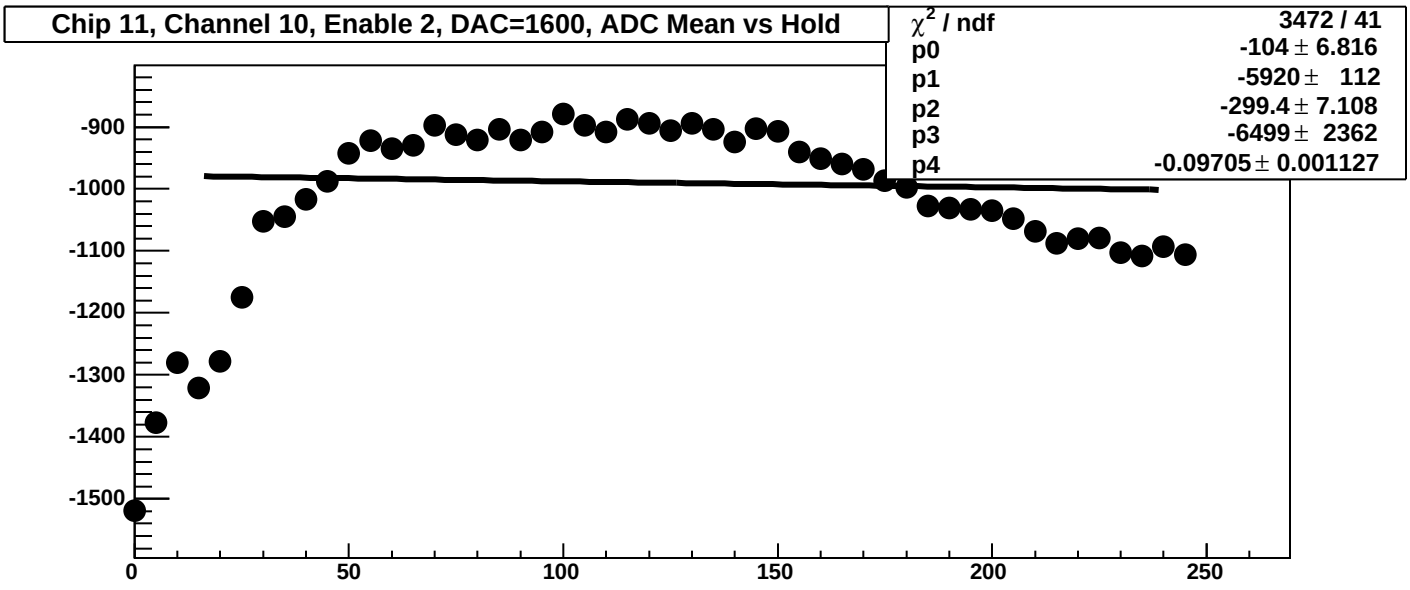
Chip 11, Channel 10, Enable 0!, DAC=1600, ADC Residuals vs Hold

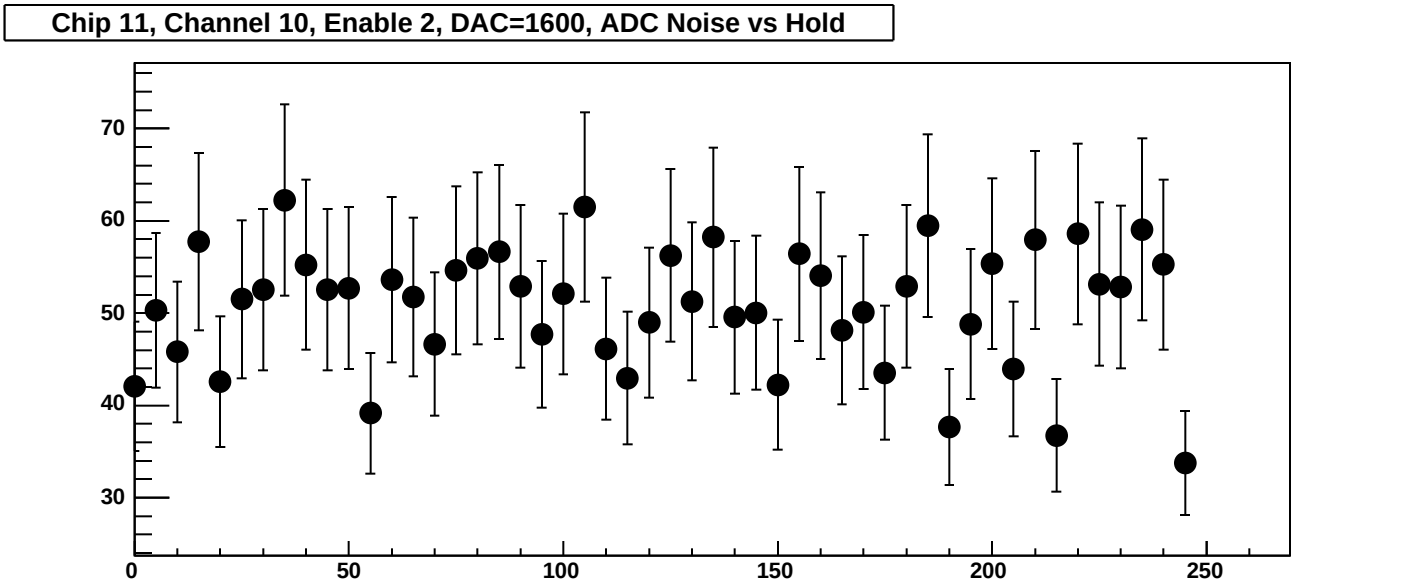


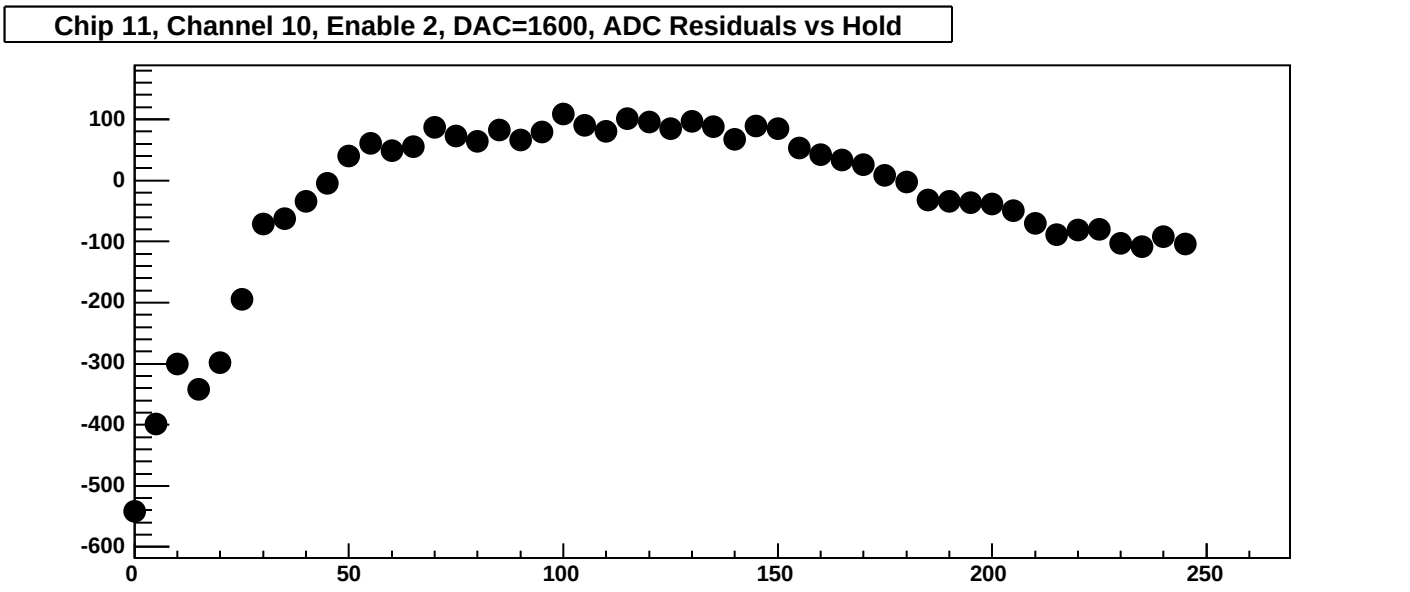

Chip 11, Channel 10, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 224.5 / 41
p0 -2125 ± 24.84
p1 -42.14 ± 1.523
p2 1946 ± 13.05
p3 65.41 ± 3.288
p4 -3.29 ± 0.09076
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250

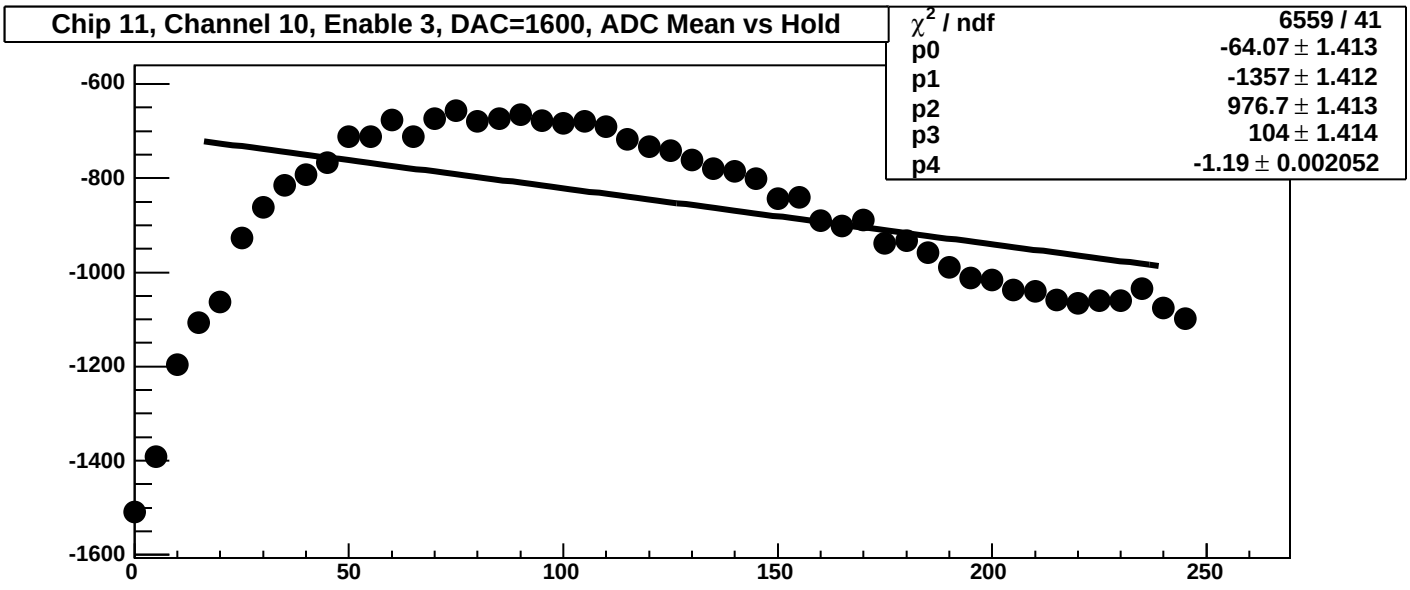

Chip 11, Channel 10, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

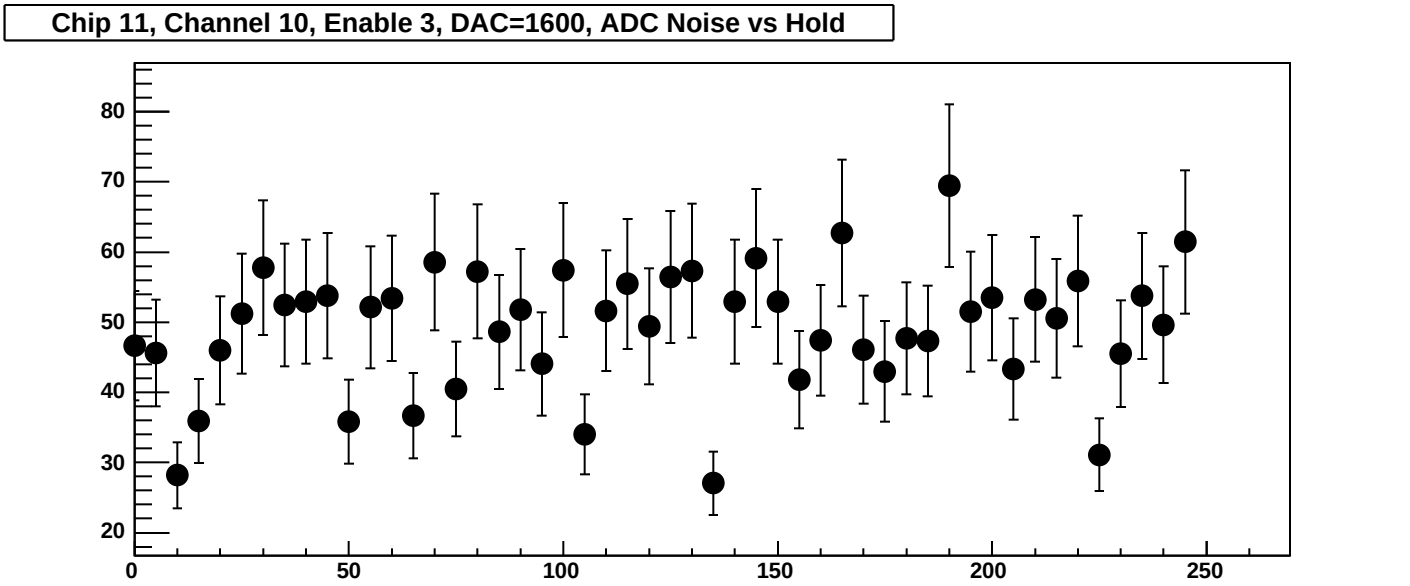

Chip 11, Channel 10, Enable 1, DAC=1600, ADC Residuals vs Hold
100
50
0
-50
-100
-150
-200
0
50
100
150
200
250

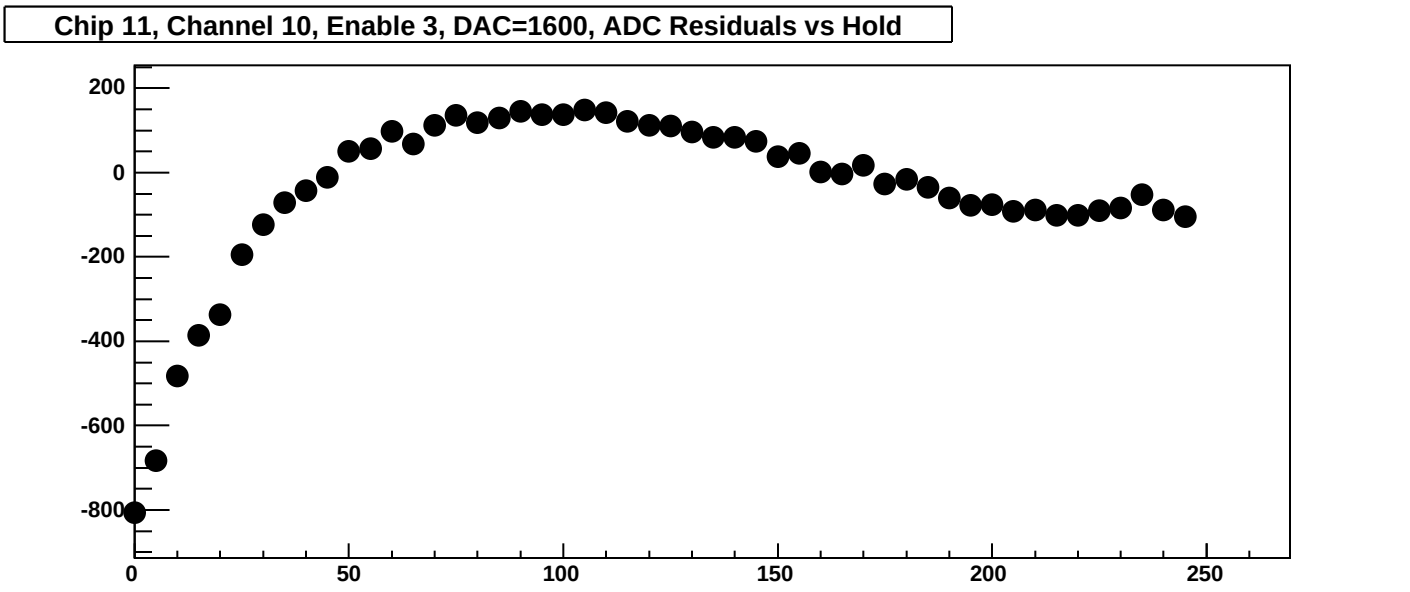

Chip 11, Channel 10, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 3472 / 41
p0 -104 ± 6.816
p1 -5920 ± 112
p2 -299.4 ± 7.108
p3 -6499 ± 2362
p4 -0.09705 ± 0.001127
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250


Chip 11, Channel 10, Enable 2, DAC=1600, ADC Noise vs Hold
70
60
50
40
30
0
50
100
150
200
250

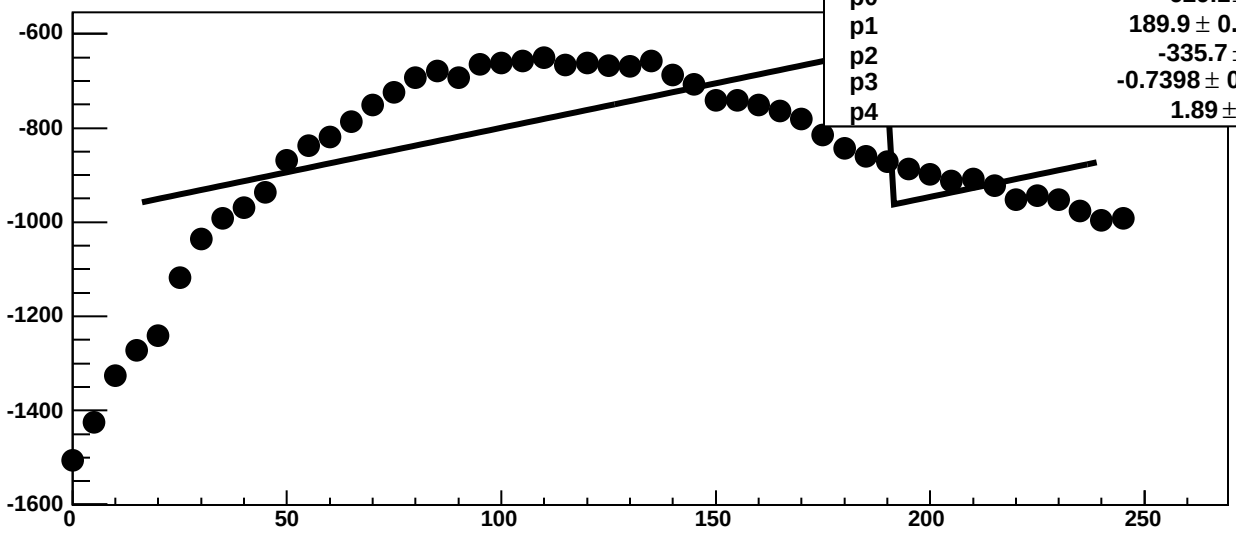

Chip 11, Channel 10, Enable 2, DAC=1600, ADC Residuals vs Hold
100
0
-100
-200
-300
-400
-500
-600
0
50
100
150
200
250


Chip 11, Channel 10, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 6559 / 41
p0 -64.07 ± 1.413
p1 -1357 ± 1.412
p2 976.7 ± 1.413
p3 104 ± 1.414
p4 -1.19 ± 0.002052
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250

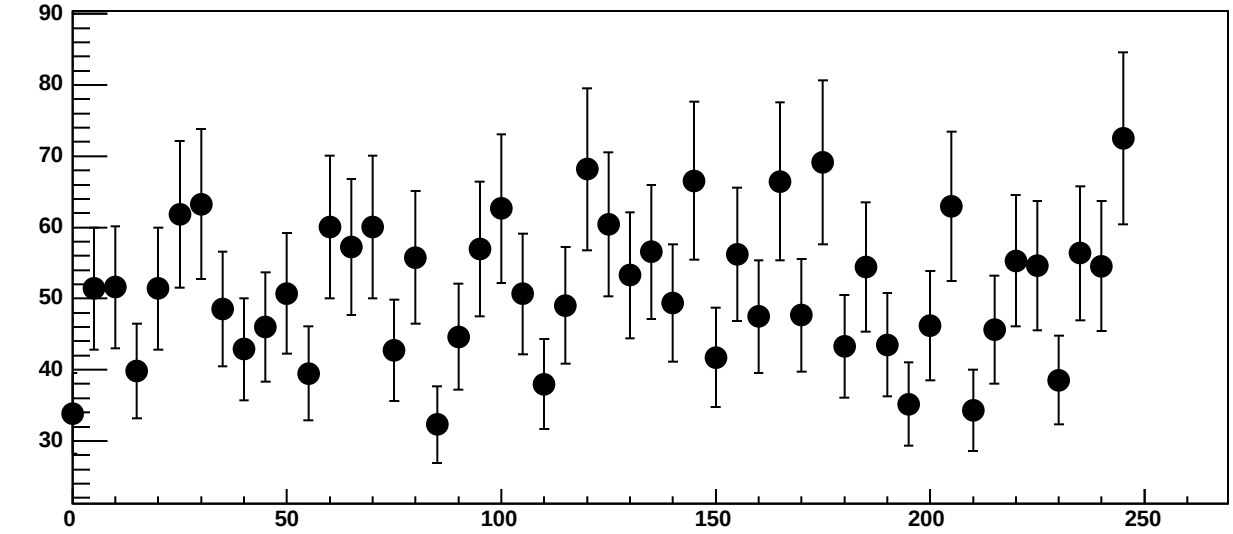

Chip 11, Channel 10, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 11, Channel 10, Enable 3, DAC=1600, ADC Residuals vs Hold
200
0
-200
-400
-600
-800
0
50
100
150
200
250

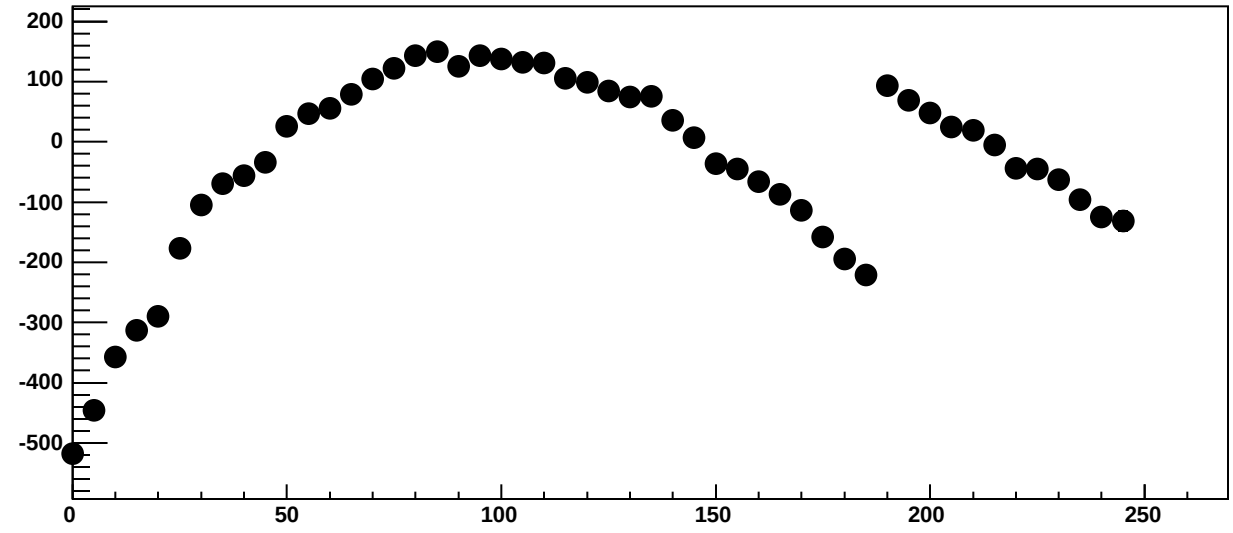
Chip 11, Channel 10, Enable 4, DAC=1600, ADC Mean vs Hold

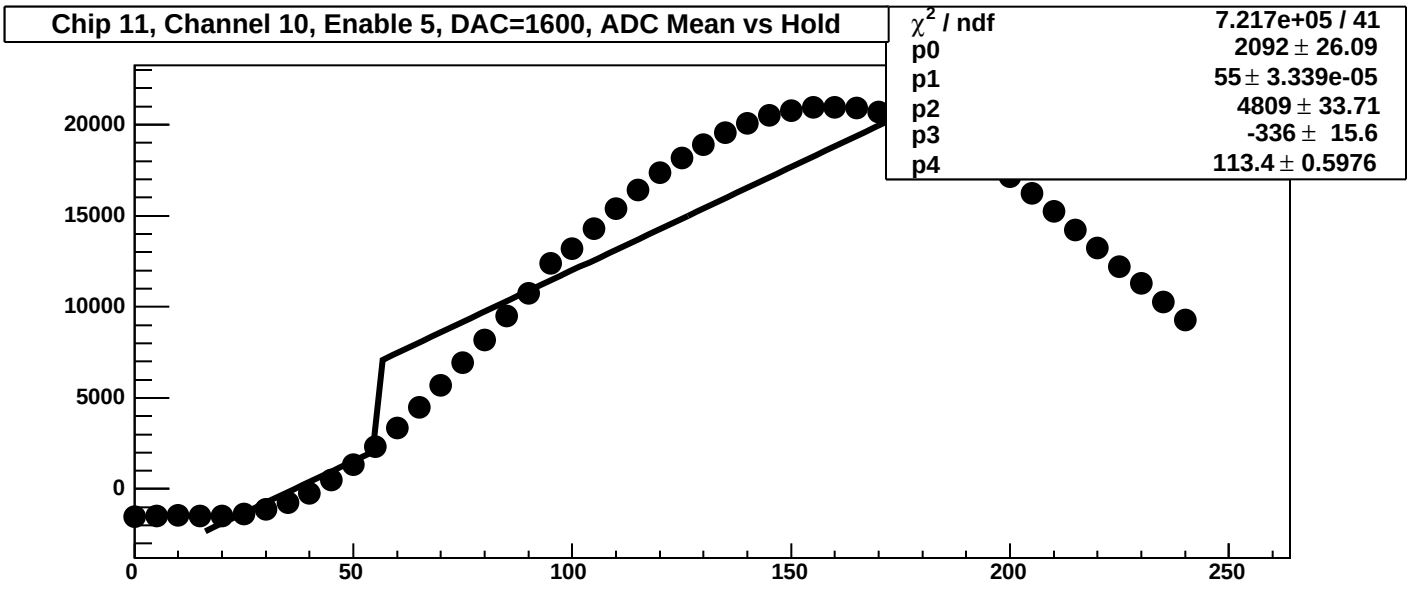


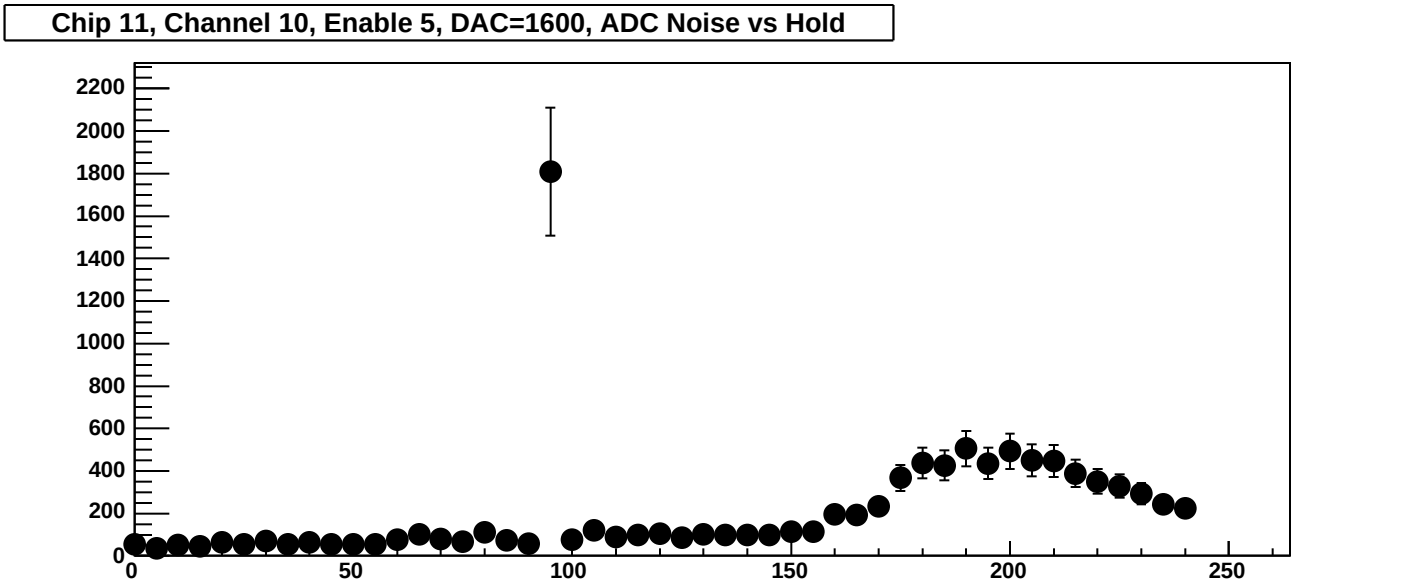
Chip 11, Channel 10, Enable 4, DAC=1600, ADC Noise vs Hold

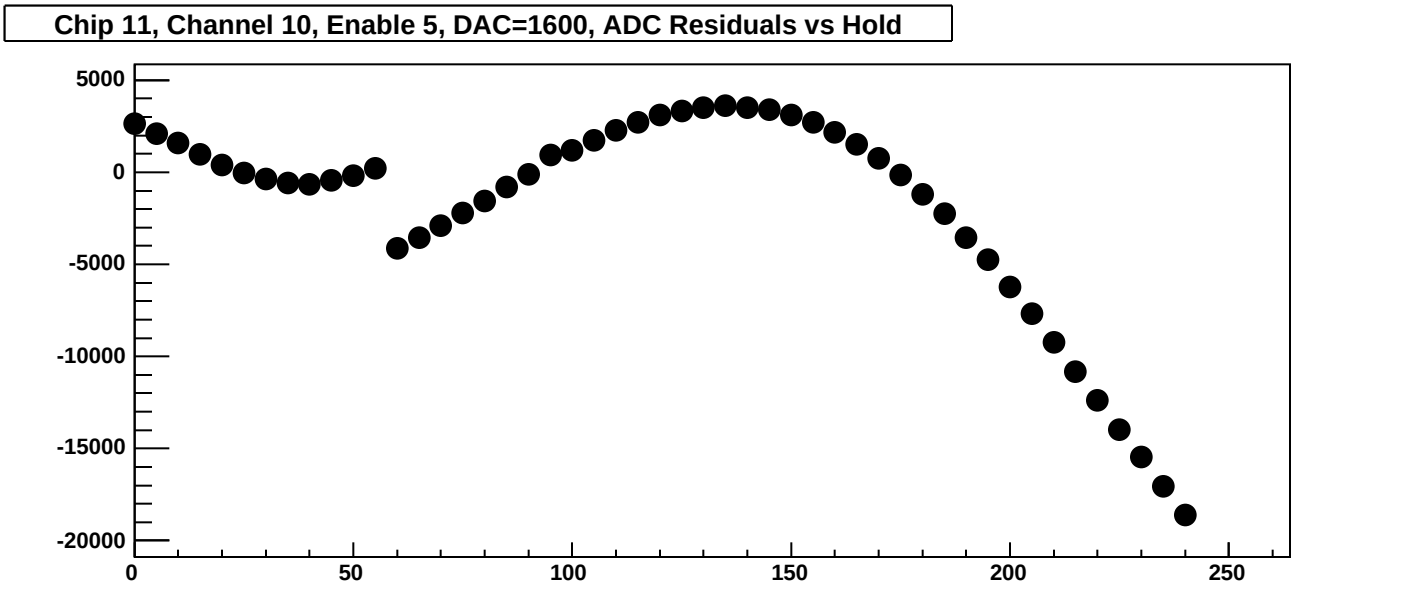


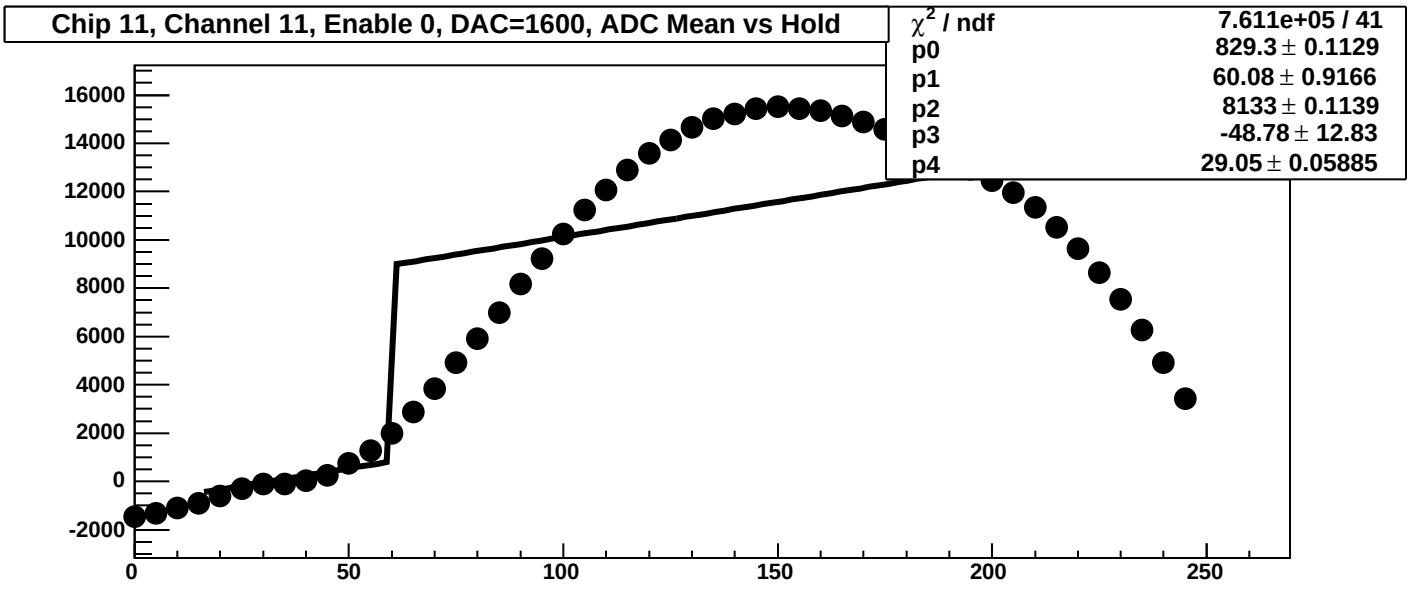
Chip 11, Channel 10, Enable 4, DAC=1600, ADC Residuals vs Hold

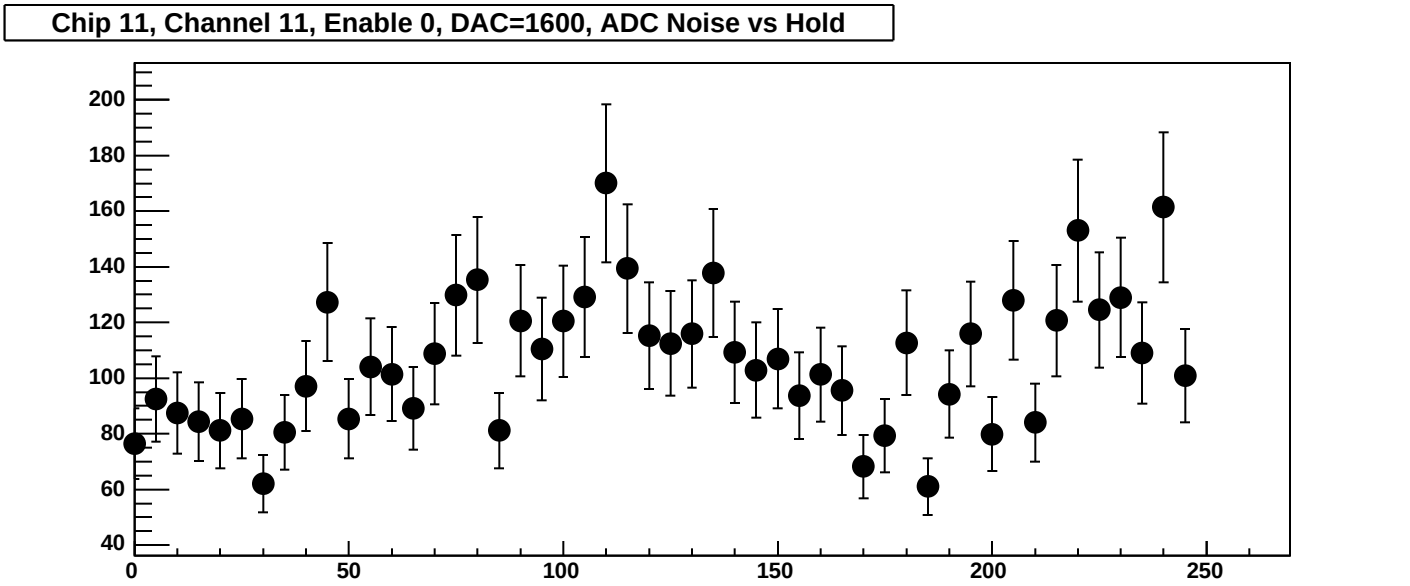


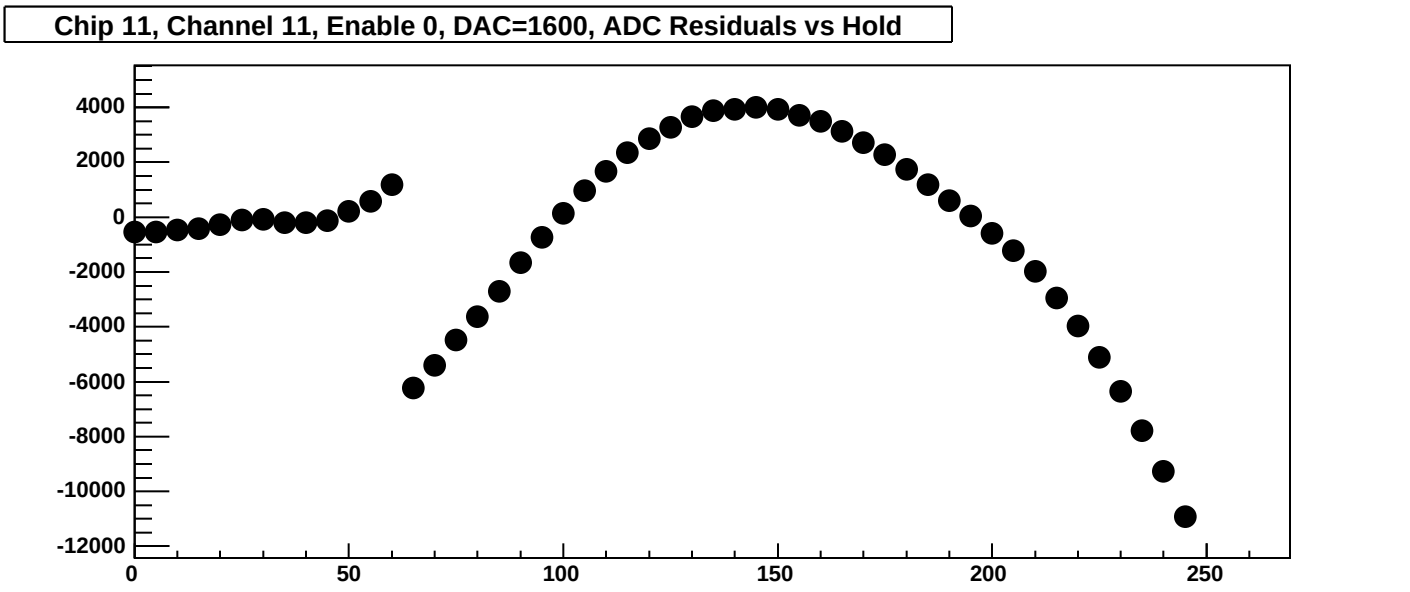

Chip 11, Channel 10, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.217e+05 / 41
p0 2092 ± 26.09
p1 55 ± 3.339e-05
p2 4809 ± 33.71
p3 -336 ± 15.6
p4 113.4 ± 0.5976
20000
15000
10000
5000
0
0
50
100
150
200
250

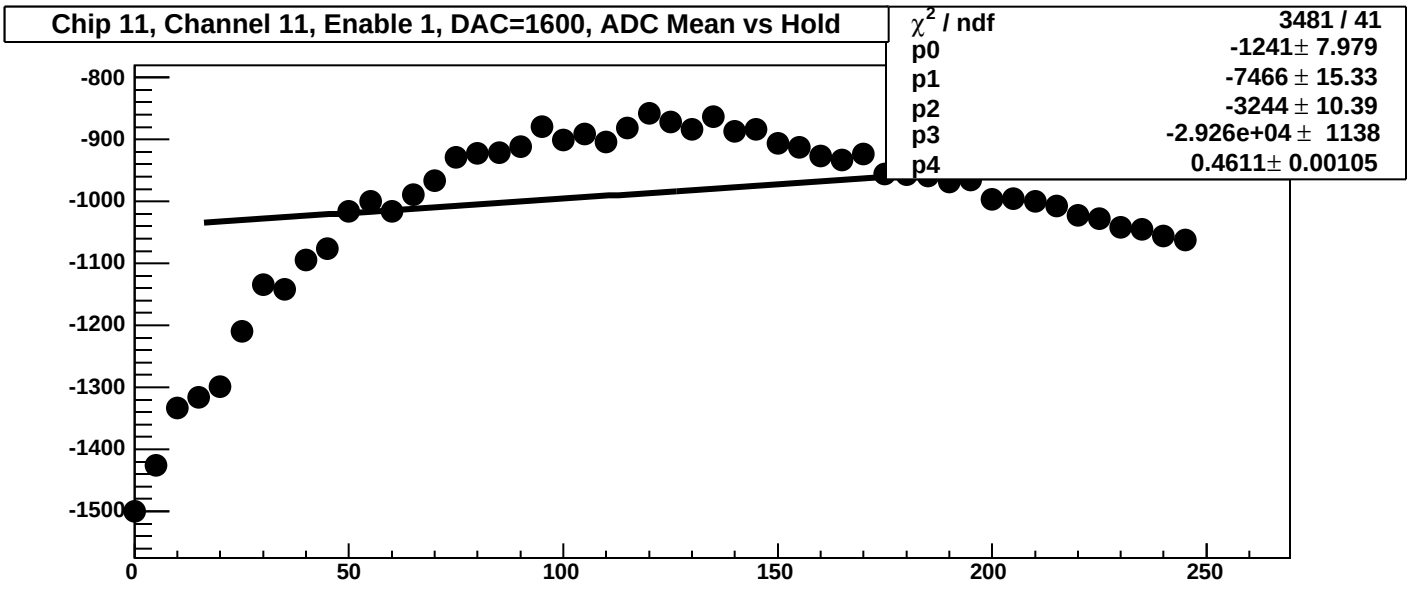

Chip 11, Channel 10, Enable 5, DAC=1600, ADC Noise vs Hold
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

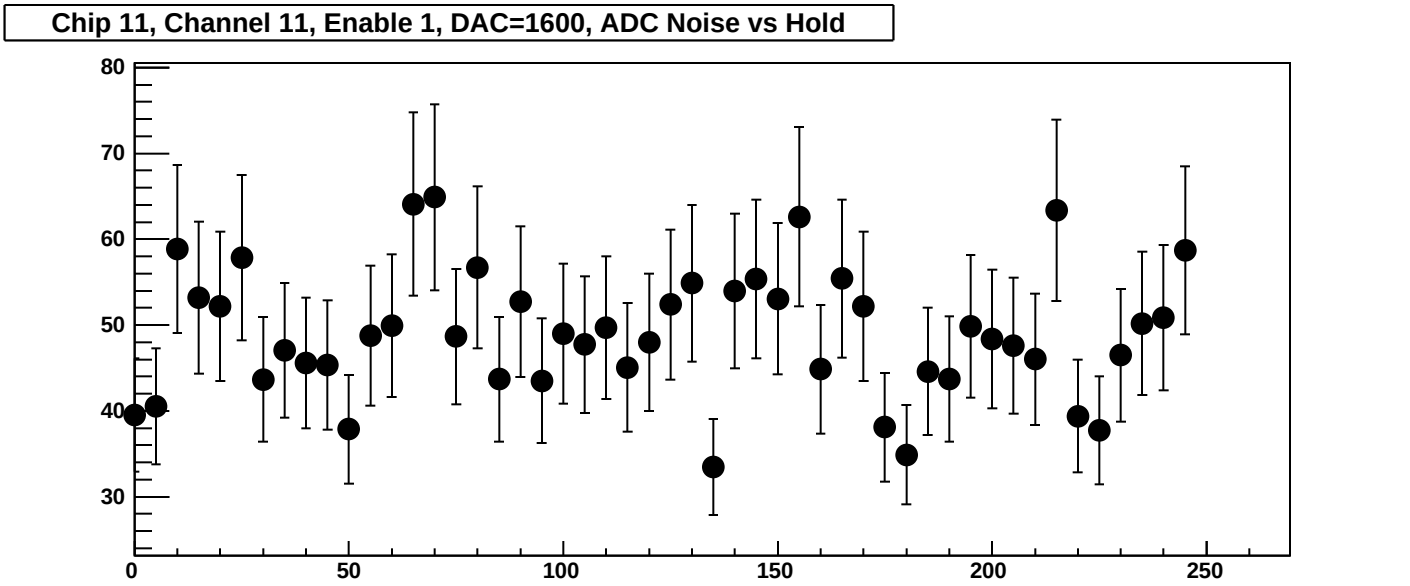

Chip 11, Channel 10, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250

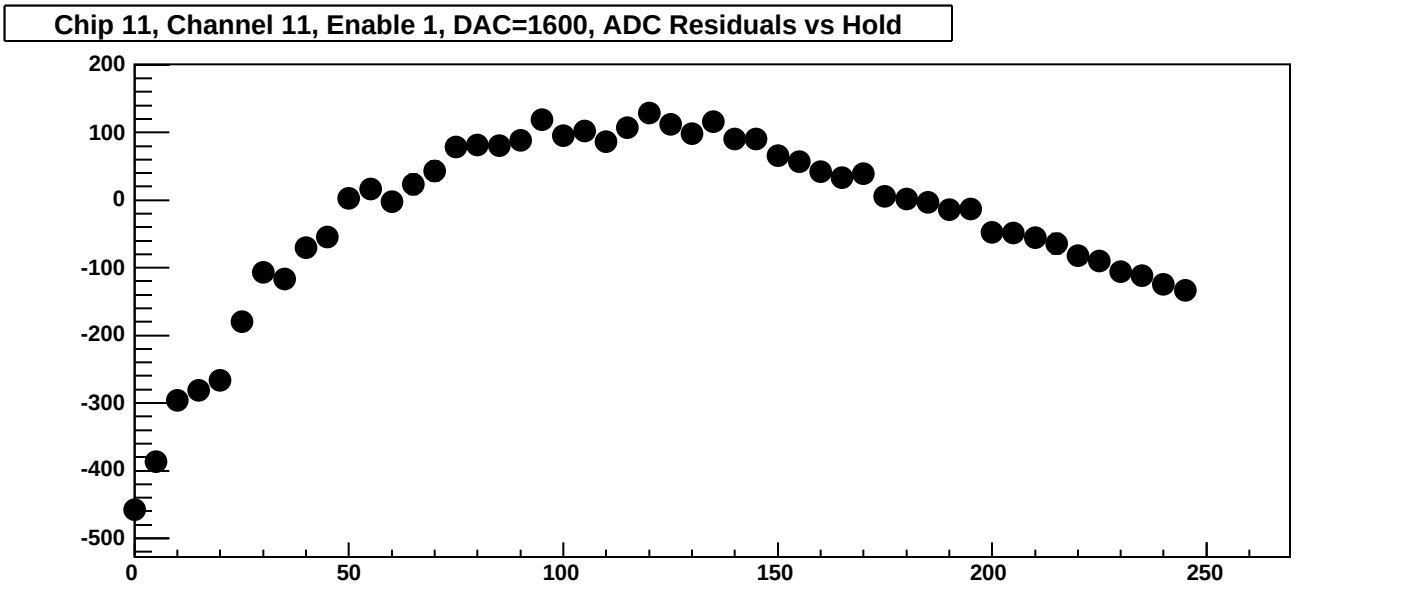

Chip 11, Channel 11, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.611e+05 / 41
p0 829.3 ± 0.1129
p1 60.08 ± 0.9166
p2 8133 ± 0.1139
p3 -48.78 ± 12.83
p4 29.05 ± 0.05885
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

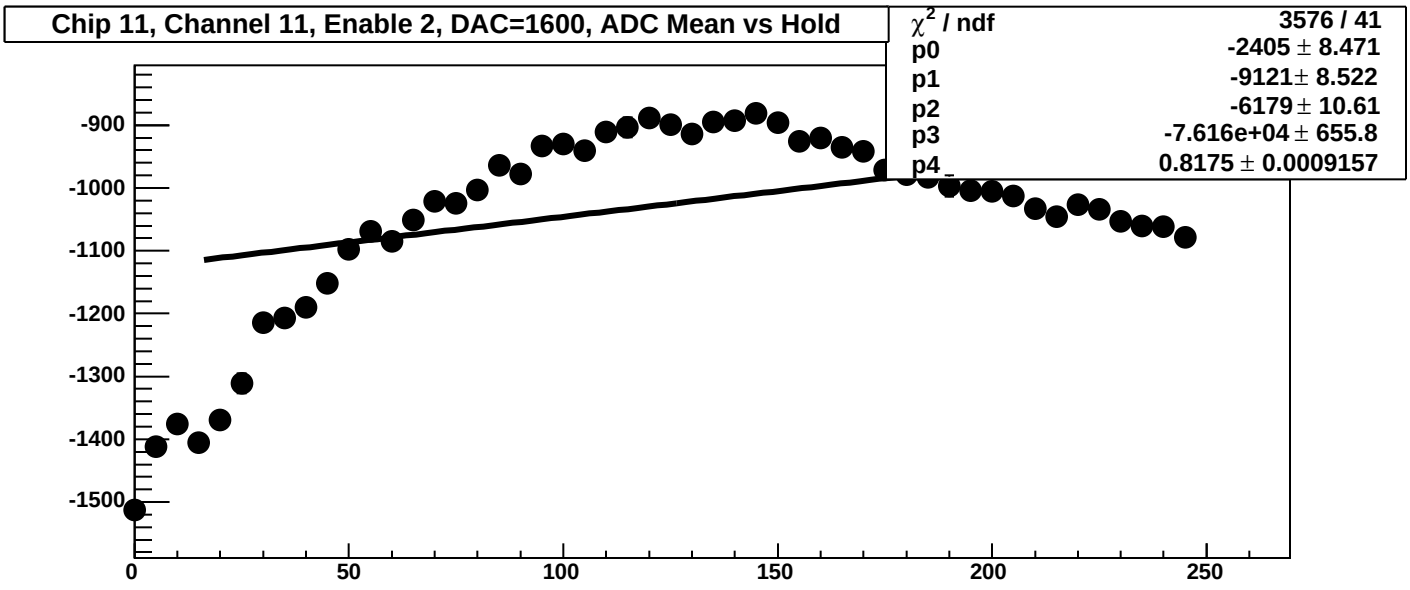

Chip 11, Channel 11, Enable 0, DAC=1600, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250

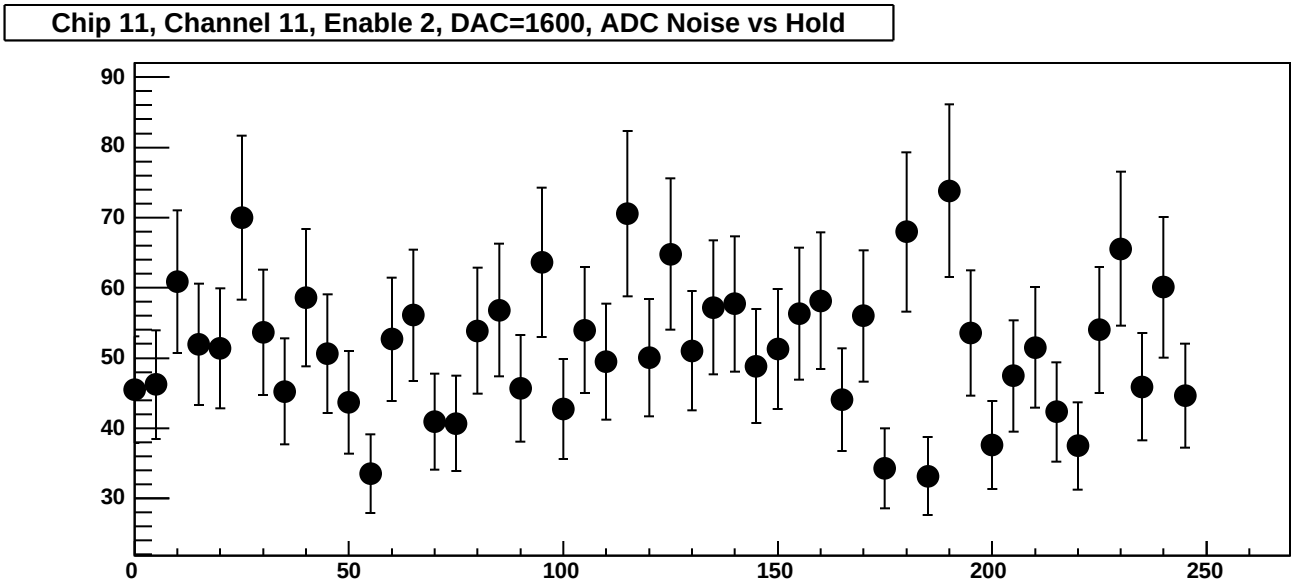

Chip 11, Channel 11, Enable 0, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
0
50
100
150
200
250

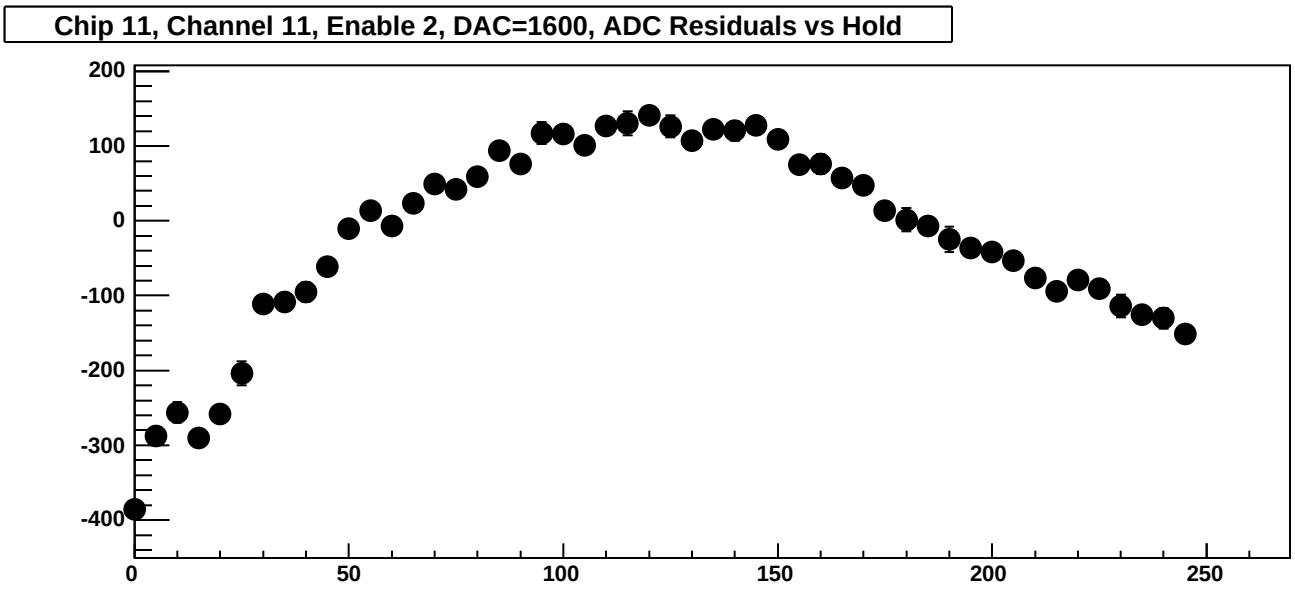

Chip 11, Channel 11, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 3481 / 41
p0 -1241 ± 7.979
p1 -7466 ± 15.33
p2 -3244 ± 10.39
p3 -2.926e+04 ± 1138
p4 0.4611 ± 0.00105
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250

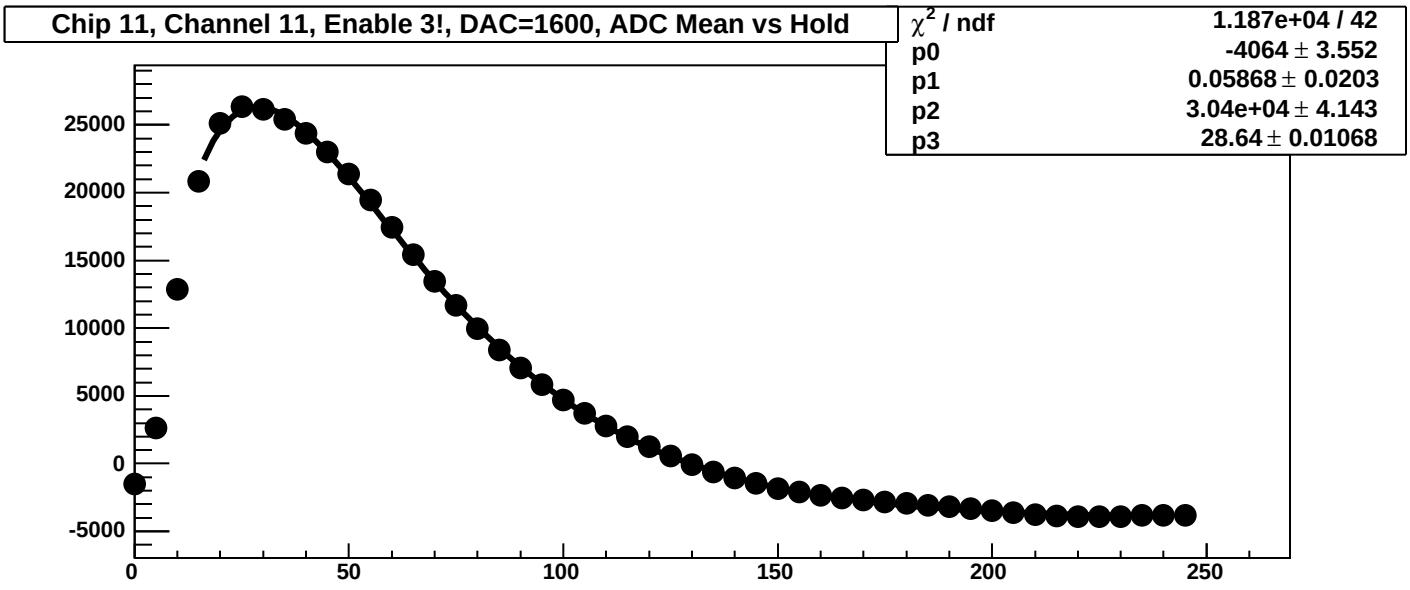

Chip 11, Channel 11, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

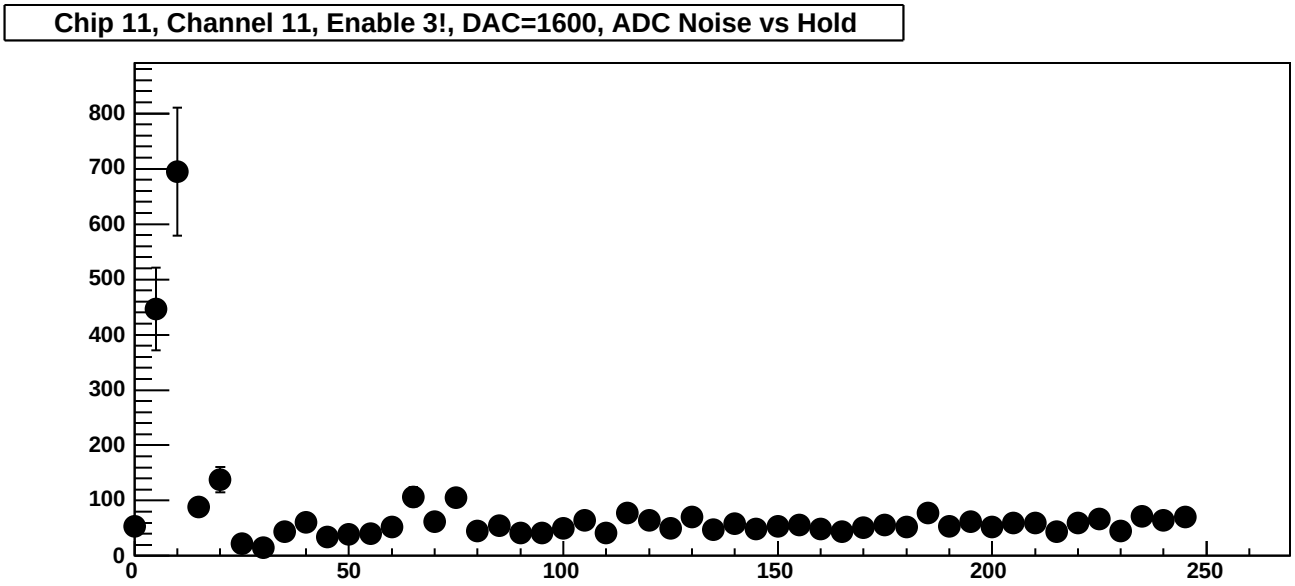

Chip 11, Channel 11, Enable 1, DAC=1600, ADC Residuals vs Hold
200
100
0
-100
-200
-300
-400
-500
0
50
100
150
200
250

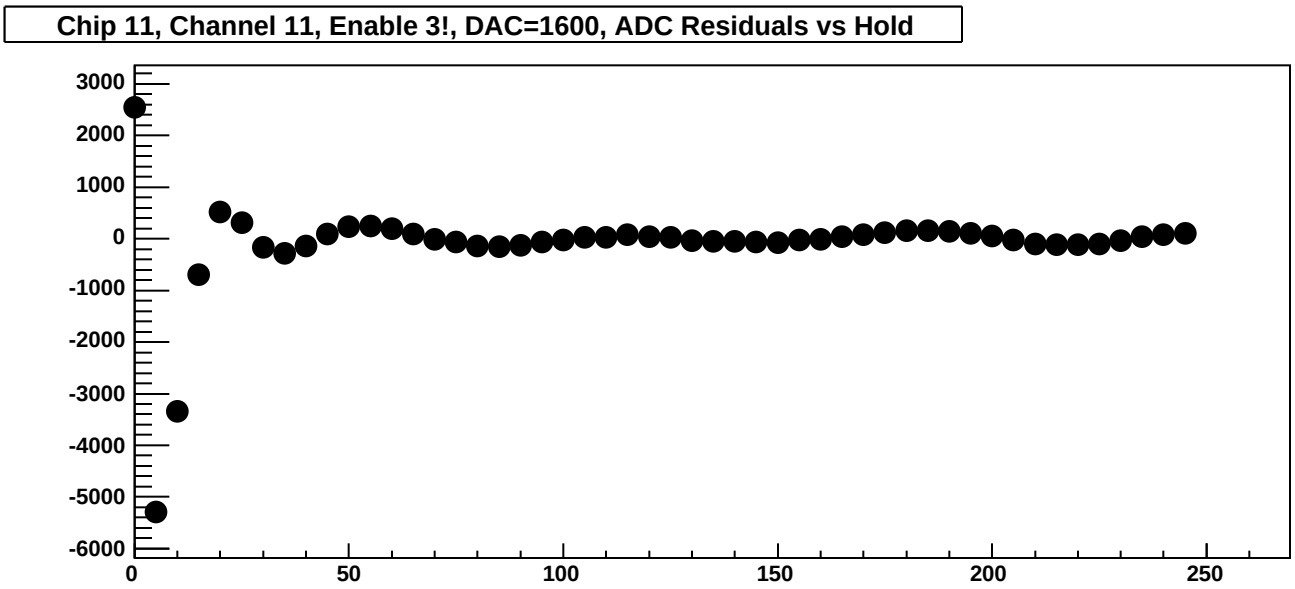

Chip 11, Channel 11, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 3576 / 41
p0 -2405 ± 8.471
p1 -9121 ± 8.522
p2 -6179 ± 10.61
p3 -7.616e+04 ± 655.8
p4 0.8175 ± 0.0009157
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250


Chip 11, Channel 11, Enable 2, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

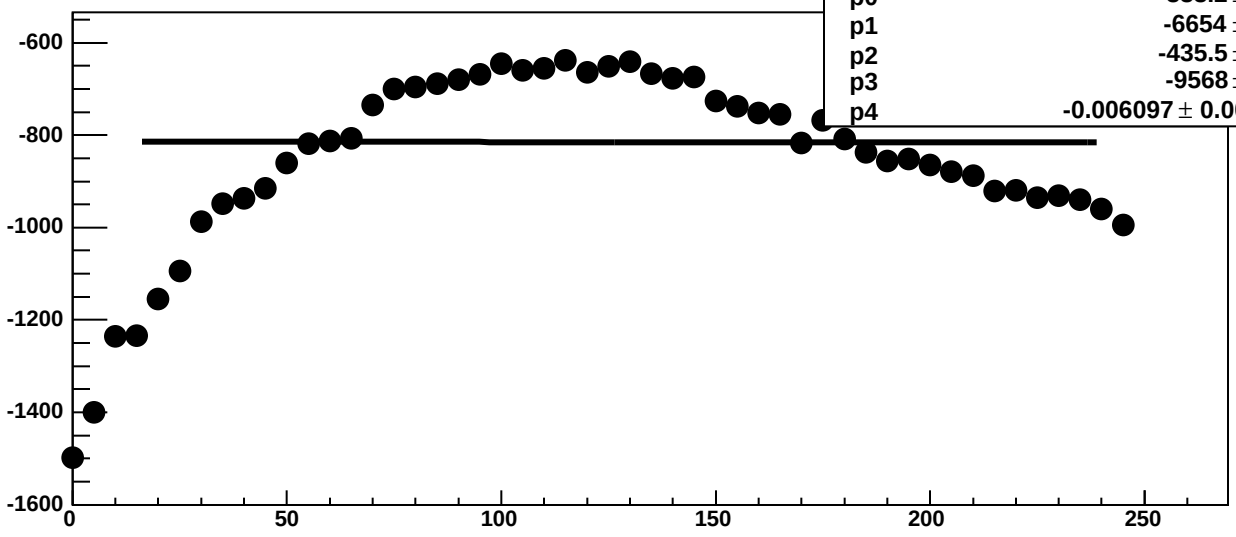

Chip 11, Channel 11, Enable 2, DAC=1600, ADC Residuals vs Hold
200
100
0
-100
-200
-300
-400
0
50
100
150
200
250


Chip 11, Channel 11, Enable 3!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.187e+04 / 42
p0 -4064 ± 3.552
p1 0.05868 ± 0.0203
p2 3.04e+04 ± 4.143
p3 28.64 ± 0.01068
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

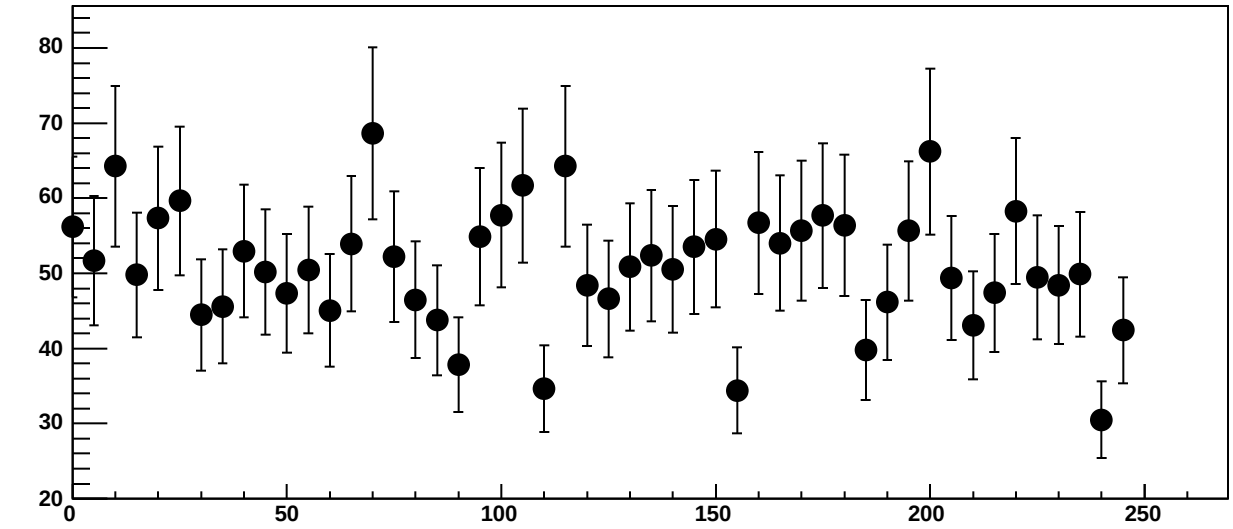

Chip 11, Channel 11, Enable 3!, DAC=1600, ADC Noise vs Hold
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 11, Channel 11, Enable 3!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
50
100
150
200
250

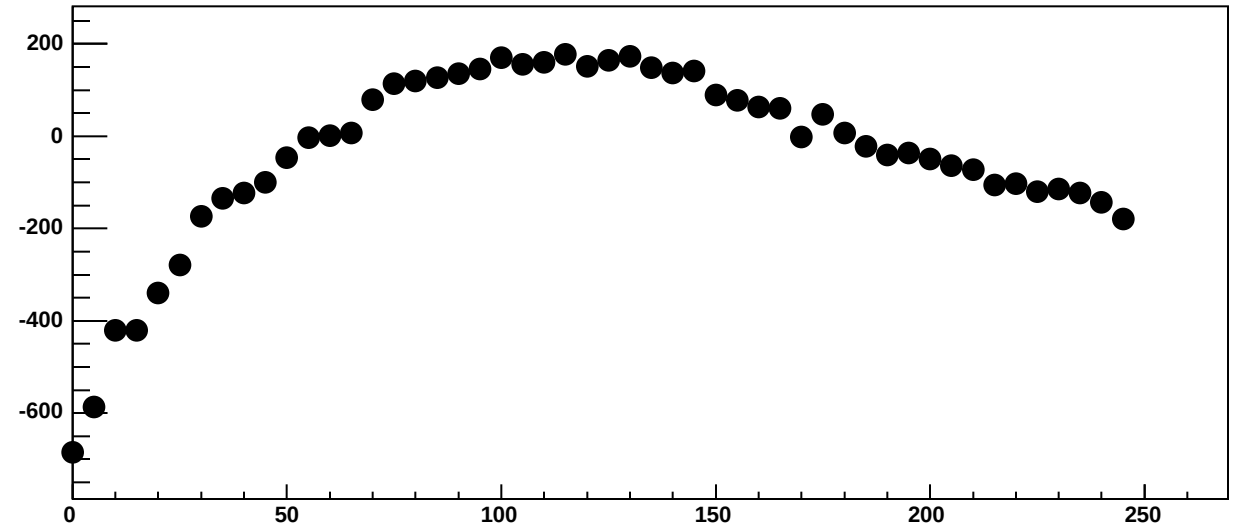
Chip 11, Channel 11, Enable 4, DAC=1600, ADC Mean vs Hold

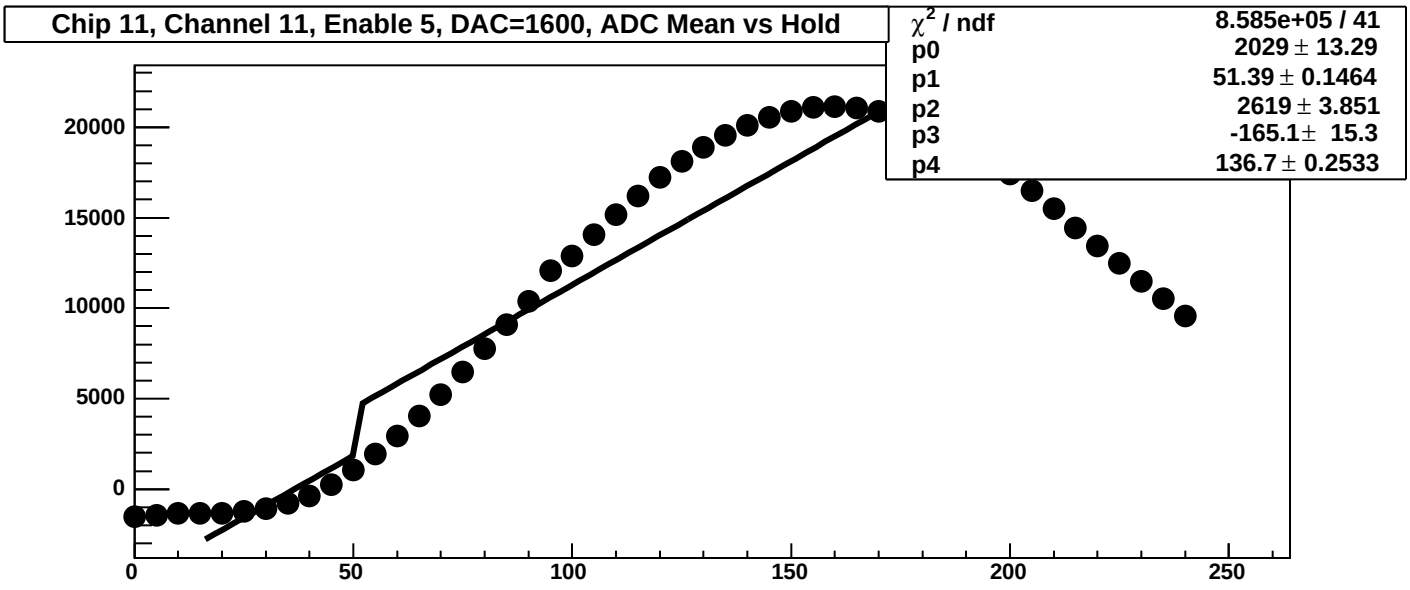


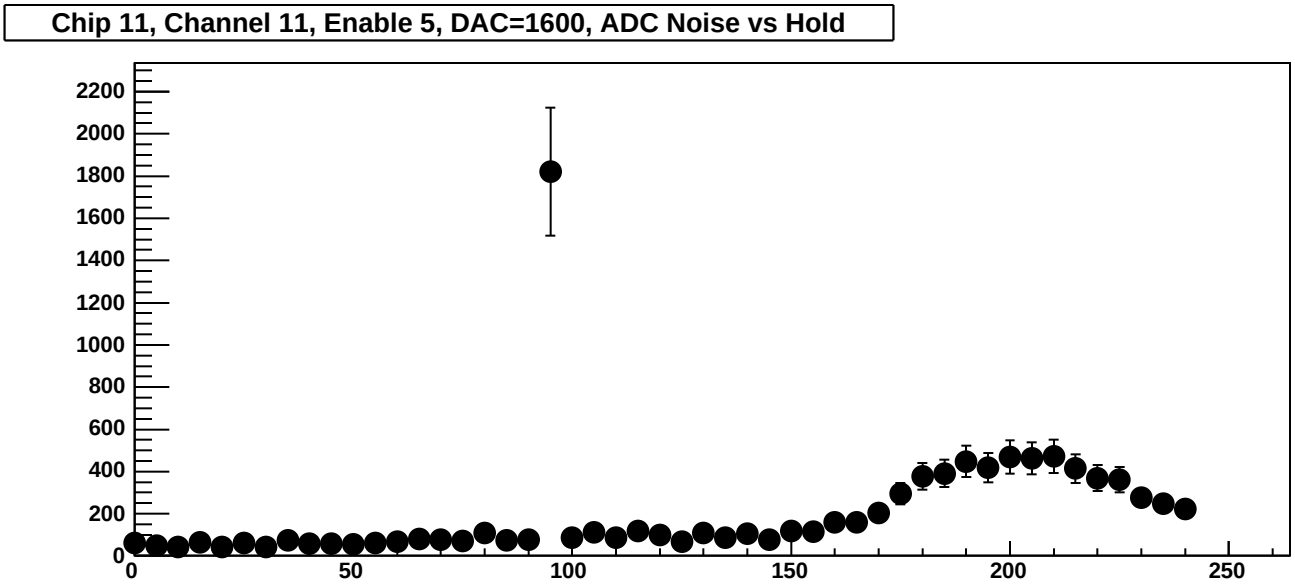
Chip 11, Channel 11, Enable 4, DAC=1600, ADC Noise vs Hold

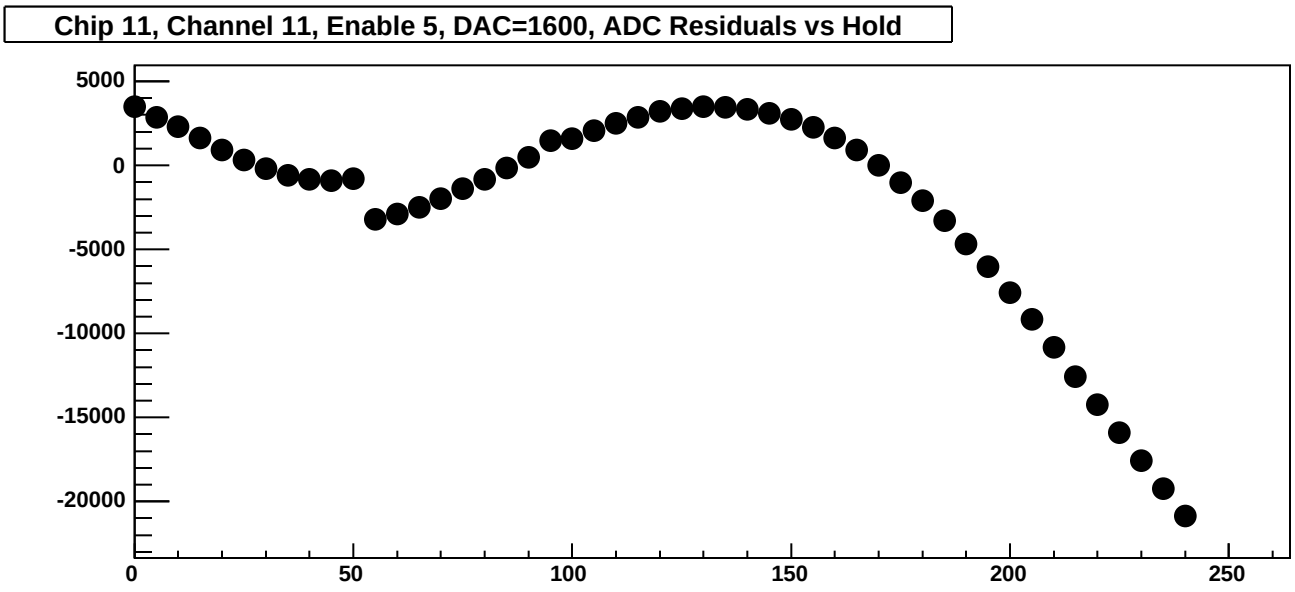


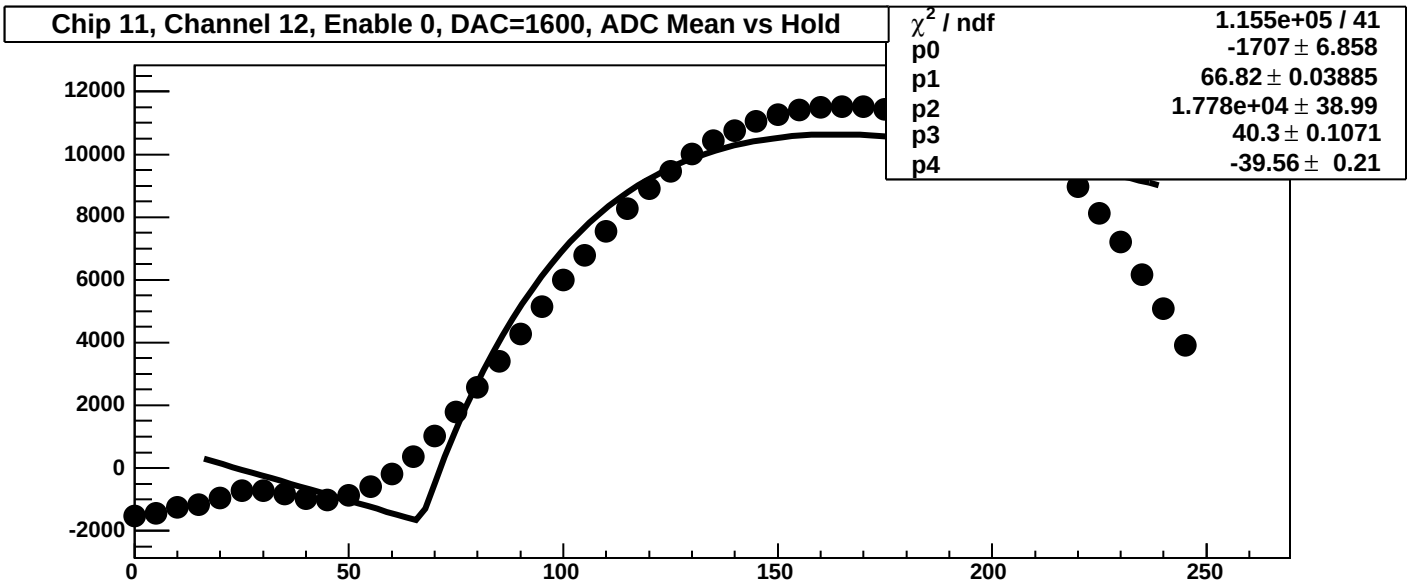
Chip 11, Channel 11, Enable 4, DAC=1600, ADC Residuals vs Hold

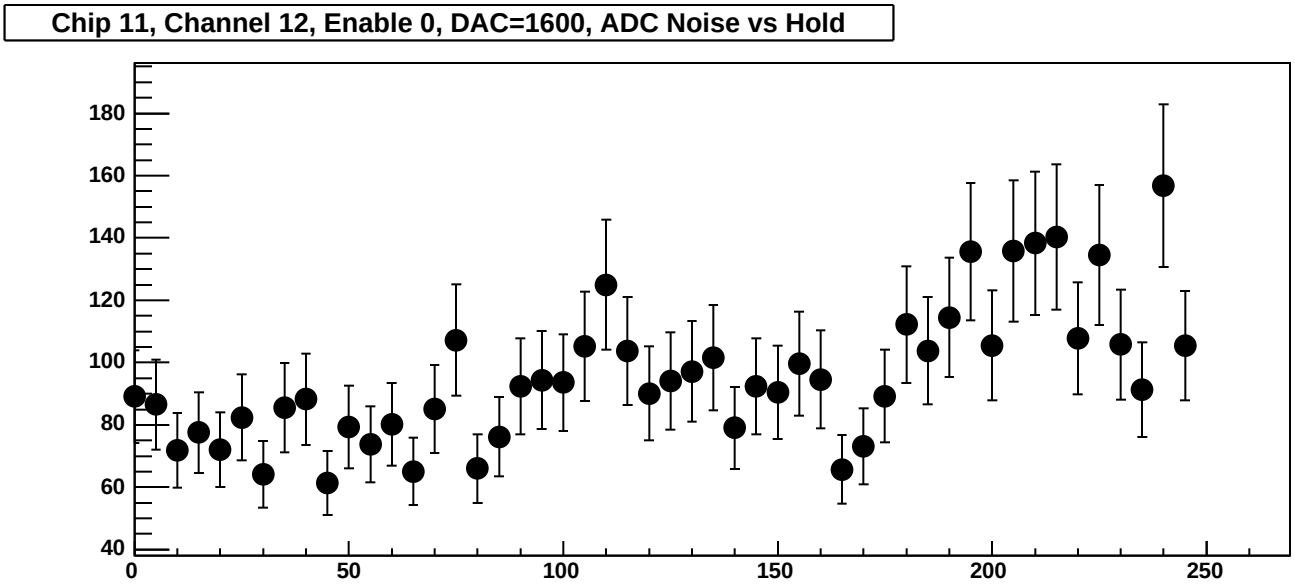


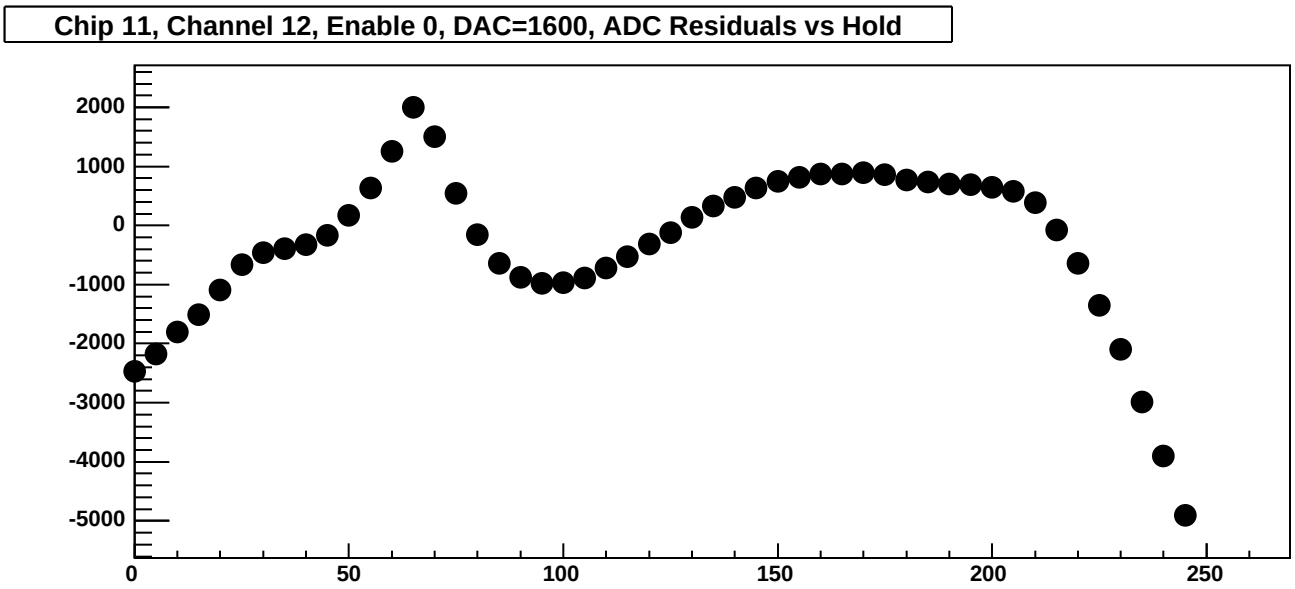

Chip 11, Channel 11, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 8.585e+05 / 41
p0 2029 ± 13.29
p1 51.39 ± 0.1464
p2 2619 ± 3.851
p3 -165.1± 15.3
p4 136.7 ± 0.2533
20000
15000
10000
5000
0
0
50
100
150
200
250


Chip 11, Channel 11, Enable 5, DAC=1600, ADC Noise vs Hold
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

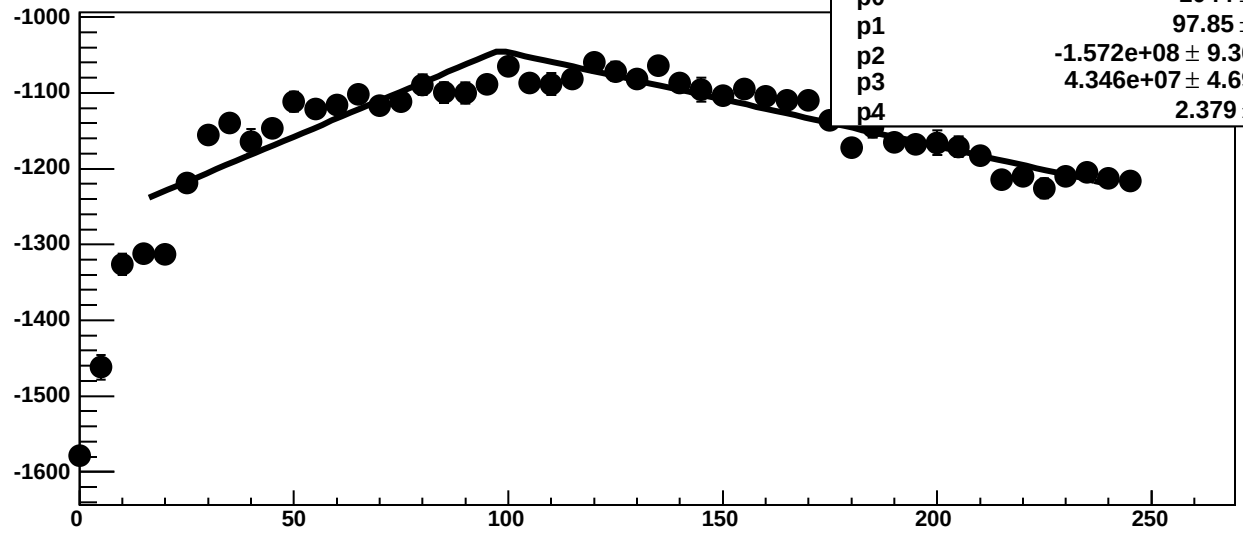

Chip 11, Channel 11, Enable 5, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
-20000
0
50
100
150
200
250


Chip 11, Channel 12, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.155e+05 / 41
p0 -1707 ± 6.858
p1 66.82 ± 0.03885
p2 1.778e+04 ± 38.99
p3 40.3 ± 0.1071
p4 -39.56 ± 0.21
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

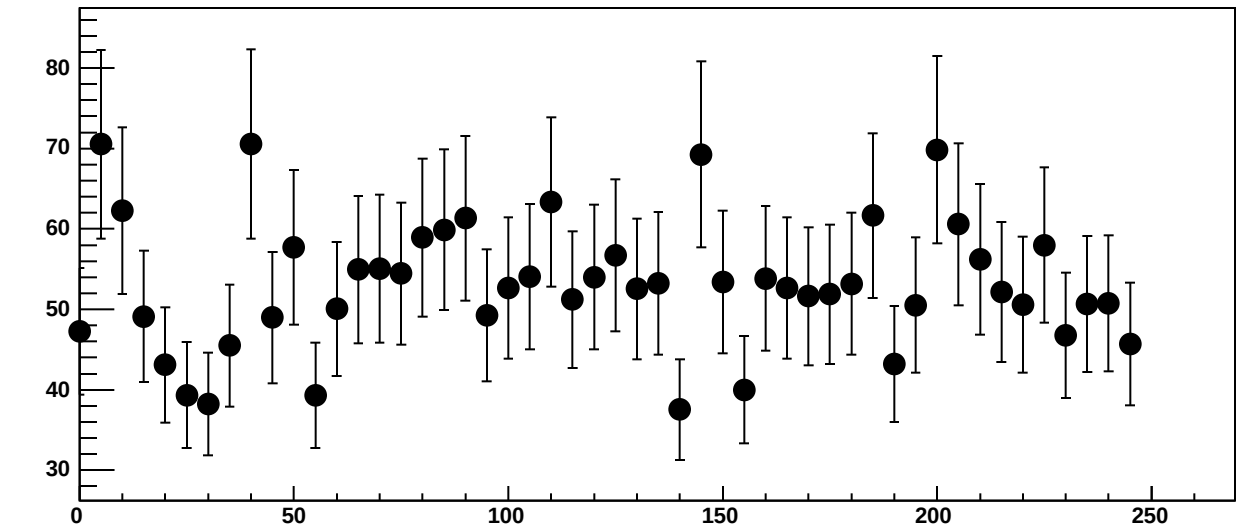

Chip 11, Channel 12, Enable 0, DAC=1600, ADC Noise vs Hold
180
160
140
120
100
80
60
40
0
50
100
150
200
250


Chip 11, Channel 12, Enable 0, DAC=1600, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

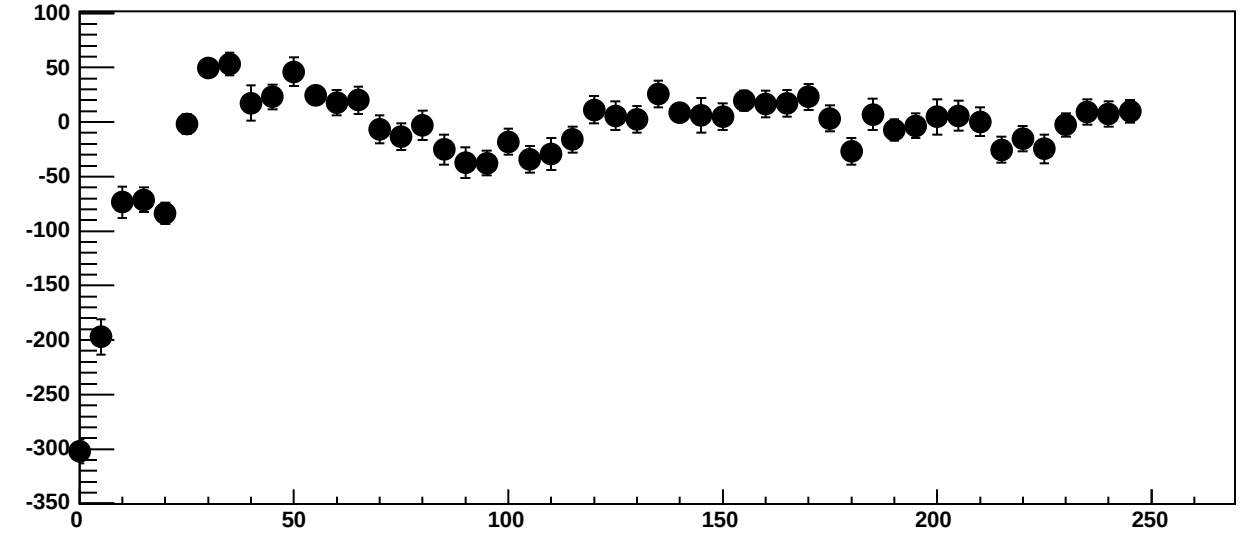
Chip 11, Channel 12, Enable 1, DAC=1600, ADC Mean vs Hold

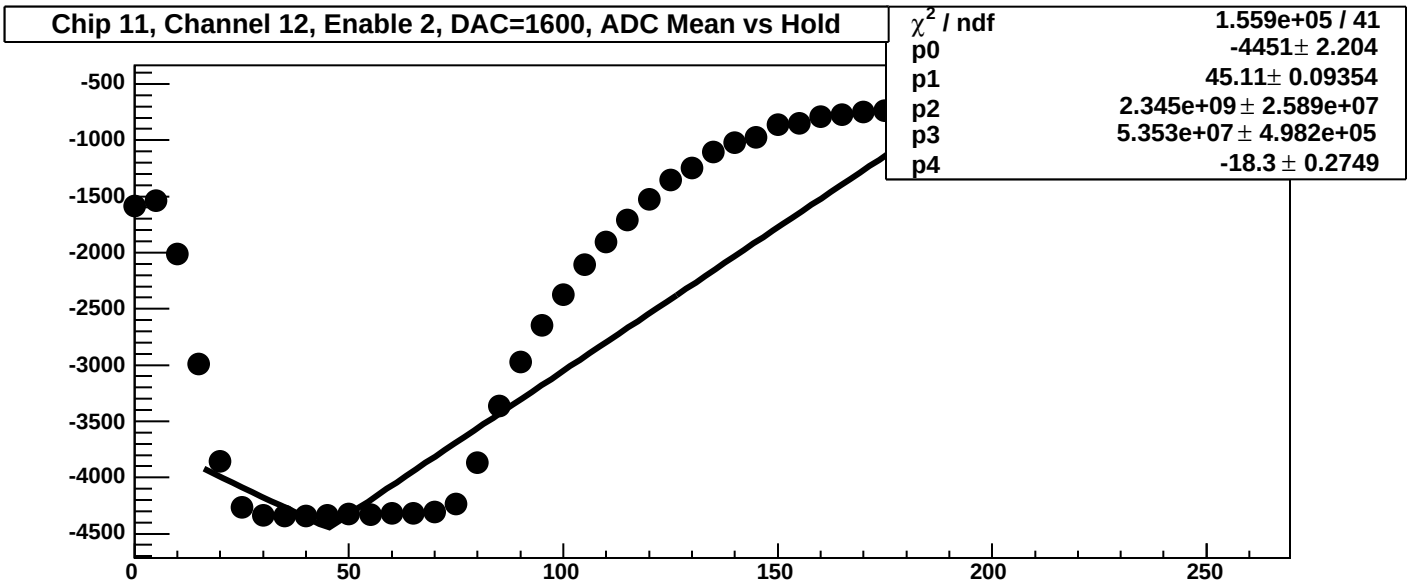


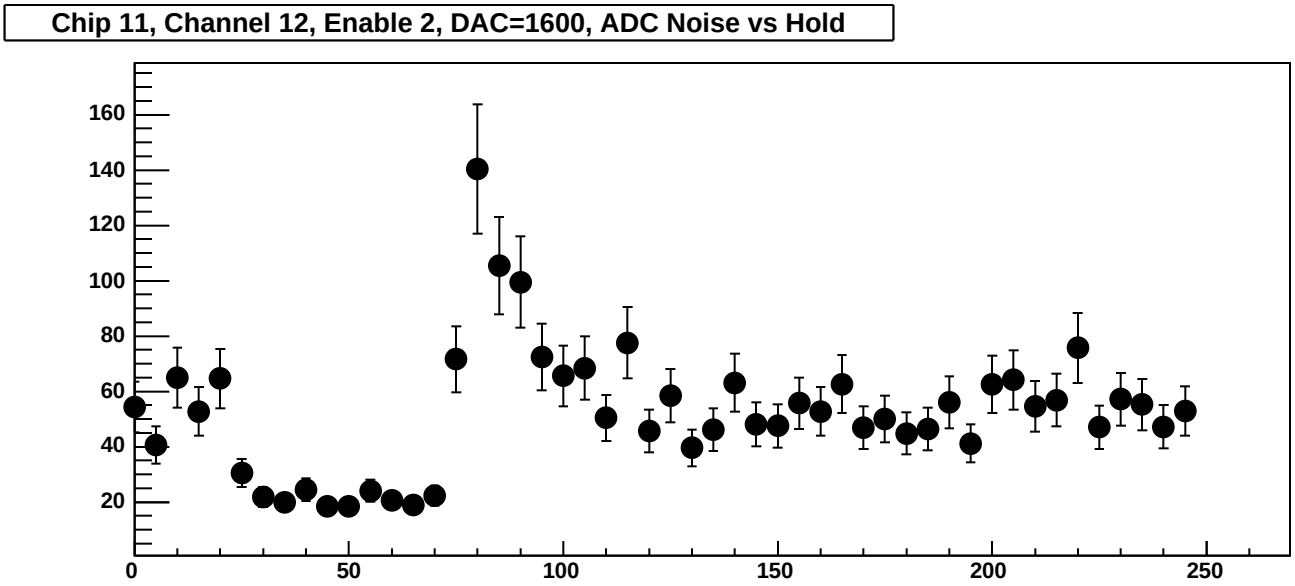
Chip 11, Channel 12, Enable 1, DAC=1600, ADC Noise vs Hold

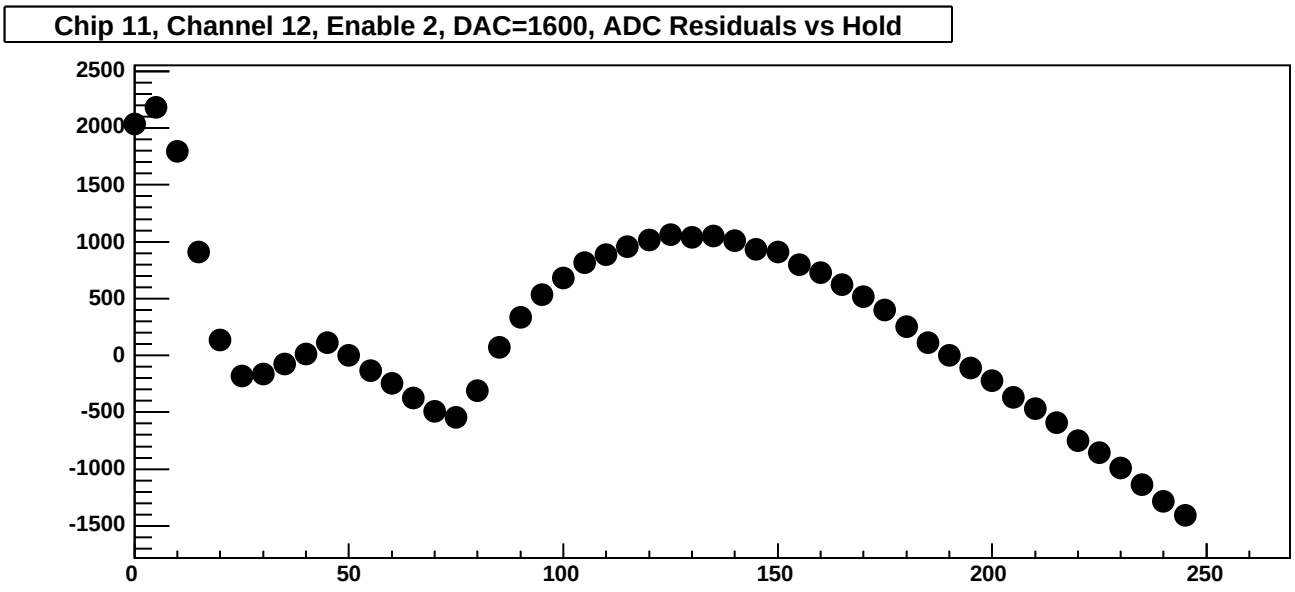


Chip 11, Channel 12, Enable 1, DAC=1600, ADC Residuals vs Hold

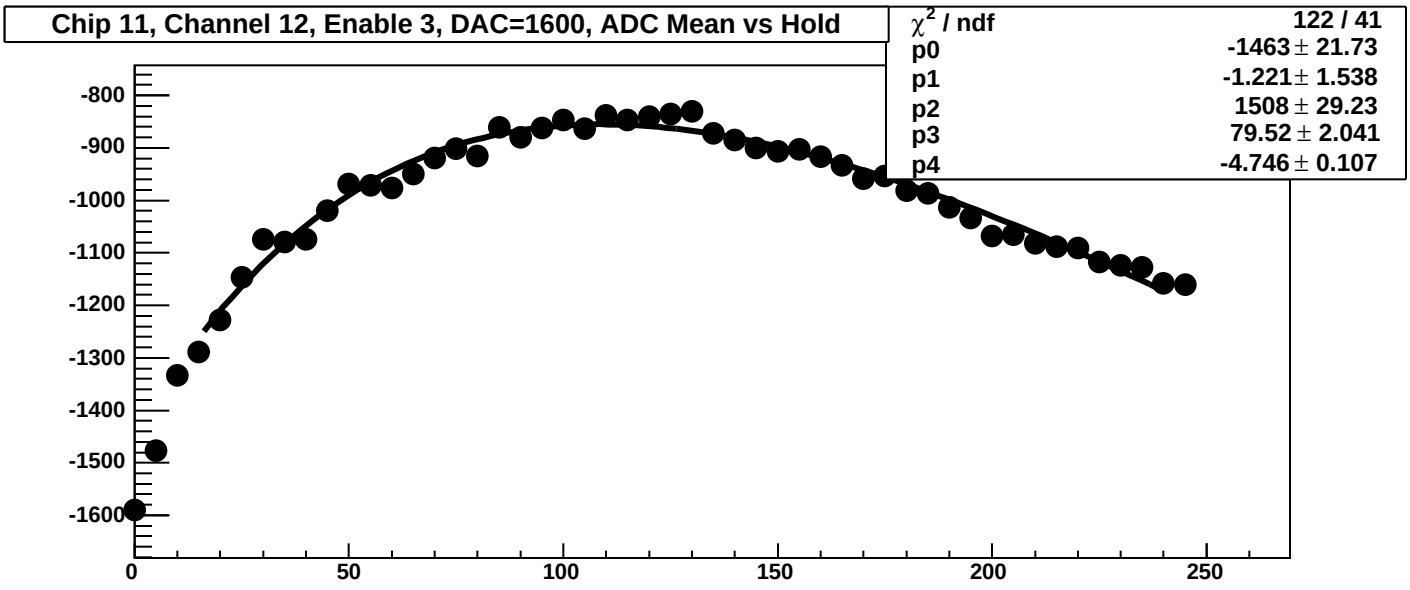



Chip 11, Channel 12, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.559e+05 / 41
p0 -4451 ± 2.204
p1 45.11 ± 0.09354
p2 2.345e+09 ± 2.589e+07
p3 5.353e+07 ± 4.982e+05
p4 -18.3 ± 0.2749
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
-4500
0
50
100
150
200
250

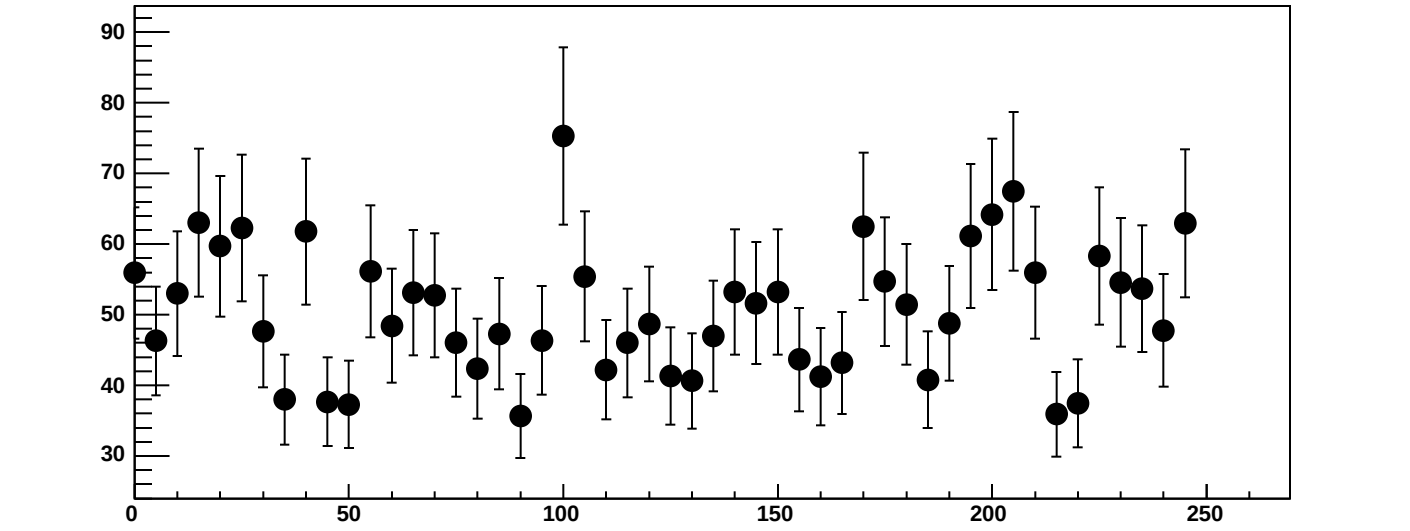

Chip 11, Channel 12, Enable 2, DAC=1600, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
50
100
150
200
250


Chip 11, Channel 12, Enable 2, DAC=1600, ADC Residuals vs Hold
2500
2000
1500
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

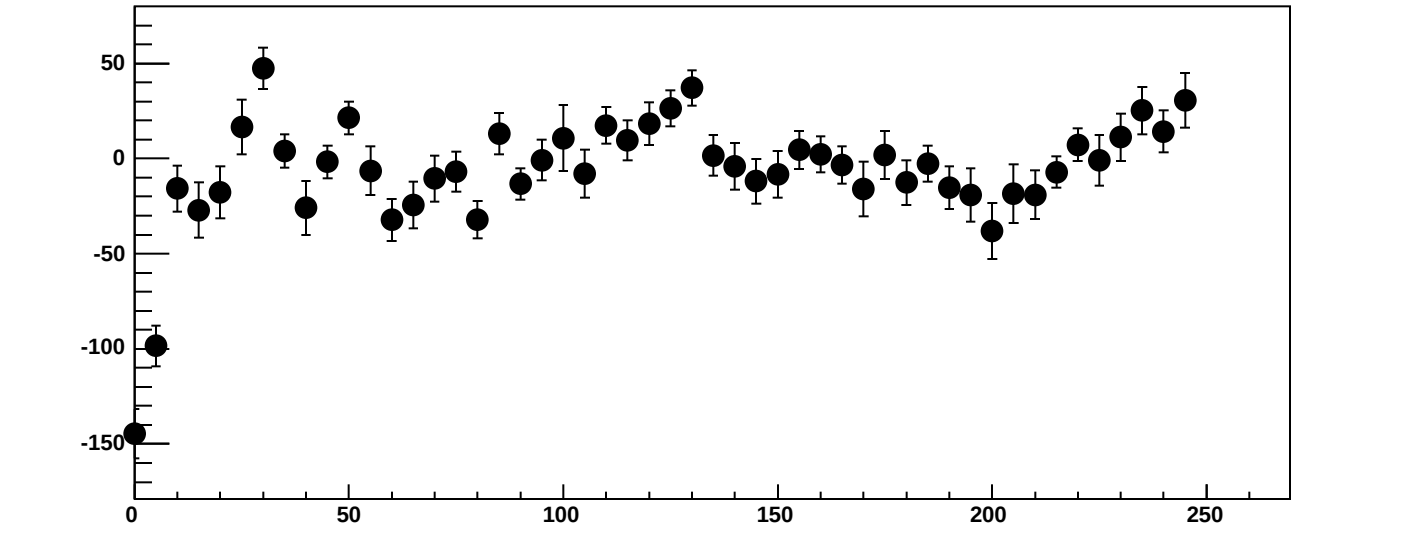
Chip 11, Channel 12, Enable 3, DAC=1600, ADC Mean vs Hold



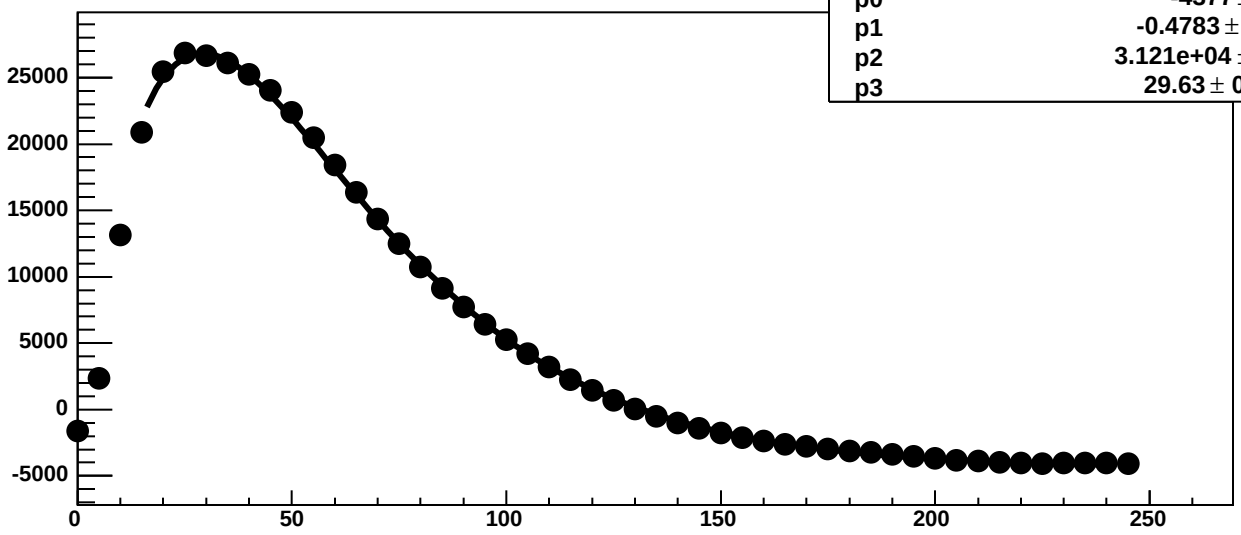
Chip 11, Channel 12, Enable 3, DAC=1600, ADC Noise vs Hold



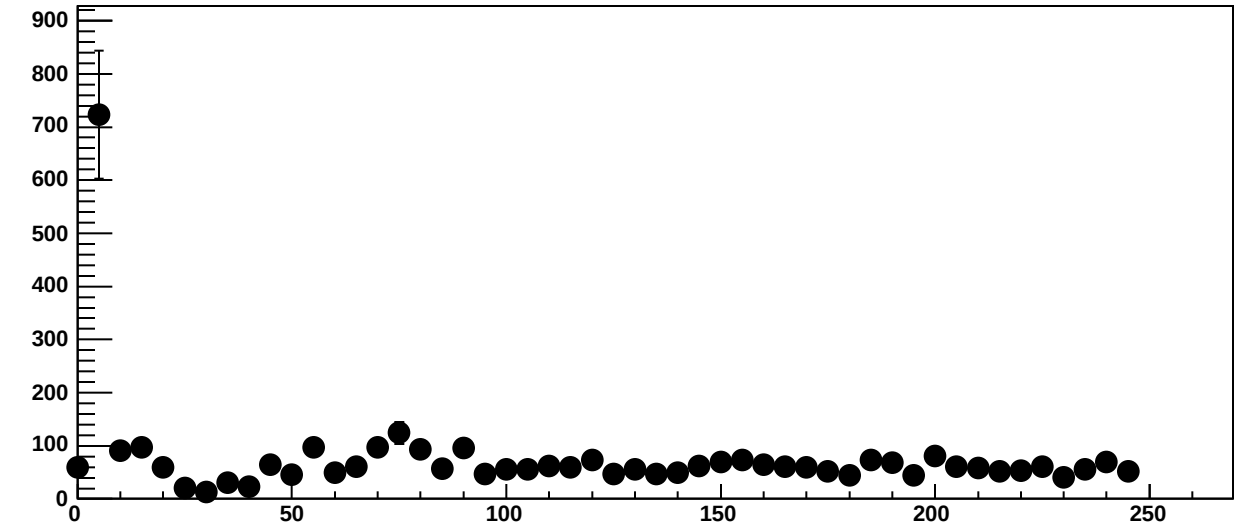
Chip 11, Channel 12, Enable 3, DAC=1600, ADC Residuals vs Hold



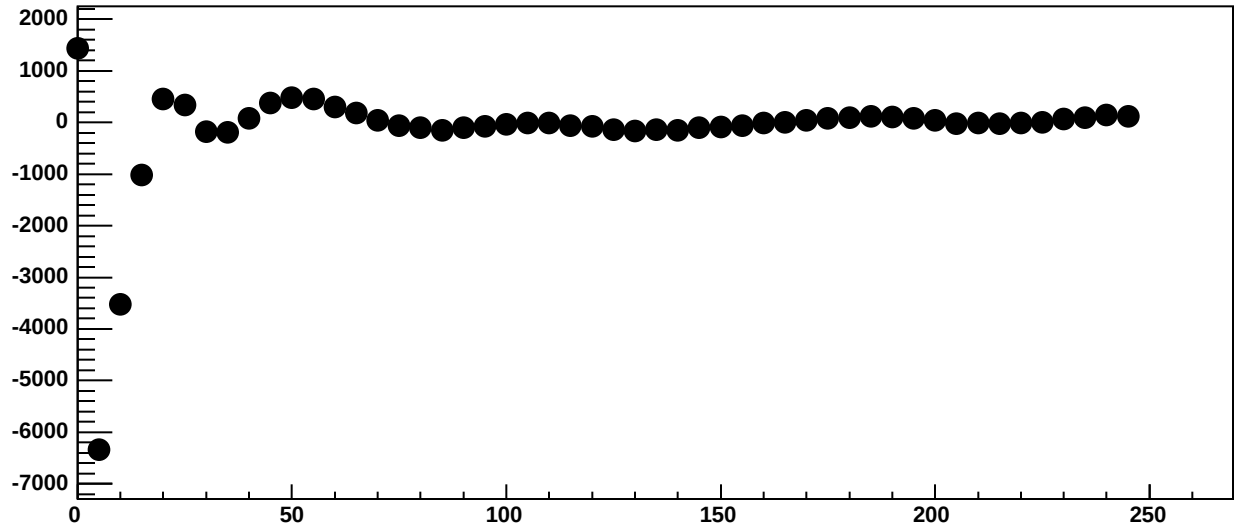
Chip 11, Channel 12, Enable 4!, DAC=1600, ADC Mean vs Hold

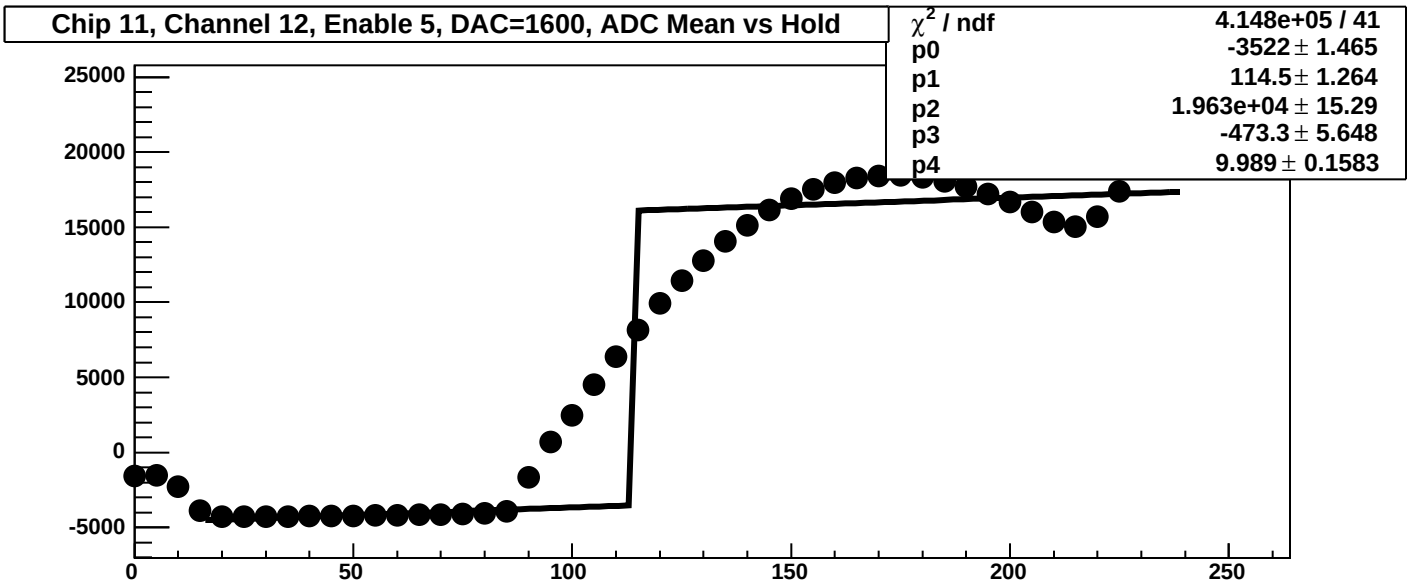


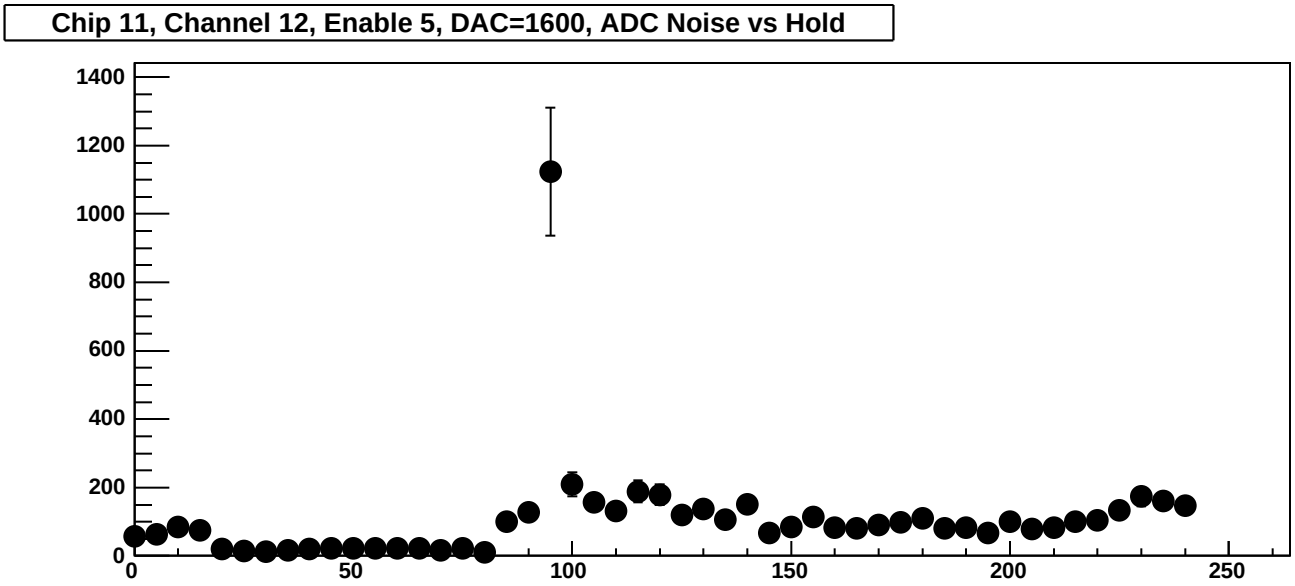
Chip 11, Channel 12, Enable 4!, DAC=1600, ADC Noise vs Hold

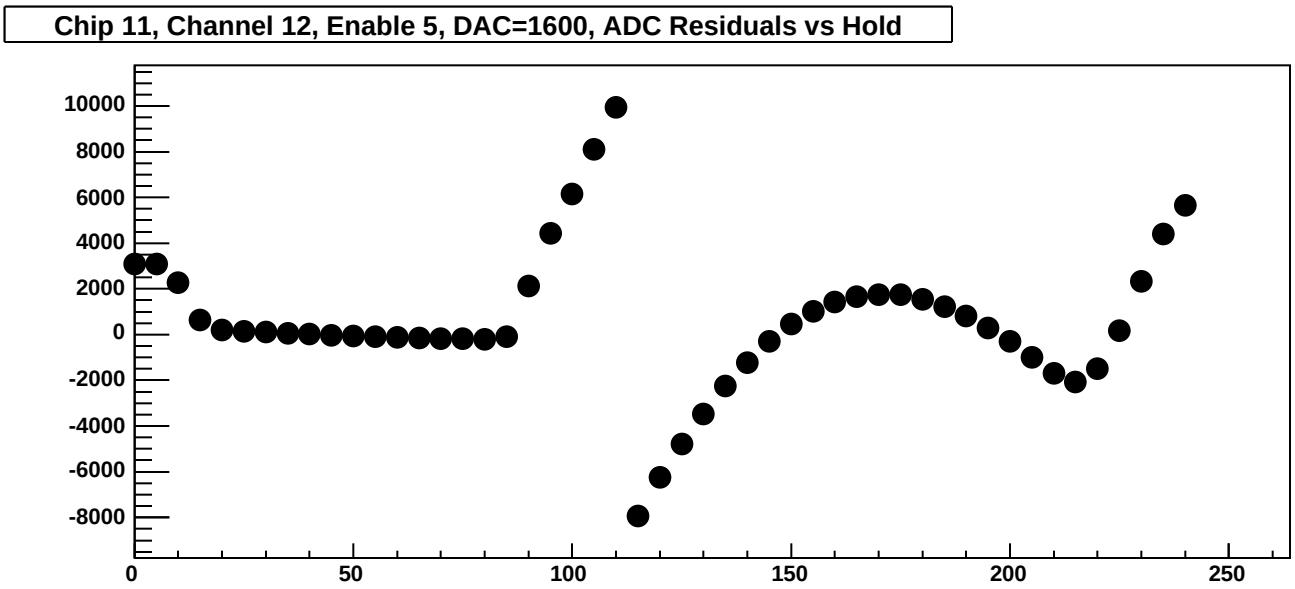


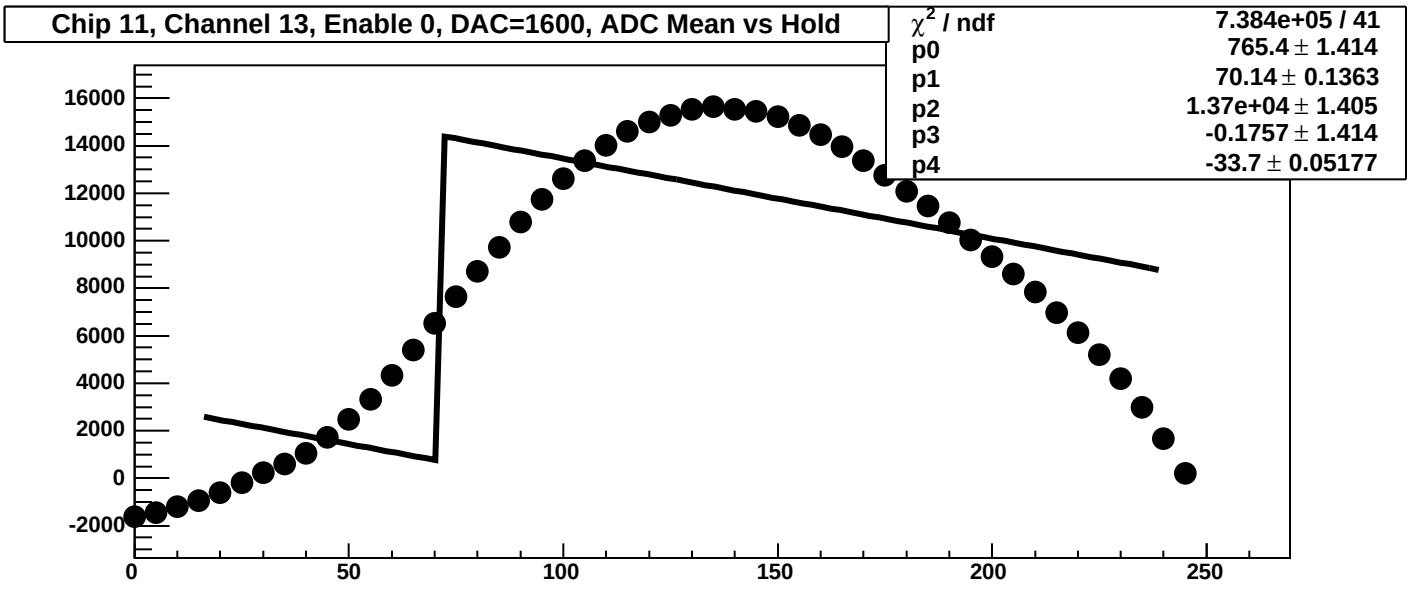
Chip 11, Channel 12, Enable 4!, DAC=1600, ADC Residuals vs Hold

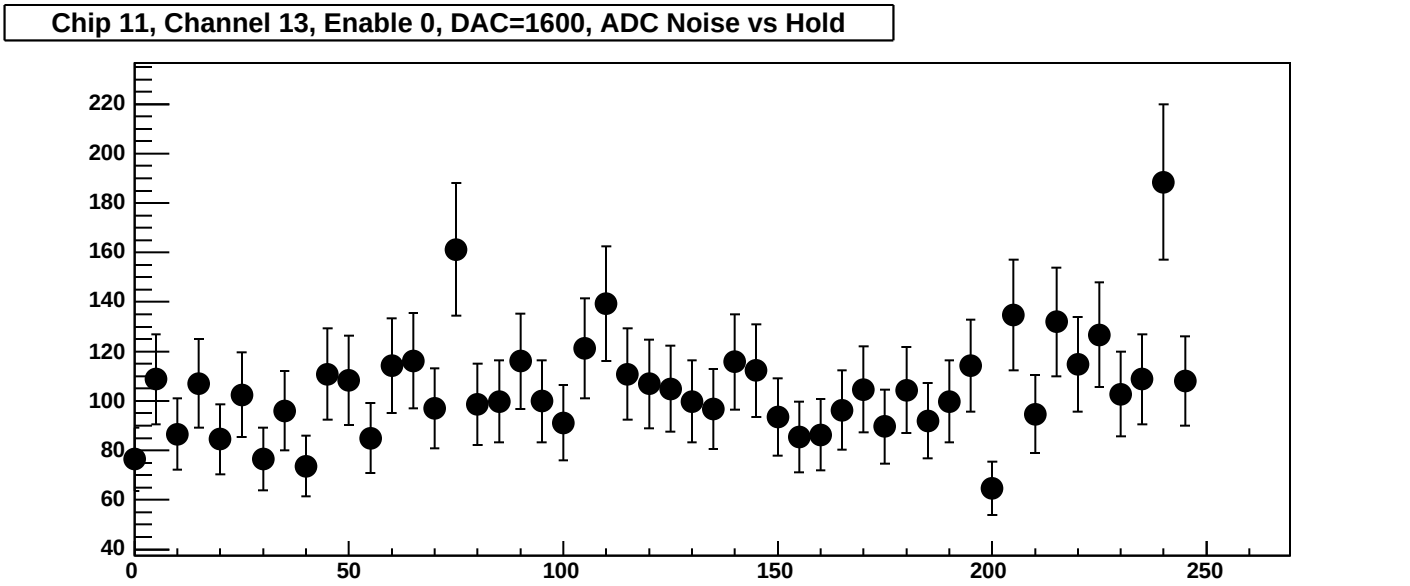


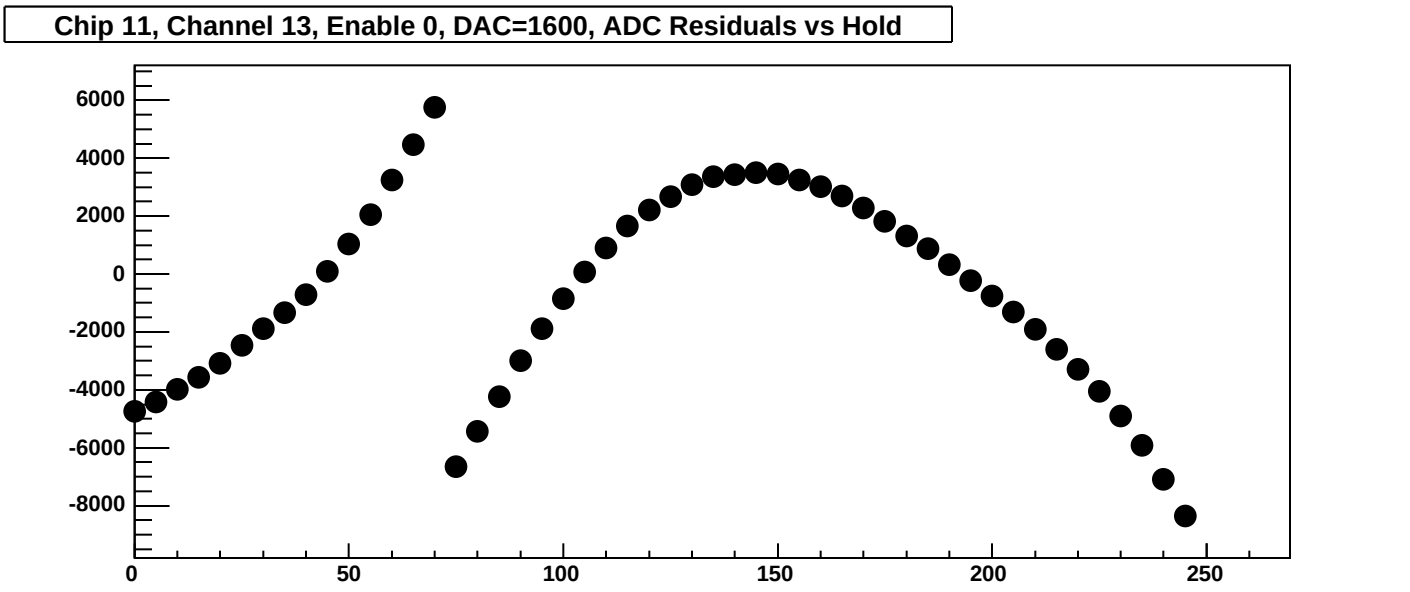

Chip 11, Channel 12, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 4.148e+05 / 41
p0 -3522 ± 1.465
p1 114.5 ± 1.264
p2 1.963e+04 ± 15.29
p3 -473.3 ± 5.648
p4 9.989 ± 0.1583
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

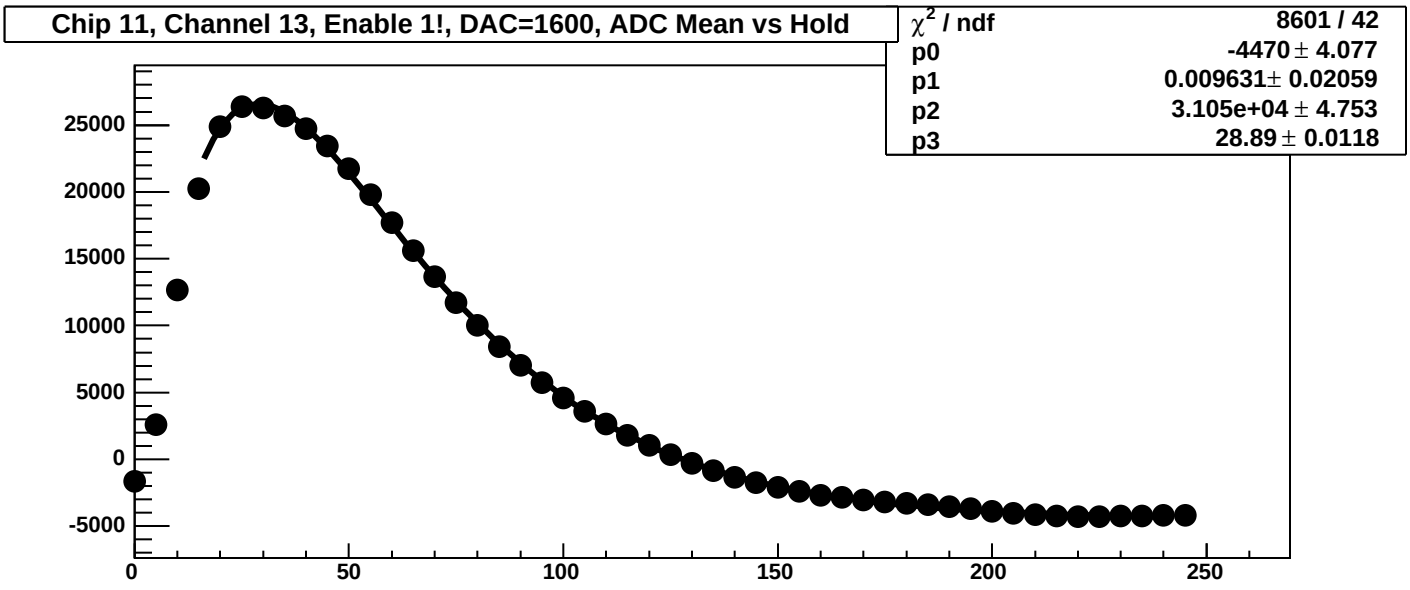

Chip 11, Channel 12, Enable 5, DAC=1600, ADC Noise vs Hold
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

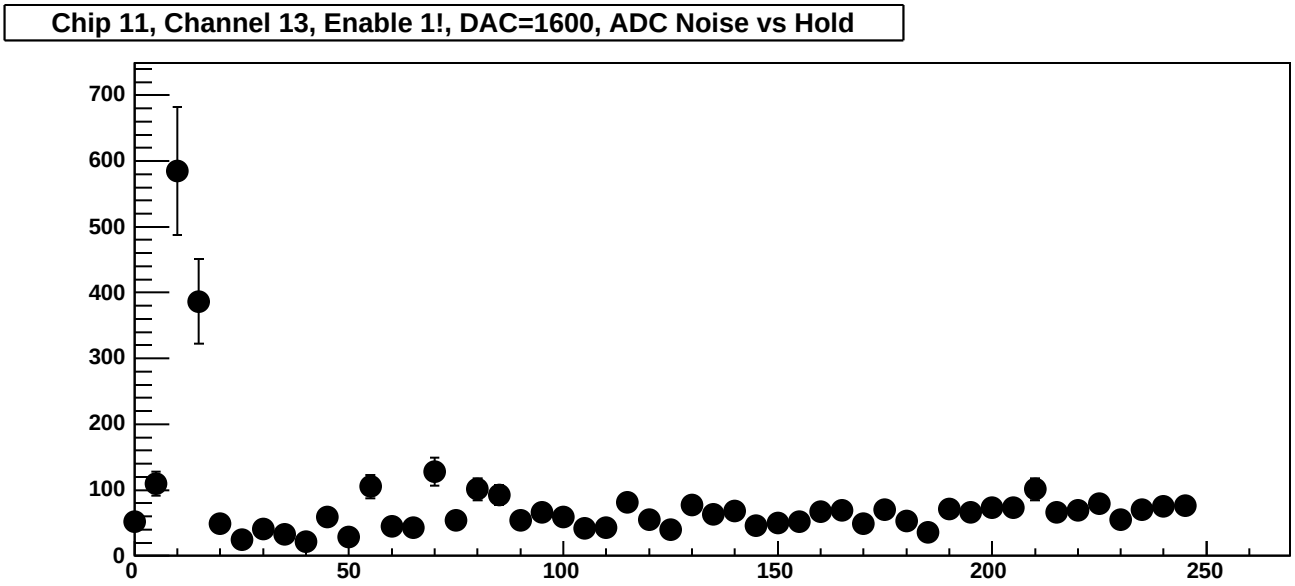

Chip 11, Channel 12, Enable 5, DAC=1600, ADC Residuals vs Hold
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250

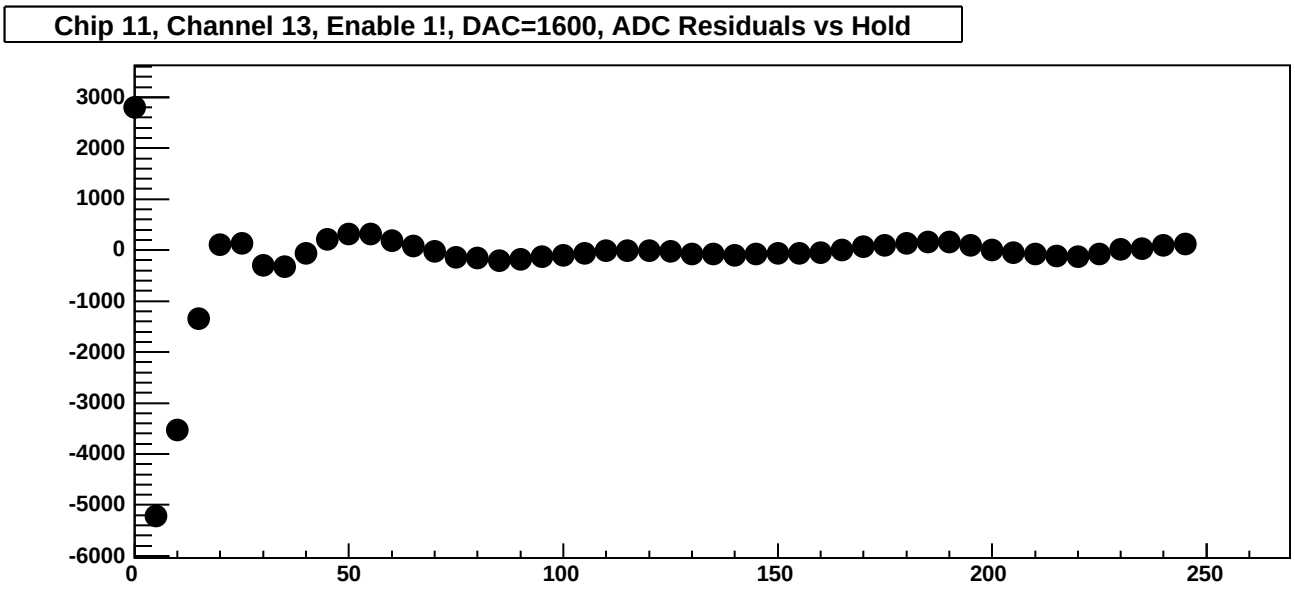

Chip 11, Channel 13, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 7.384e+05 / 41
p0 765.4 ± 1.414
p1 70.14 ± 0.1363
p2 1.37e+04 ± 1.405
p3 -0.1757 ± 1.414
p4 -33.7 ± 0.05177
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250


Chip 11, Channel 13, Enable 0, DAC=1600, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250

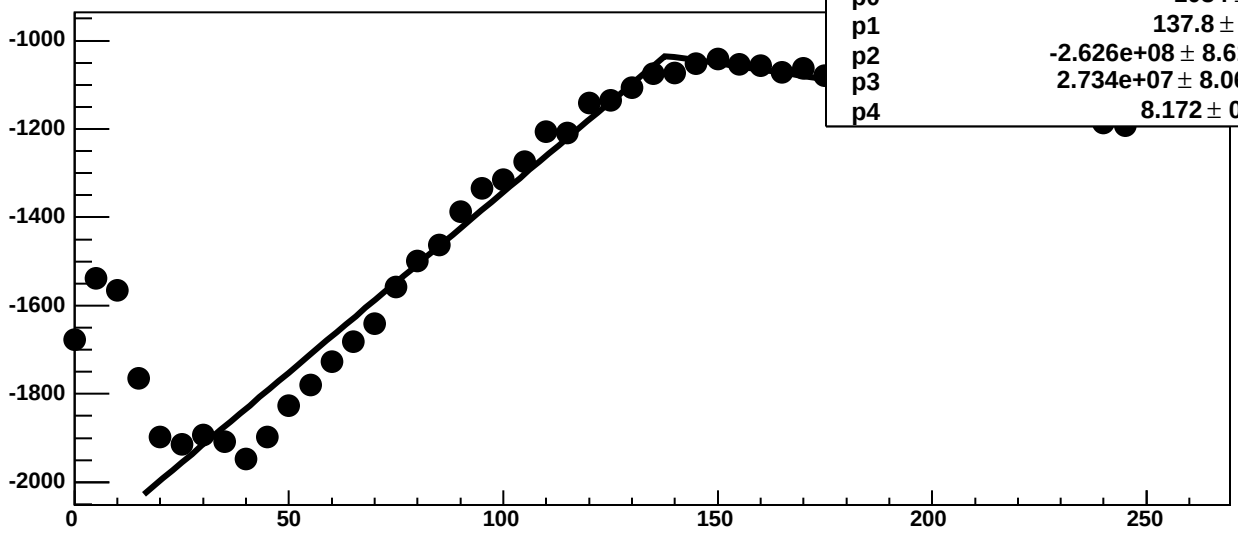

Chip 11, Channel 13, Enable 0, DAC=1600, ADC Residuals vs Hold
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250


Chip 11, Channel 13, Enable 1!, DAC=1600, ADC Mean vs Hold
χ² / ndf 8601 / 42
p0 -4470 ± 4.077
p1 0.009631 ± 0.02059
p2 3.105e+04 ± 4.753
p3 28.89 ± 0.0118
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

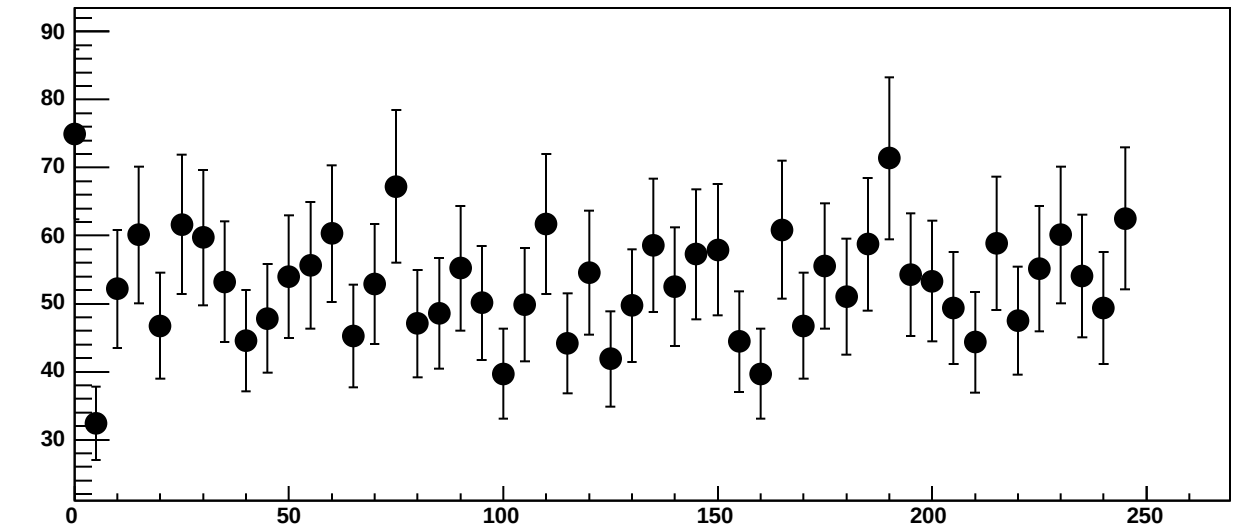

Chip 11, Channel 13, Enable 1!, DAC=1600, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 11, Channel 13, Enable 1!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
50
100
150
200
250

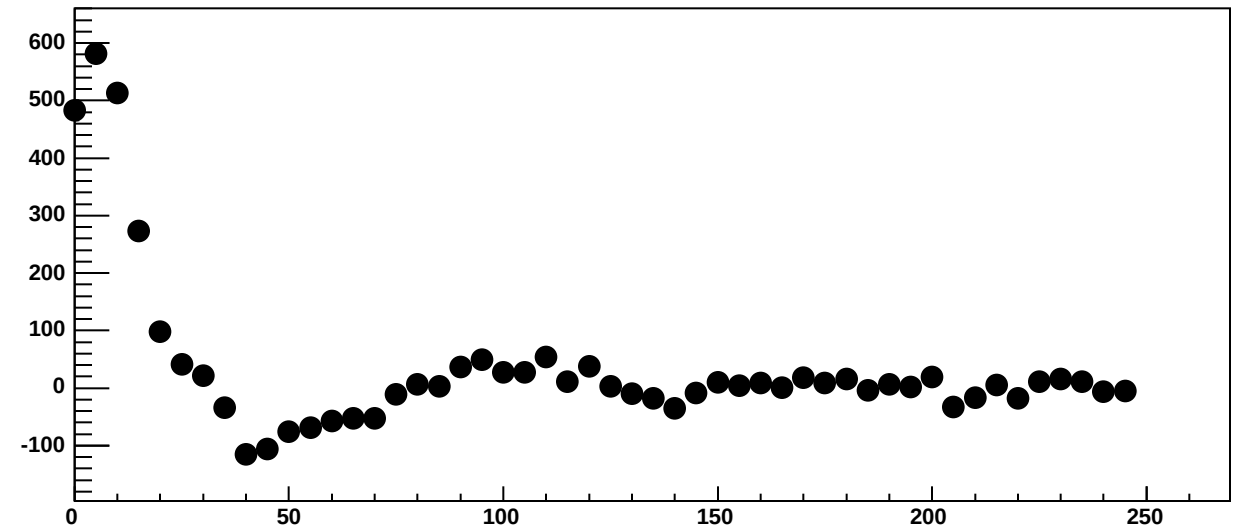
Chip 11, Channel 13, Enable 2, DAC=1600, ADC Mean vs Hold

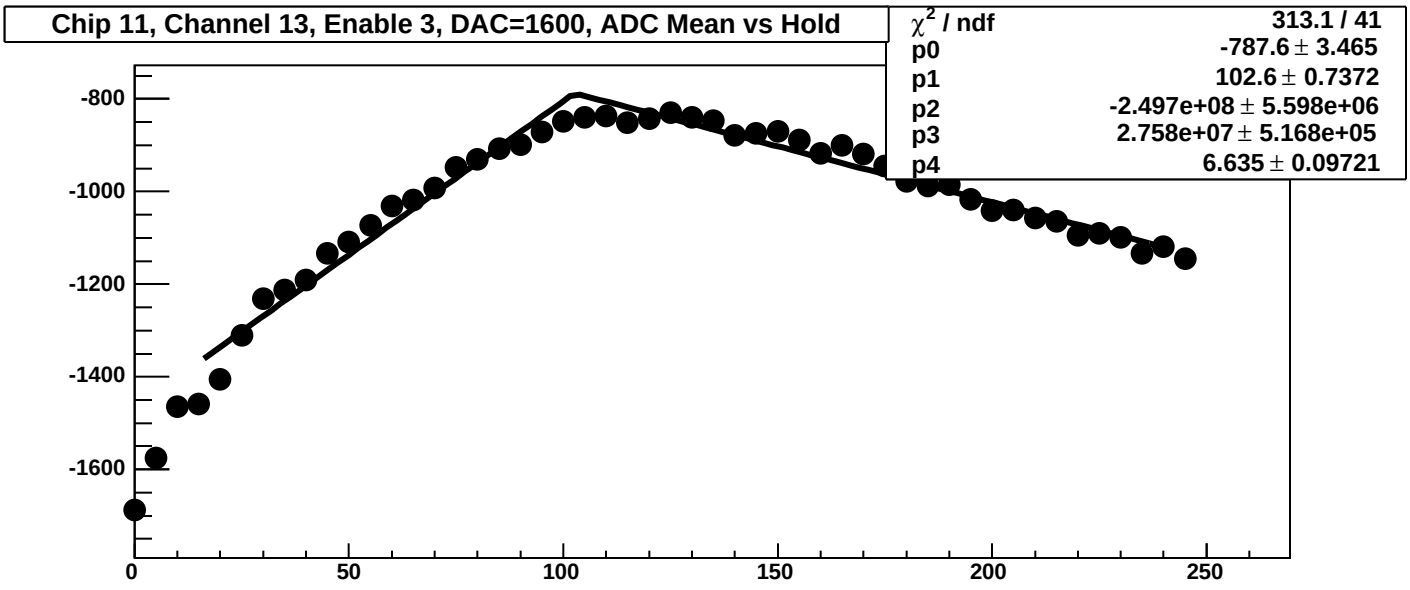


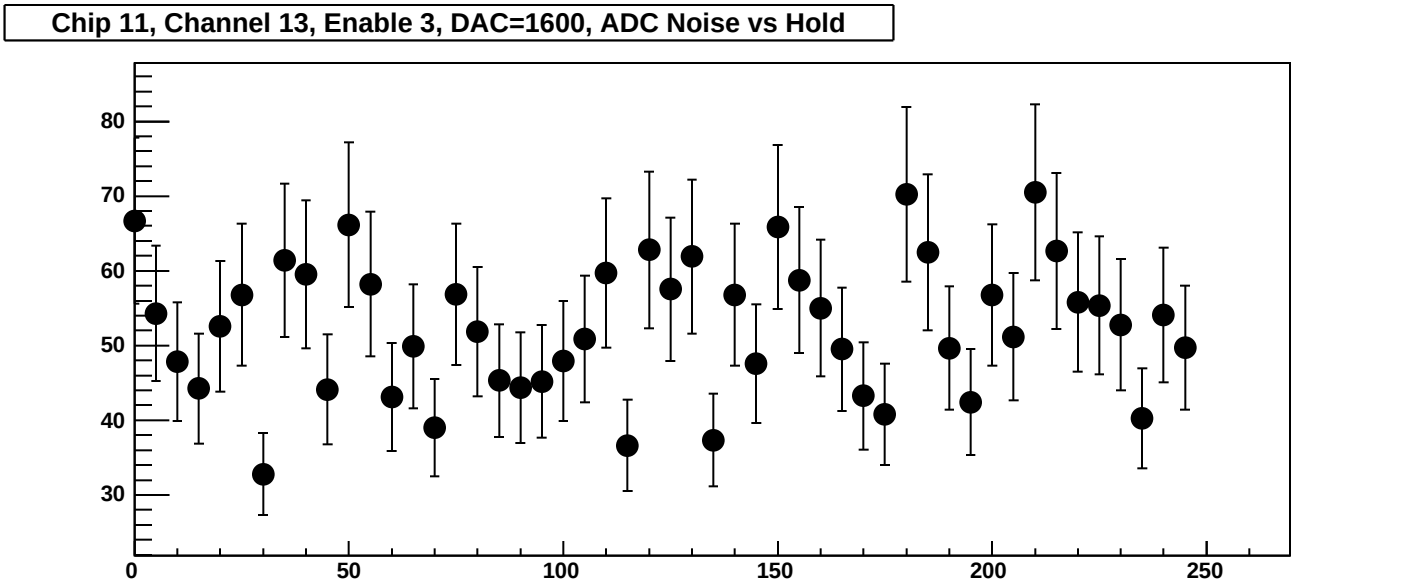
Chip 11, Channel 13, Enable 2, DAC=1600, ADC Noise vs Hold

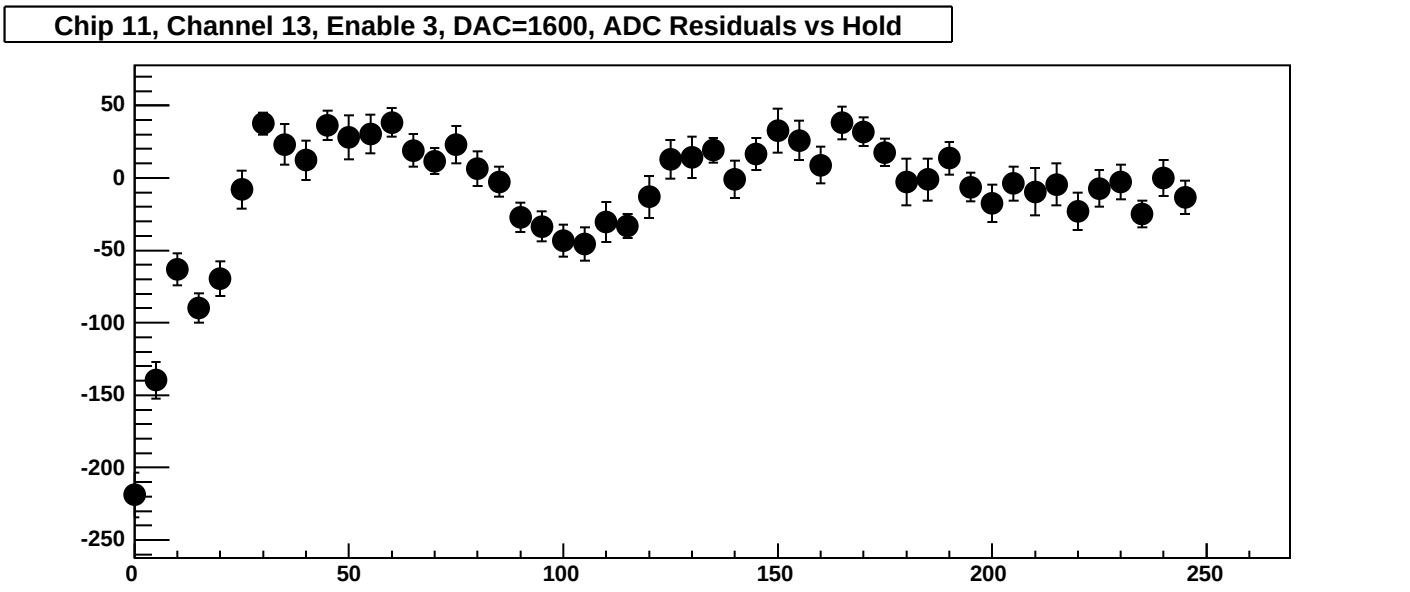


Chip 11, Channel 13, Enable 2, DAC=1600, ADC Residuals vs Hold

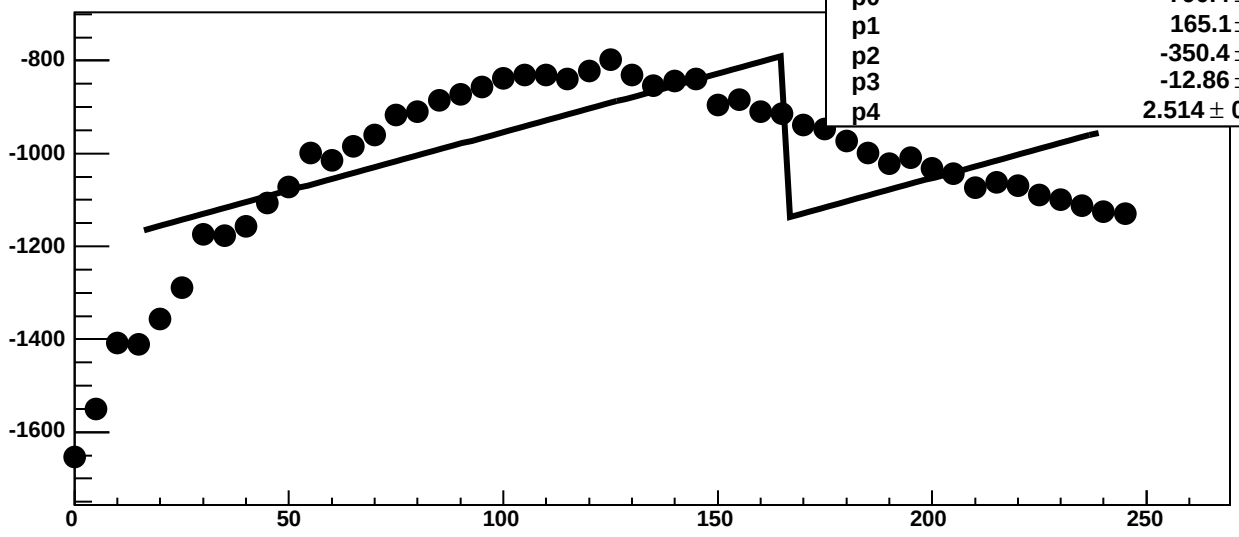



Chip 11, Channel 13, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 313.1 / 41
p0 -787.6 ± 3.465
p1 102.6 ± 0.7372
p2 -2.497e+08 ± 5.598e+06
p3 2.758e+07 ± 5.168e+05
p4 6.635 ± 0.09721
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250

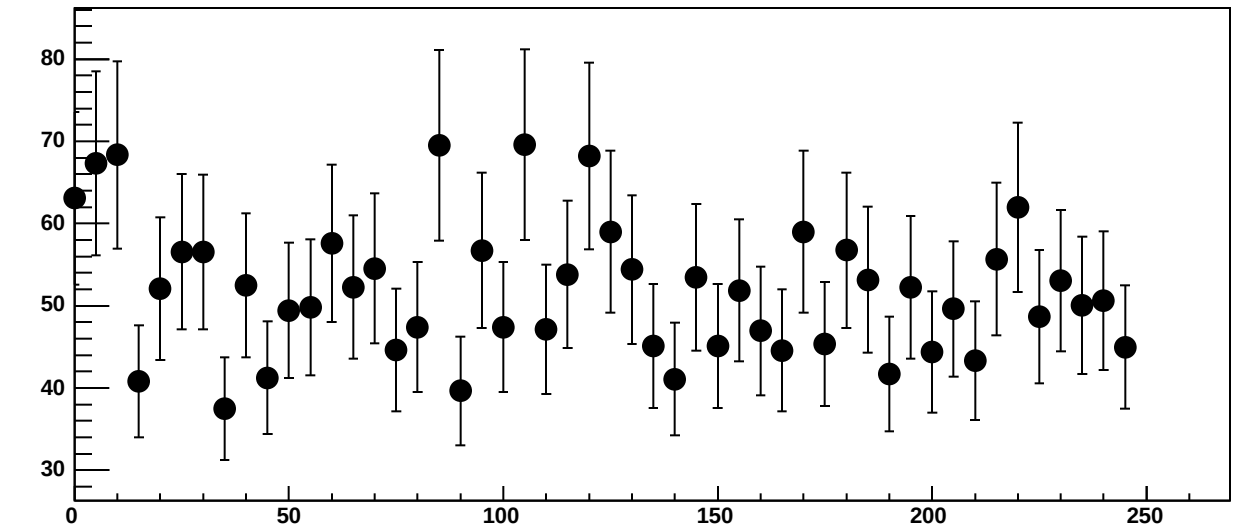

Chip 11, Channel 13, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 11, Channel 13, Enable 3, DAC=1600, ADC Residuals vs Hold
50
0
-50
-100
-150
-200
-250
0
50
100
150
200
250

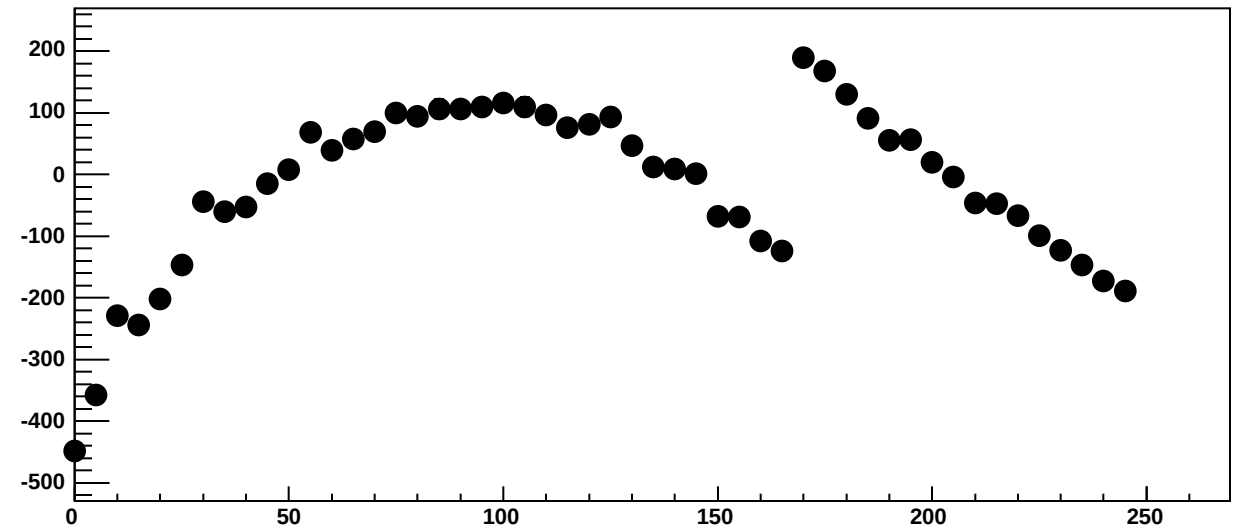
Chip 11, Channel 13, Enable 4, DAC=1600, ADC Mean vs Hold



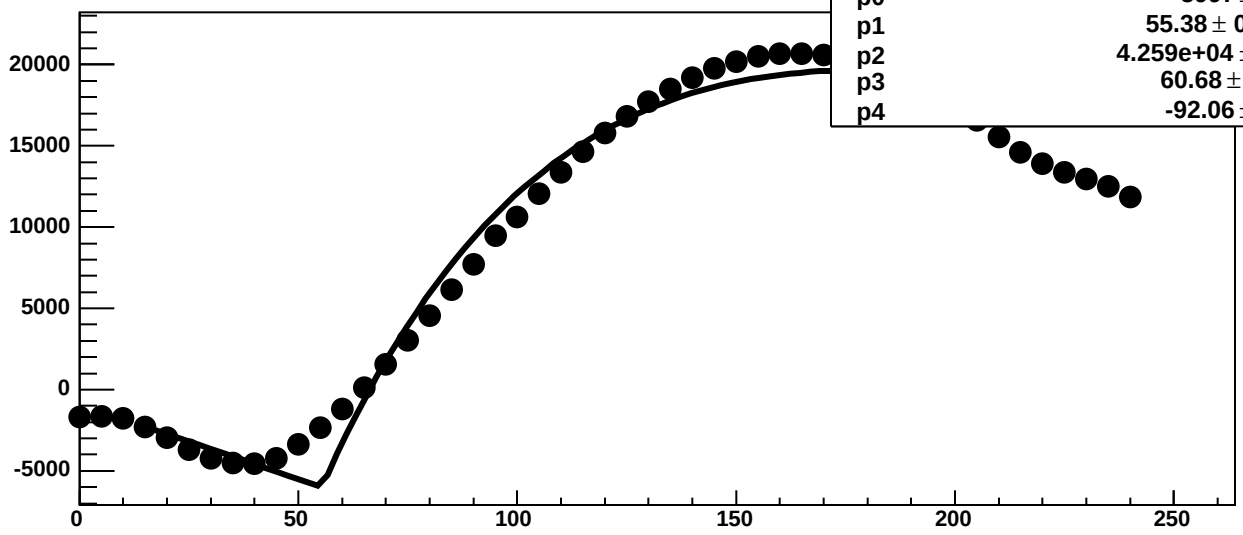
Chip 11, Channel 13, Enable 4, DAC=1600, ADC Noise vs Hold



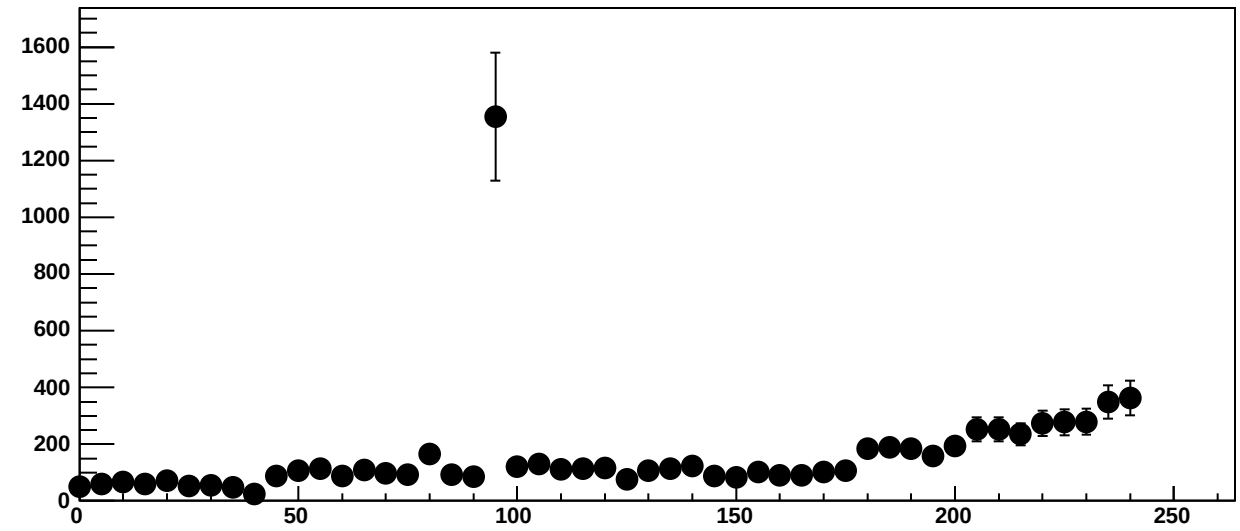
Chip 11, Channel 13, Enable 4, DAC=1600, ADC Residuals vs Hold



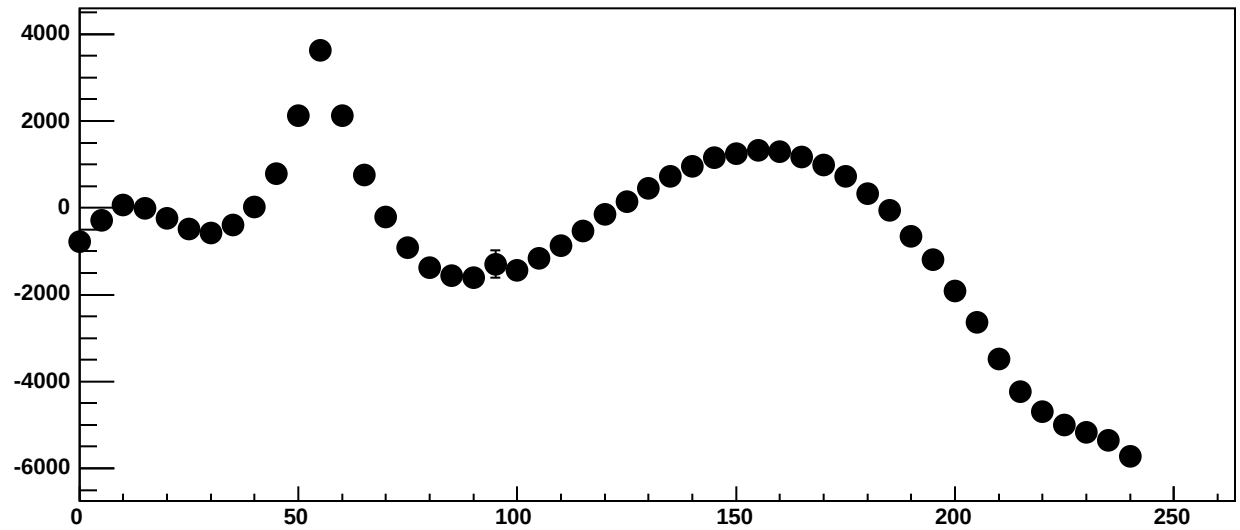
Chip 11, Channel 13, Enable 5, DAC=1600, ADC Mean vs Hold



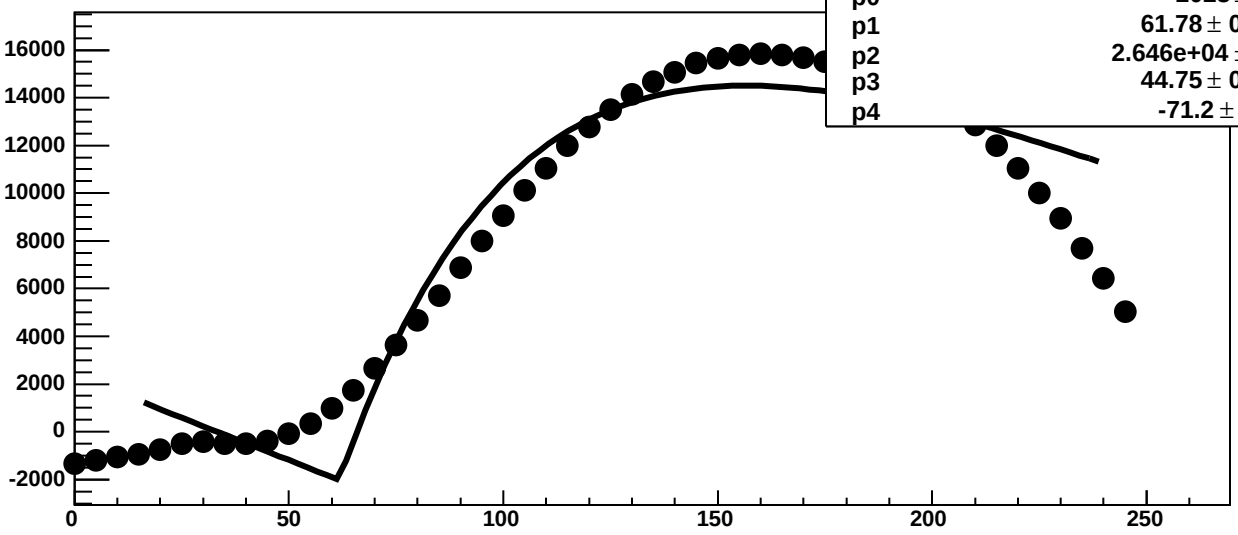
Chip 11, Channel 13, Enable 5, DAC=1600, ADC Noise vs Hold



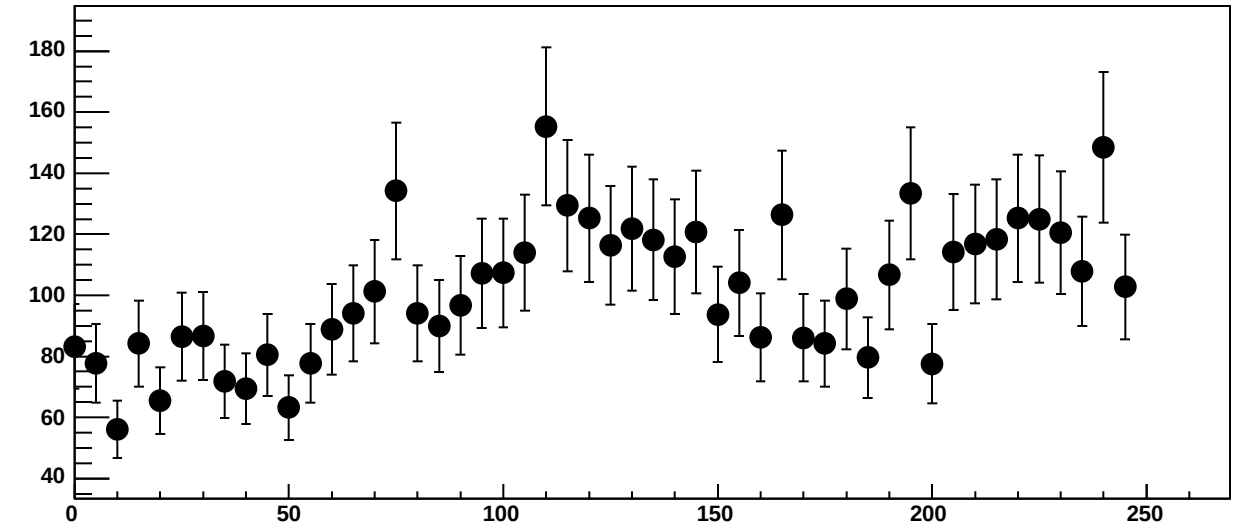
Chip 11, Channel 13, Enable 5, DAC=1600, ADC Residuals vs Hold



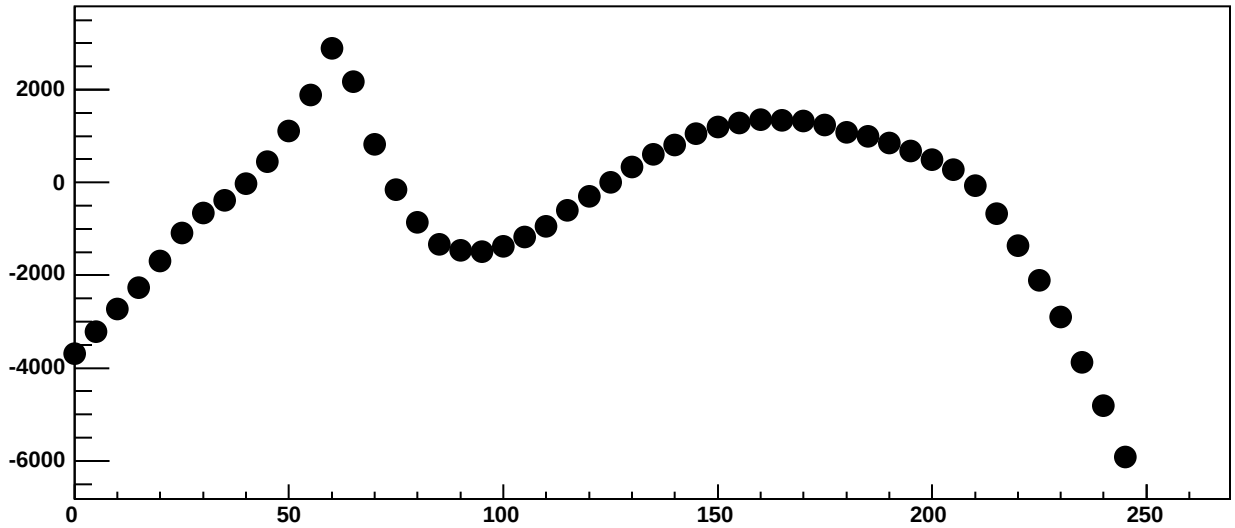
Chip 11, Channel 14, Enable 0, DAC=1600, ADC Mean vs Hold

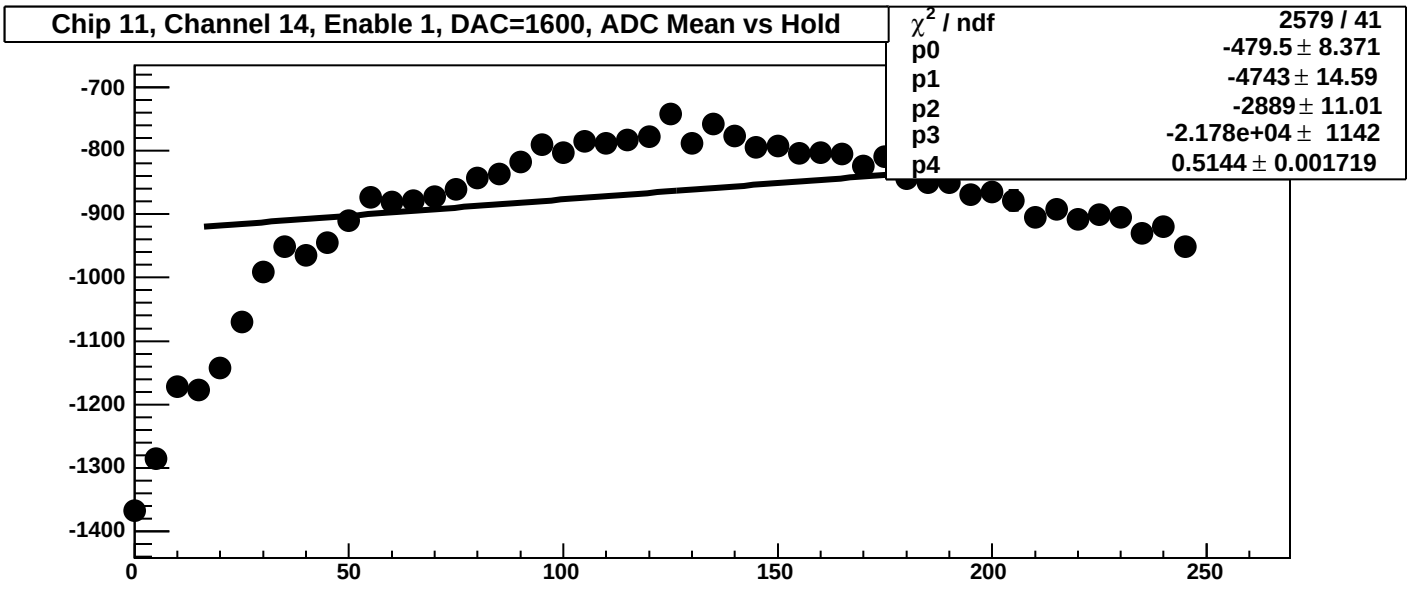


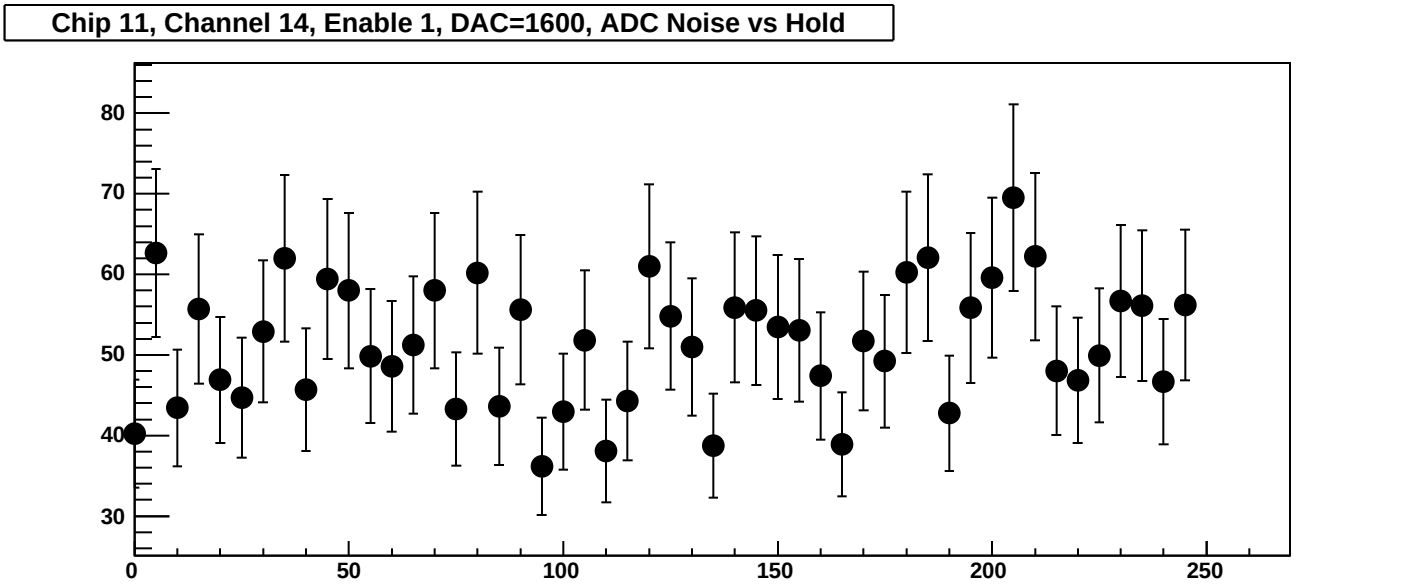
Chip 11, Channel 14, Enable 0, DAC=1600, ADC Noise vs Hold

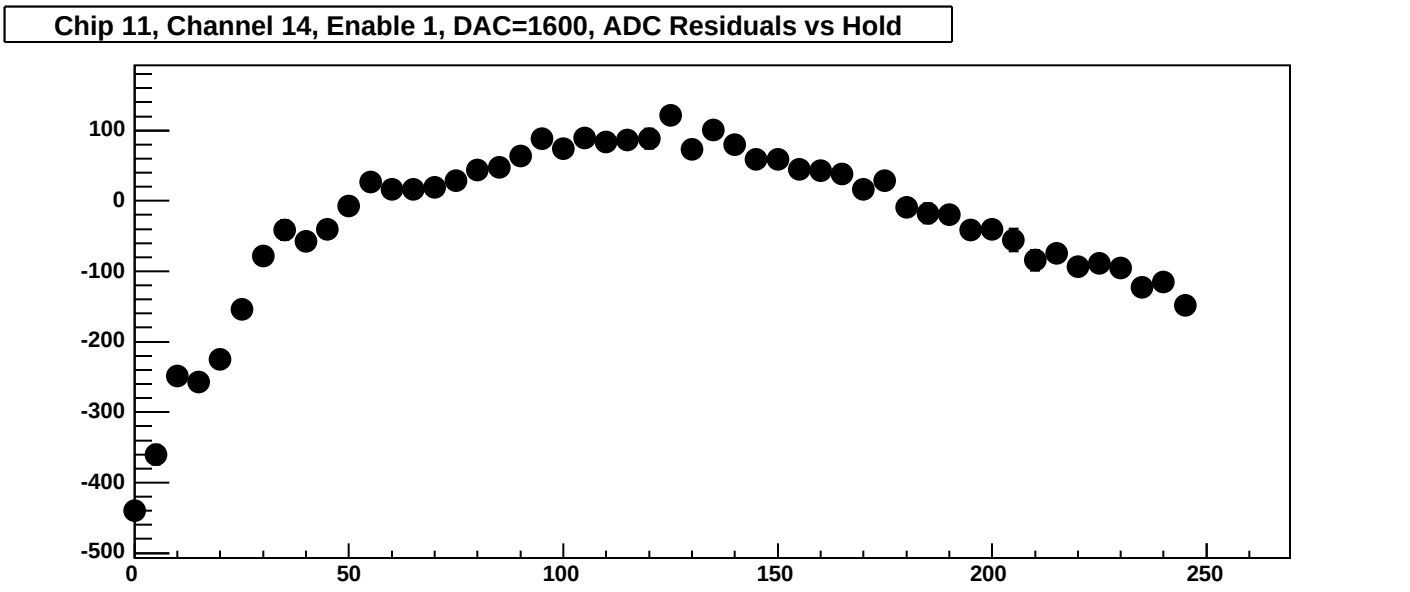


Chip 11, Channel 14, Enable 0, DAC=1600, ADC Residuals vs Hold

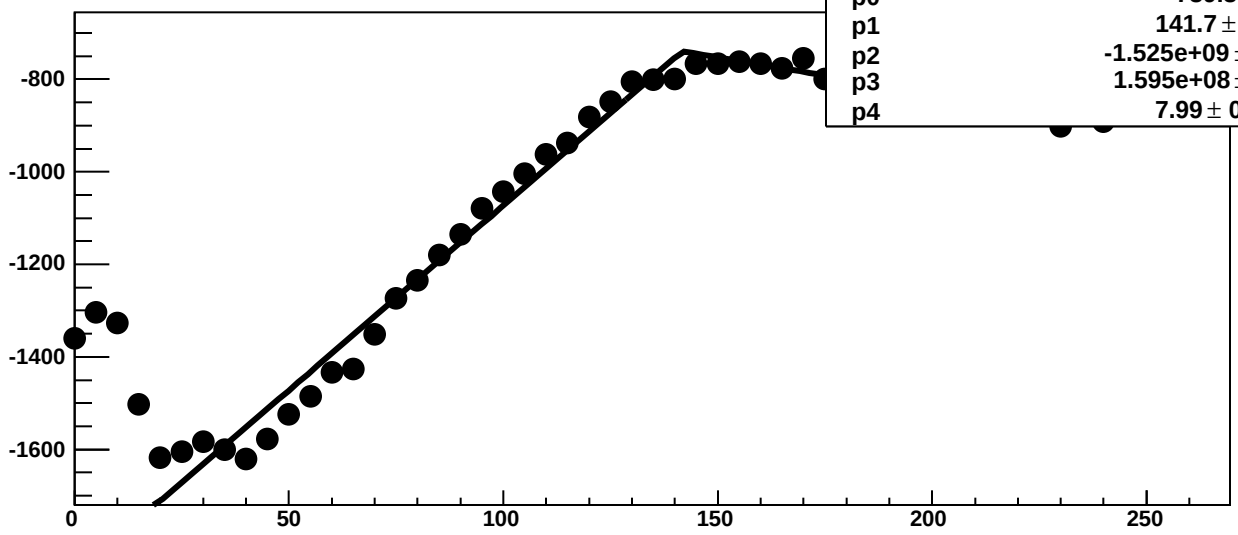



Chip 11, Channel 14, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 2579 / 41
p0 -479.5 ± 8.371
p1 -4743 ± 14.59
p2 -2889 ± 11.01
p3 -2.178e+04 ± 1142
p4 0.5144 ± 0.001719
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
50
100
150
200
250

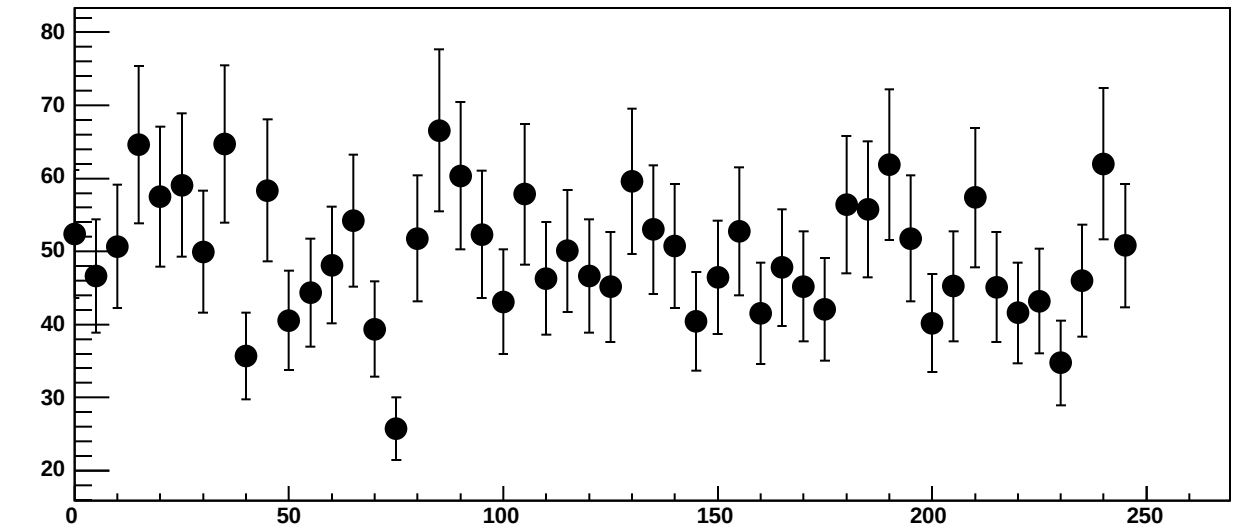

Chip 11, Channel 14, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 11, Channel 14, Enable 1, DAC=1600, ADC Residuals vs Hold
100
0
-100
-200
-300
-400
-500
0
50
100
150
200
250

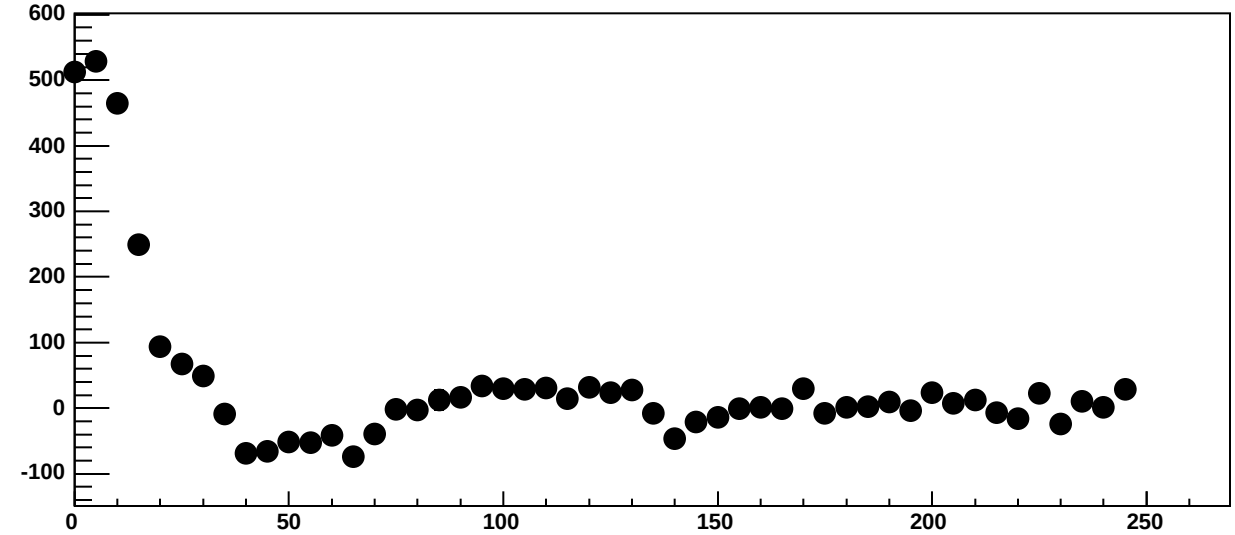
Chip 11, Channel 14, Enable 2, DAC=1600, ADC Mean vs Hold

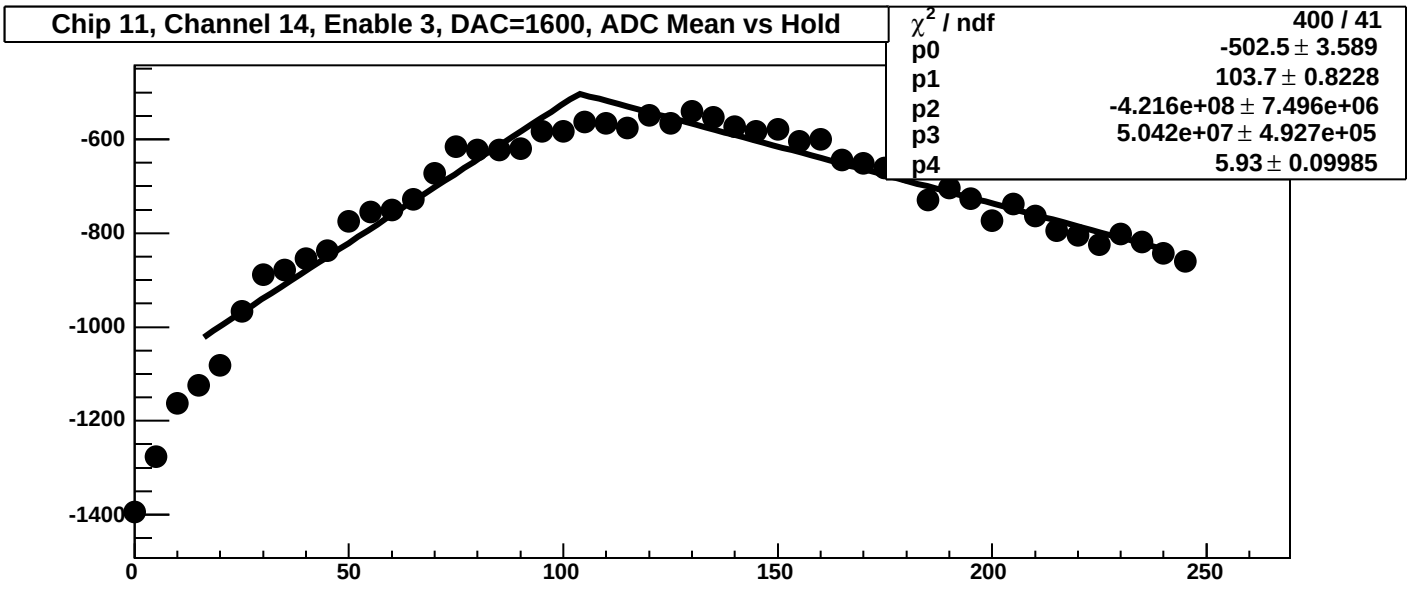


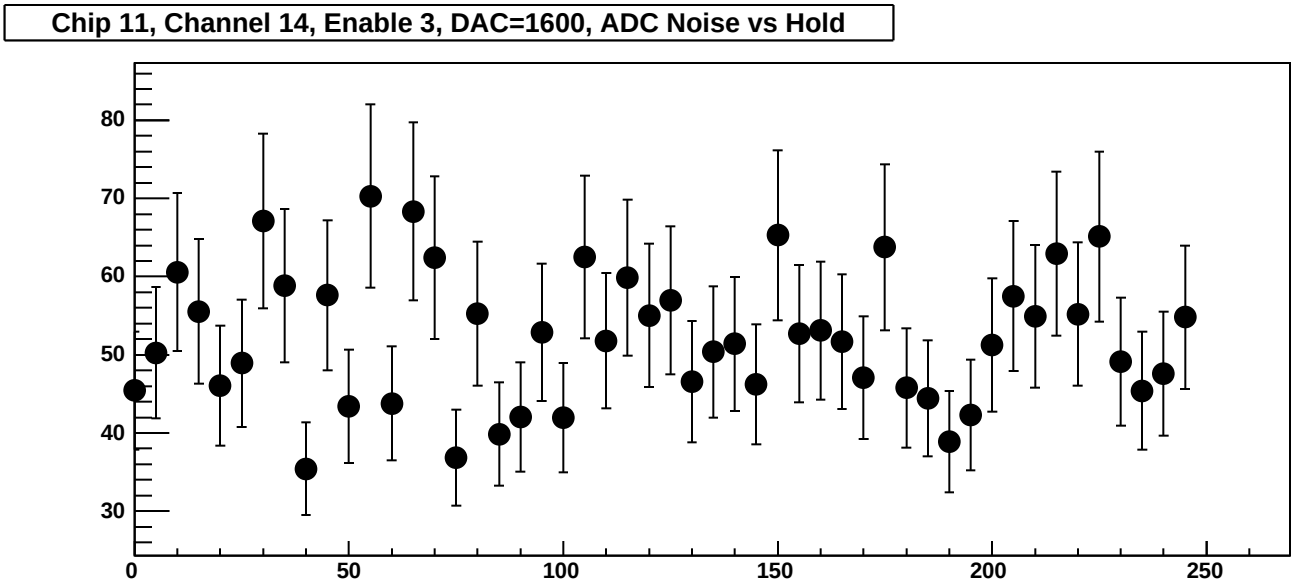
Chip 11, Channel 14, Enable 2, DAC=1600, ADC Noise vs Hold

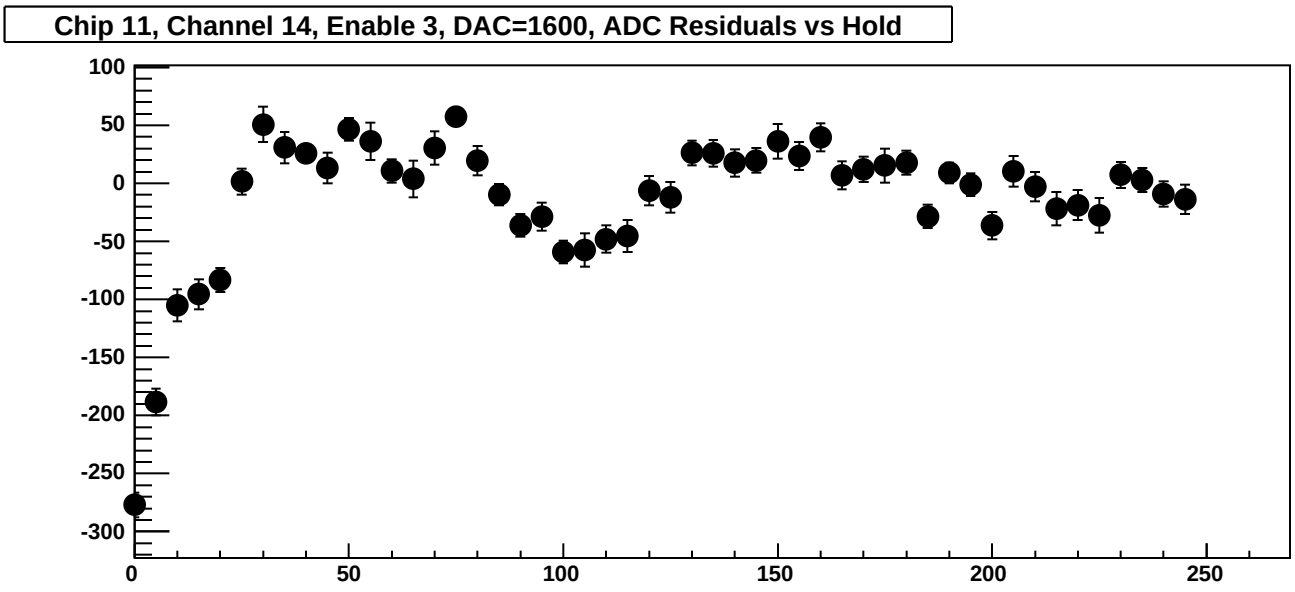


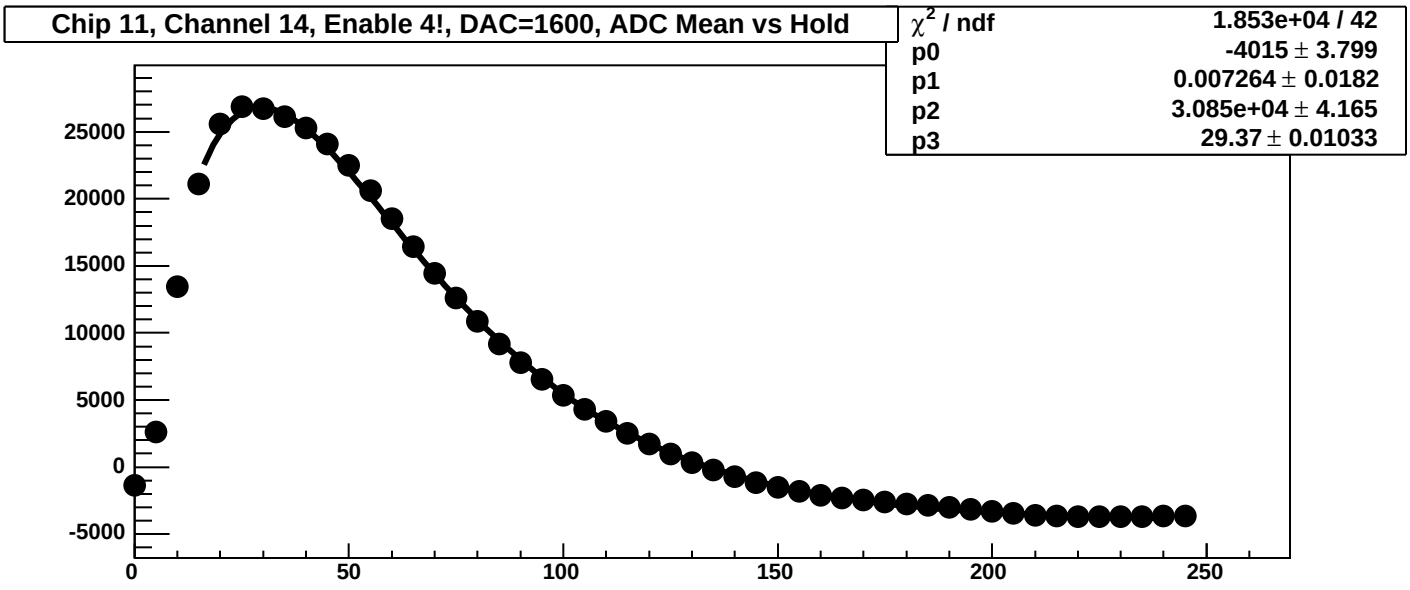
Chip 11, Channel 14, Enable 2, DAC=1600, ADC Residuals vs Hold

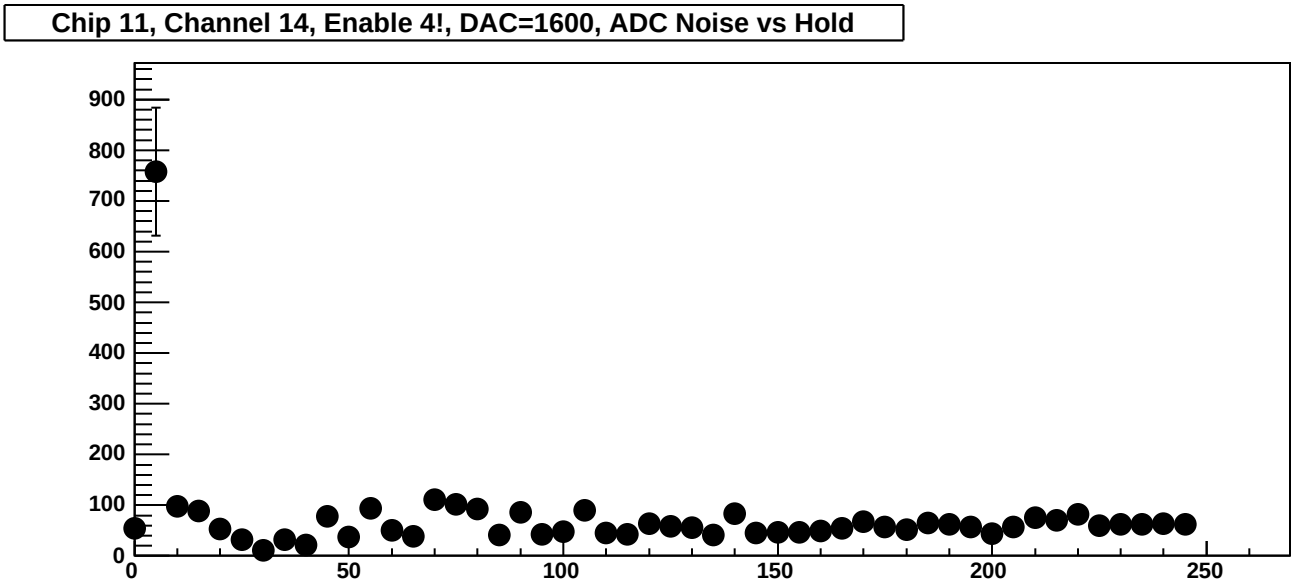


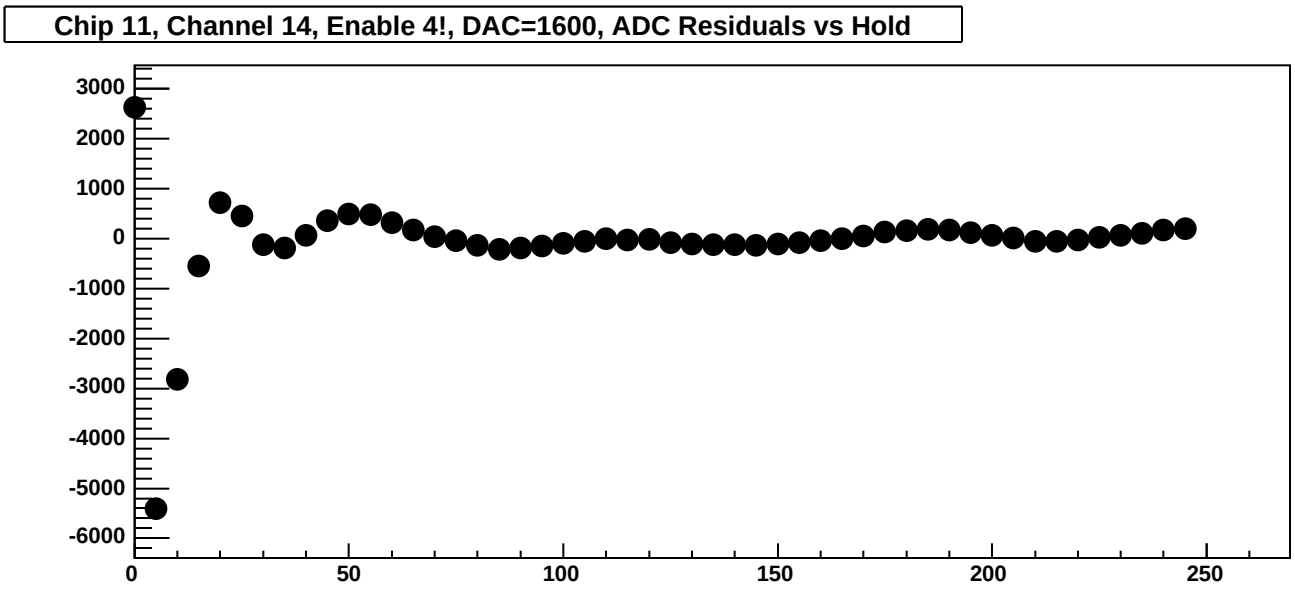

Chip 11, Channel 14, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 400 / 41
p0 -502.5 ± 3.589
p1 103.7 ± 0.8228
p2 -4.216e+08 ± 7.496e+06
p3 5.042e+07 ± 4.927e+05
p4 5.93 ± 0.09985
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

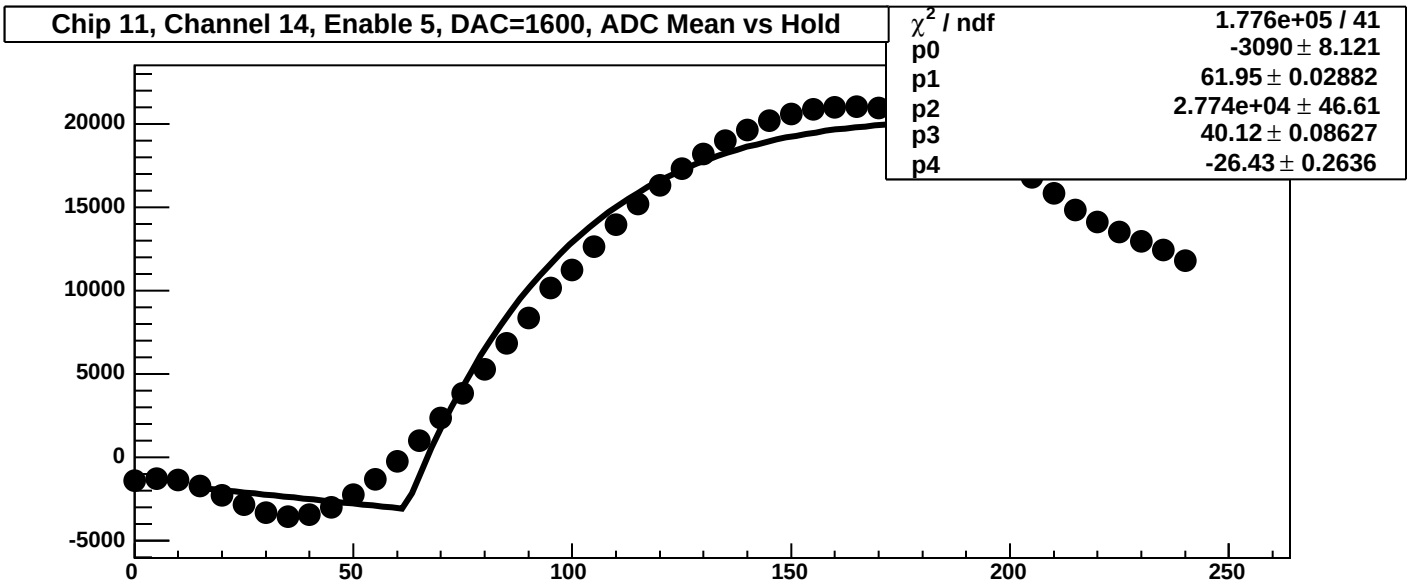

Chip 11, Channel 14, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250

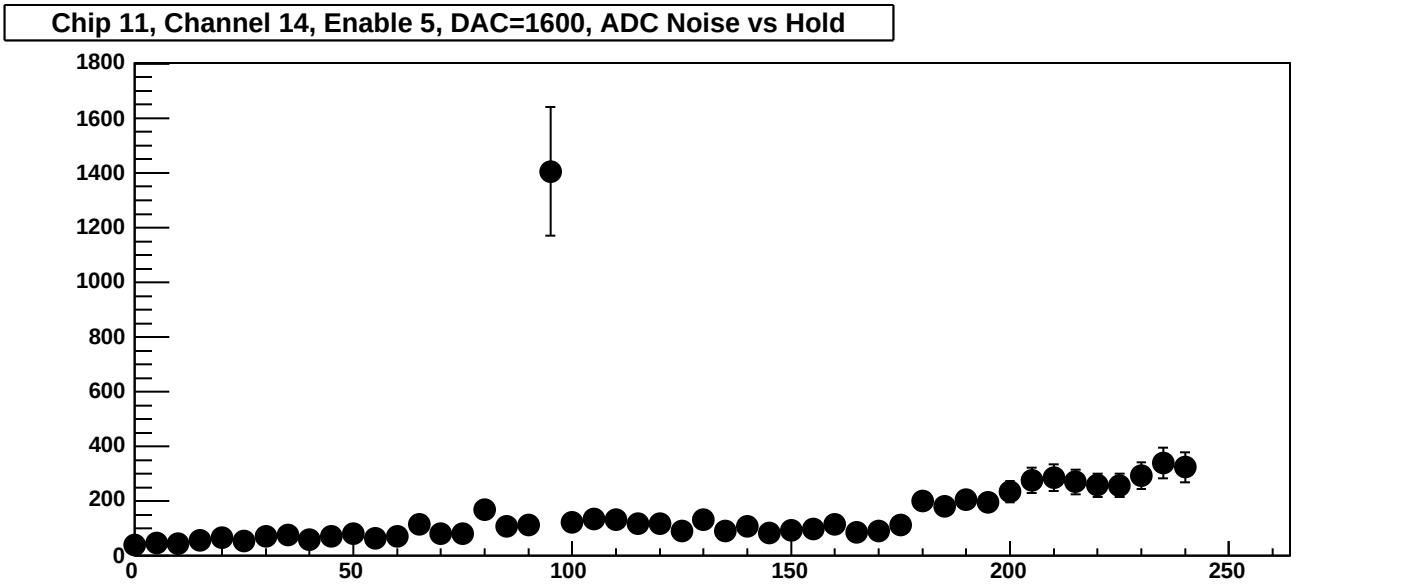

Chip 11, Channel 14, Enable 3, DAC=1600, ADC Residuals vs Hold
100
50
0
-50
-100
-150
-200
-250
-300
0
50
100
150
200
250

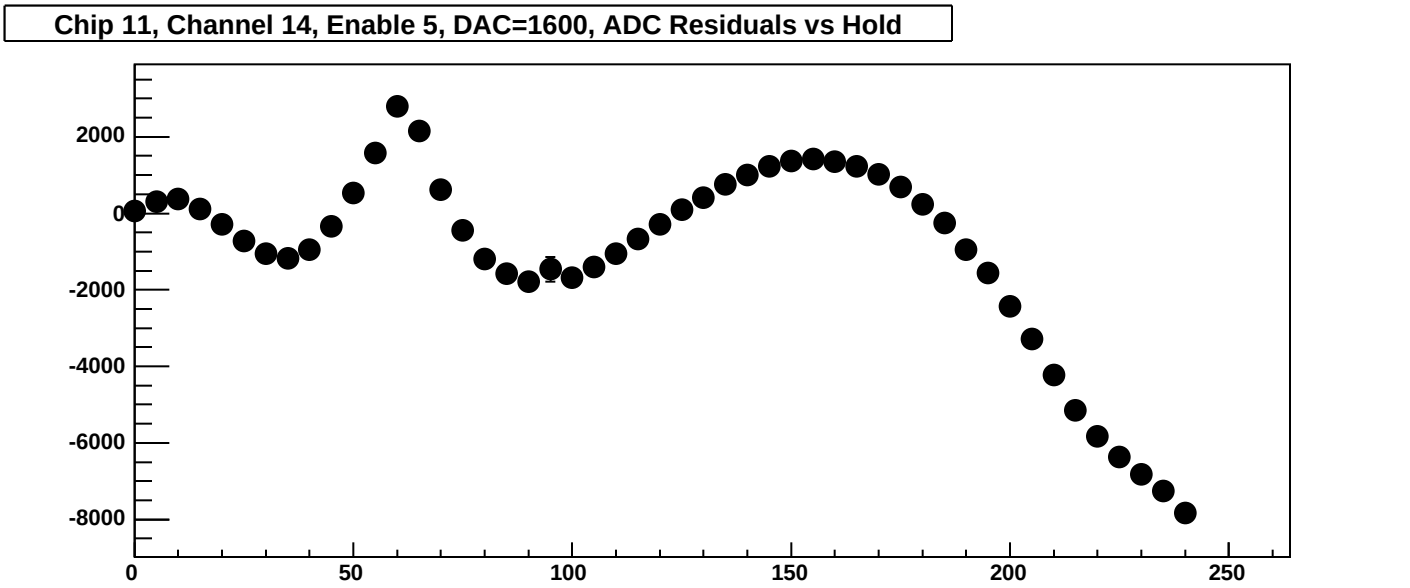

Chip 11, Channel 14, Enable 4!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.853e+04 / 42
p0 -4015 ± 3.799
p1 0.007264 ± 0.0182
p2 3.085e+04 ± 4.165
p3 29.37 ± 0.01033
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250


Chip 11, Channel 14, Enable 4!, DAC=1600, ADC Noise vs Hold
900
800
700
600
500
400
300
200
100
0
0
50
100
150
200
250

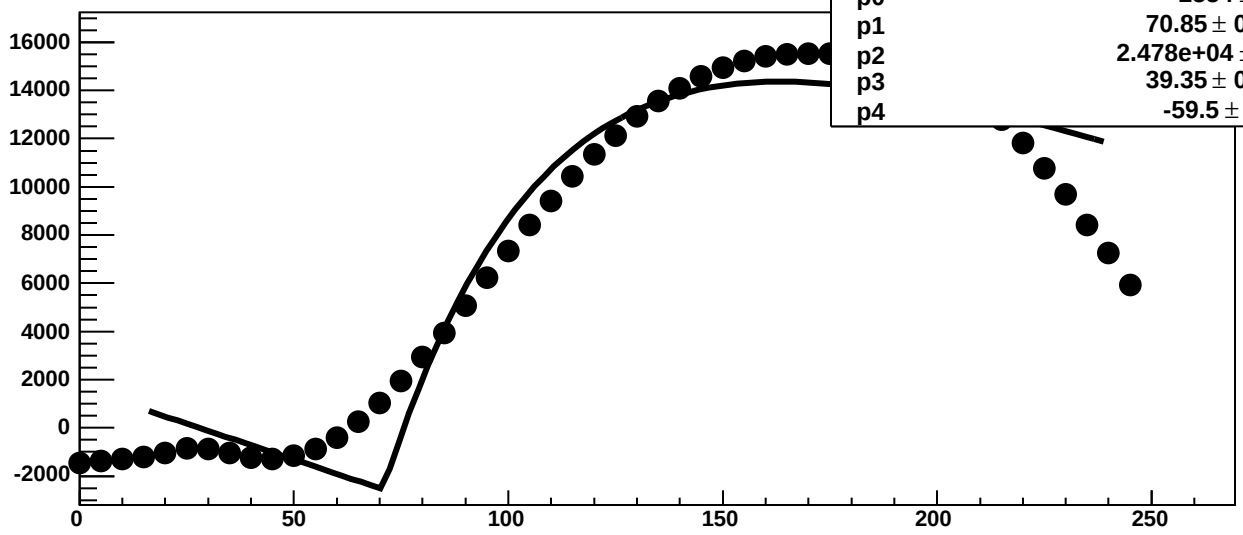

Chip 11, Channel 14, Enable 4!, DAC=1600, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
50
100
150
200
250


Chip 11, Channel 14, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.776e+05 / 41
p0 -3090 ± 8.121
p1 61.95 ± 0.02882
p2 2.774e+04 ± 46.61
p3 40.12 ± 0.08627
p4 -26.43 ± 0.2636
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

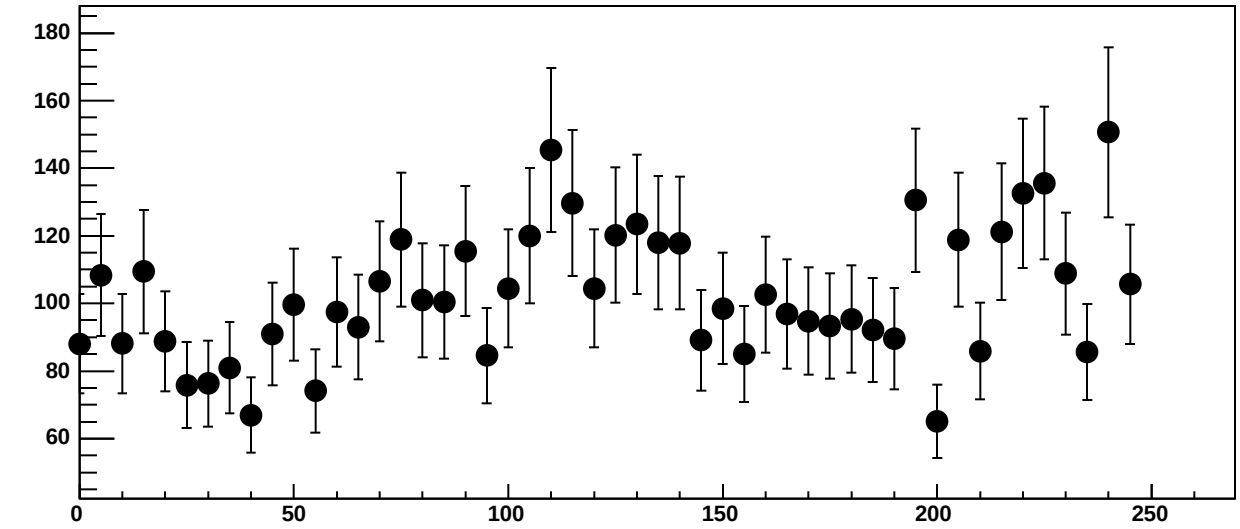

Chip 11, Channel 14, Enable 5, DAC=1600, ADC Noise vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 11, Channel 14, Enable 5, DAC=1600, ADC Residuals vs Hold
2000
0
-2000
-4000
-6000
-8000
0
50
100
150
200
250

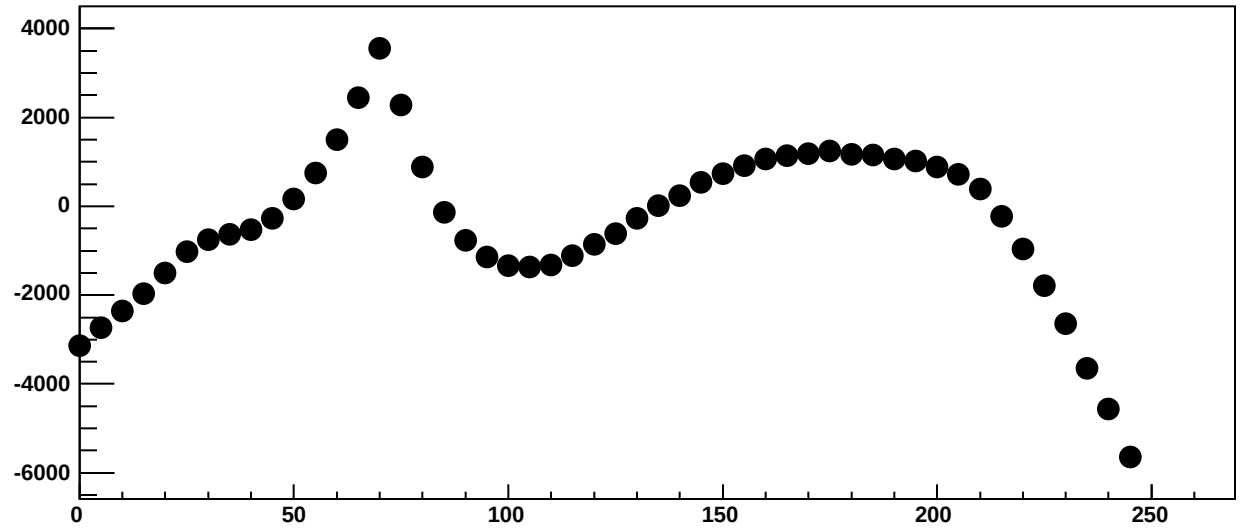
Chip 11, Channel 15, Enable 0, DAC=1600, ADC Mean vs Hold

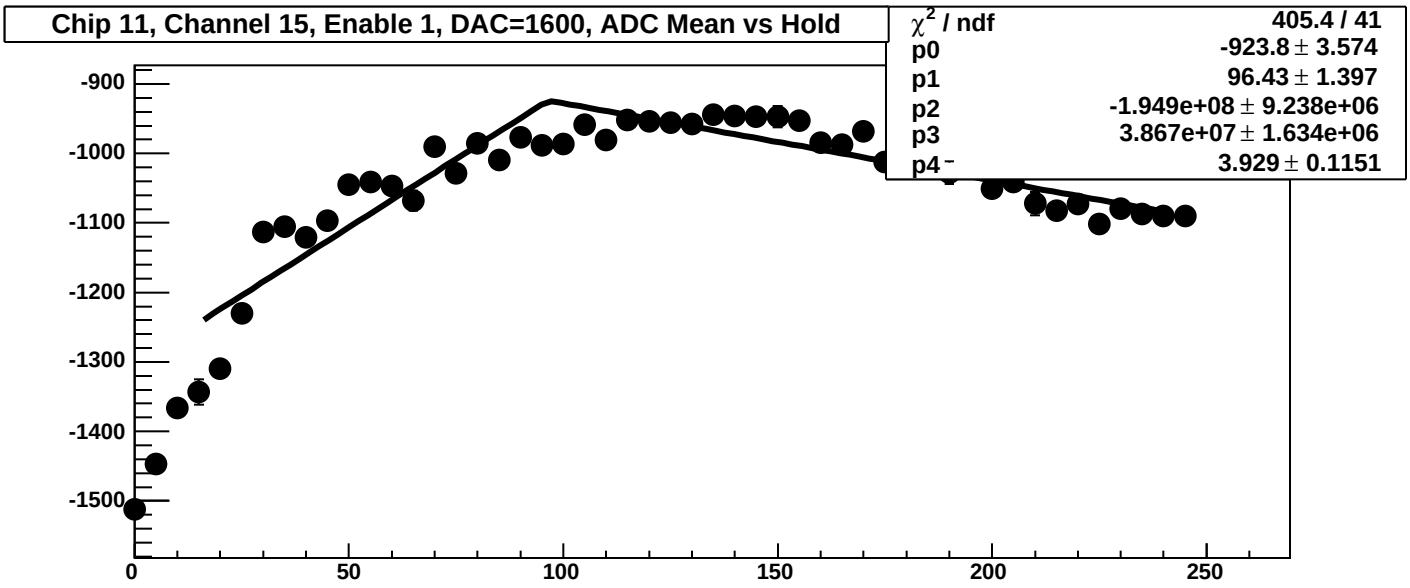


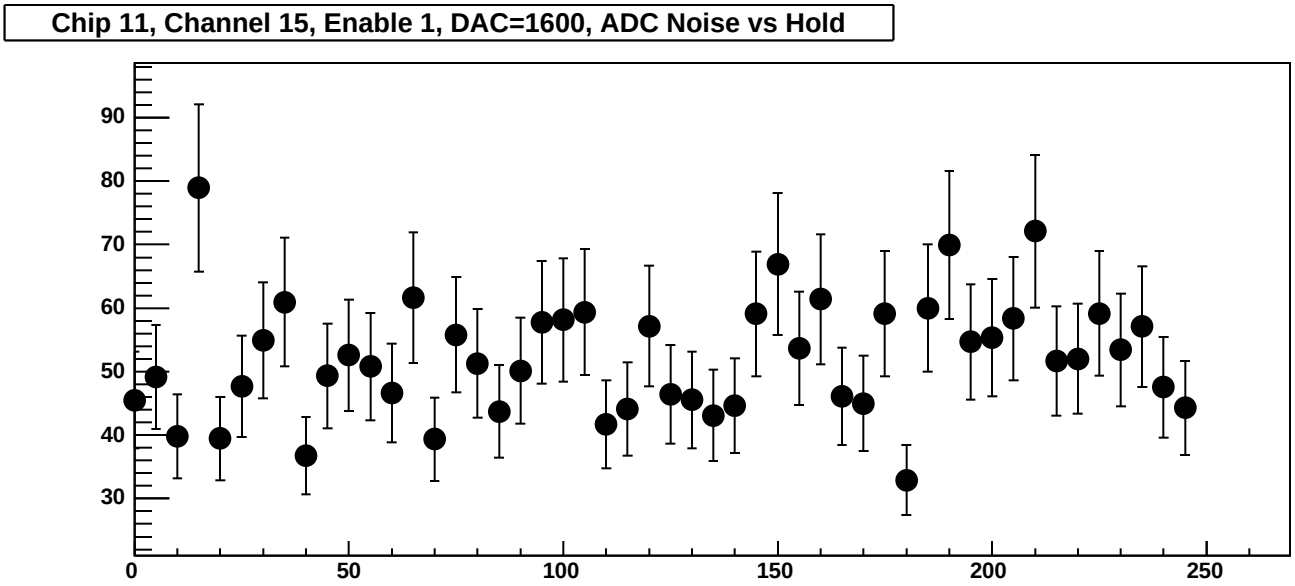
Chip 11, Channel 15, Enable 0, DAC=1600, ADC Noise vs Hold

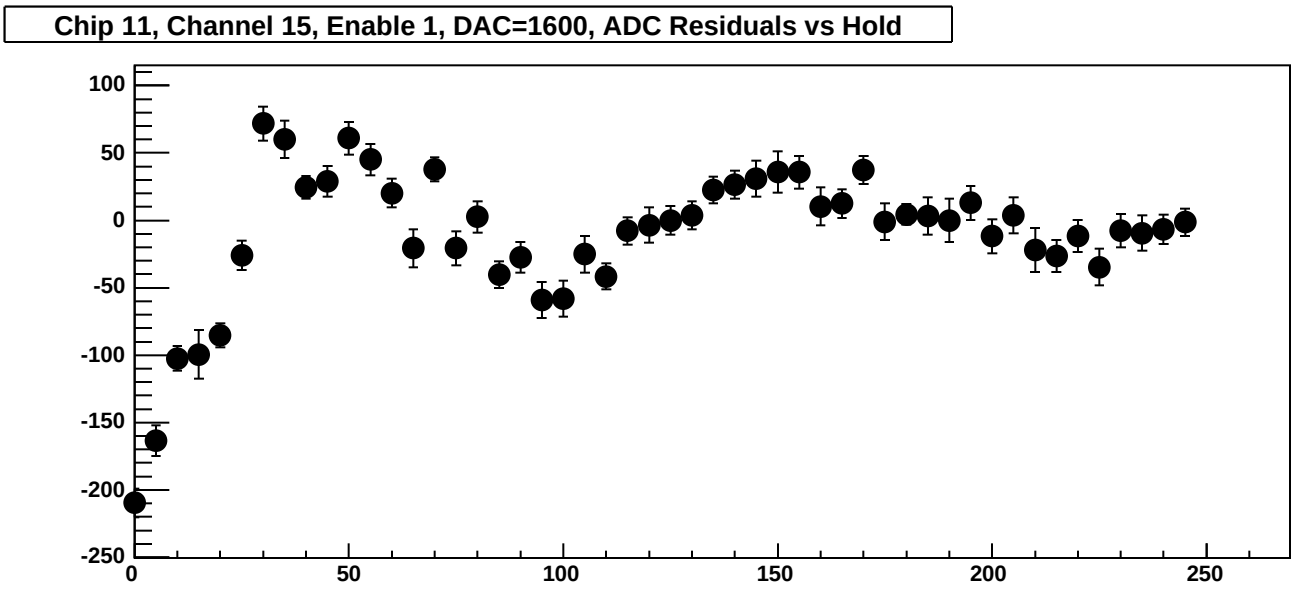


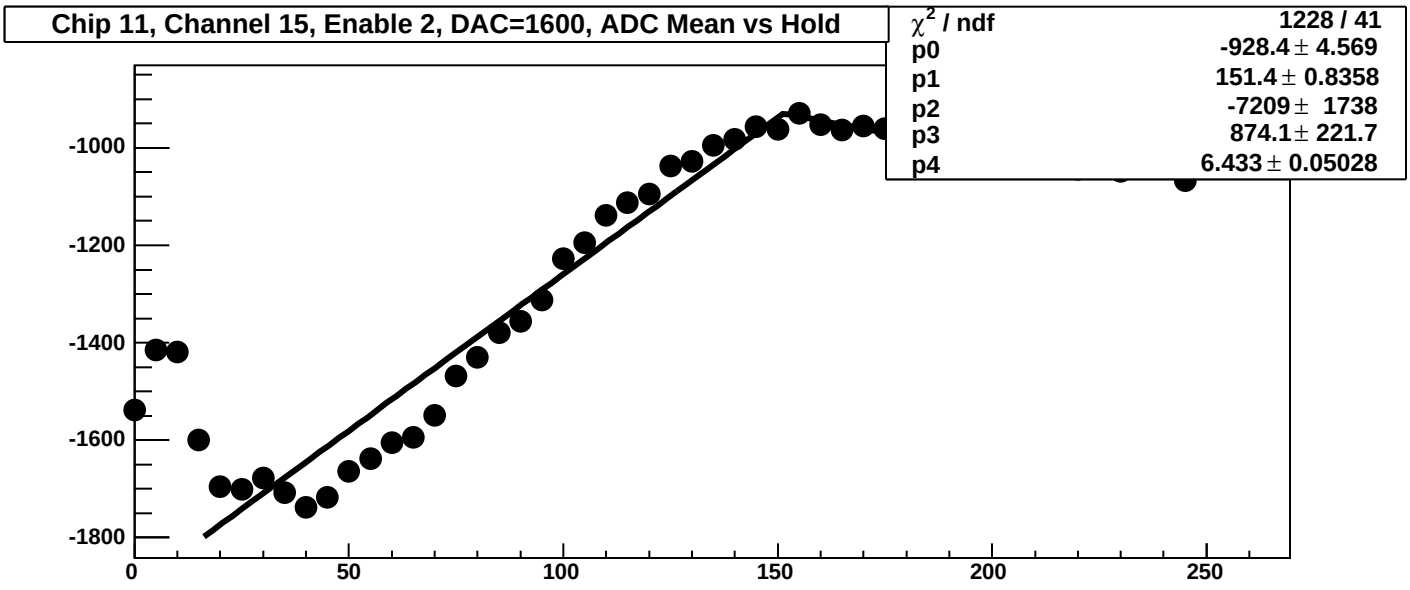
Chip 11, Channel 15, Enable 0, DAC=1600, ADC Residuals vs Hold

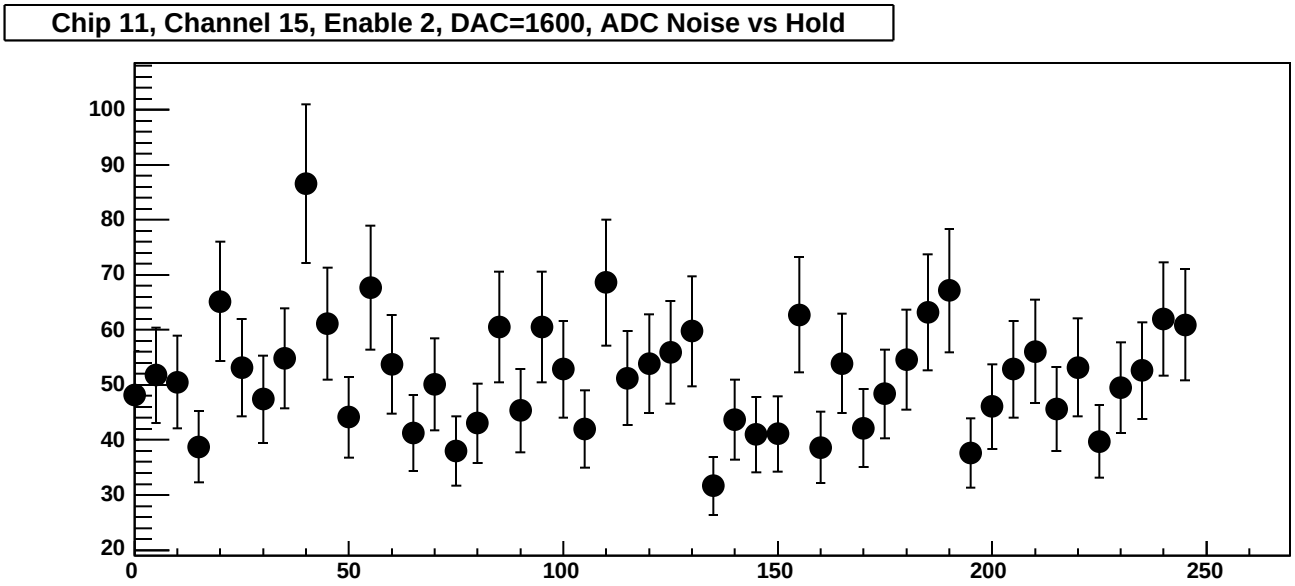


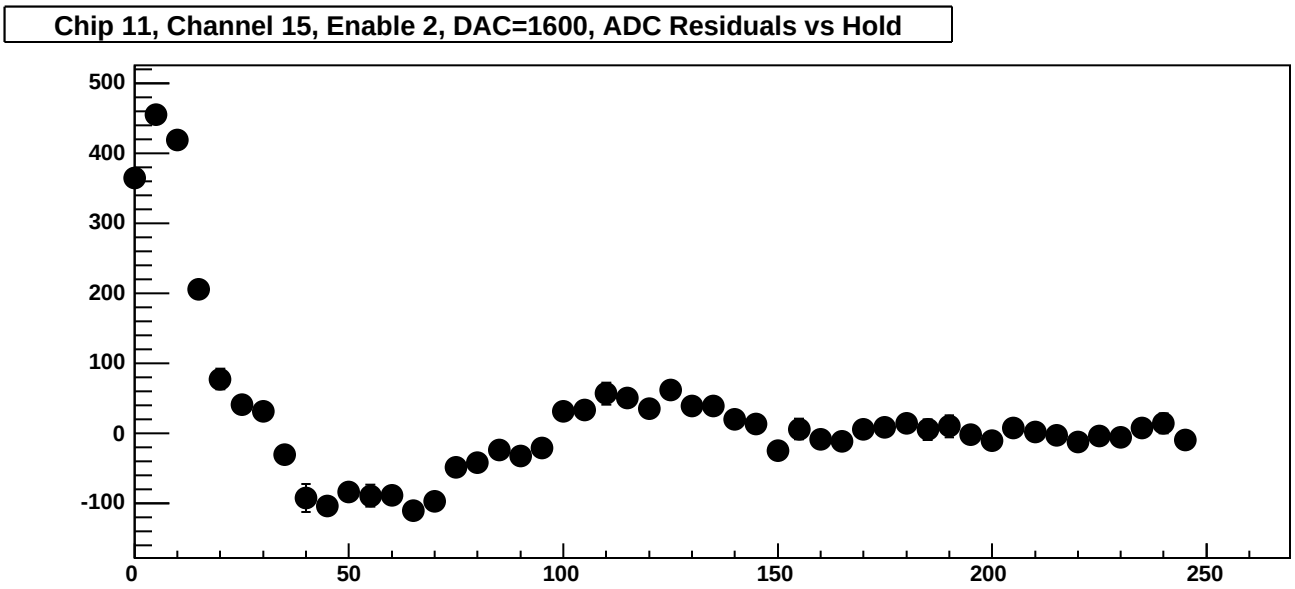

Chip 11, Channel 15, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 405.4 / 41
p0 -923.8 ± 3.574
p1 96.43 ± 1.397
p2 -1.949e+08 ± 9.238e+06
p3 3.867e+07 ± 1.634e+06
p4 3.929 ± 0.1151
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250

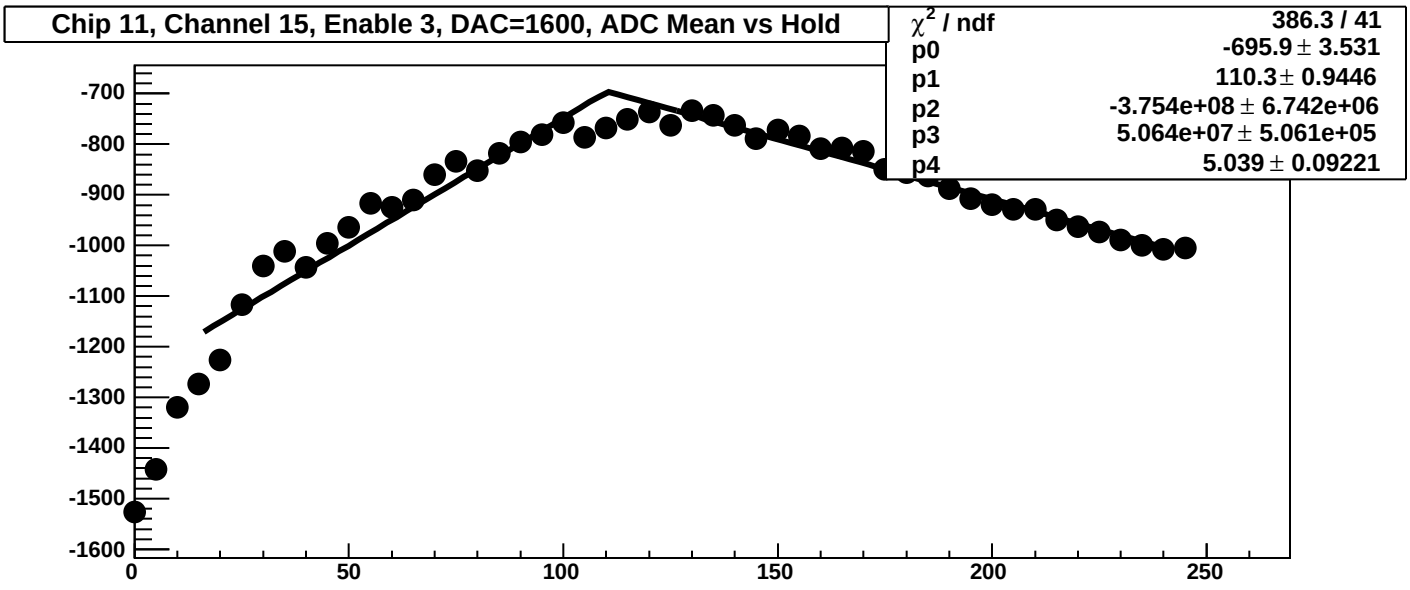

Chip 11, Channel 15, Enable 1, DAC=1600, ADC Noise vs Hold
90
80
70
60
50
40
30
0
50
100
150
200
250

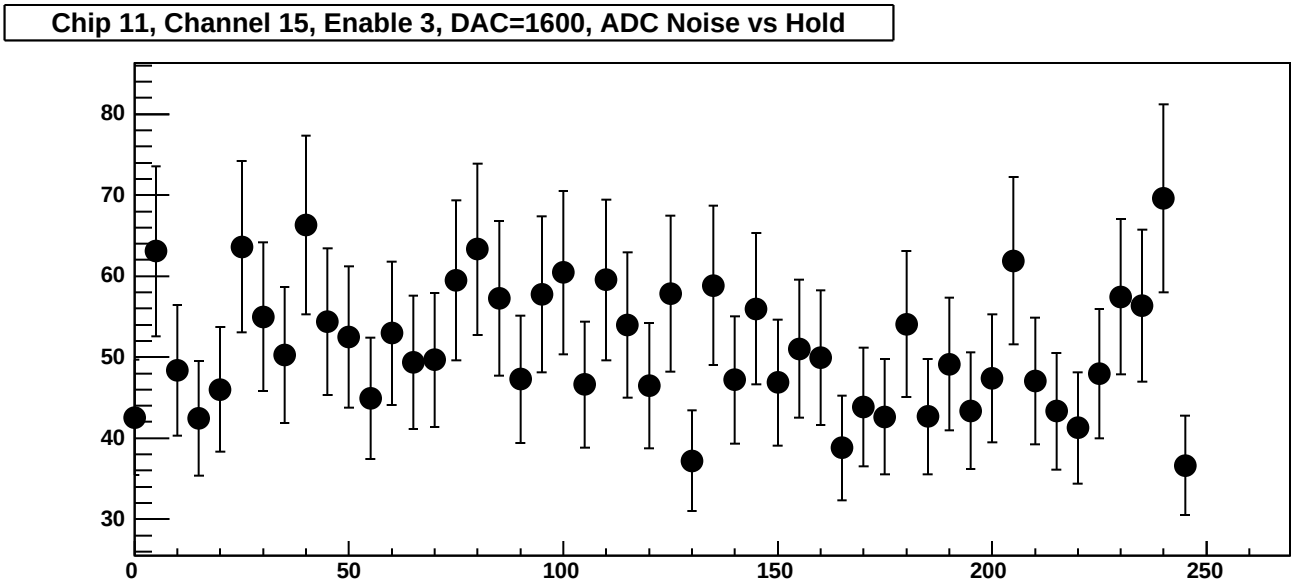

Chip 11, Channel 15, Enable 1, DAC=1600, ADC Residuals vs Hold
100
50
0
-50
-100
-150
-200
-250
0
50
100
150
200
250

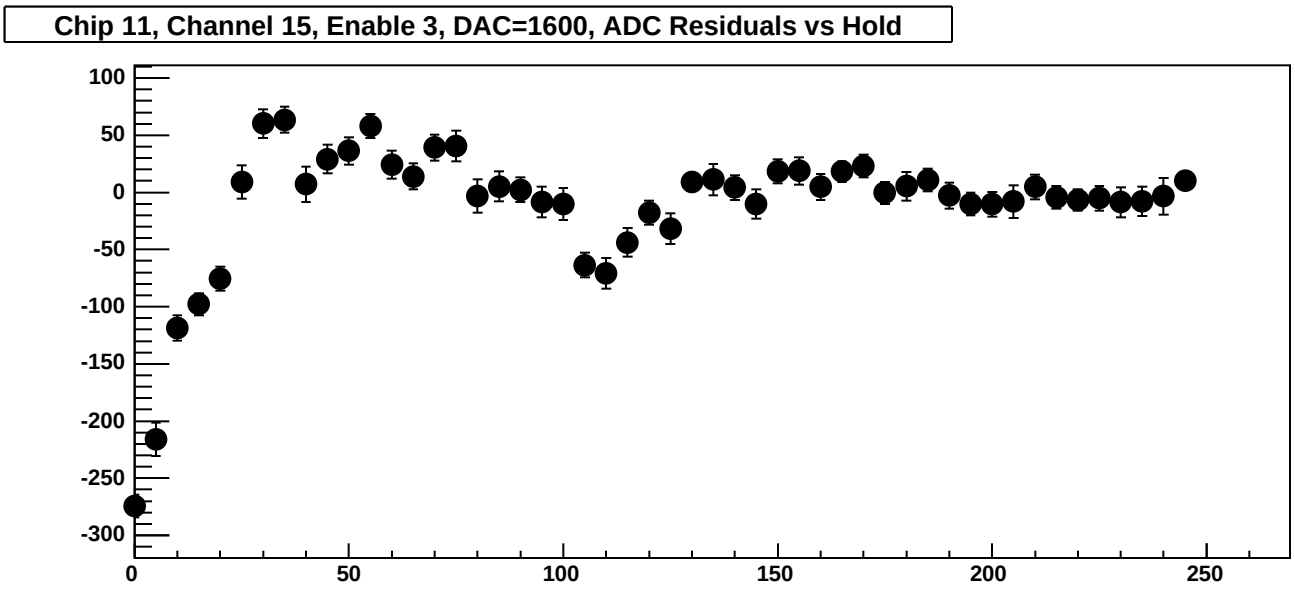

Chip 11, Channel 15, Enable 2, DAC=1600, ADC Mean vs Hold
χ² / ndf 1228 / 41
p0 -928.4 ± 4.569
p1 151.4 ± 0.8358
p2 -7209 ± 1738
p3 874.1 ± 221.7
p4 6.433 ± 0.05028
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250


Chip 11, Channel 15, Enable 2, DAC=1600, ADC Noise vs Hold
100
90
80
70
60
50
40
30
20
0
50
100
150
200
250

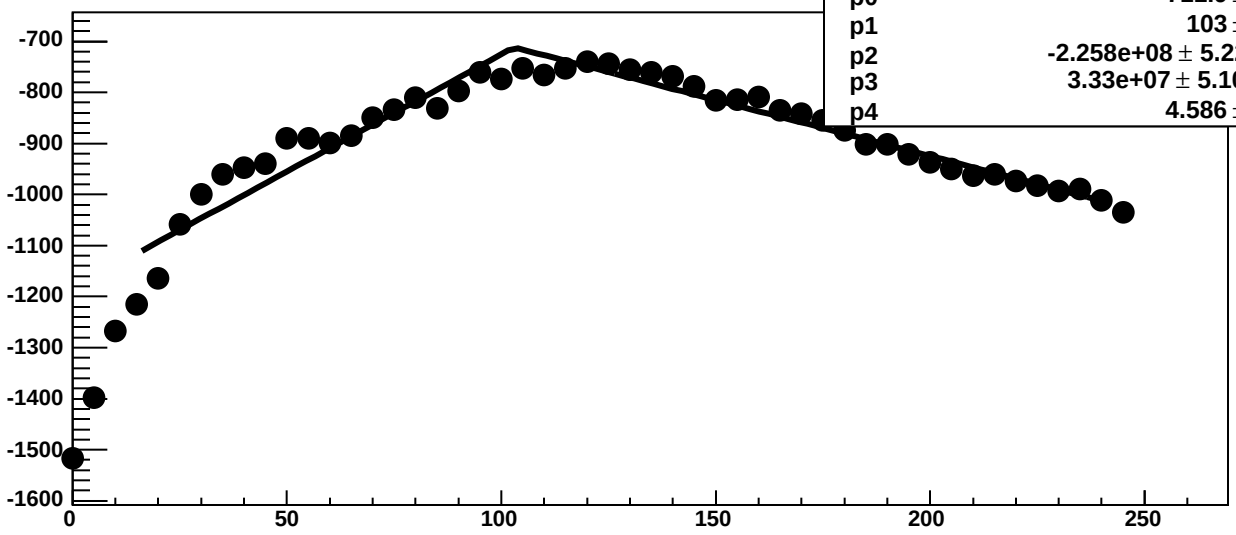

Chip 11, Channel 15, Enable 2, DAC=1600, ADC Residuals vs Hold
500
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 11, Channel 15, Enable 3, DAC=1600, ADC Mean vs Hold
χ² / ndf 386.3 / 41
p0 -695.9 ± 3.531
p1 110.3 ± 0.9446
p2 -3.754e+08 ± 6.742e+06
p3 5.064e+07 ± 5.061e+05
p4 5.039 ± 0.09221
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
50
100
150
200
250

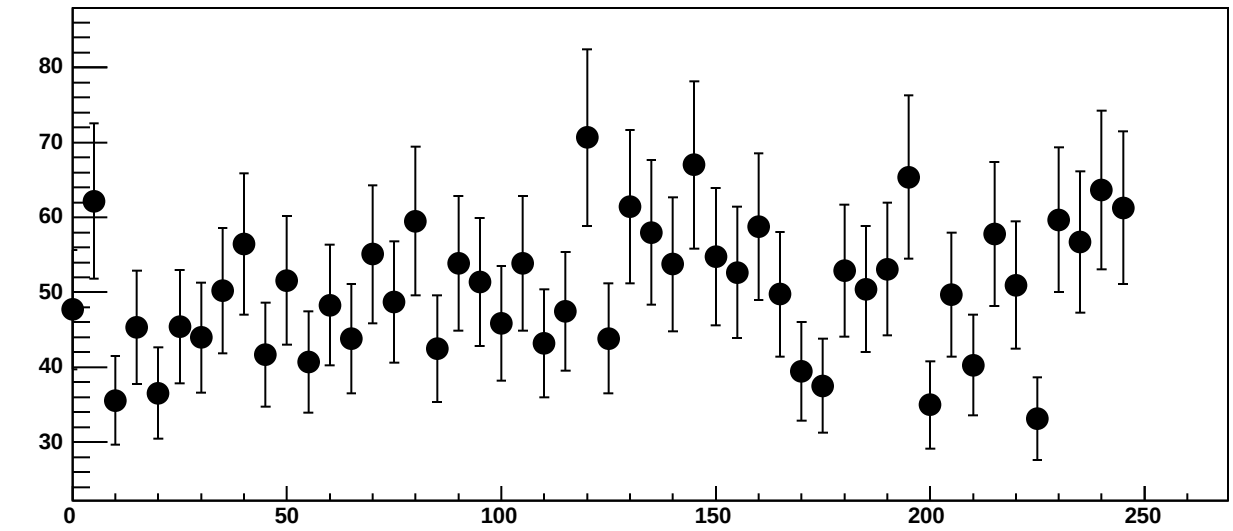

Chip 11, Channel 15, Enable 3, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
0
50
100
150
200
250


Chip 11, Channel 15, Enable 3, DAC=1600, ADC Residuals vs Hold
100
50
0
-50
-100
-150
-200
-250
-300
0
50
100
150
200
250

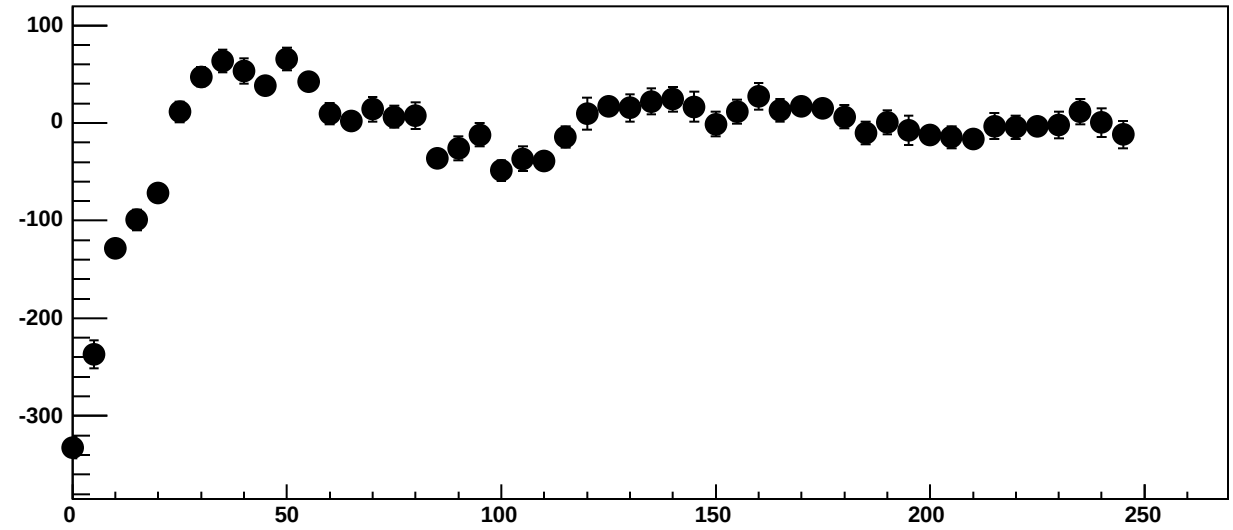
Chip 11, Channel 15, Enable 4, DAC=1600, ADC Mean vs Hold

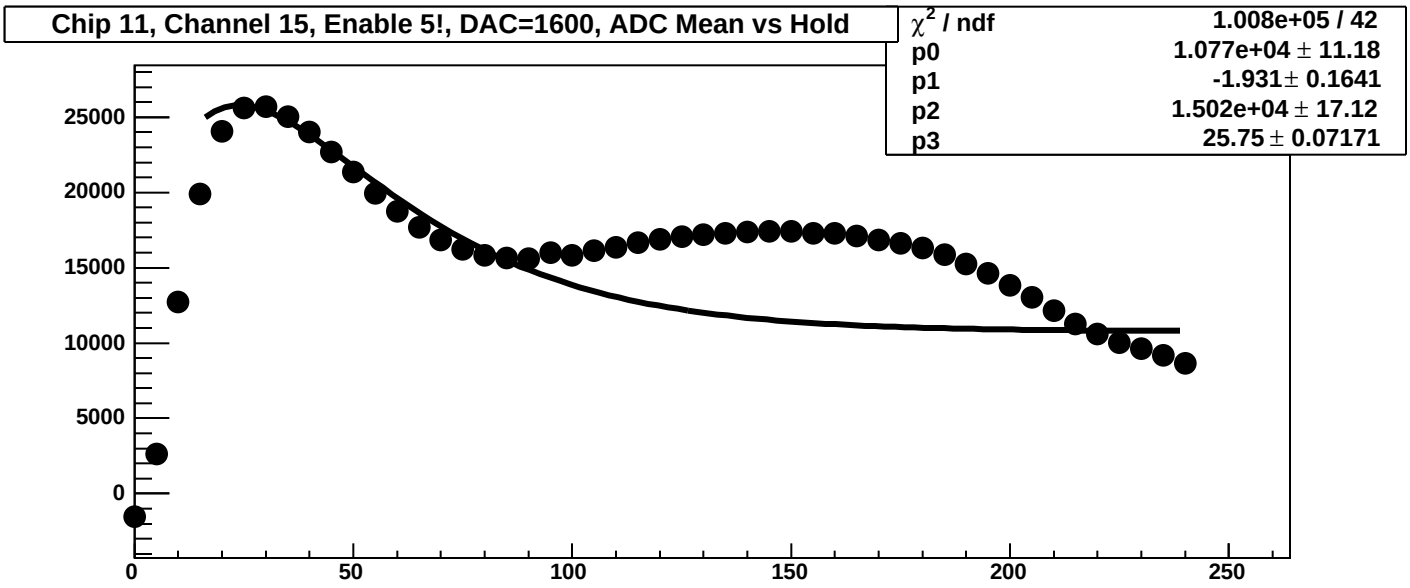


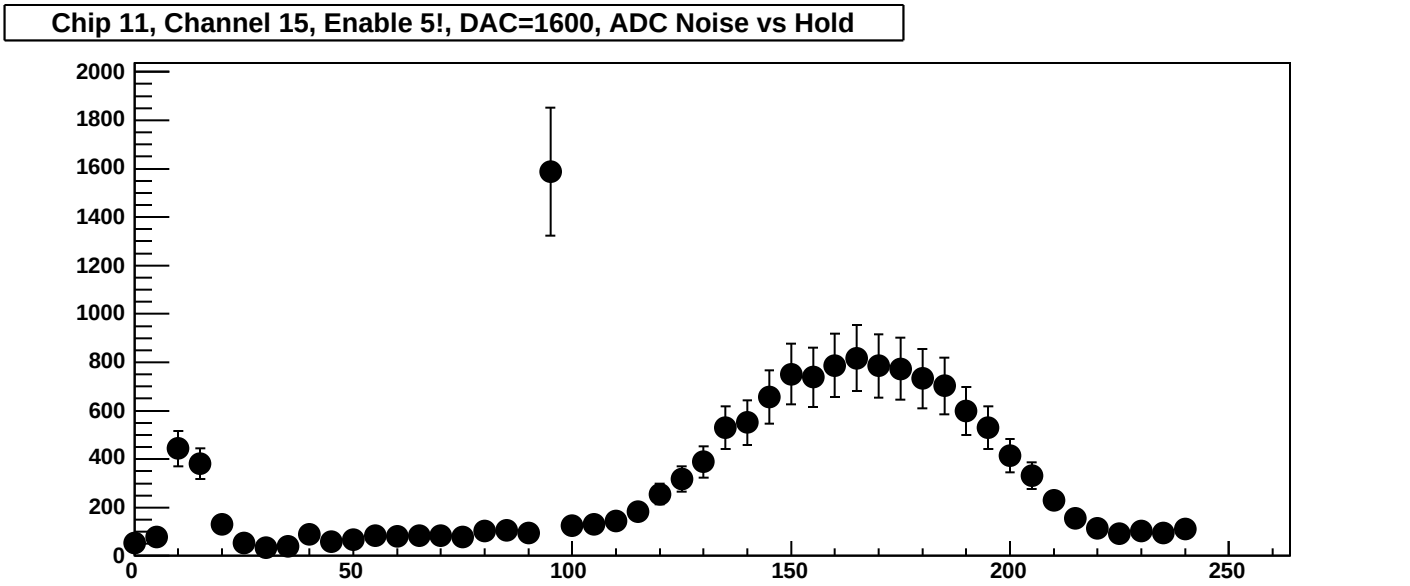
Chip 11, Channel 15, Enable 4, DAC=1600, ADC Noise vs Hold

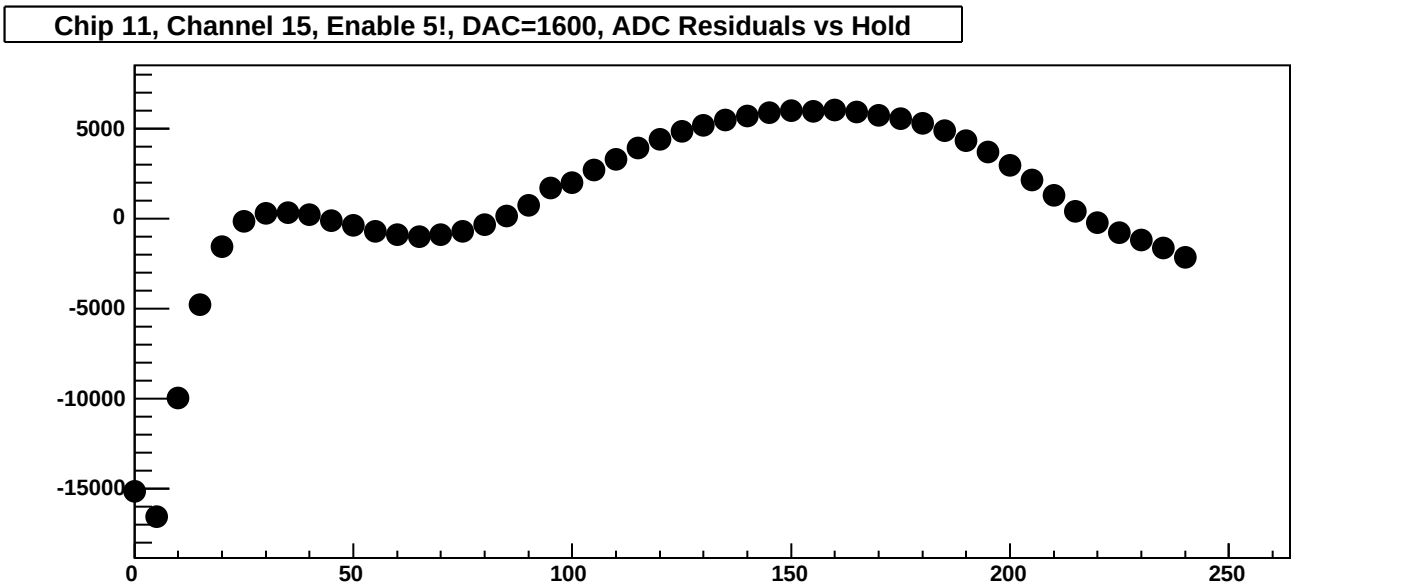


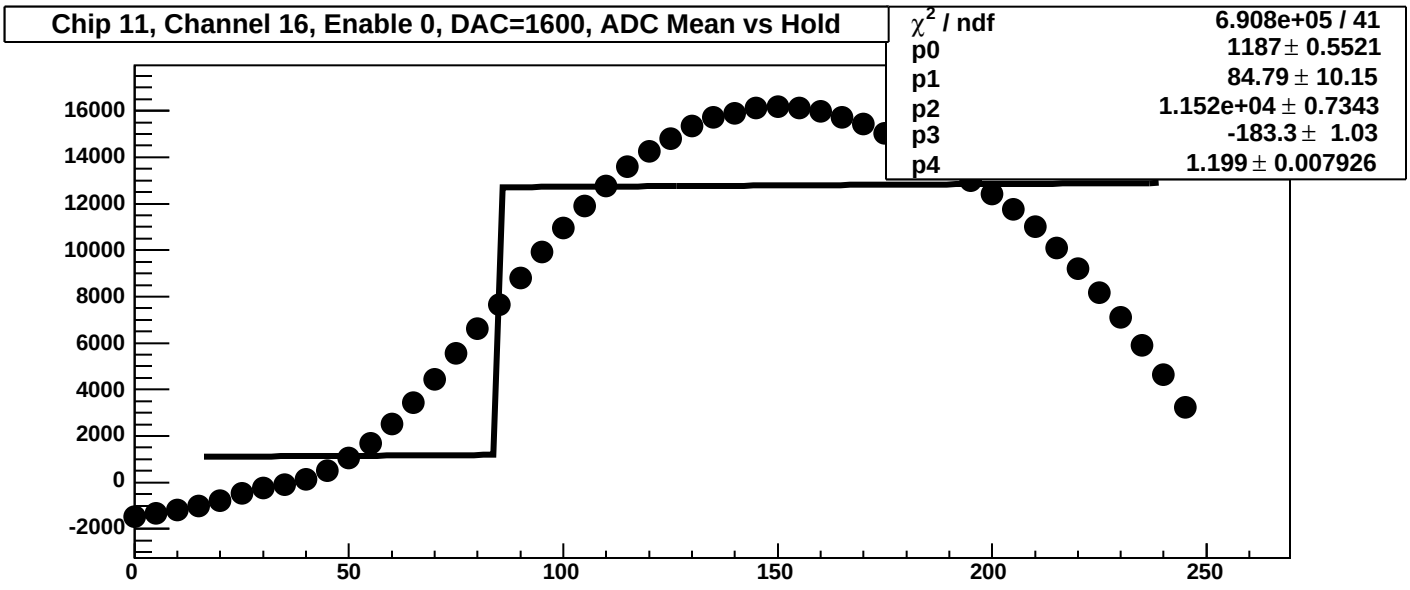
Chip 11, Channel 15, Enable 4, DAC=1600, ADC Residuals vs Hold

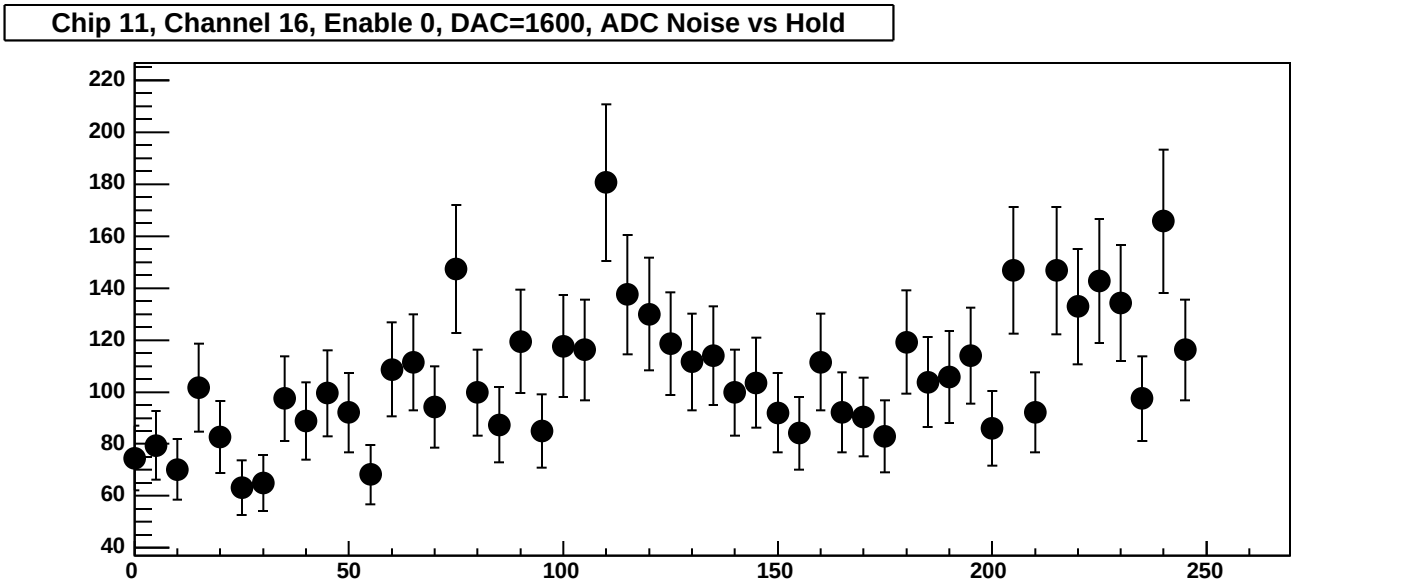


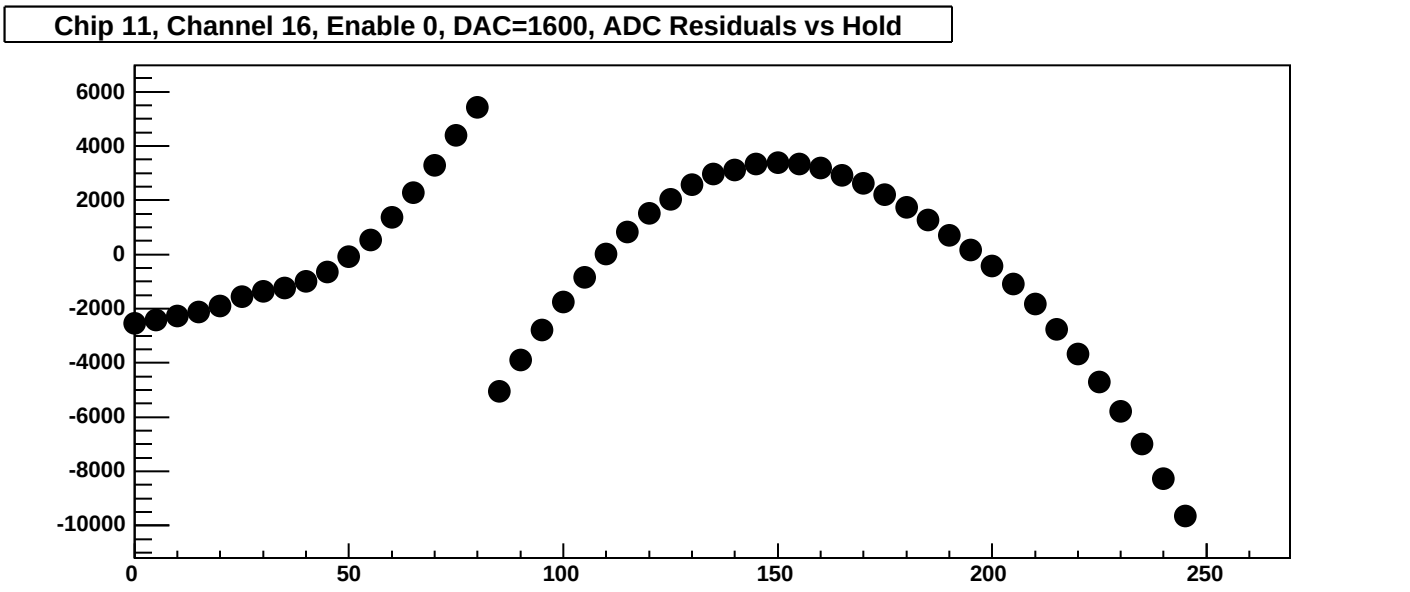

Chip 11, Channel 15, Enable 5!, DAC=1600, ADC Mean vs Hold
χ² / ndf 1.008e+05 / 42
p0 1.077e+04 ± 11.18
p1 -1.931 ± 0.1641
p2 1.502e+04 ± 17.12
p3 25.75 ± 0.07171
25000
20000
15000
10000
5000
0
0
50
100
150
200
250

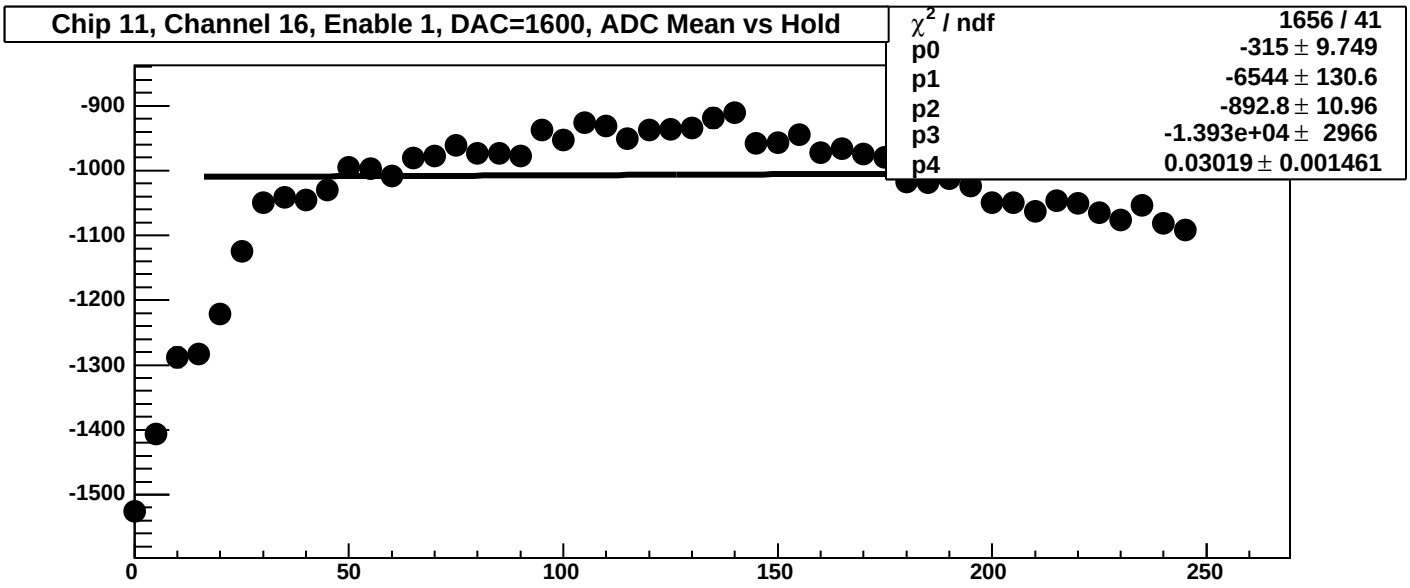

Chip 11, Channel 15, Enable 5!, DAC=1600, ADC Noise vs Hold
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

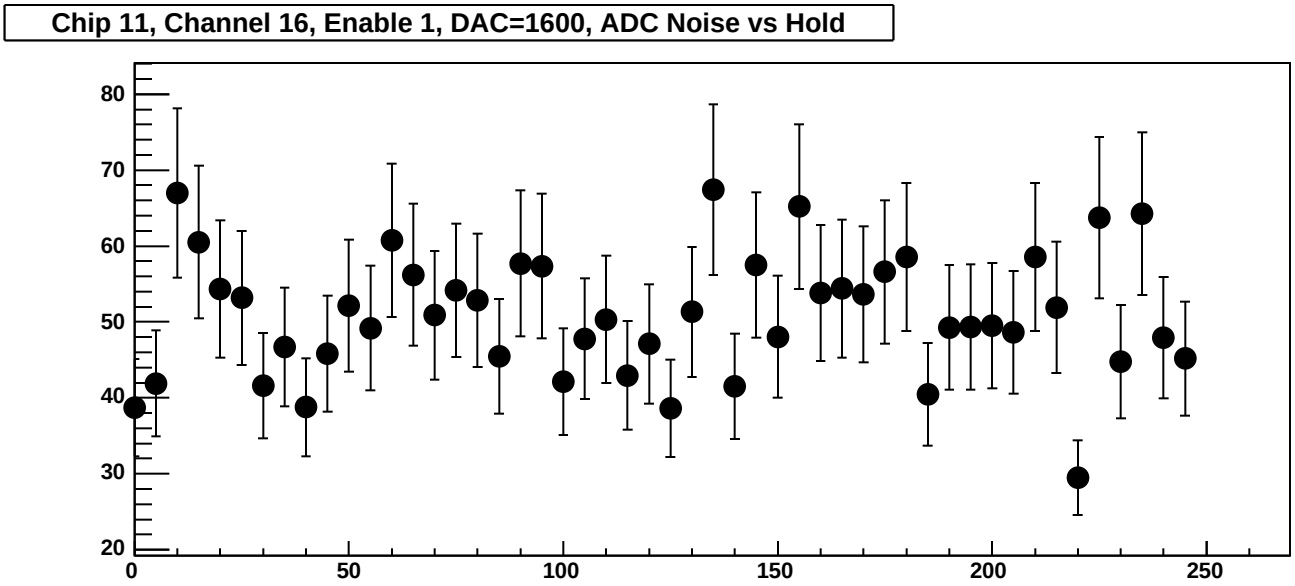

Chip 11, Channel 15, Enable 5!, DAC=1600, ADC Residuals vs Hold
5000
0
-5000
-10000
-15000
0
50
100
150
200
250

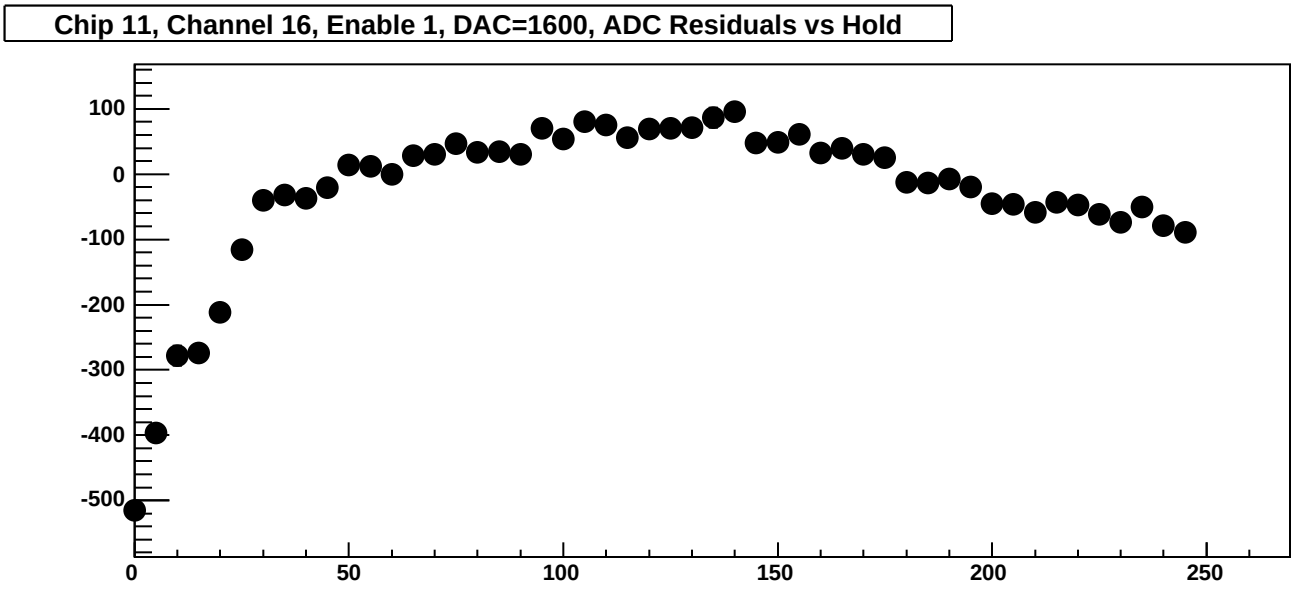

Chip 11, Channel 16, Enable 0, DAC=1600, ADC Mean vs Hold
χ² / ndf 6.908e+05 / 41
p0 1187 ± 0.5521
p1 84.79 ± 10.15
p2 1.152e+04 ± 0.7343
p3 -183.3 ± 1.03
p4 1.199 ± 0.007926
16000
14000
12000
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

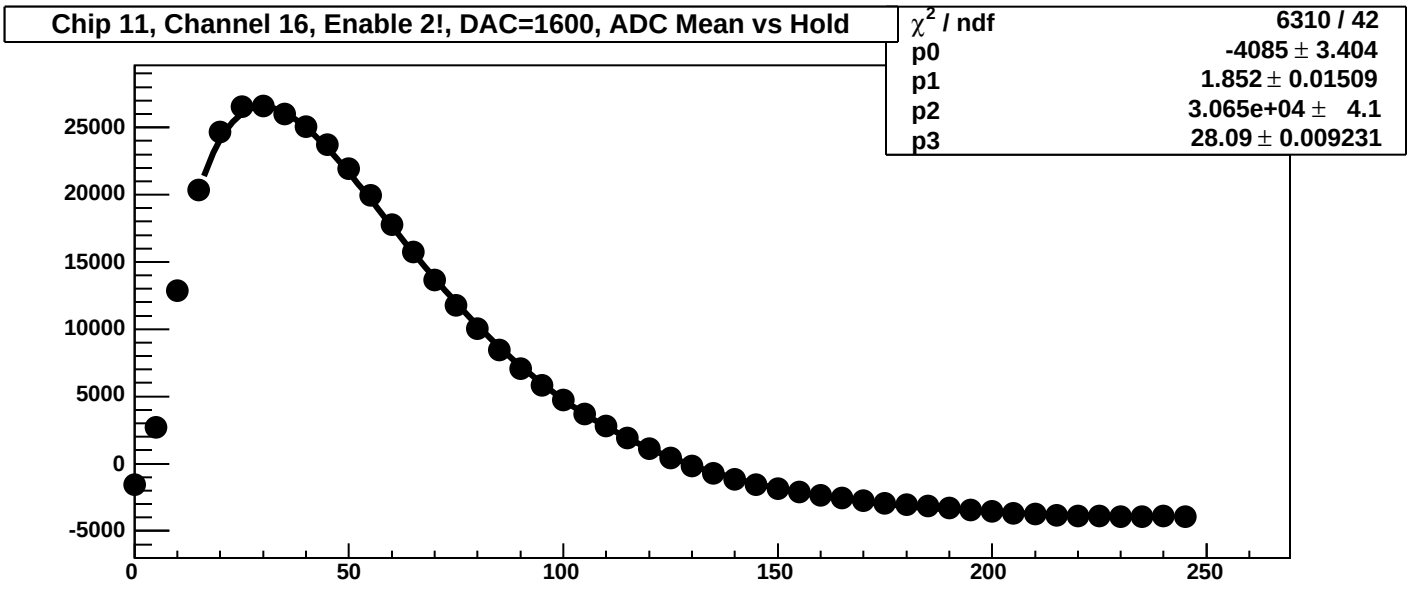

Chip 11, Channel 16, Enable 0, DAC=1600, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250

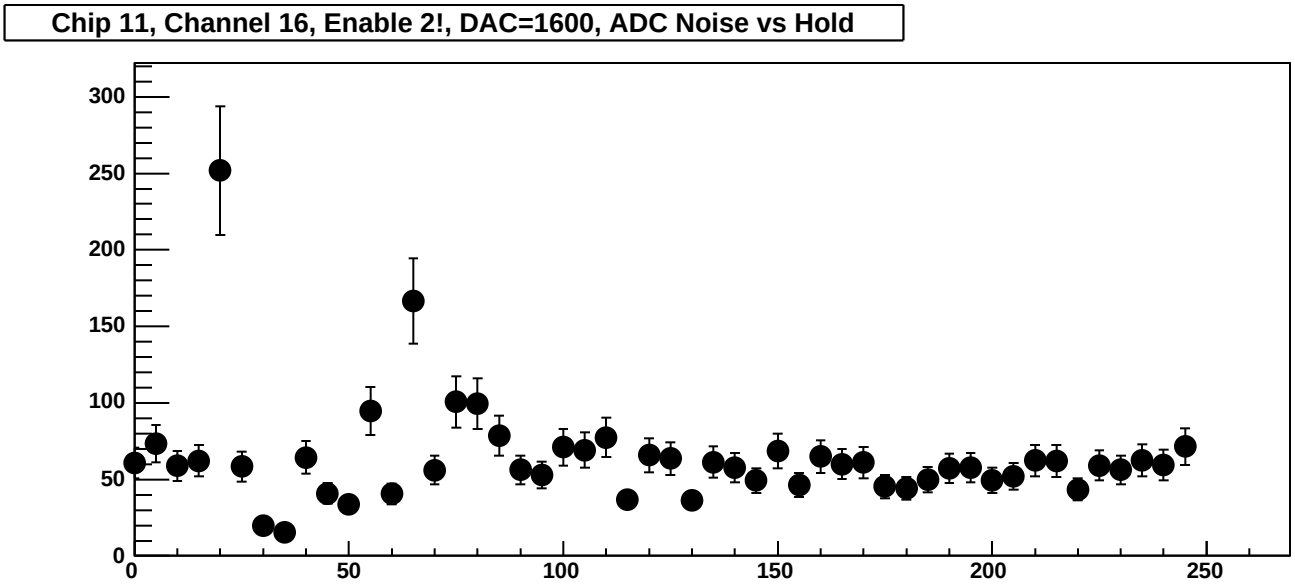

Chip 11, Channel 16, Enable 0, DAC=1600, ADC Residuals vs Hold
6000
4000
2000
0
-2000
-4000
-6000
-8000
-10000
0
50
100
150
200
250

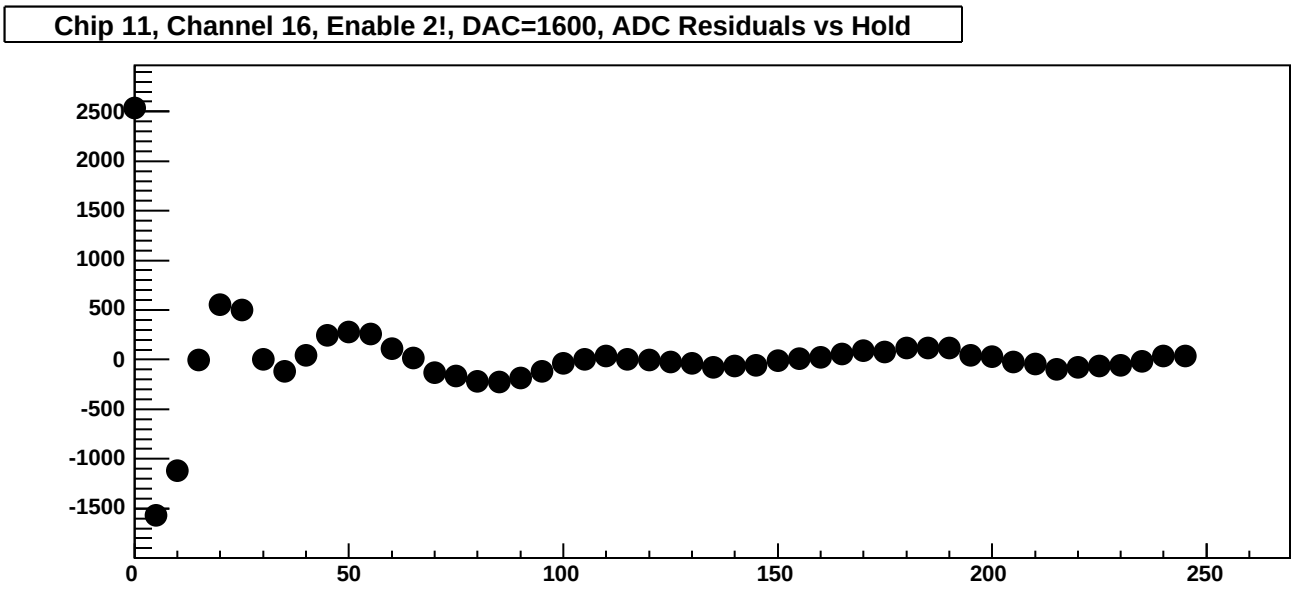

Chip 11, Channel 16, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 1656 / 41
p0 -315 ± 9.749
p1 -6544 ± 130.6
p2 -892.8 ± 10.96
p3 -1.393e+04 ± 2966
p4 0.03019 ± 0.001461
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250


Chip 11, Channel 16, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
20
0
50
100
150
200
250

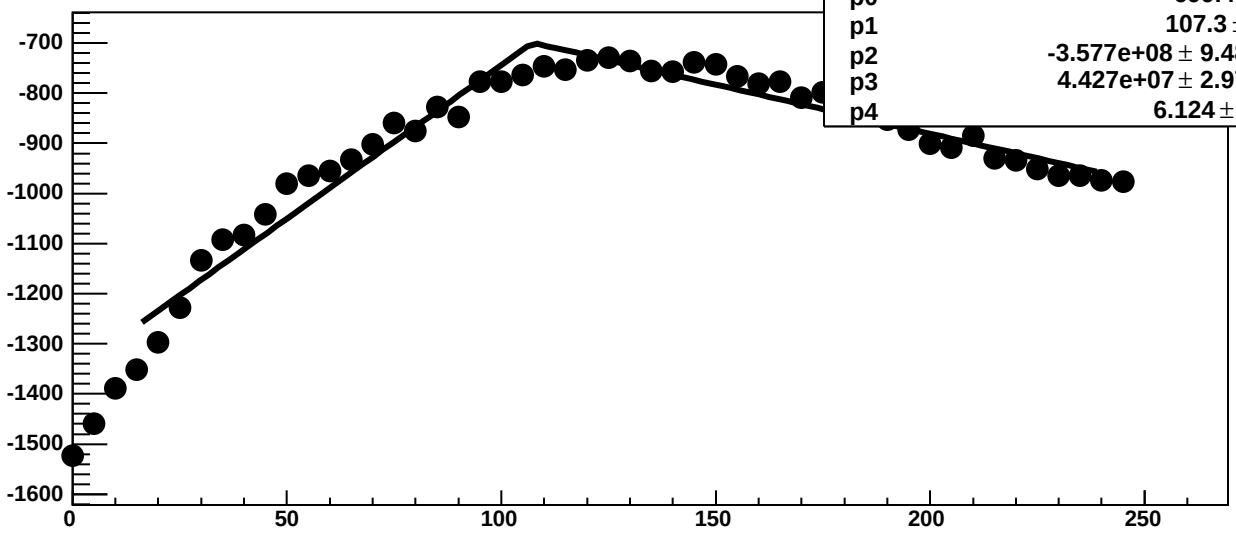

Chip 11, Channel 16, Enable 1, DAC=1600, ADC Residuals vs Hold
100
0
-100
-200
-300
-400
-500
0
50
100
150
200
250


Chip 11, Channel 16, Enable 2!, DAC=1600, ADC Mean vs Hold
χ² / ndf 6310 / 42
p0 -4085 ± 3.404
p1 1.852 ± 0.01509
p2 3.065e+04 ± 4.1
p3 28.09 ± 0.009231
25000
20000
15000
10000
5000
0
-5000
0
50
100
150
200
250

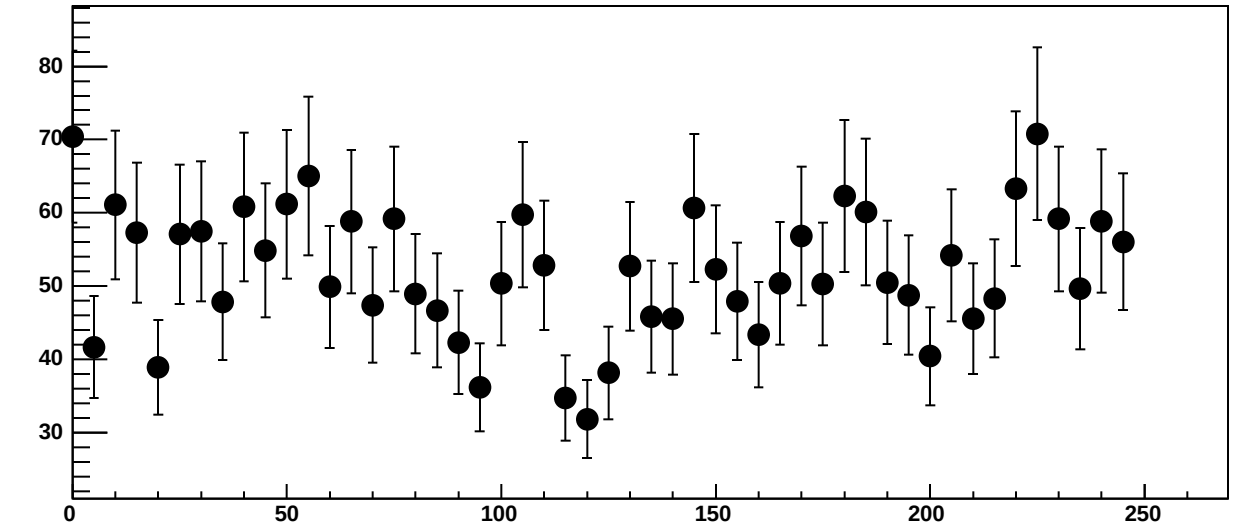

Chip 11, Channel 16, Enable 2!, DAC=1600, ADC Noise vs Hold
300
250
200
150
100
50
0
0
50
100
150
200
250


Chip 11, Channel 16, Enable 2!, DAC=1600, ADC Residuals vs Hold
2500
2000
1500
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

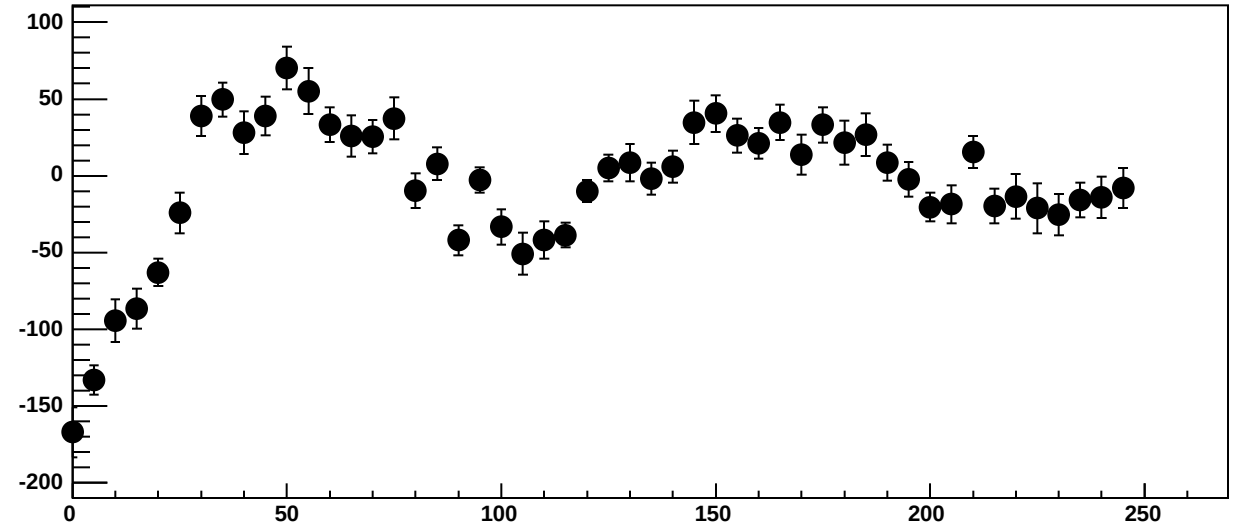
Chip 11, Channel 16, Enable 3, DAC=1600, ADC Mean vs Hold



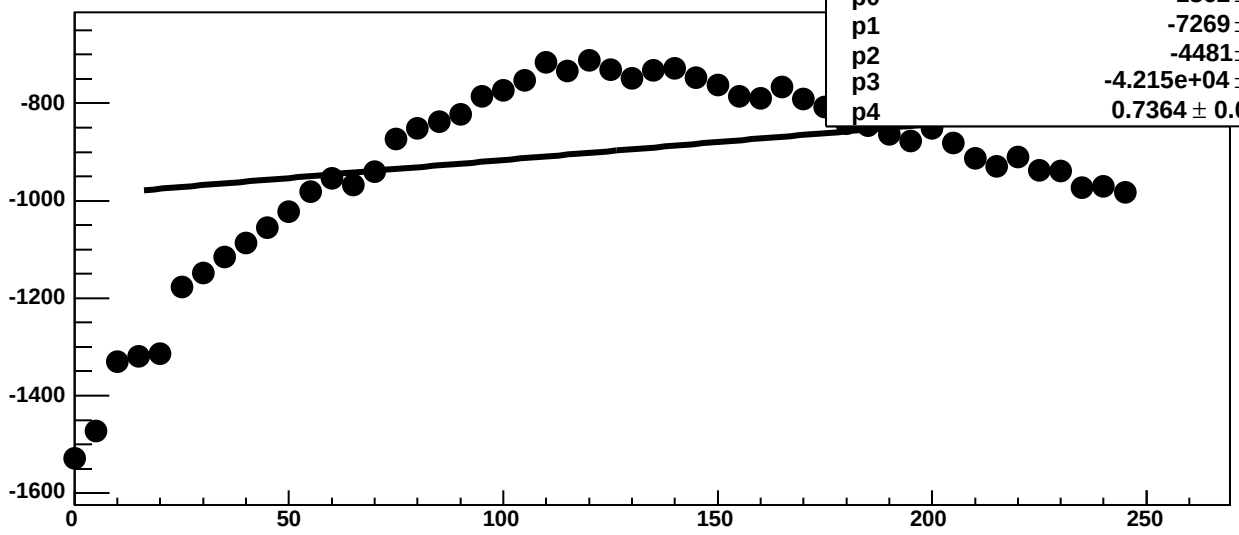
Chip 11, Channel 16, Enable 3, DAC=1600, ADC Noise vs Hold



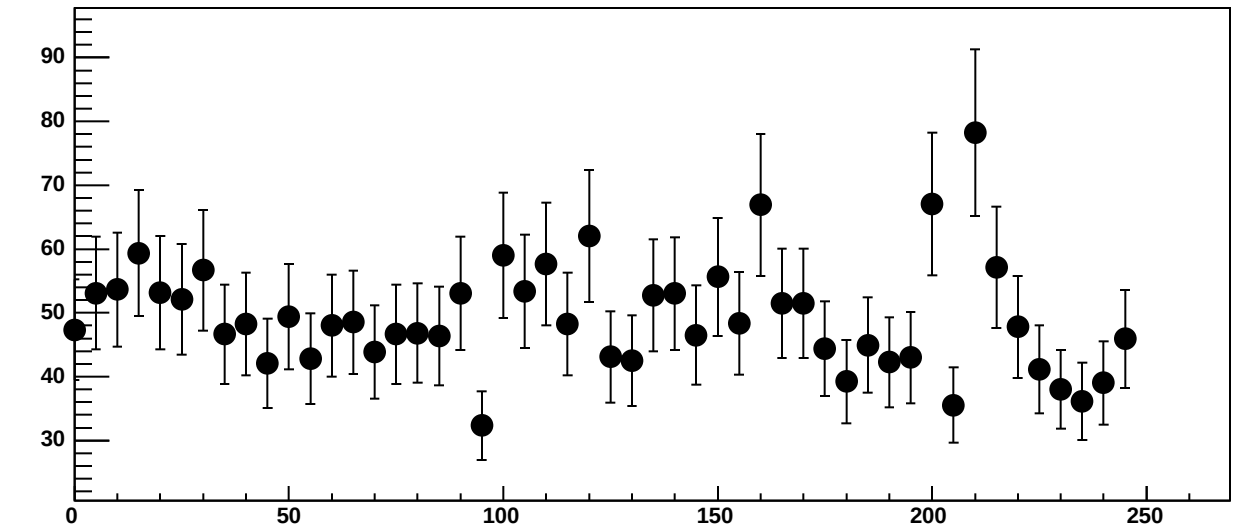
Chip 11, Channel 16, Enable 3, DAC=1600, ADC Residuals vs Hold



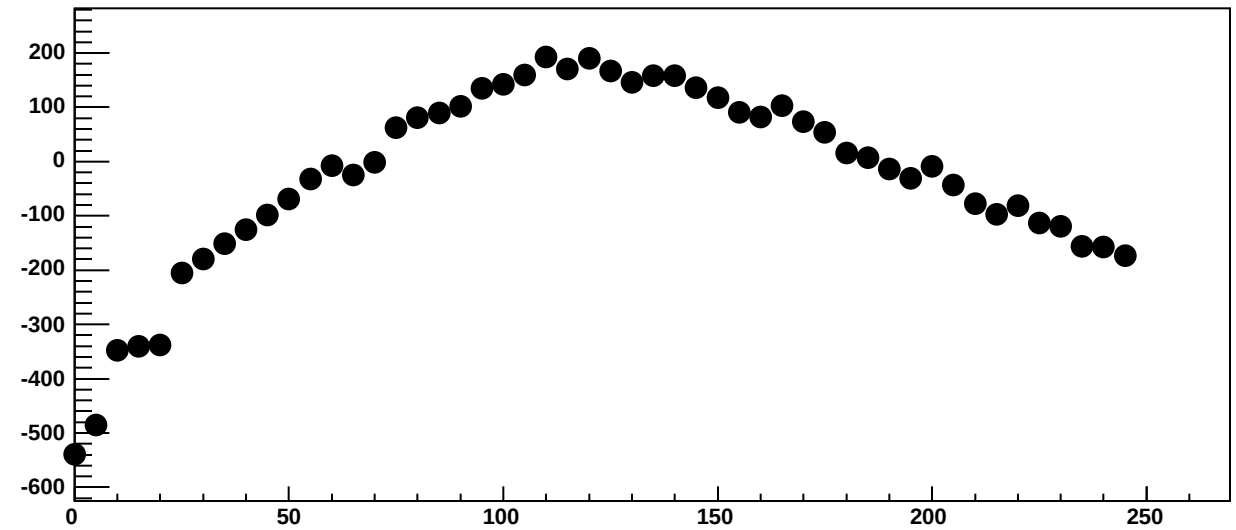
Chip 11, Channel 16, Enable 4, DAC=1600, ADC Mean vs Hold

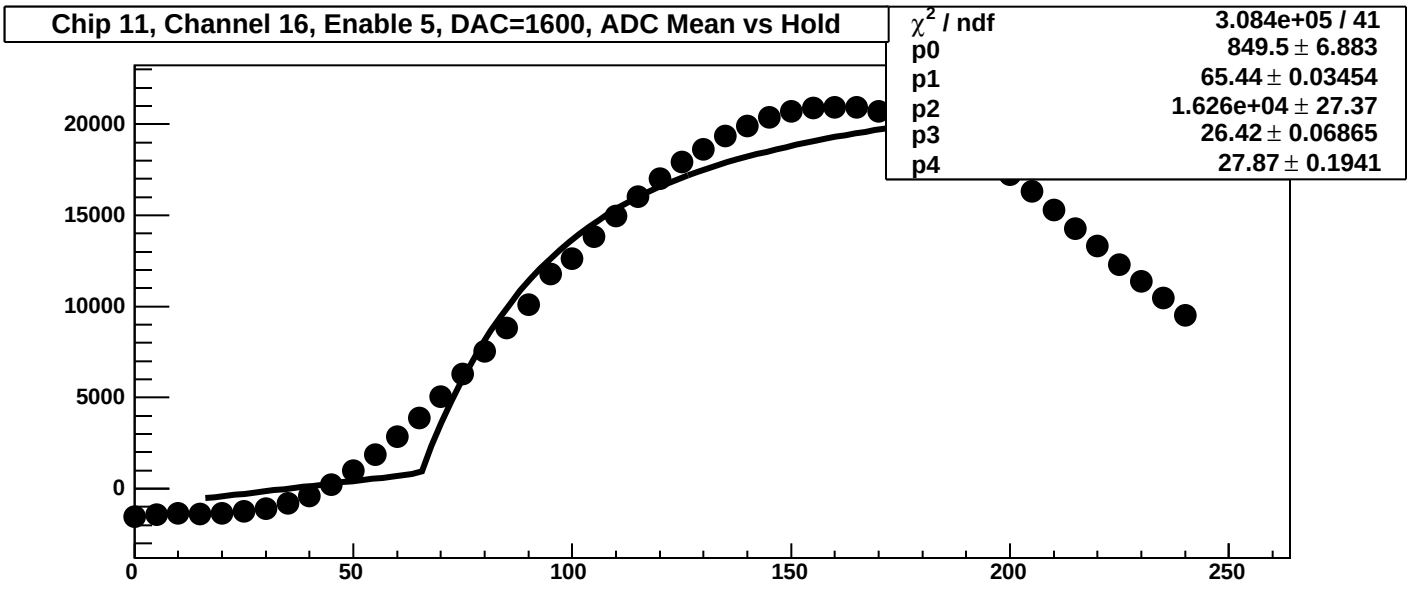


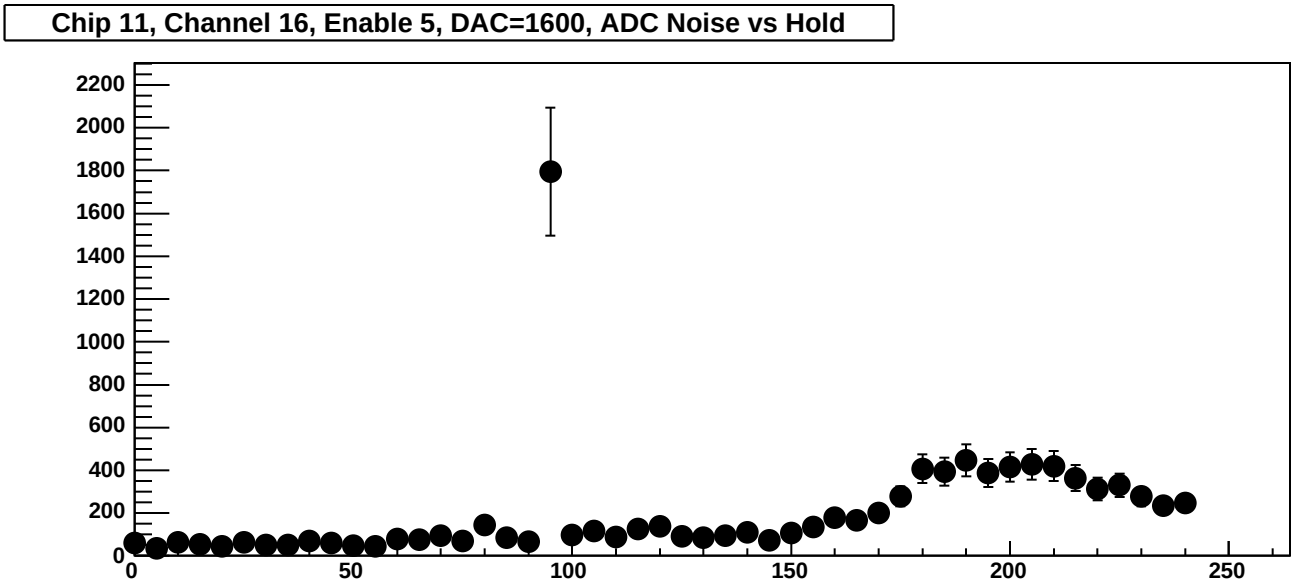
Chip 11, Channel 16, Enable 4, DAC=1600, ADC Noise vs Hold

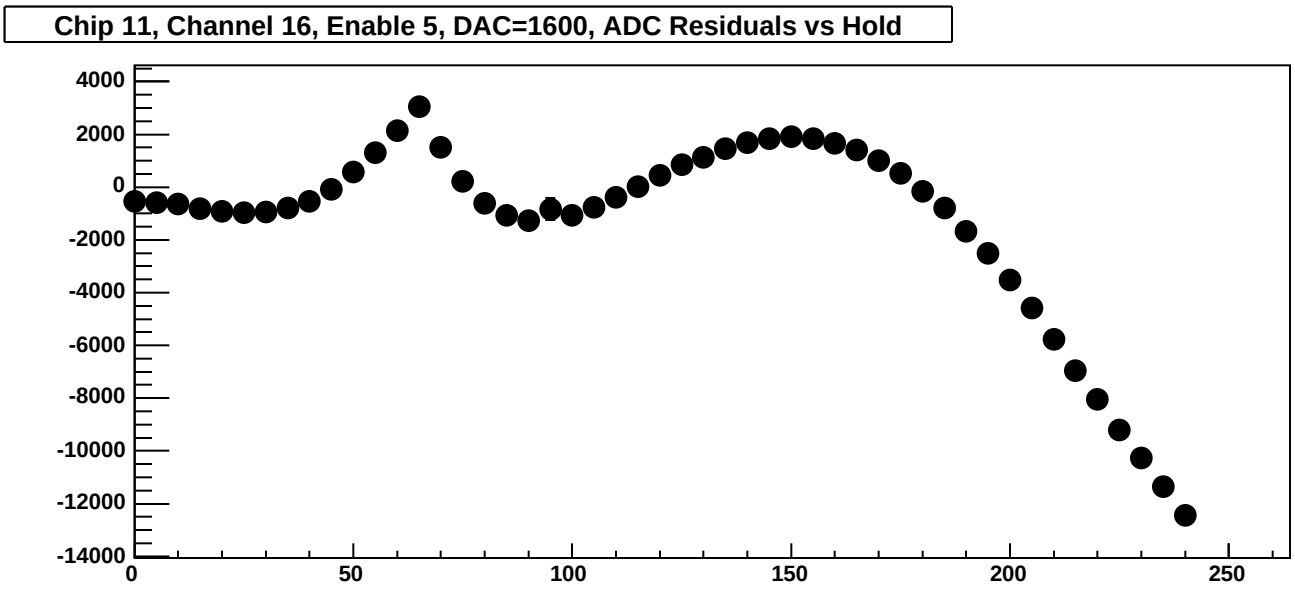


Chip 11, Channel 16, Enable 4, DAC=1600, ADC Residuals vs Hold

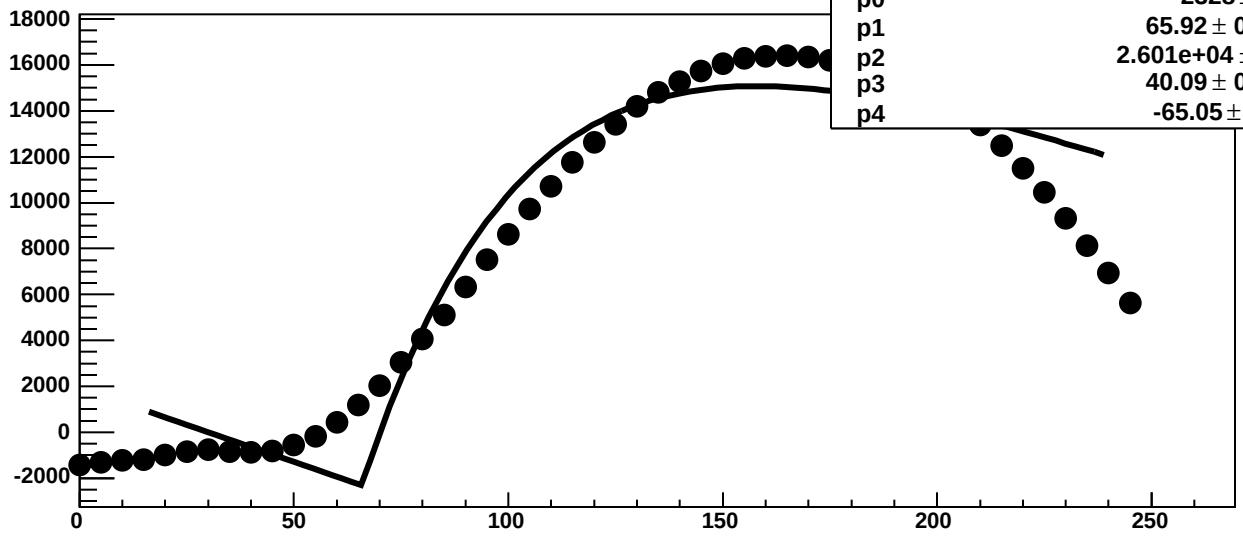



Chip 11, Channel 16, Enable 5, DAC=1600, ADC Mean vs Hold
χ² / ndf 3.084e+05 / 41
p0 849.5 ± 6.883
p1 65.44 ± 0.03454
p2 1.626e+04 ± 27.37
p3 26.42 ± 0.06865
p4 27.87 ± 0.1941
20000
15000
10000
5000
0
0
50
100
150
200
250

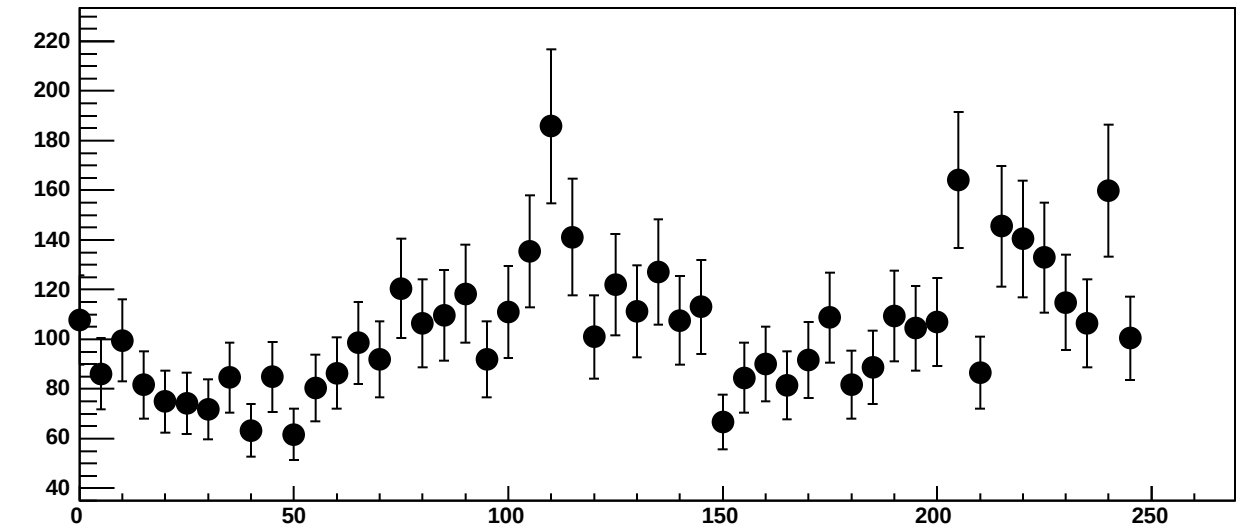

Chip 11, Channel 16, Enable 5, DAC=1600, ADC Noise vs Hold
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 11, Channel 16, Enable 5, DAC=1600, ADC Residuals vs Hold
4000
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
50
100
150
200
250

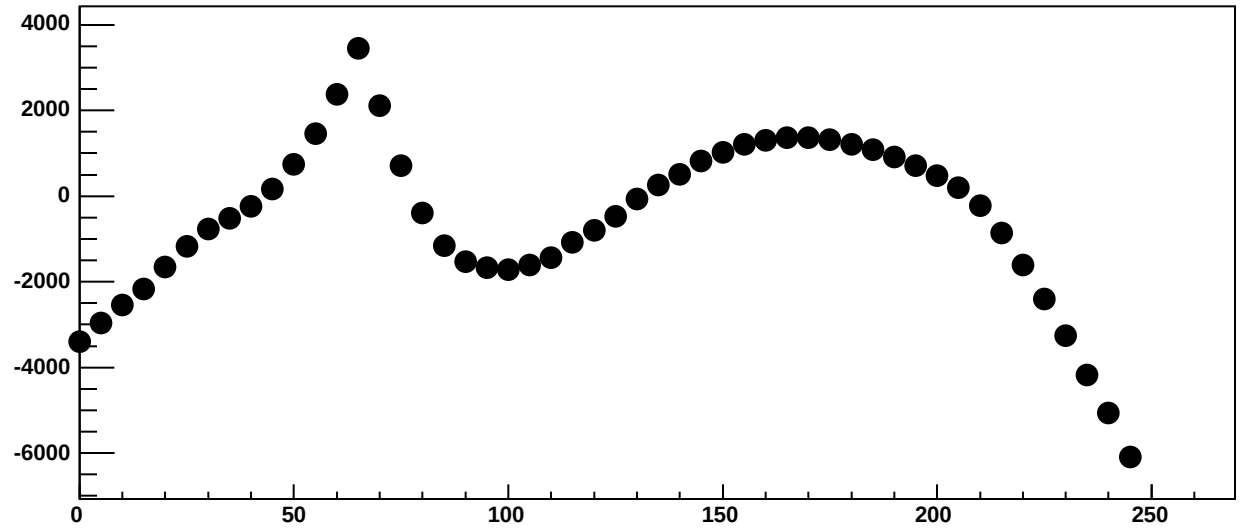
Chip 11, Channel 17, Enable 0, DAC=1600, ADC Mean vs Hold

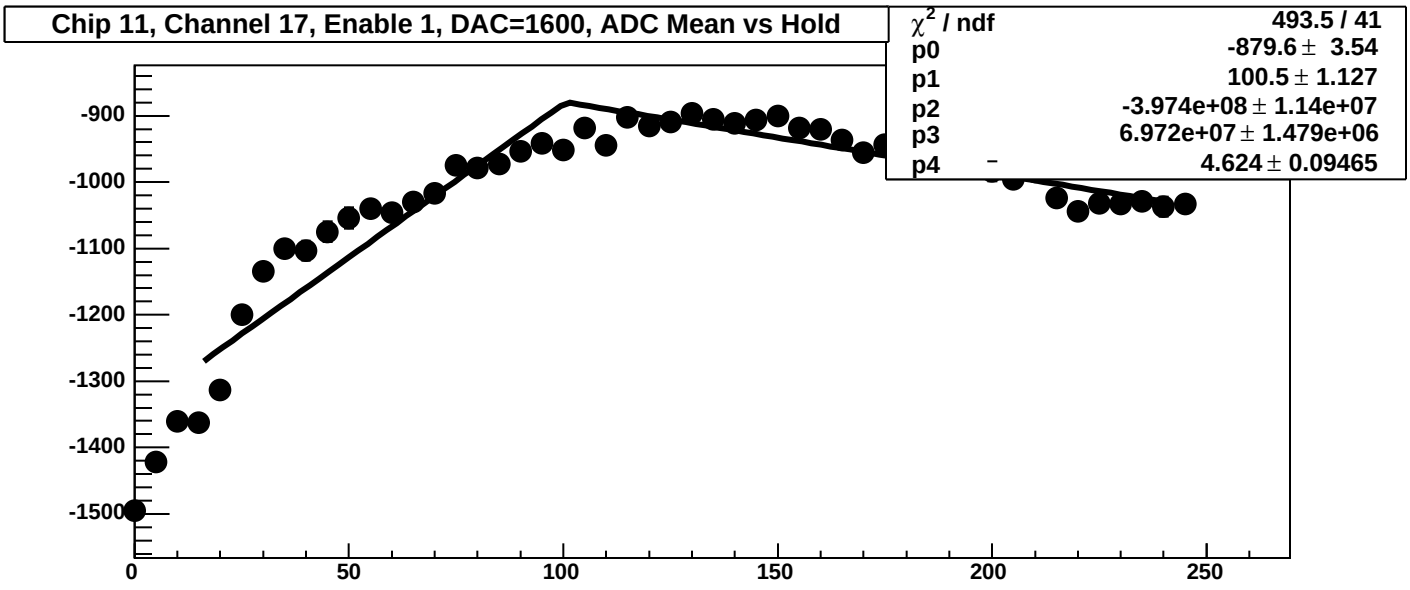


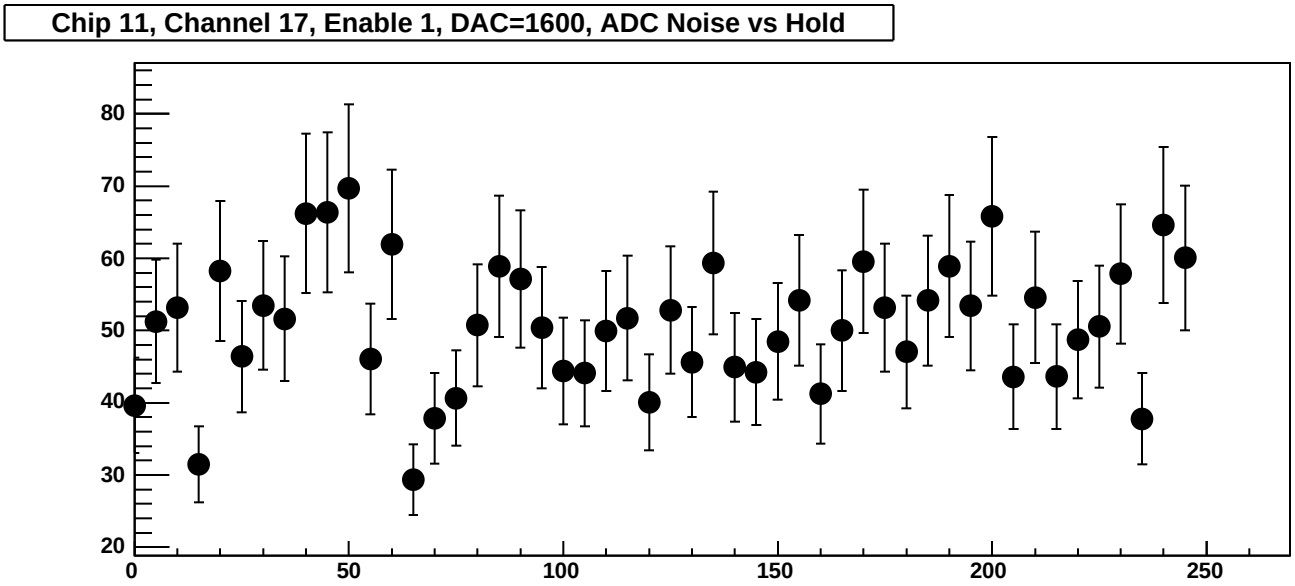
Chip 11, Channel 17, Enable 0, DAC=1600, ADC Noise vs Hold

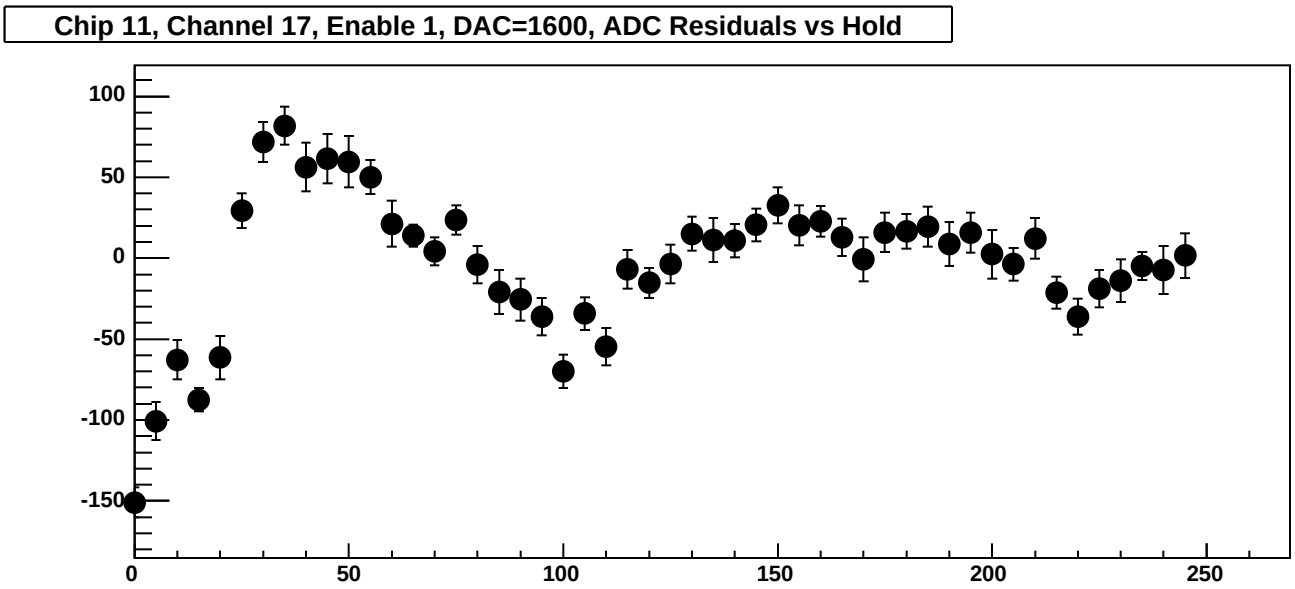


Chip 11, Channel 17, Enable 0, DAC=1600, ADC Residuals vs Hold

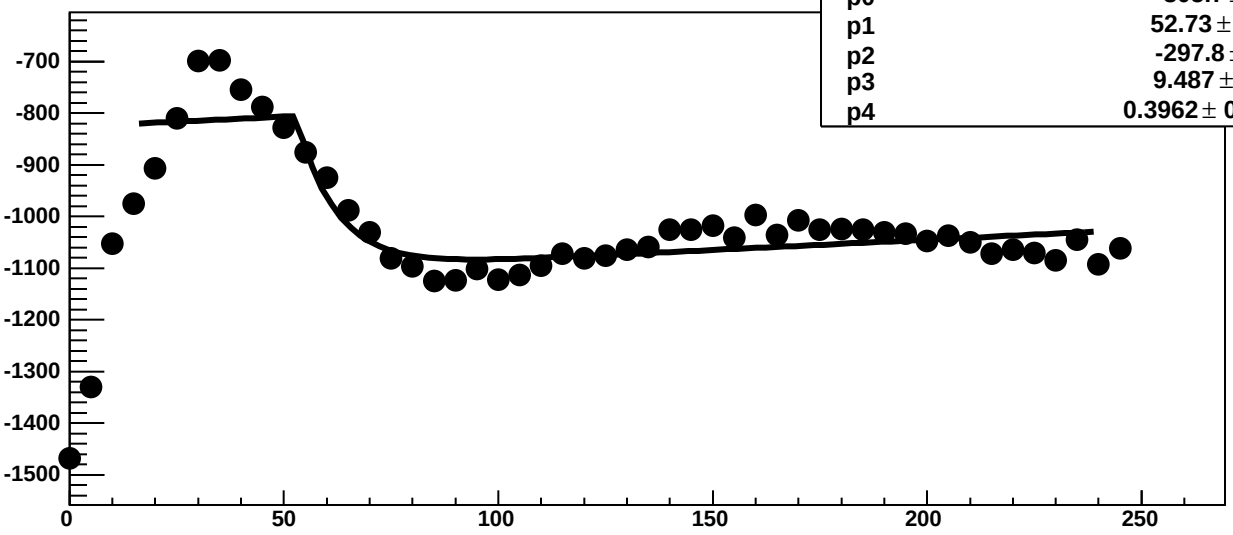



Chip 11, Channel 17, Enable 1, DAC=1600, ADC Mean vs Hold
χ² / ndf 493.5 / 41
p0 -879.6 ± 3.54
p1 100.5 ± 1.127
p2 -3.974e+08 ± 1.14e+07
p3 6.972e+07 ± 1.479e+06
p4 4.624 ± 0.09465
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
50
100
150
200
250

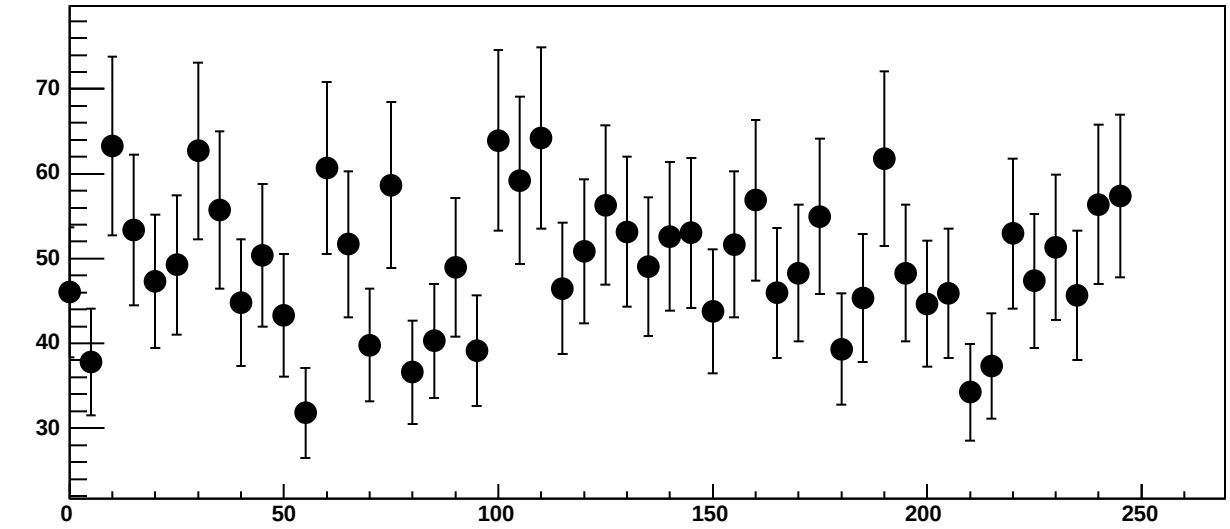

Chip 11, Channel 17, Enable 1, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 11, Channel 17, Enable 1, DAC=1600, ADC Residuals vs Hold
100
50
0
-50
-100
-150
0
50
100
150
200
250

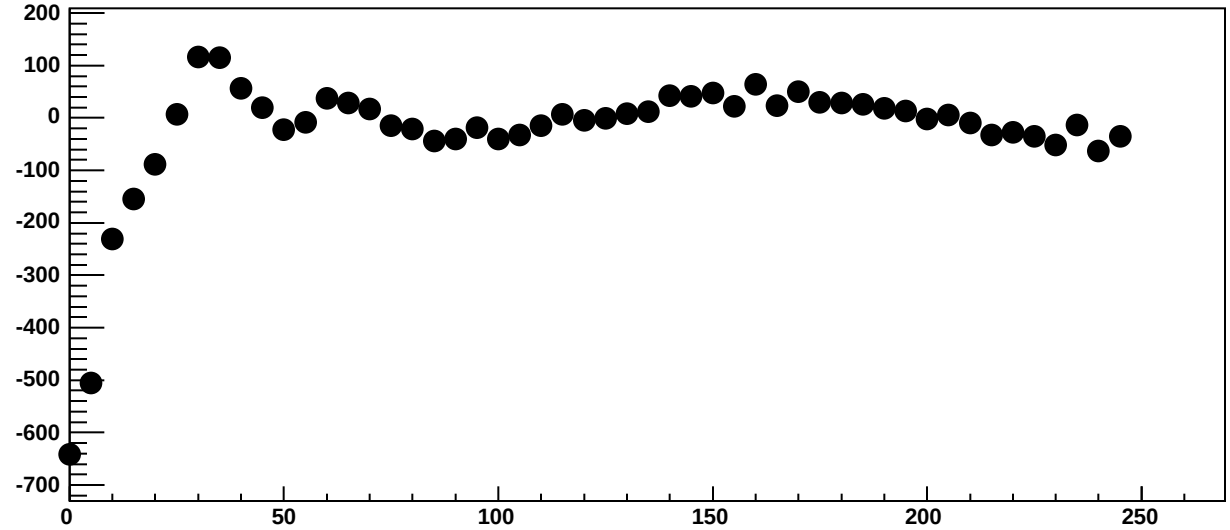
Chip 11, Channel 17, Enable 2, DAC=1600, ADC Mean vs Hold



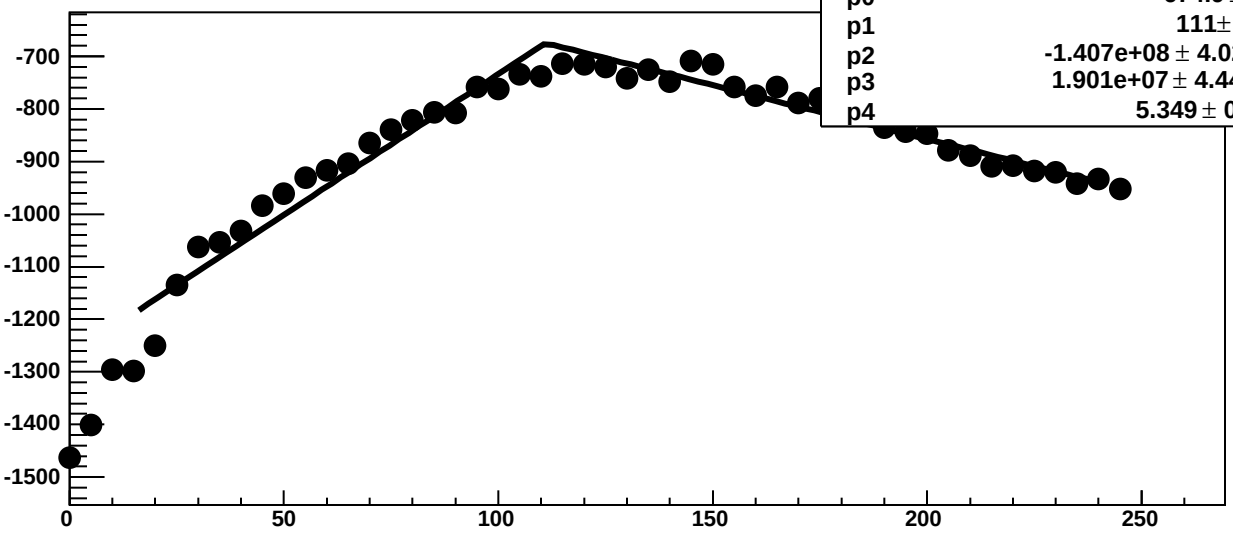
Chip 11, Channel 17, Enable 2, DAC=1600, ADC Noise vs Hold



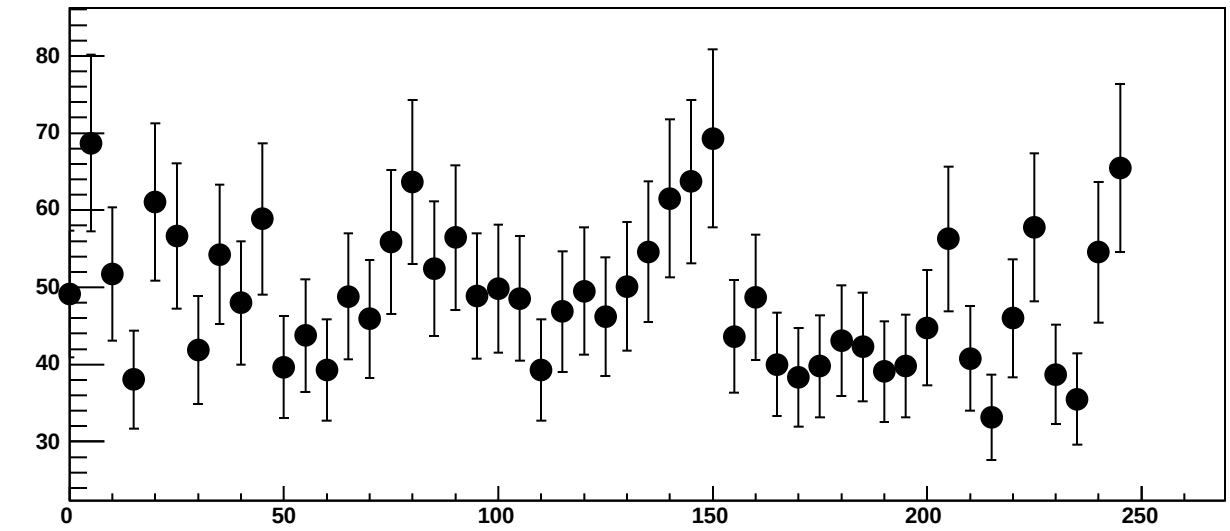
Chip 11, Channel 17, Enable 2, DAC=1600, ADC Residuals vs Hold



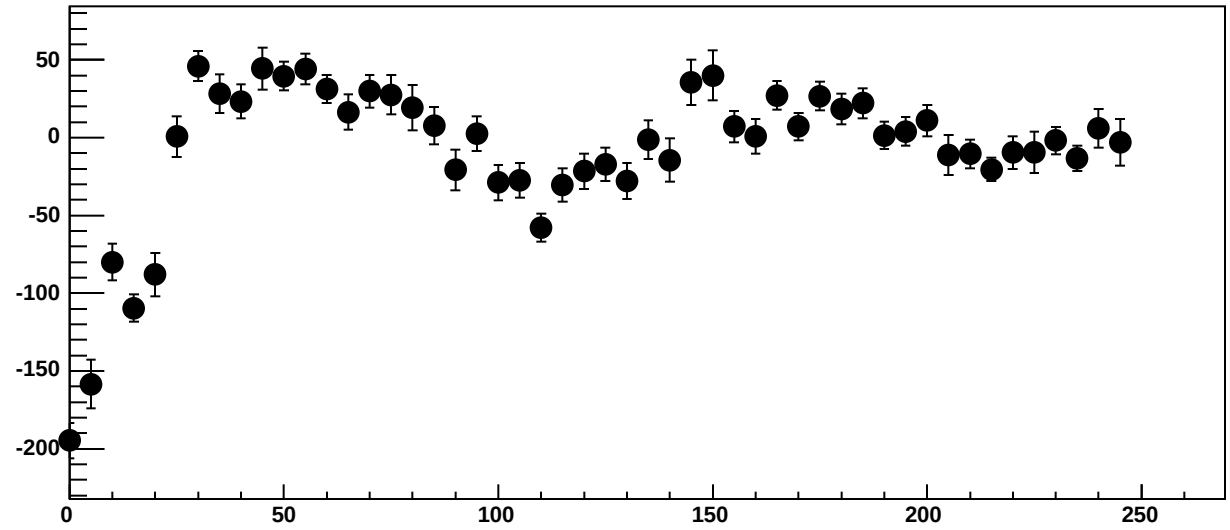
Chip 11, Channel 17, Enable 3, DAC=1600, ADC Mean vs Hold

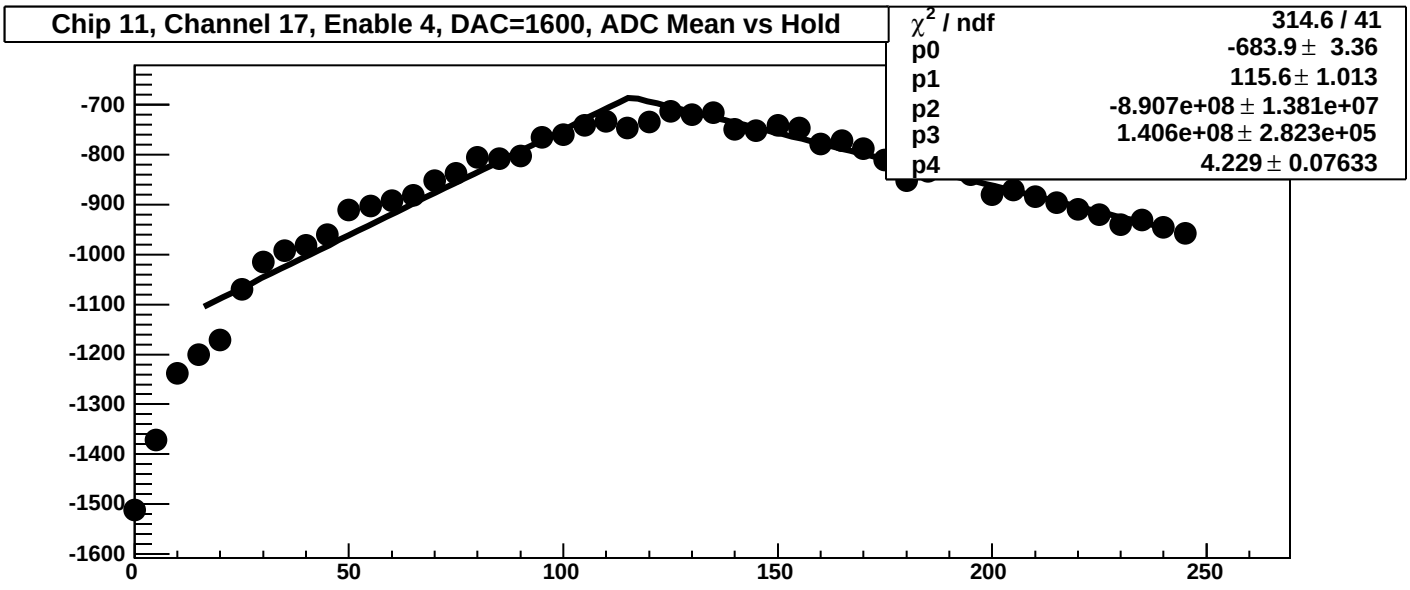


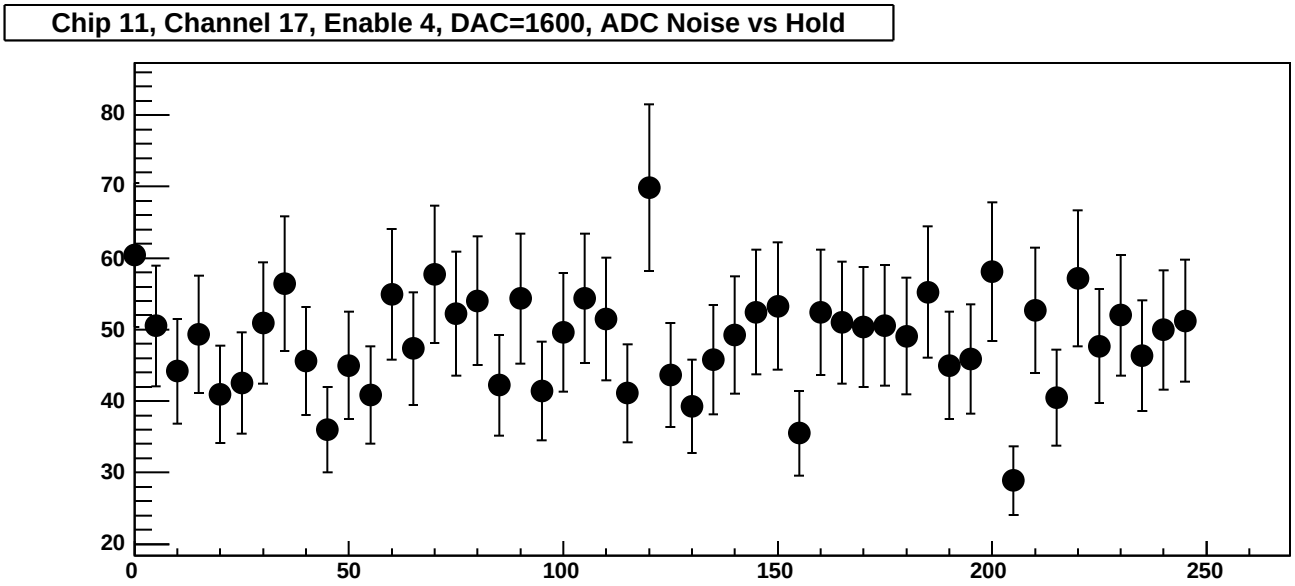
Chip 11, Channel 17, Enable 3, DAC=1600, ADC Noise vs Hold

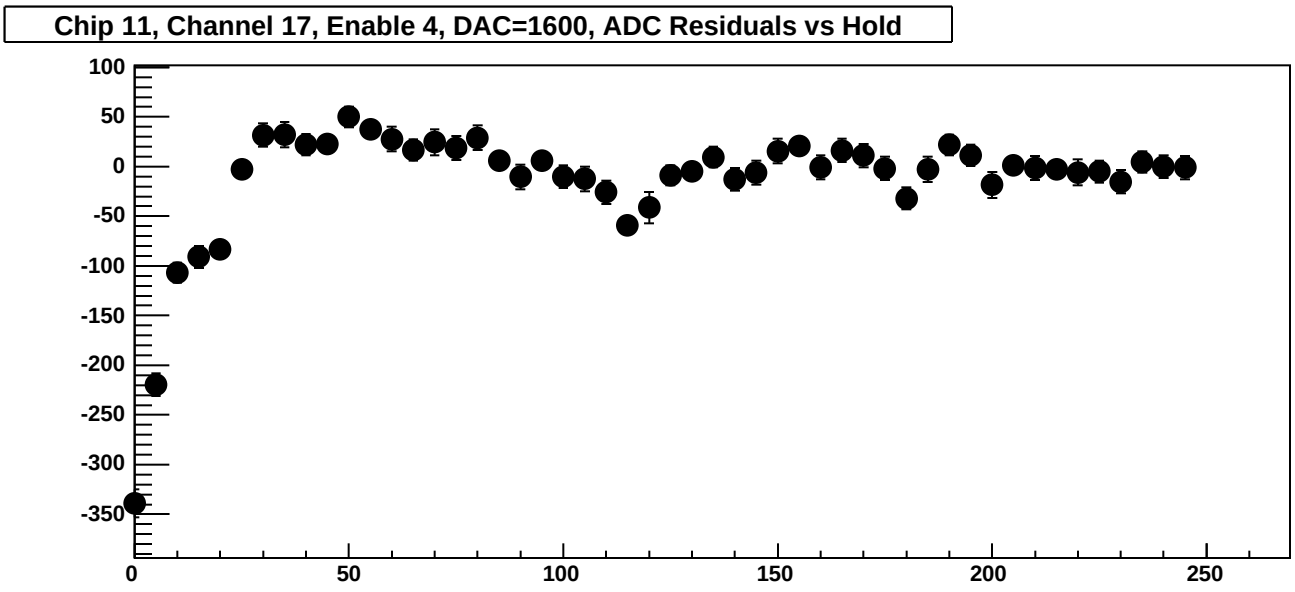


Chip 11, Channel 17, Enable 3, DAC=1600, ADC Residuals vs Hold




Chip 11, Channel 17, Enable 4, DAC=1600, ADC Mean vs Hold
χ² / ndf 314.6 / 41
p0 -683.9 ± 3.36
p1 115.6 ± 1.013
p2 -8.907e+08 ± 1.381e+07
p3 1.406e+08 ± 2.823e+05
p4 4.229 ± 0.07633
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
50
100
150
200
250


Chip 11, Channel 17, Enable 4, DAC=1600, ADC Noise vs Hold
80
70
60
50
40
30
20
0
50
100
150
200
250


Chip 11, Channel 17, Enable 4, DAC=1600, ADC Residuals vs Hold
100
50
0
-50
-100
-150
-200
-250
-300
-350
0
50
100
150
200
250