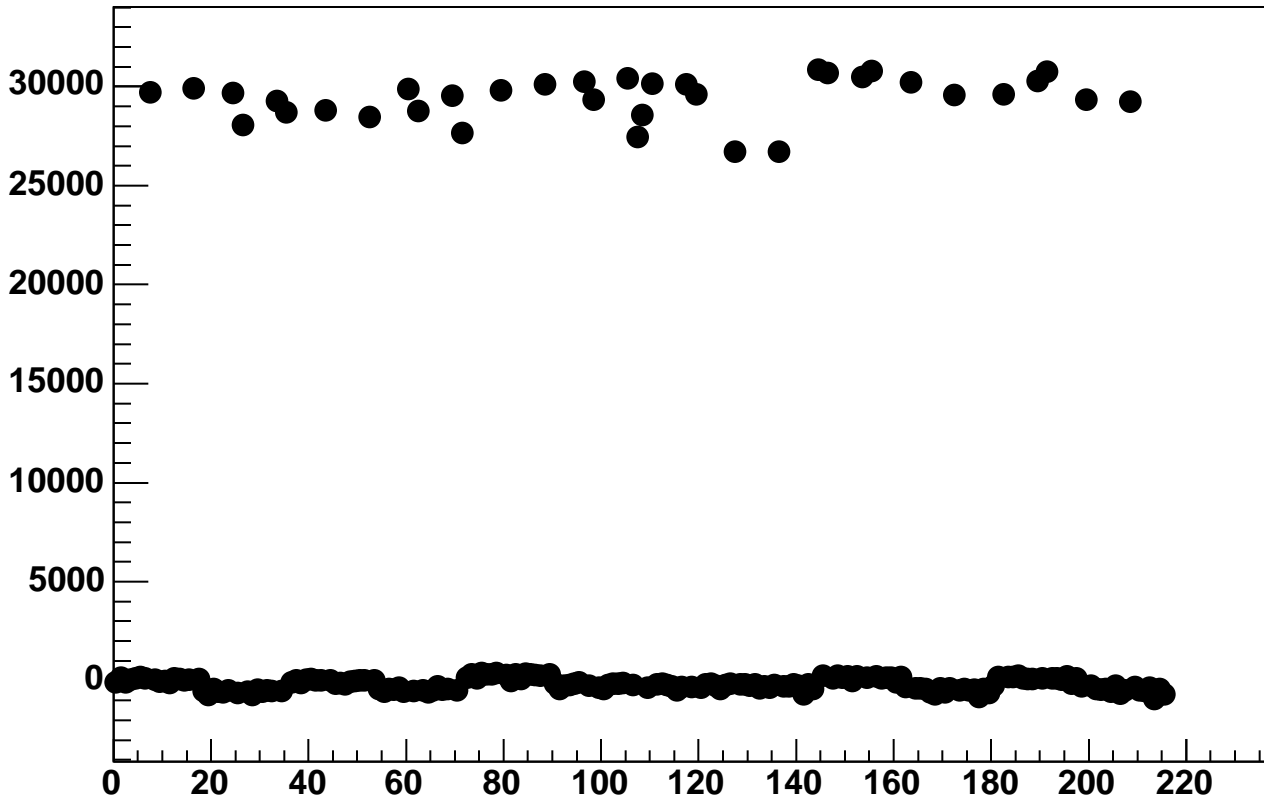
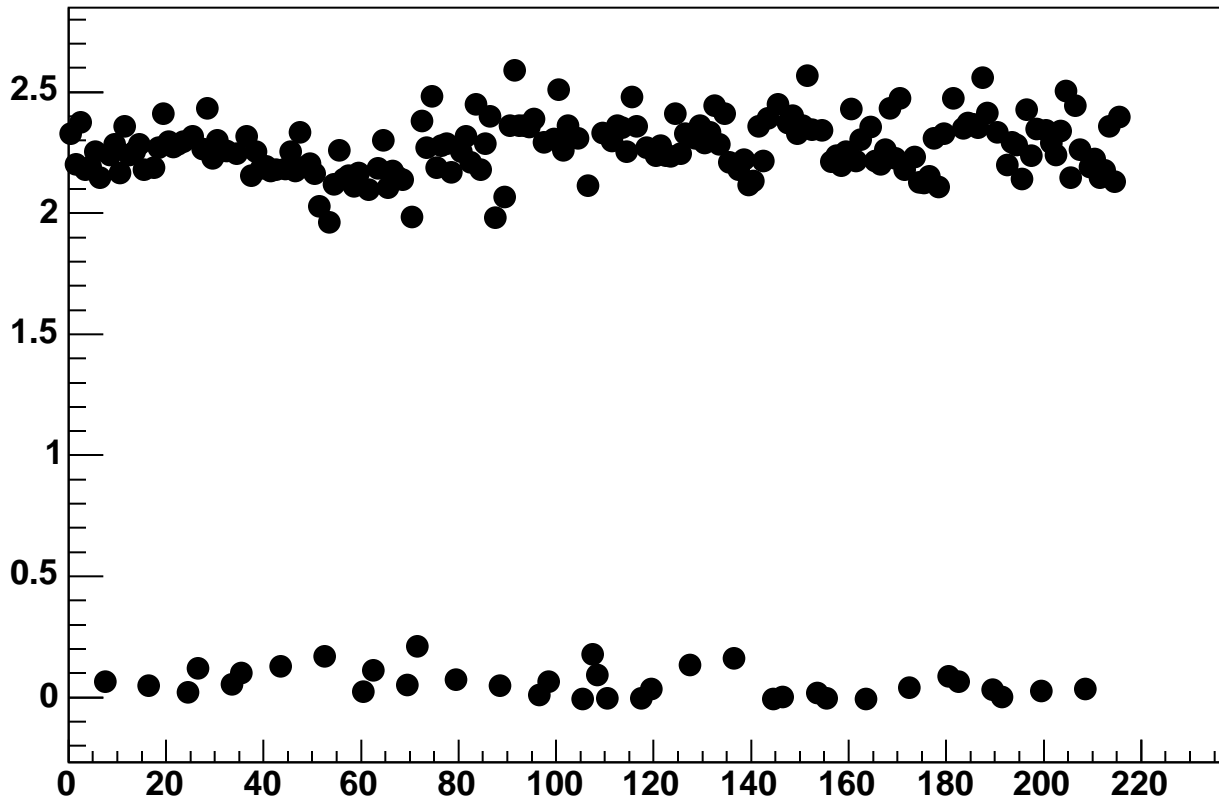


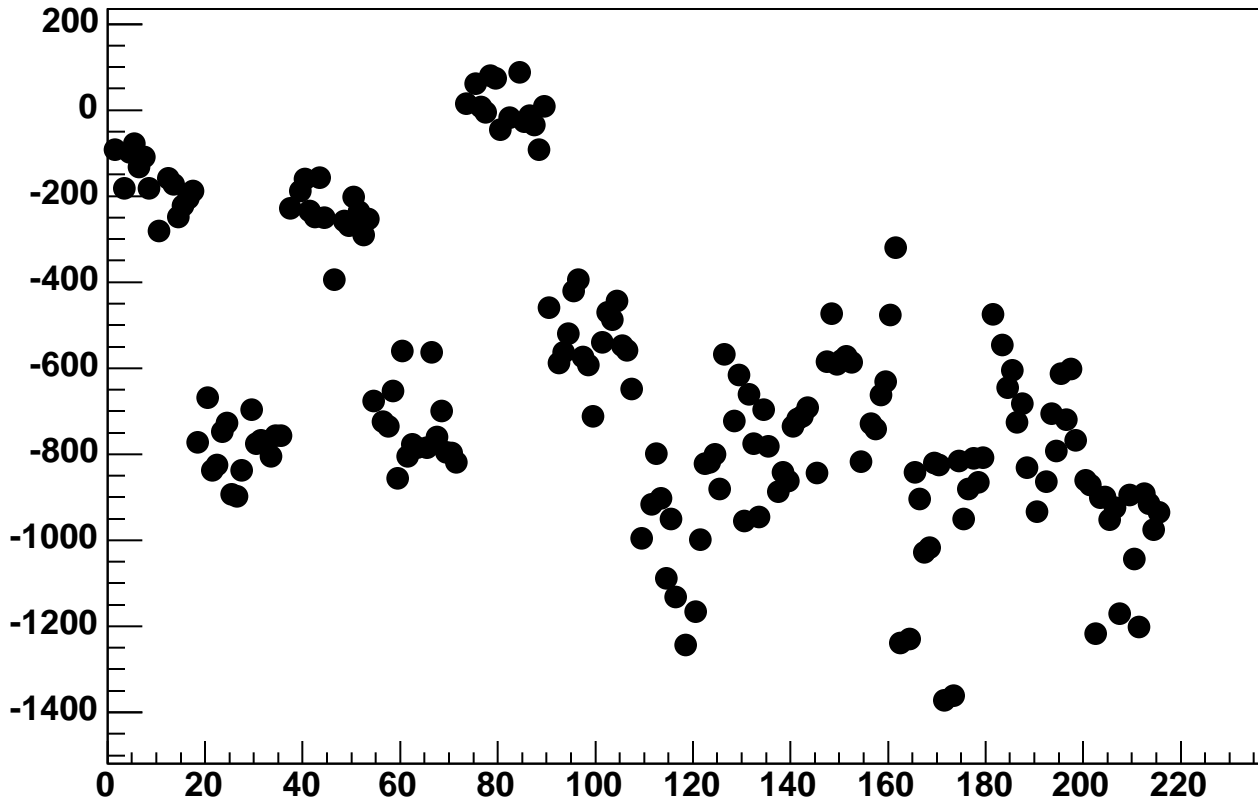
Channel Enabled, Hold=35, Fit Intercept vs $18 \cdot \text{Chip} + \text{Chan}$



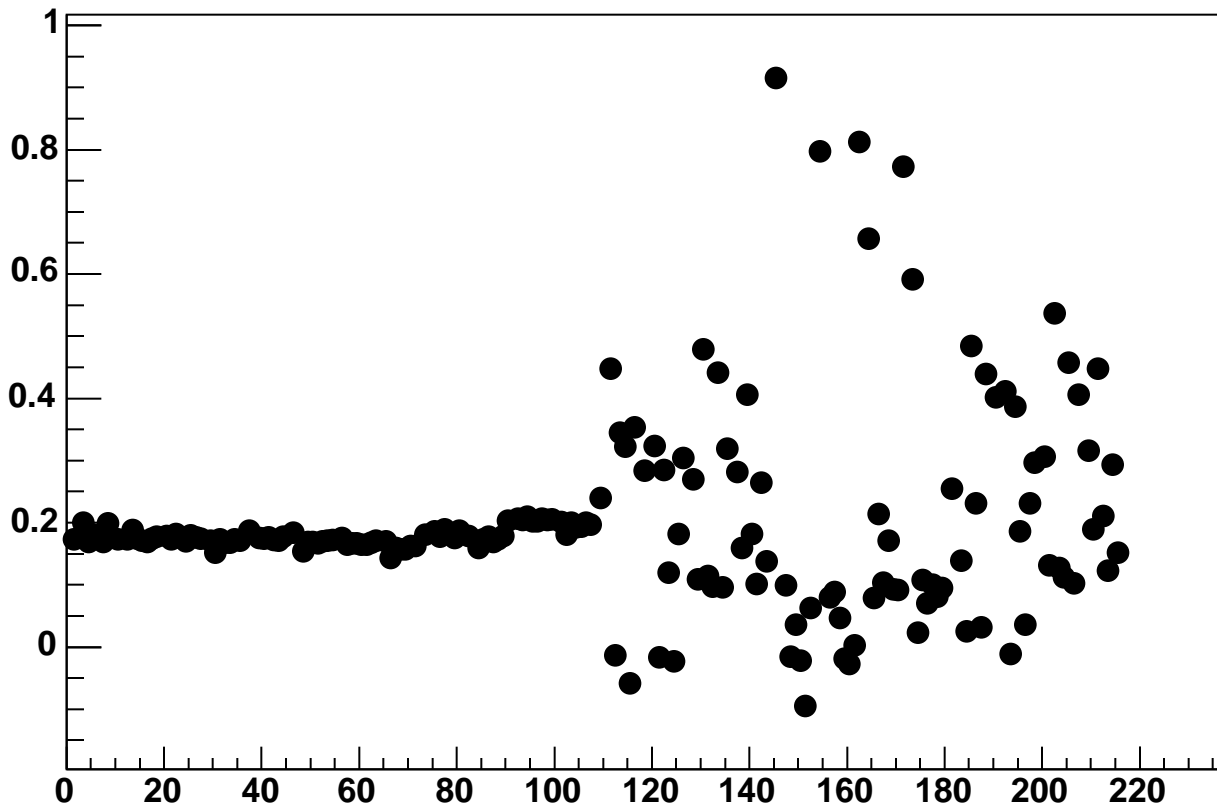
Channel Enabled, Hold=35, Fit Slope vs $18 \cdot \text{Chip} + \text{Chan}$



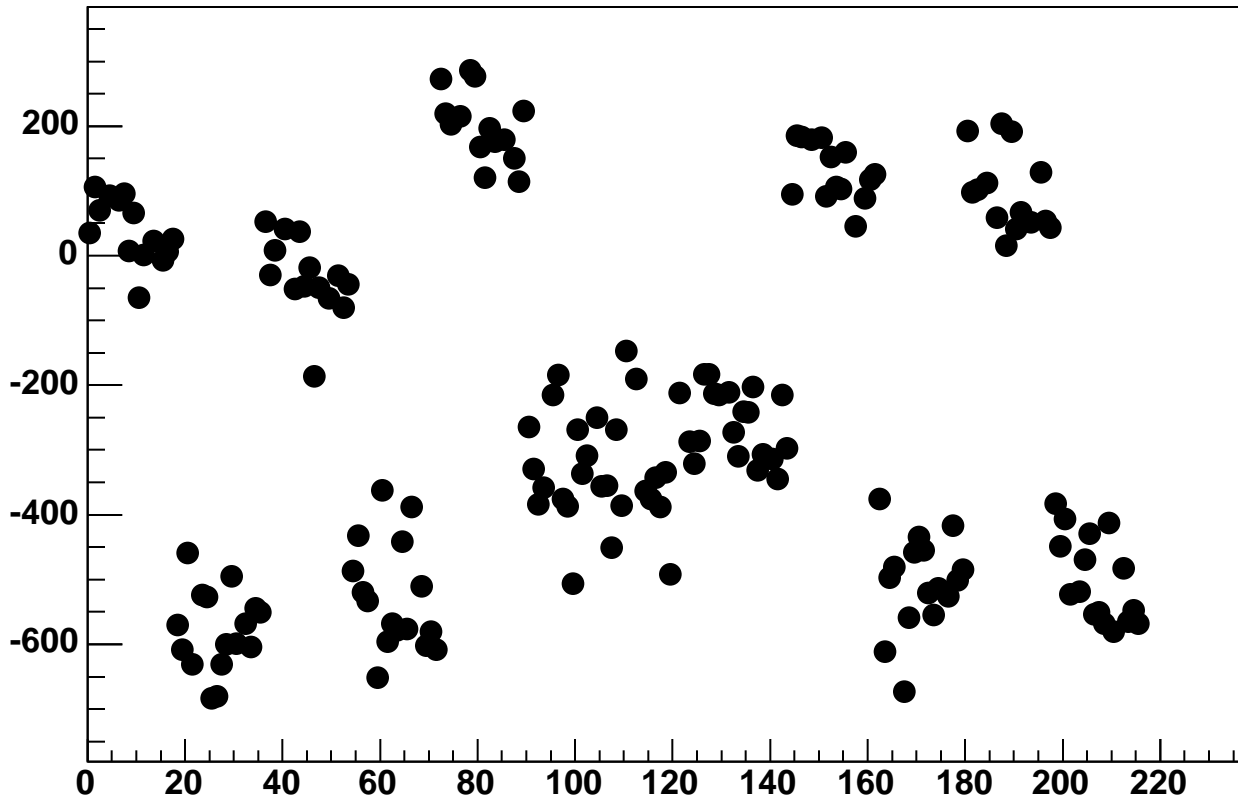
Disabled 0, Hold=35, Fit Intercept vs $18 \cdot \text{Chip} + \text{Chan}$



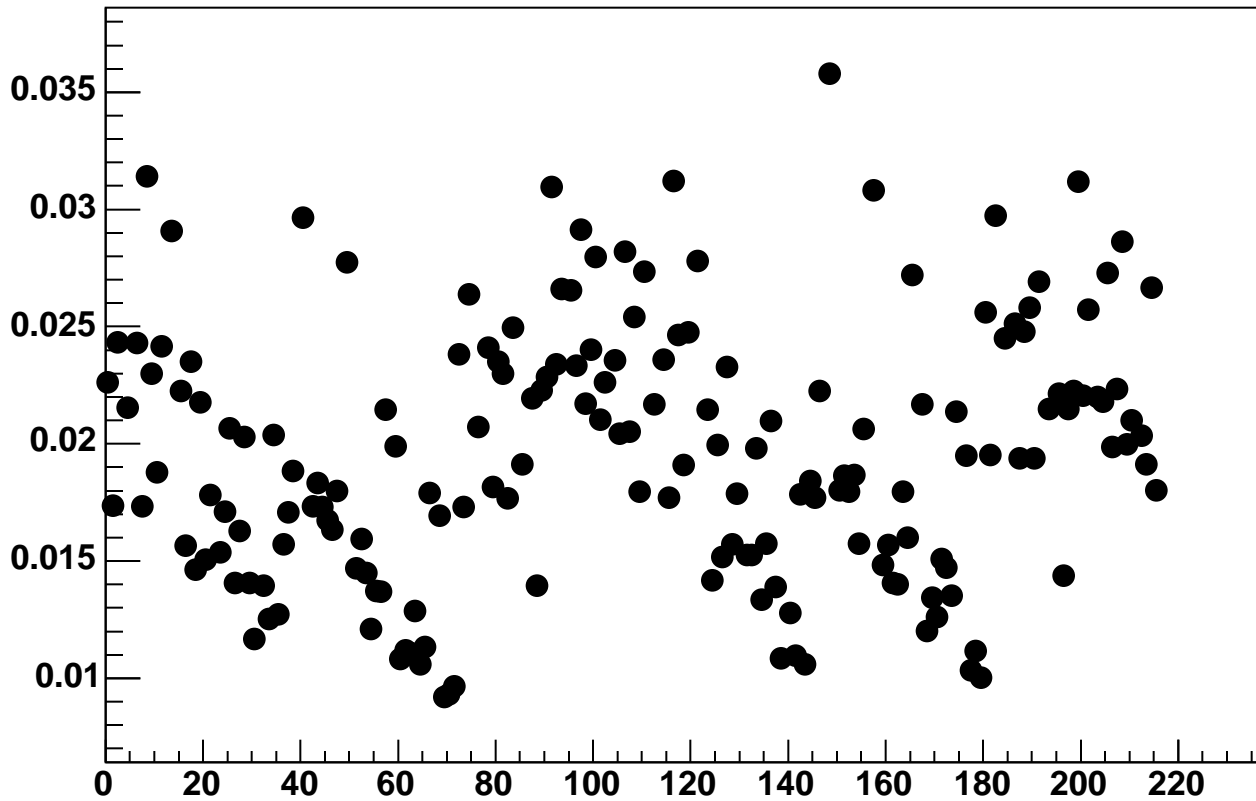
Disabled 0, Hold=35, Fit Slope vs $18 \cdot \text{Chip} + \text{Chan}$



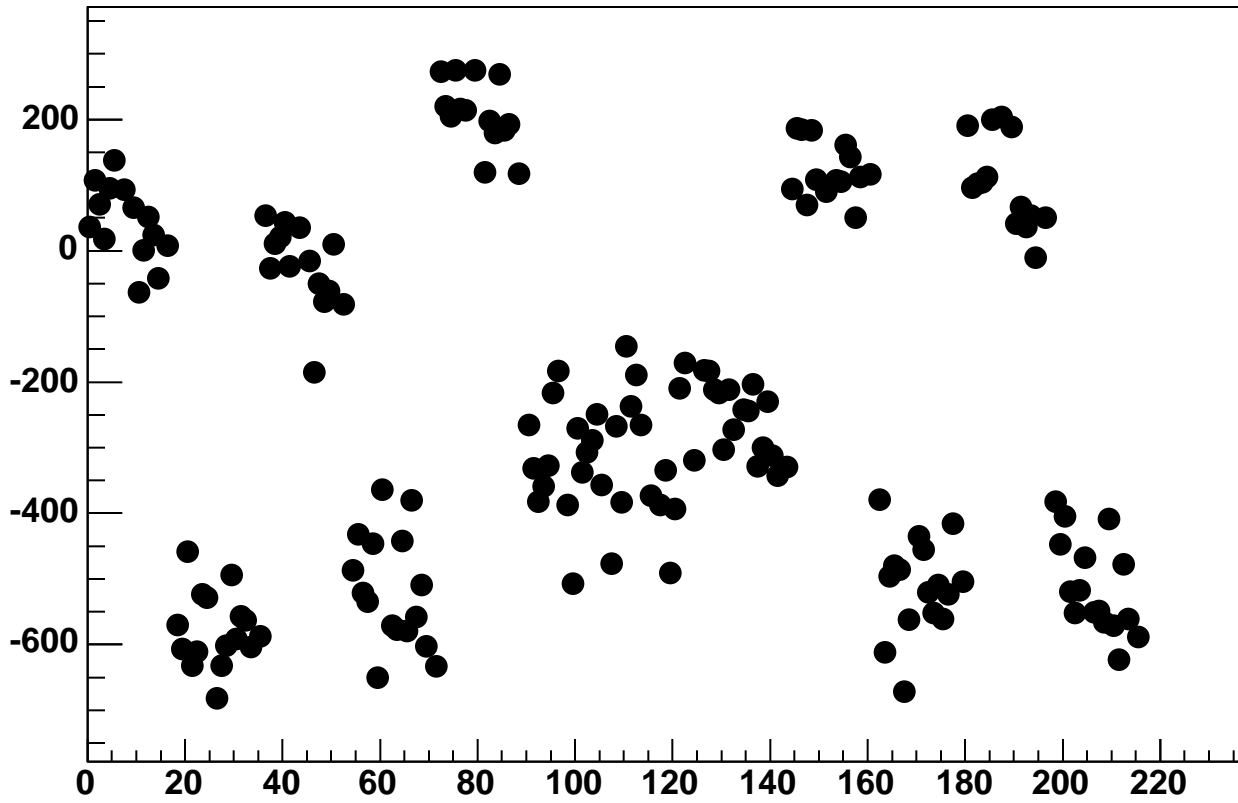
Disabled 1, Hold=35, Fit Intercept vs 18*Chip+Chan



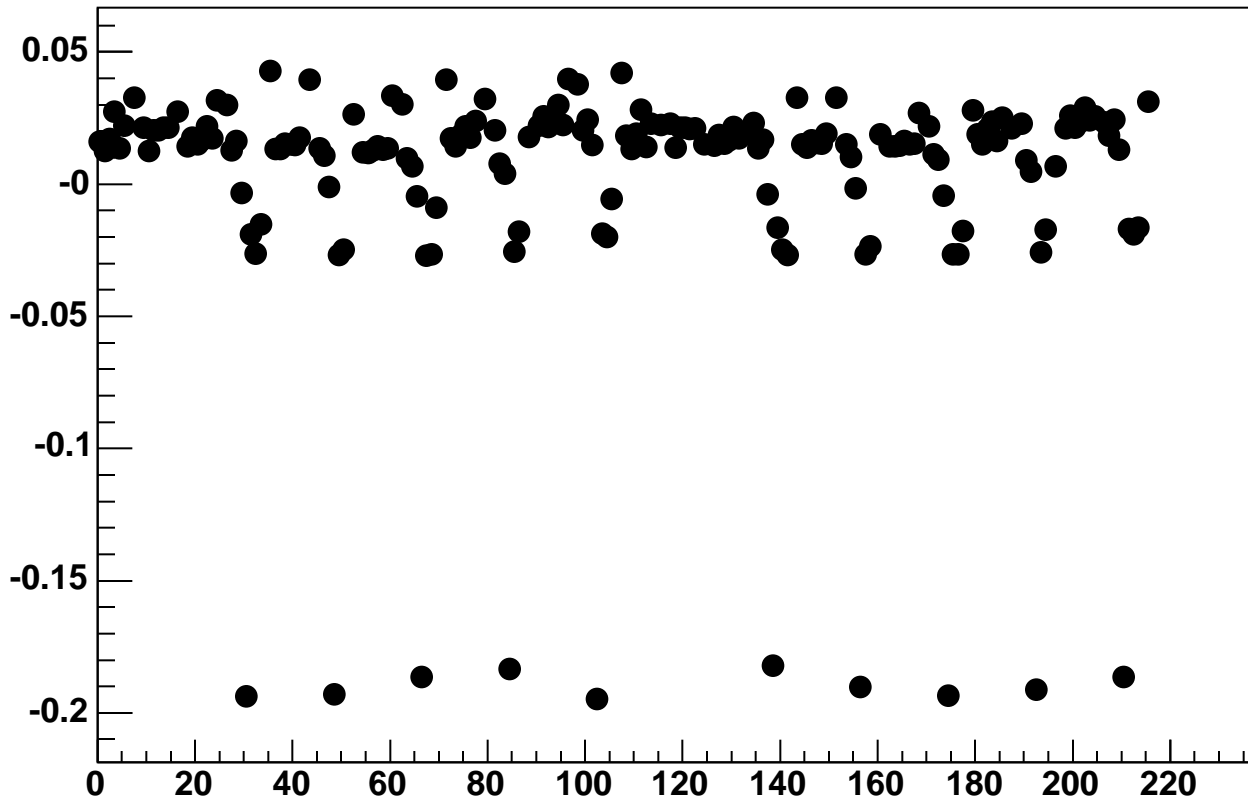
Disabled 1, Hold=35, Fit Slope vs 18*Chip+Chan



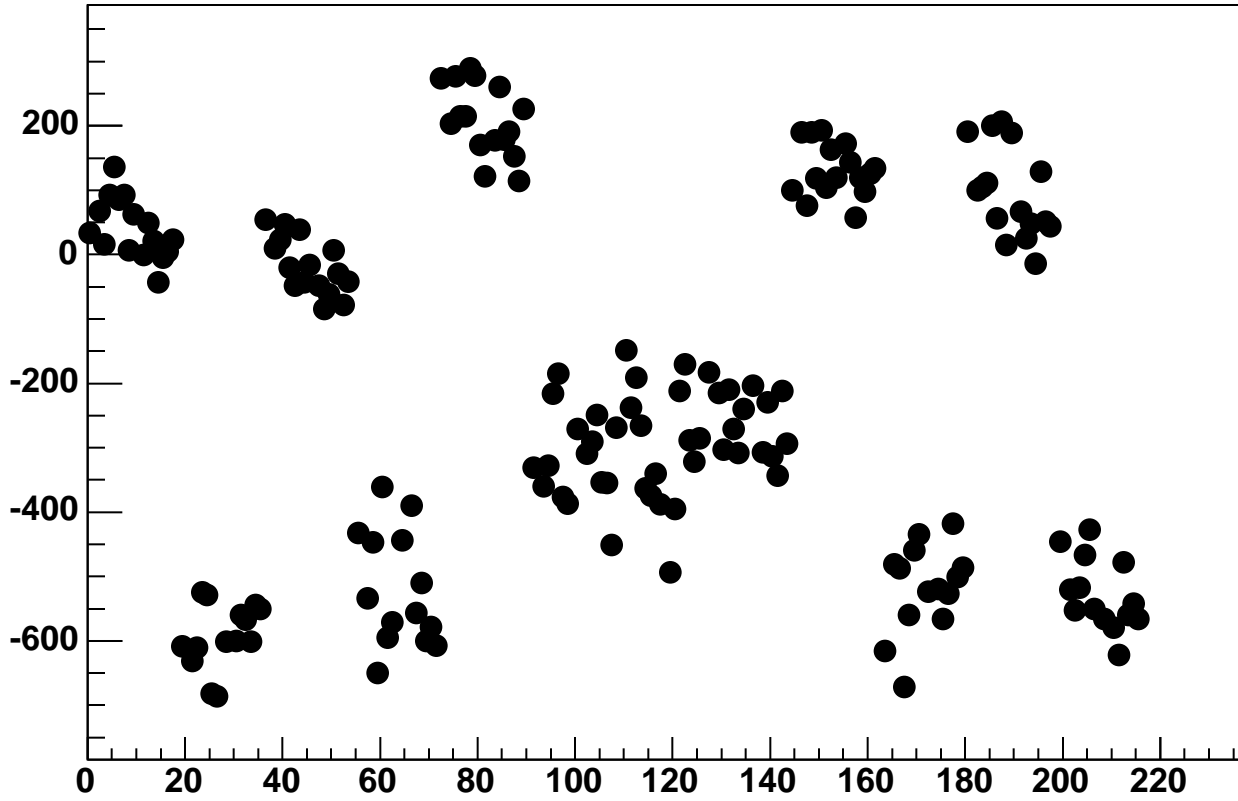
Disabled 2, Hold=35, Fit Intercept vs $18 \cdot \text{Chip} + \text{Chan}$



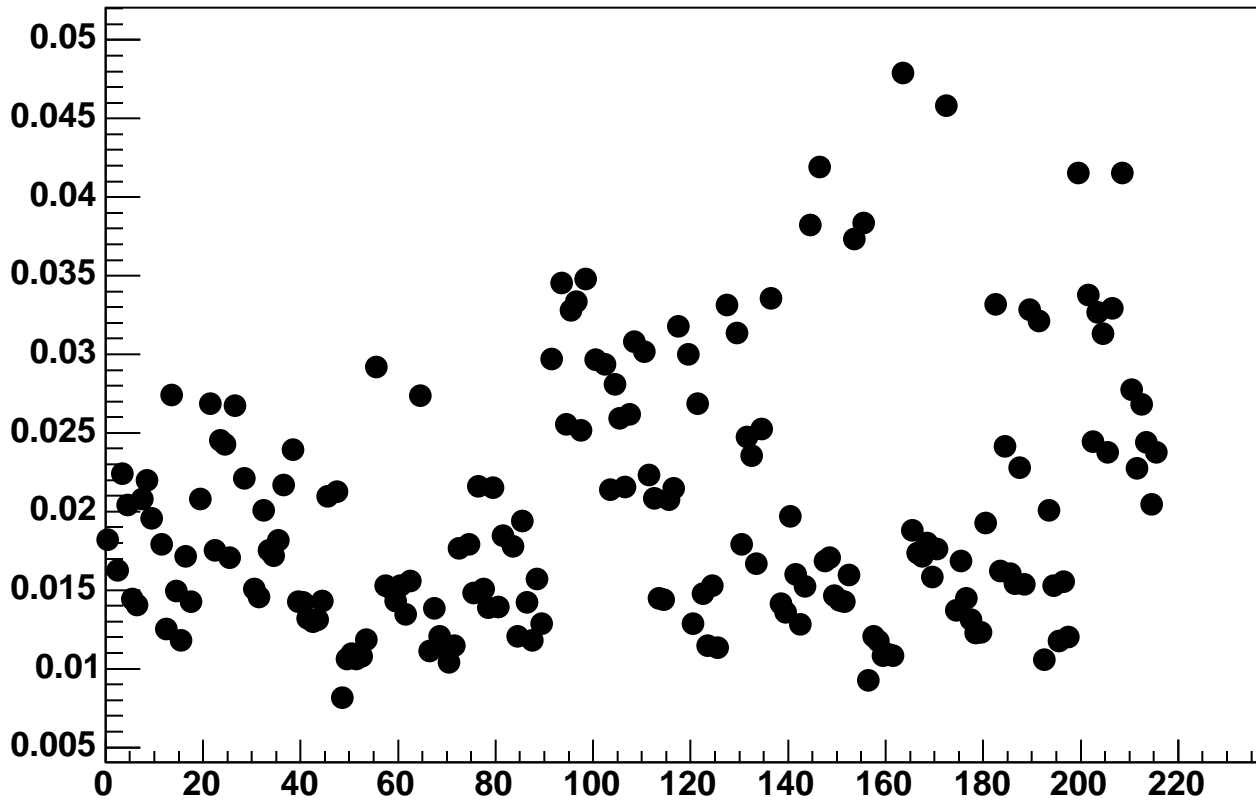
Disabled 2, Hold=35, Fit Slope vs $18 \cdot \text{Chip} + \text{Chan}$



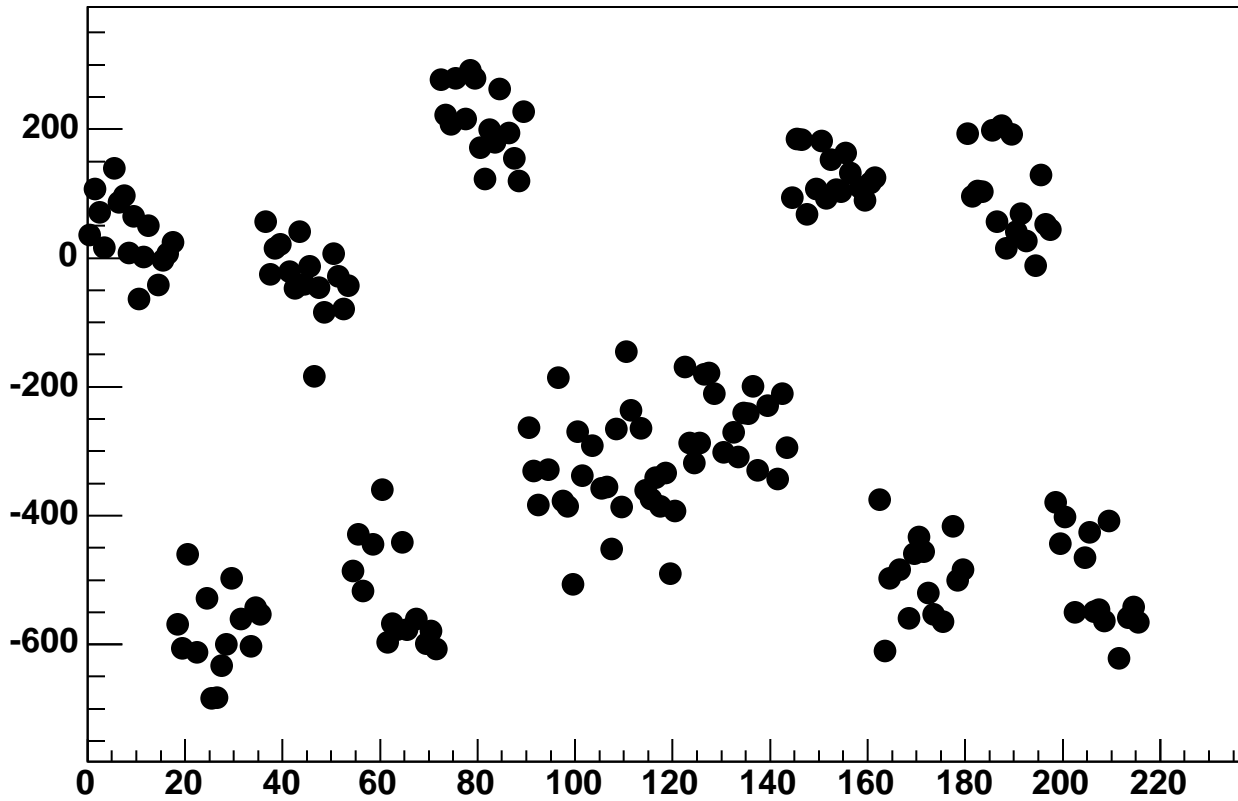
Disabled 3, Hold=35, Fit Intercept vs 18*Chip+Chan



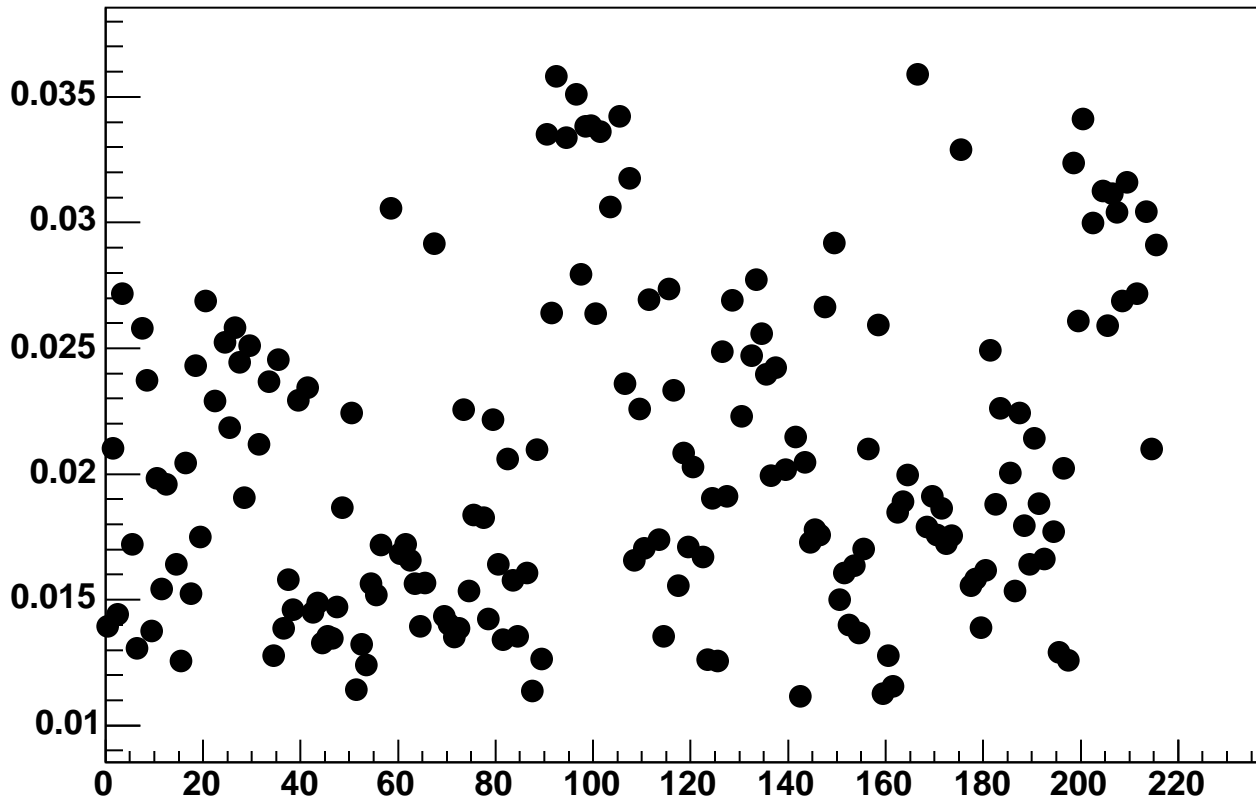
Disabled 3, Hold=35, Fit Slope vs 18*Chip+Chan



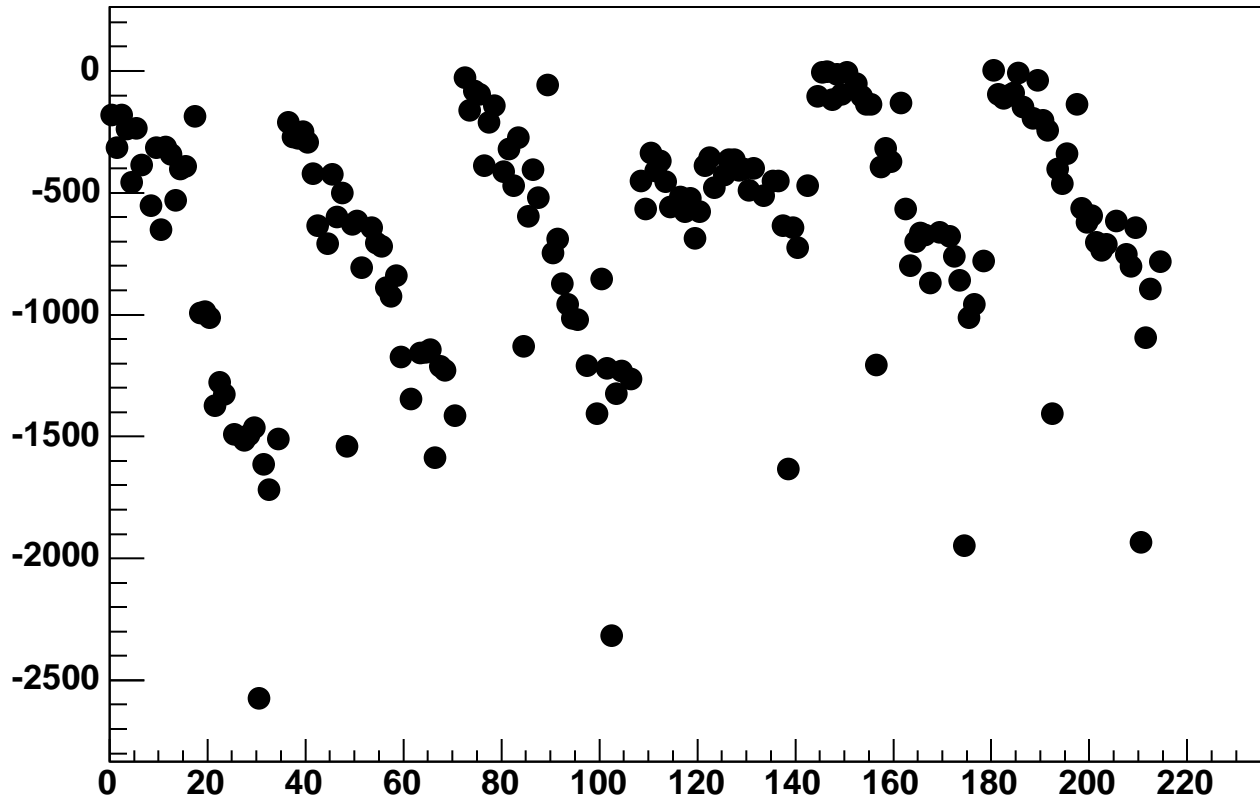
Disabled 4, Hold=35, Fit Intercept vs $18 \cdot \text{Chip} + \text{Chan}$



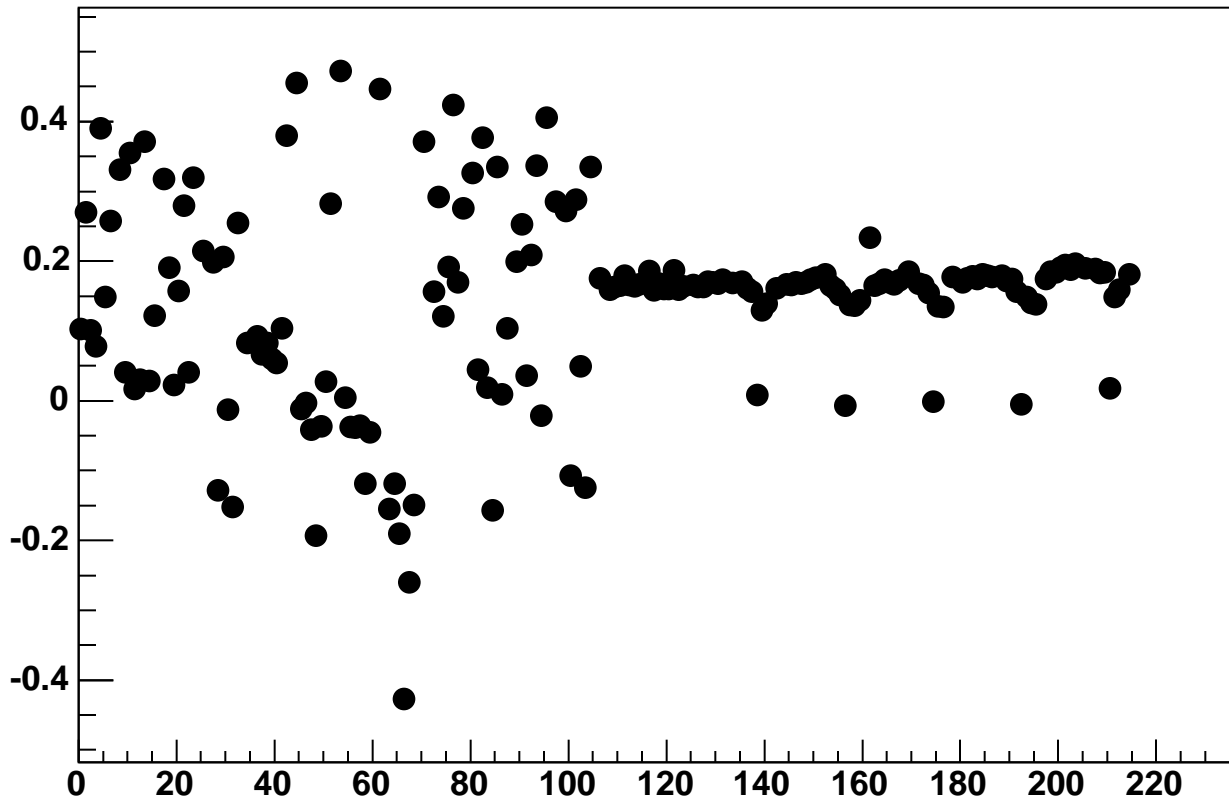
Disabled 4, Hold=35, Fit Slope vs $18 \cdot \text{Chip} + \text{Chan}$



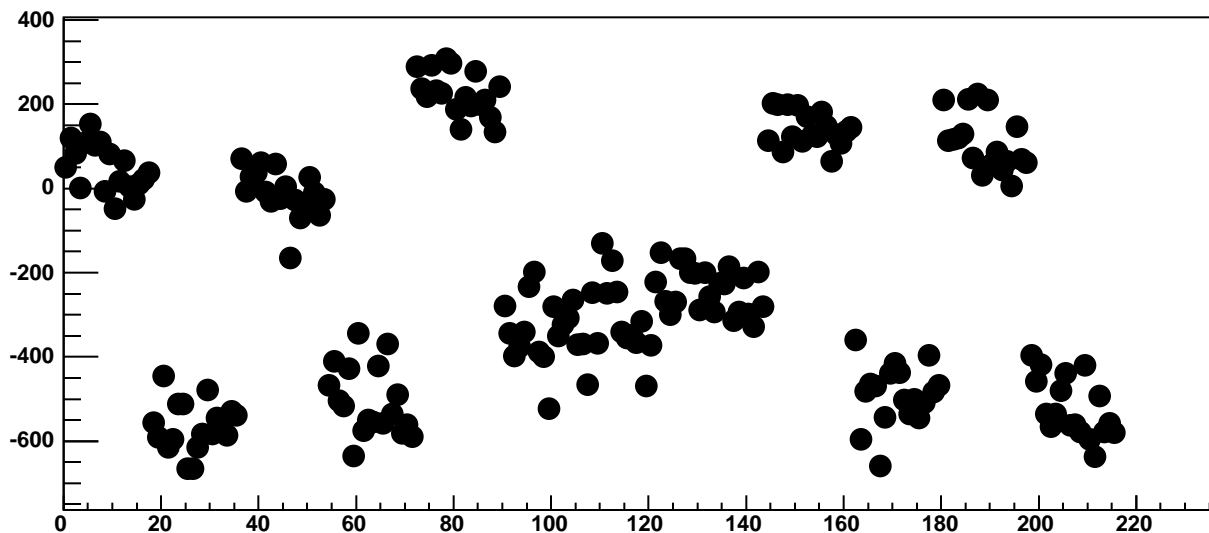
Disabled 5, Hold=35, Fit Intercept vs $18 \cdot \text{Chip} + \text{Chan}$



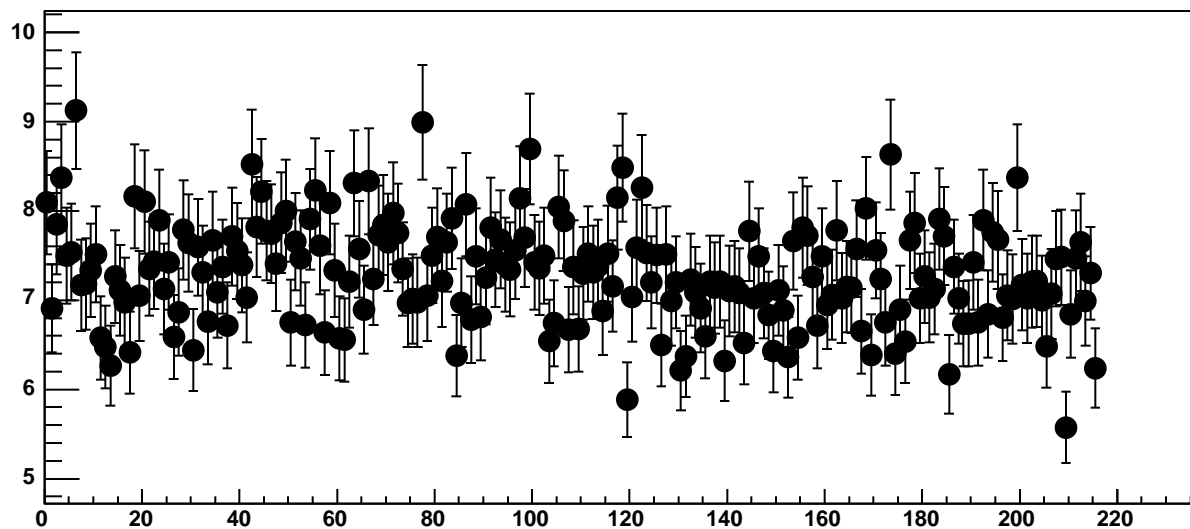
Disabled 5, Hold=35, Fit Slope vs $18 \cdot \text{Chip} + \text{Chan}$



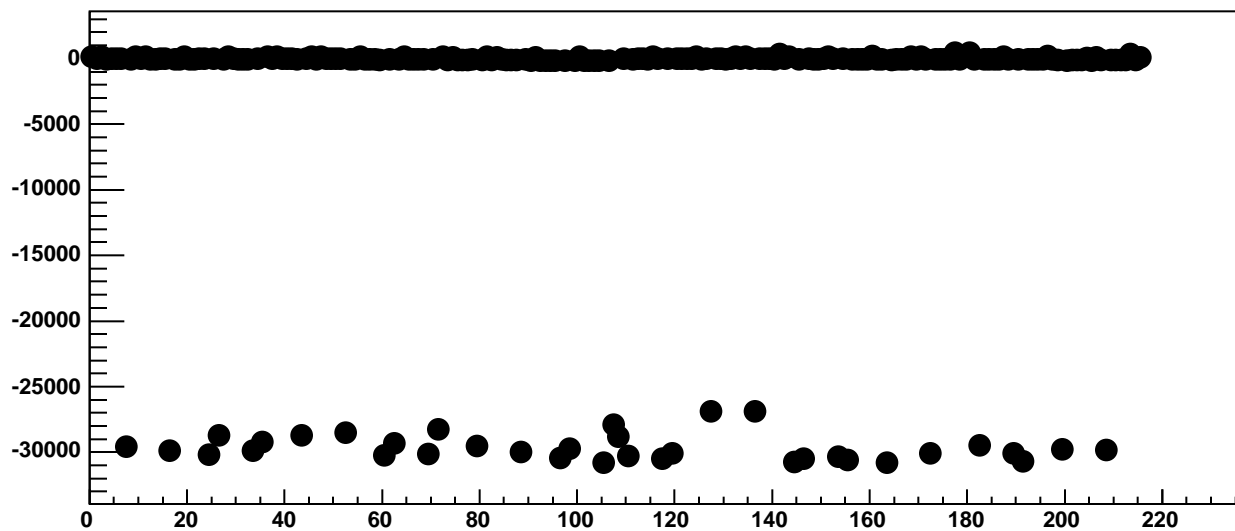
Enables off, ADC Mean vs 18*Chip+Chan



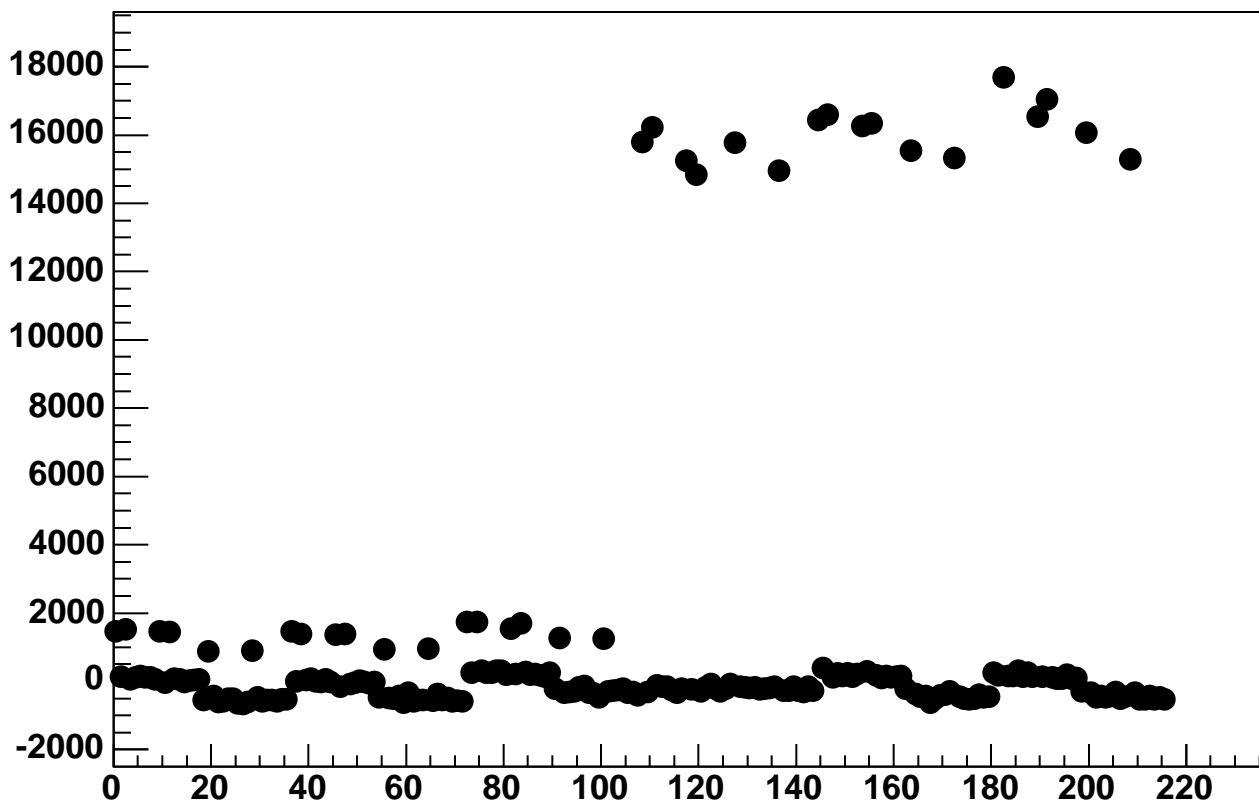
Enables off, ADC Noise vs 18*Chip+Chan



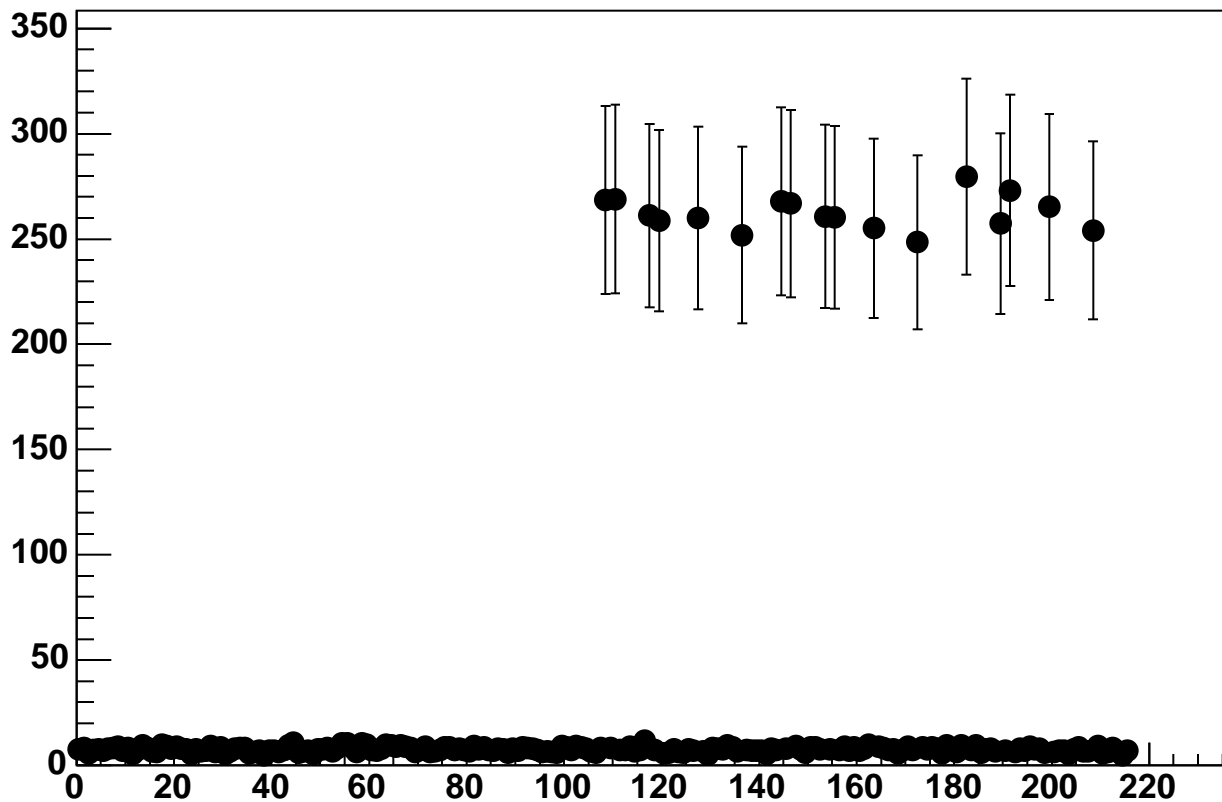
Enables off, ADC Residuals vs 18*Chip+Chan



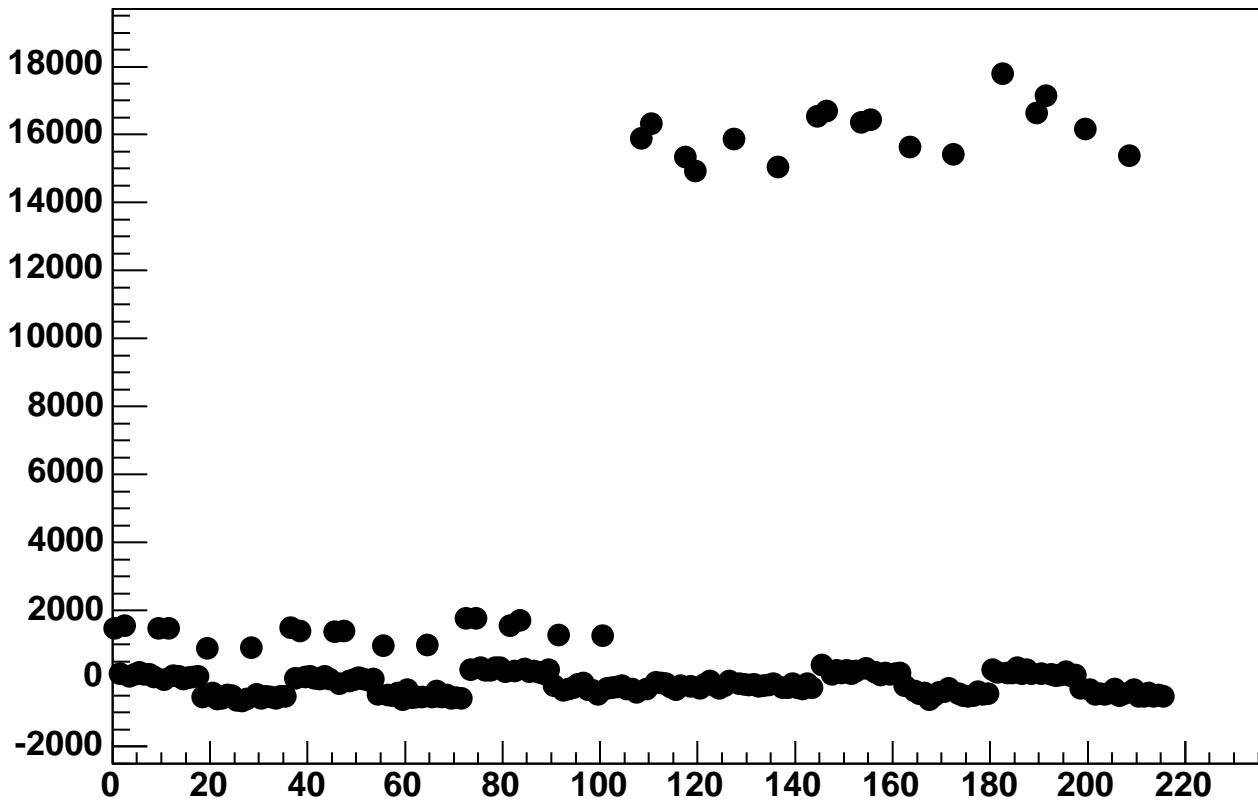
Enable 0, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



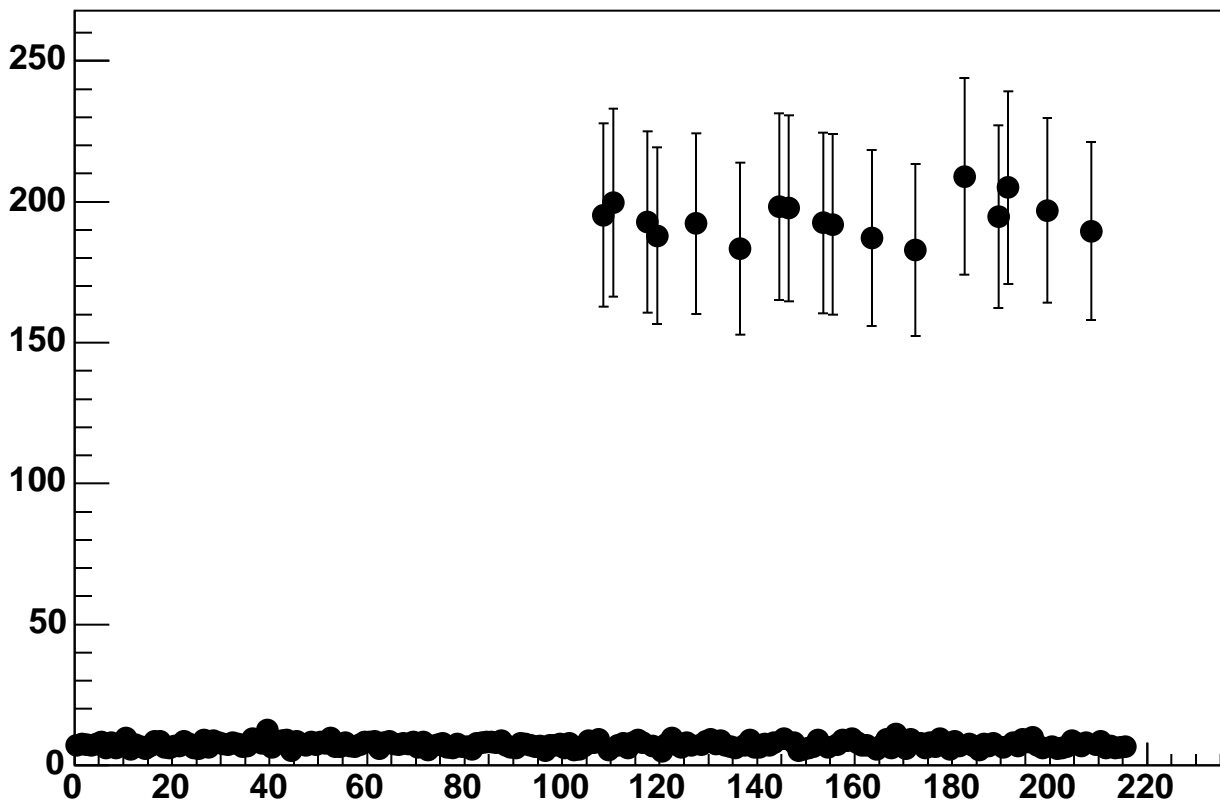
Enable 0, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



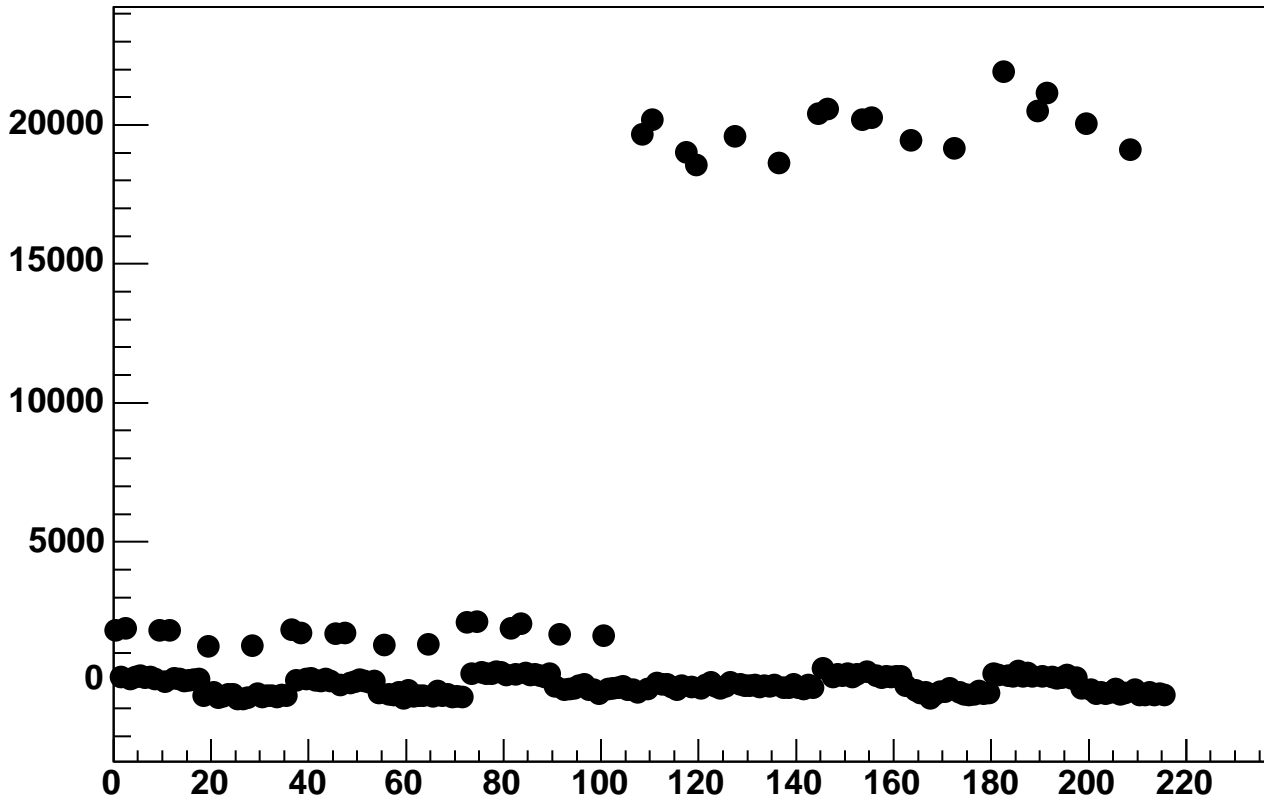
Enable 0, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



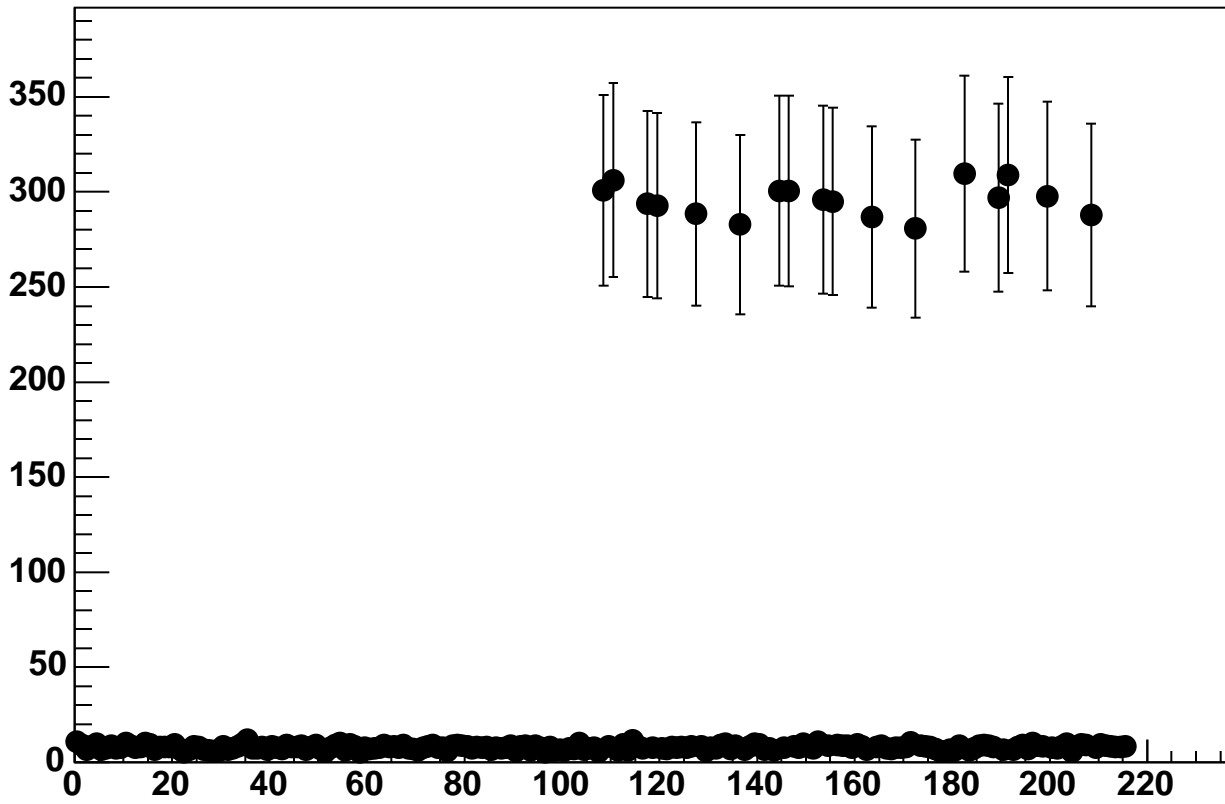
Enable 0, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



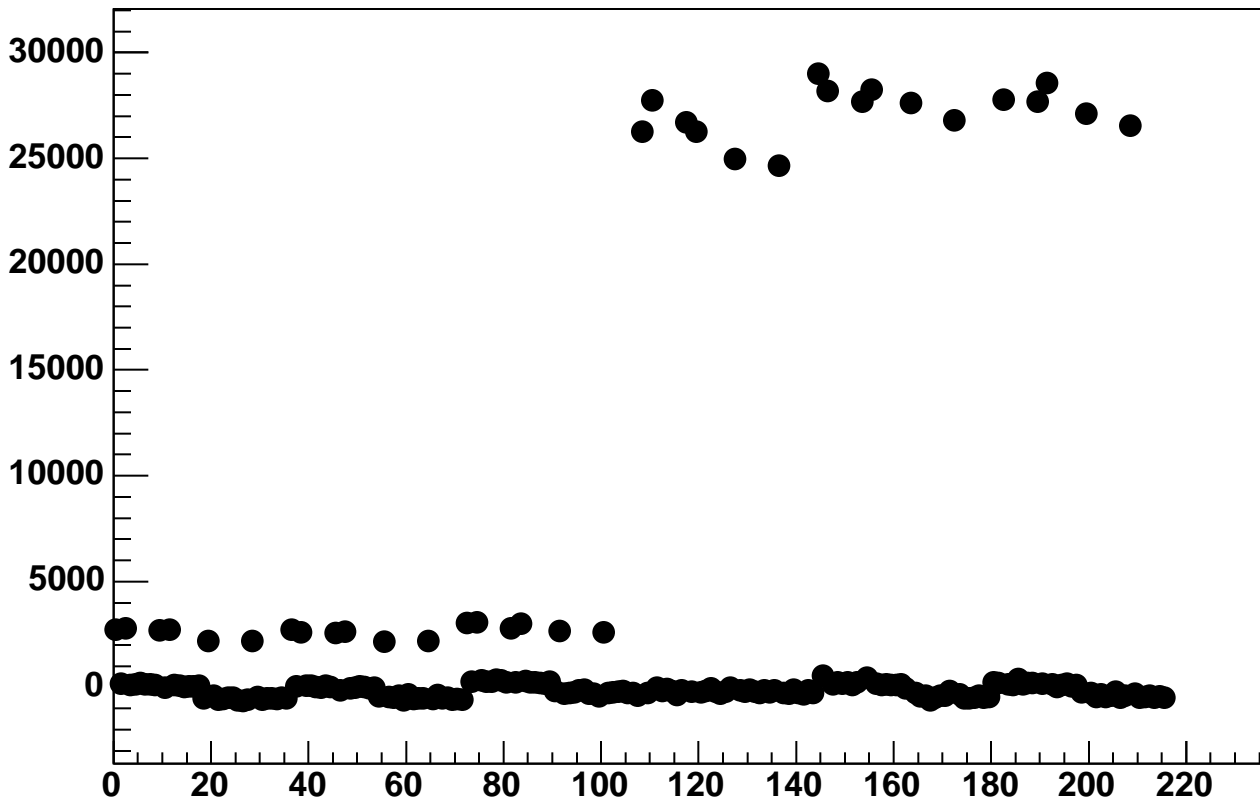
Enable 0, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



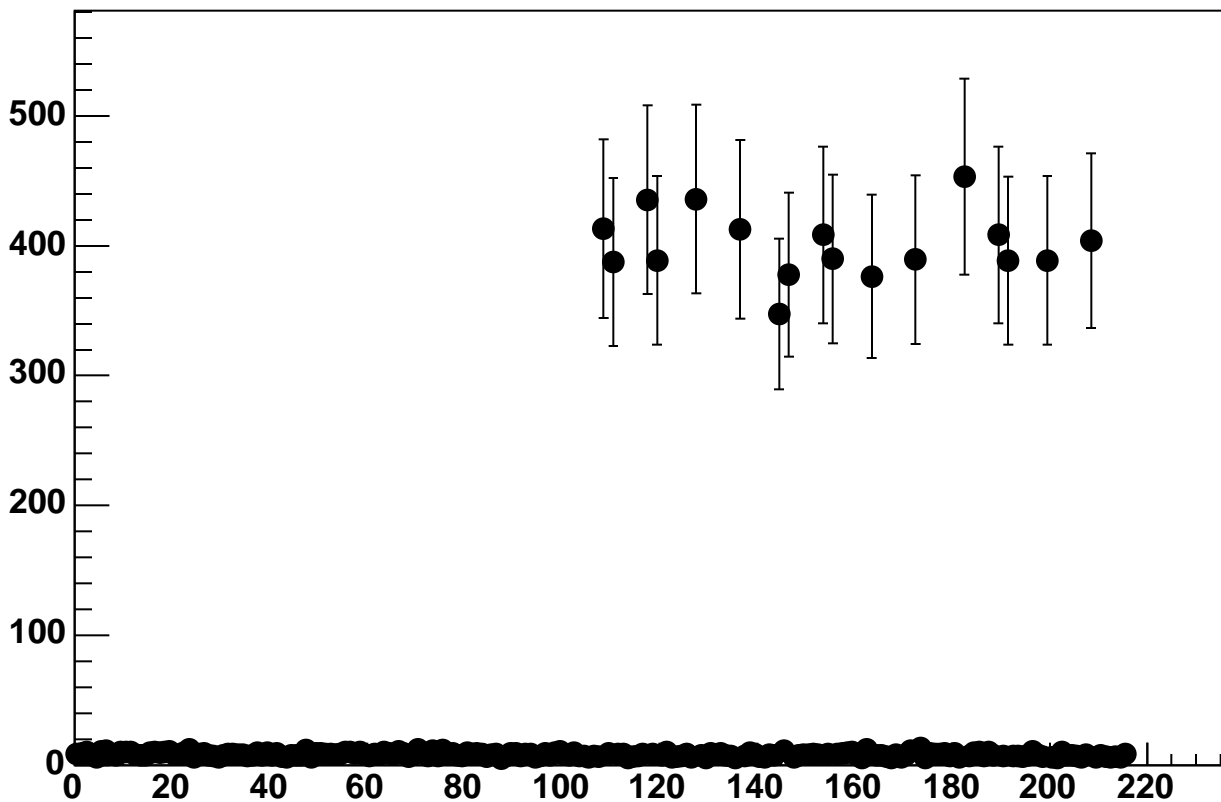
Enable 0, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



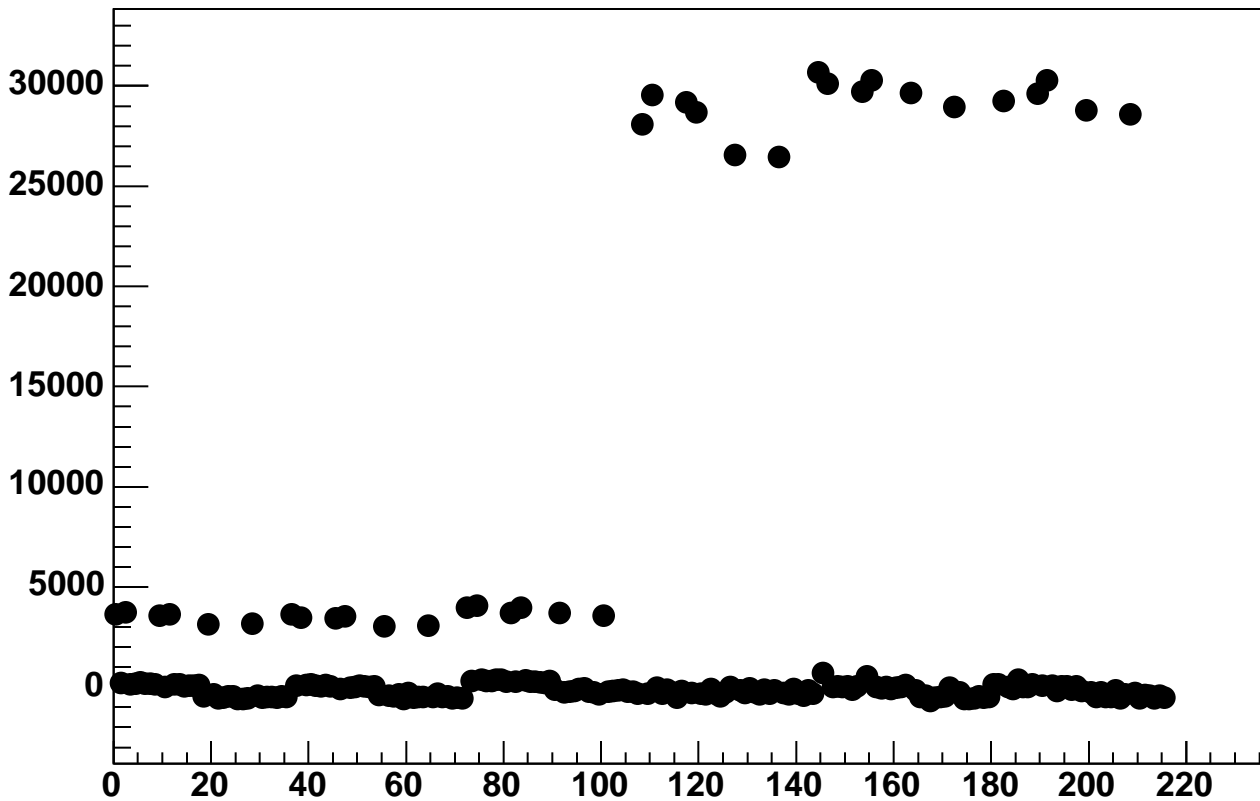
Enable 0, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



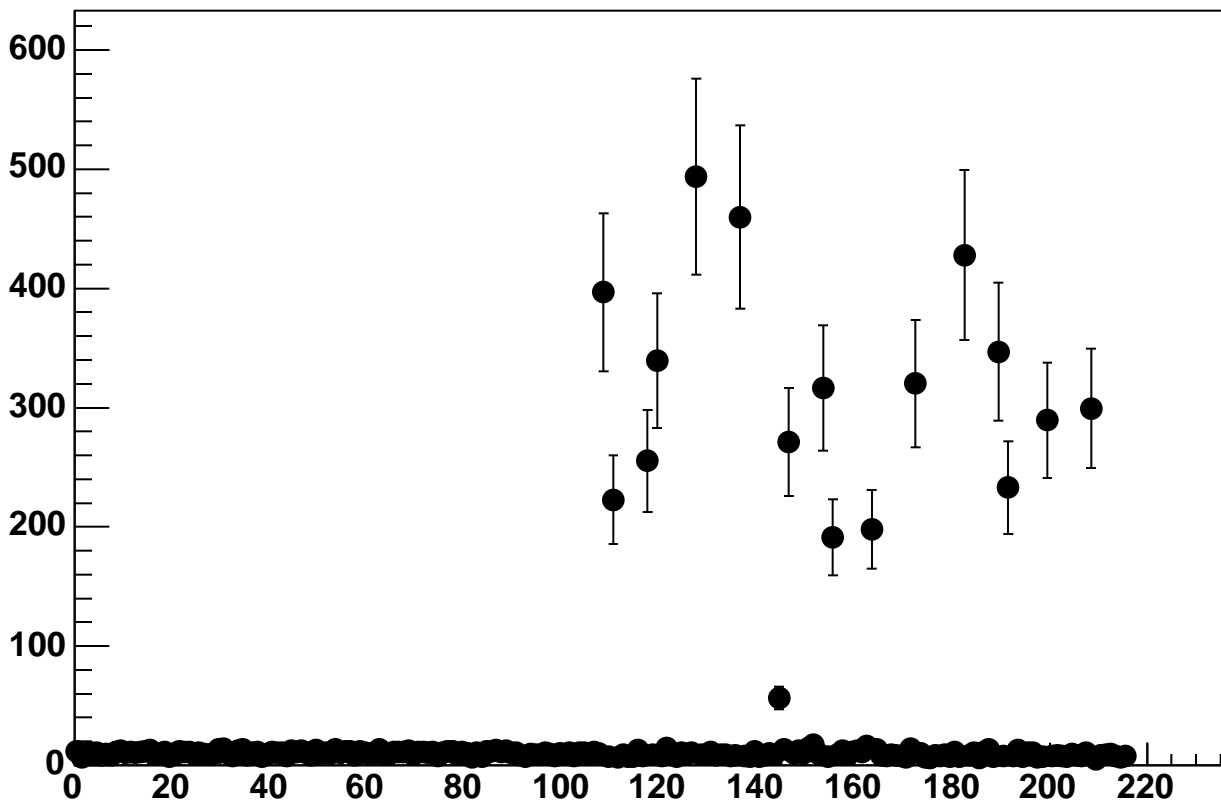
Enable 0, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



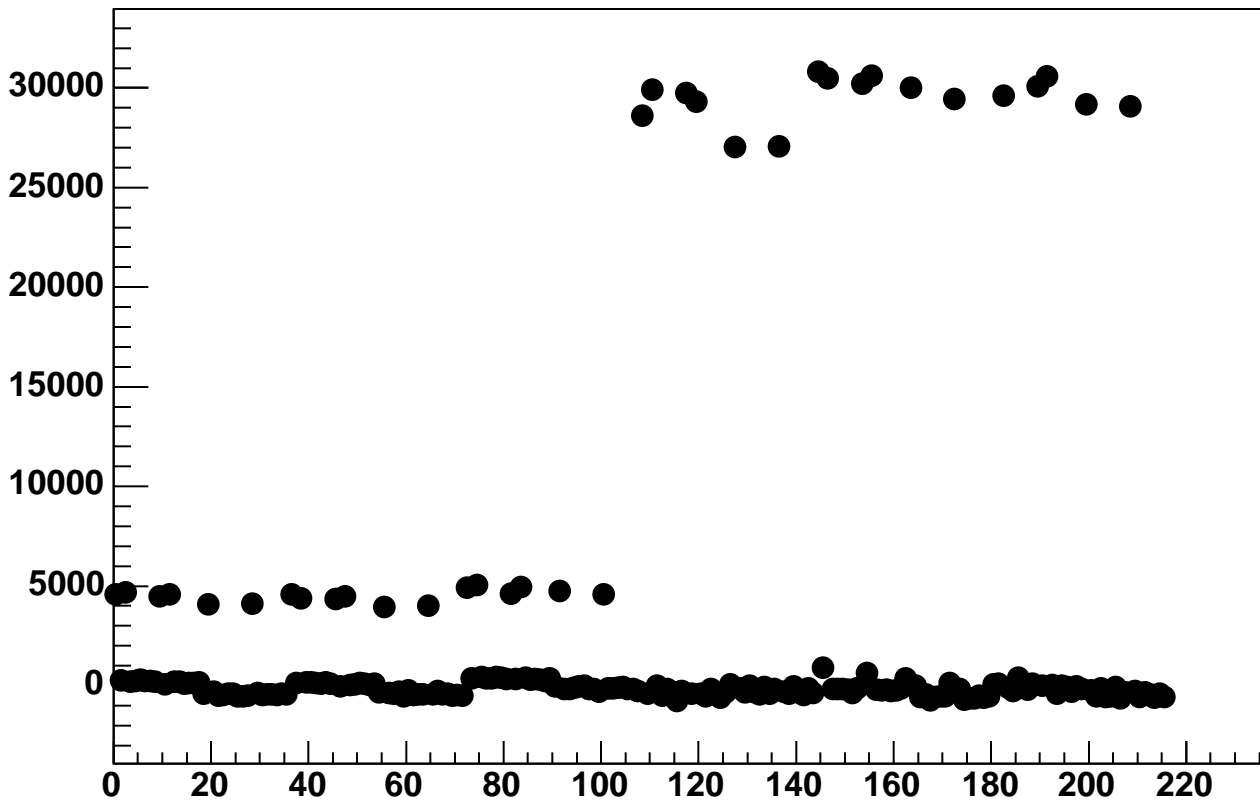
Enable 0, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



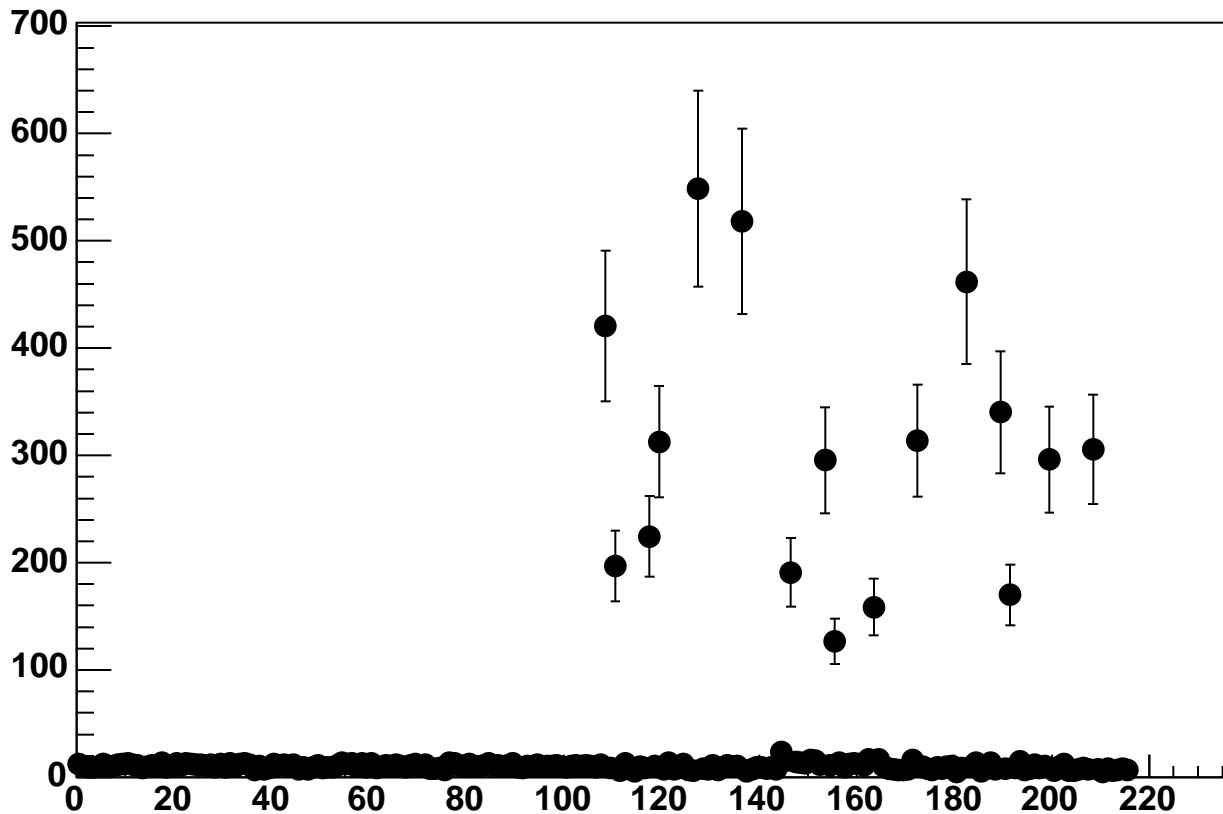
Enable 0, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



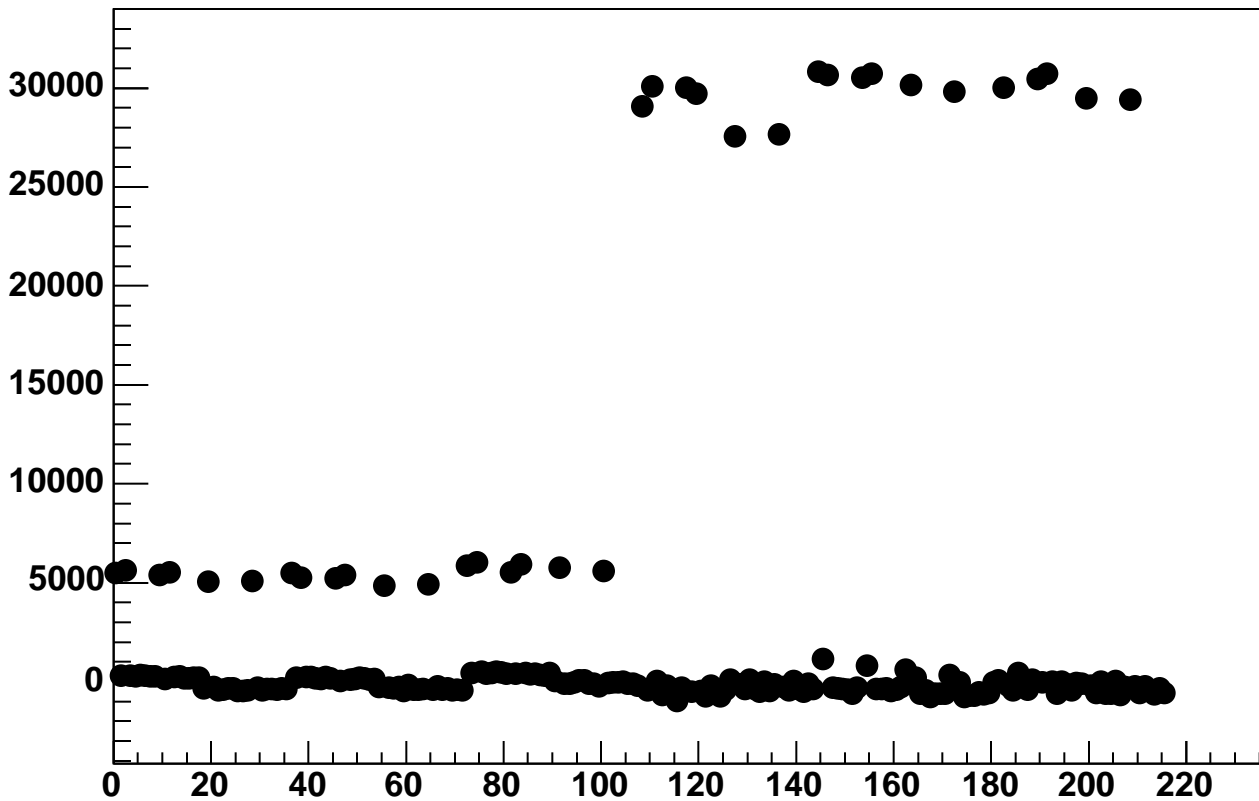
Enable 0, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



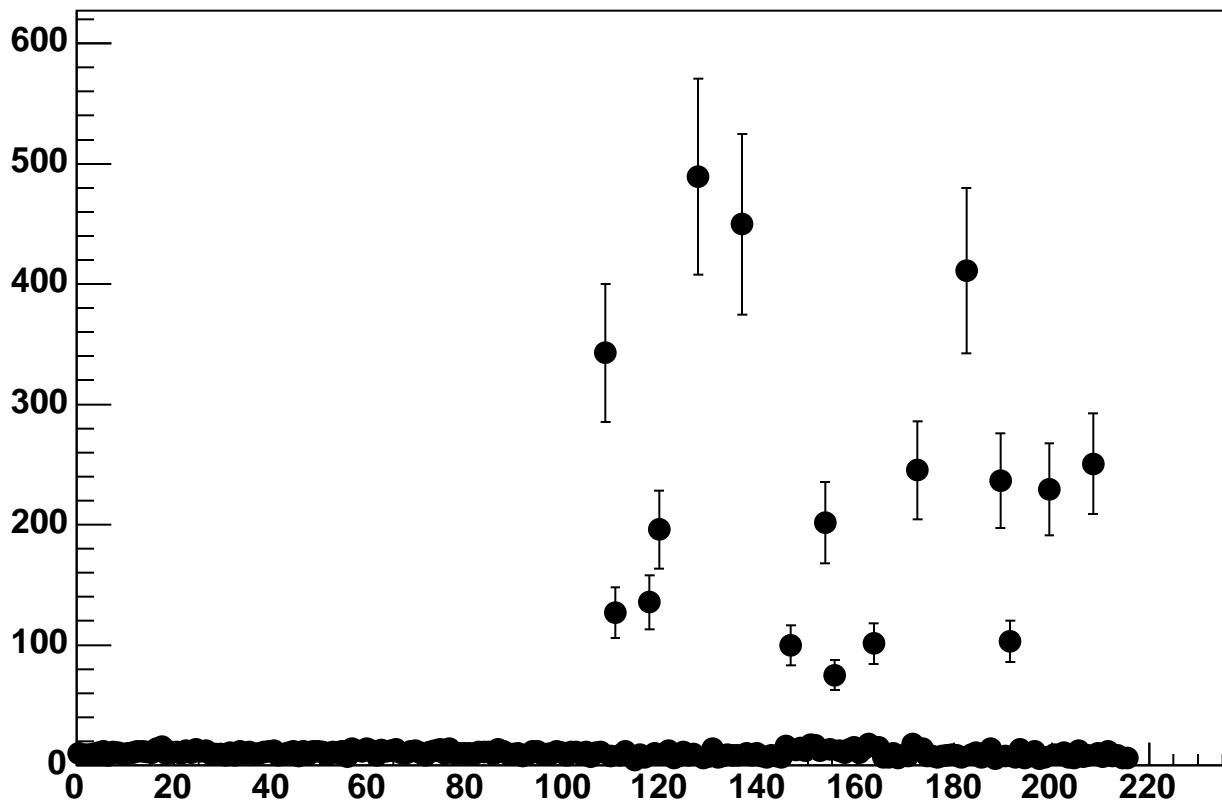
Enable 0, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



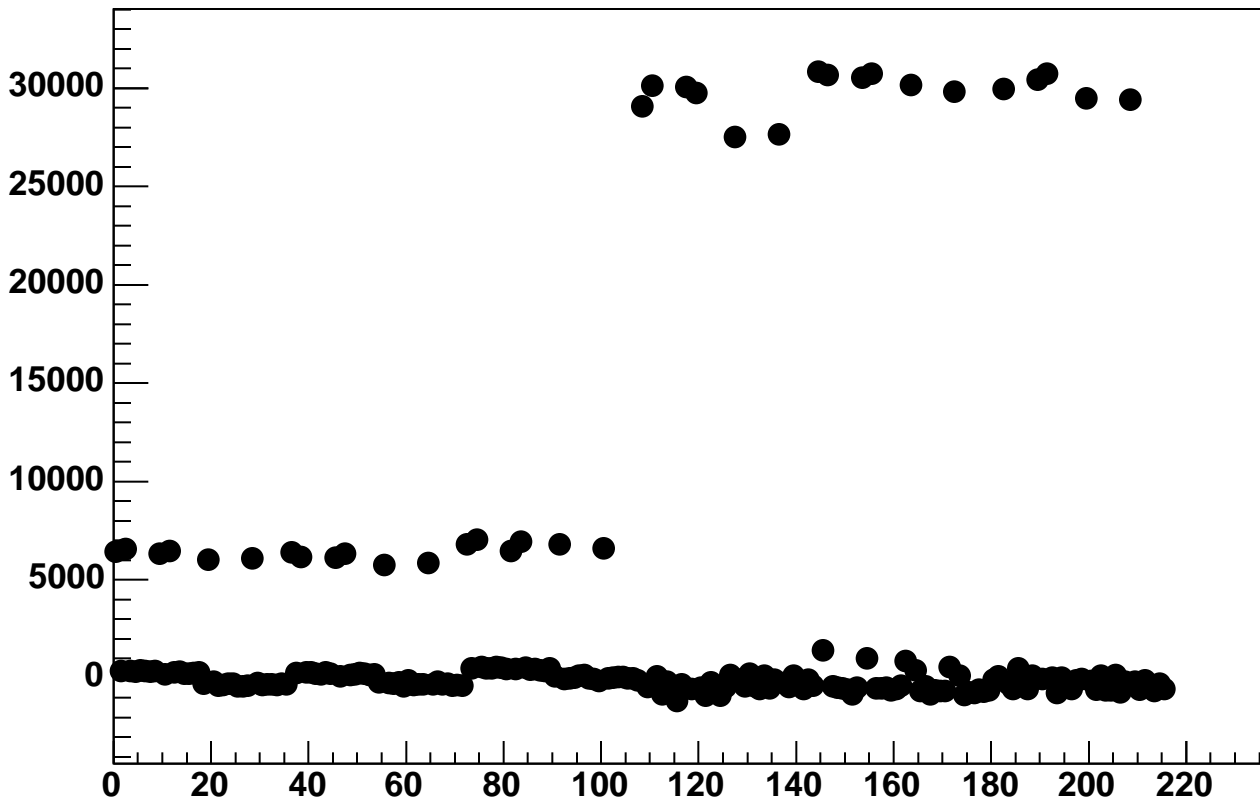
Enable 0, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



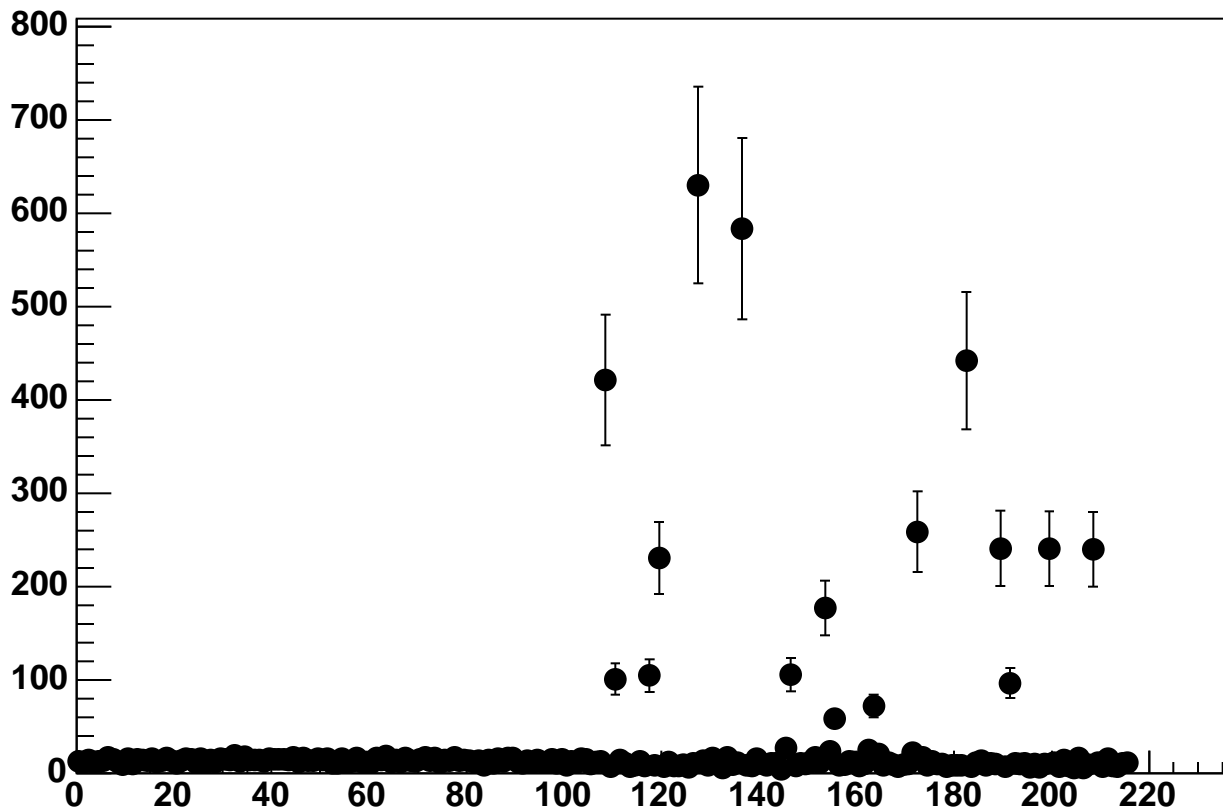
Enable 0, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



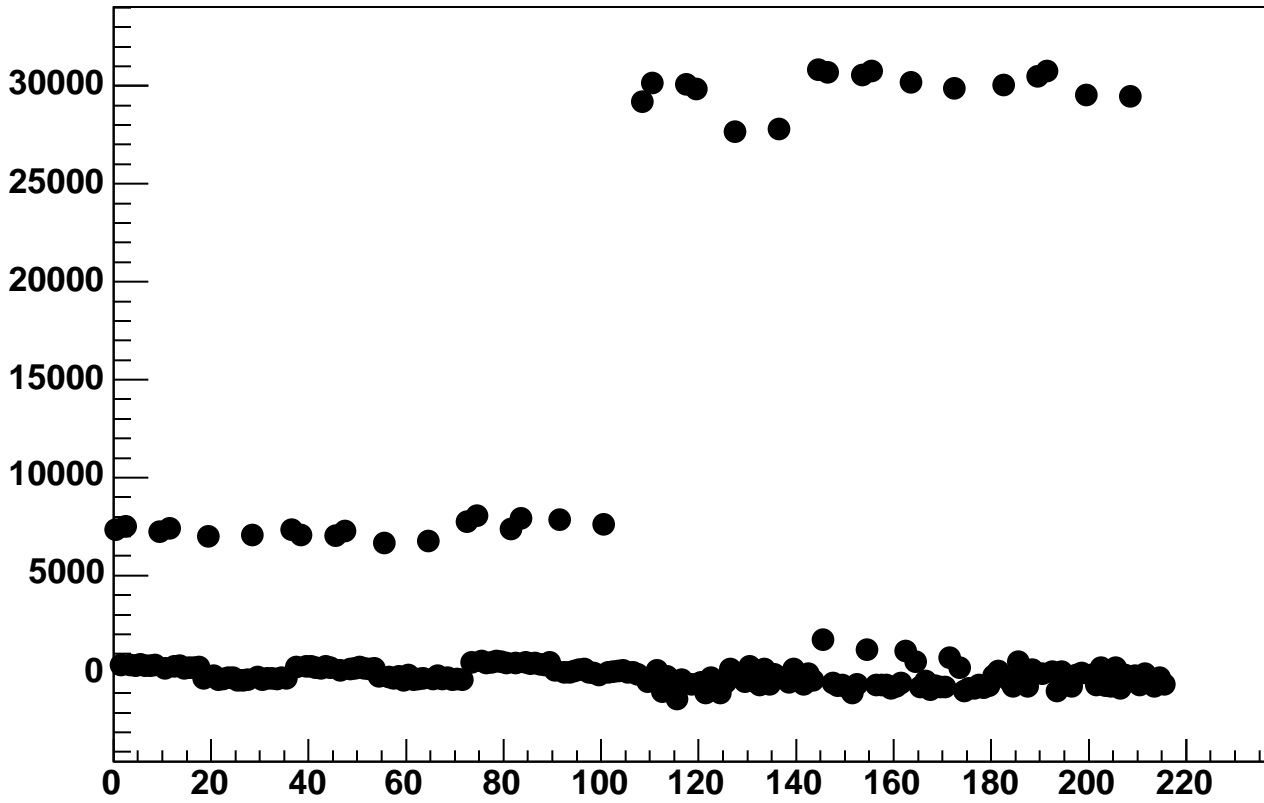
Enable 0, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



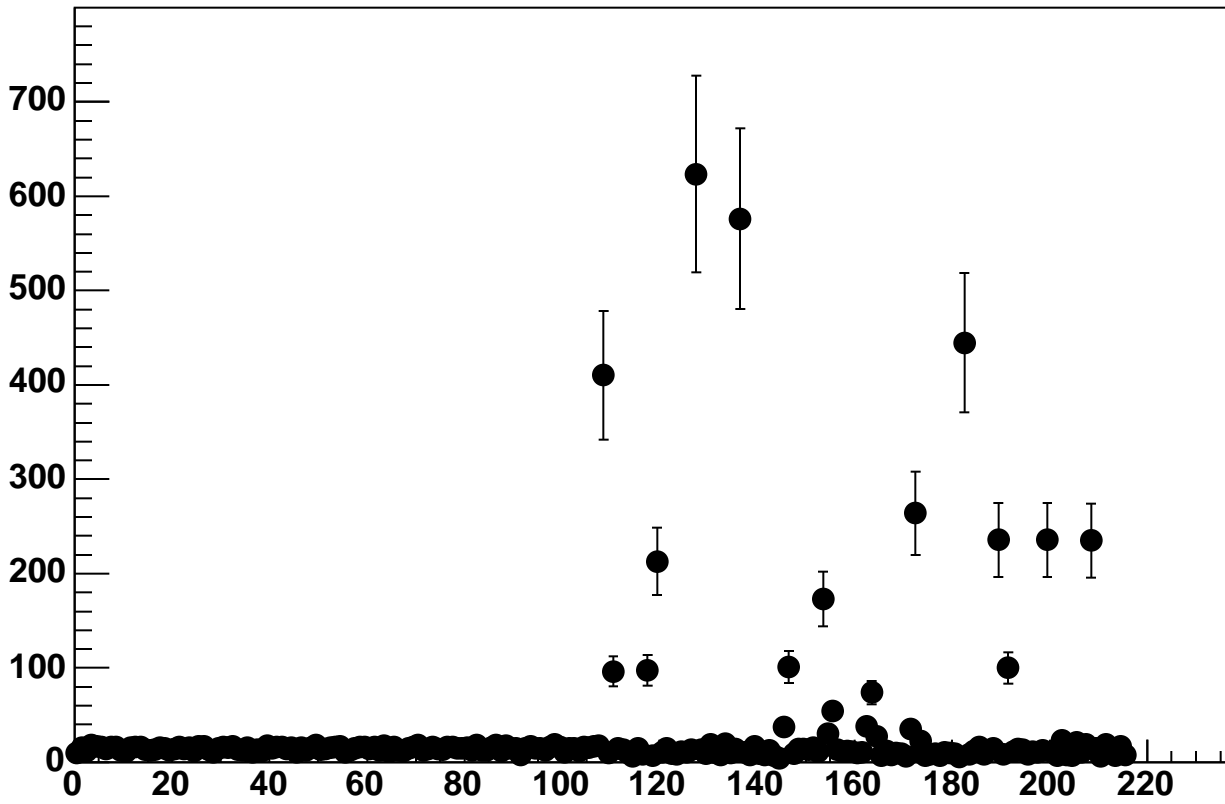
Enable 0, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



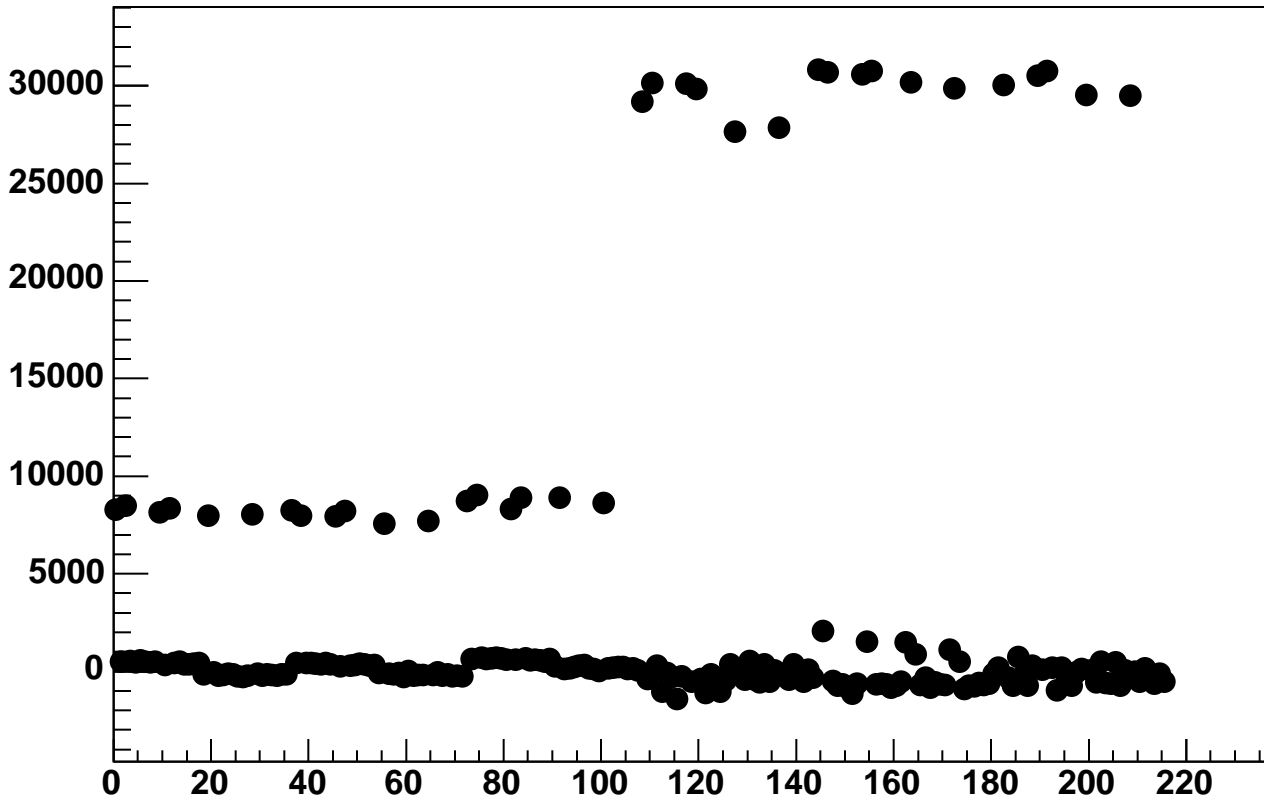
Enable 0, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



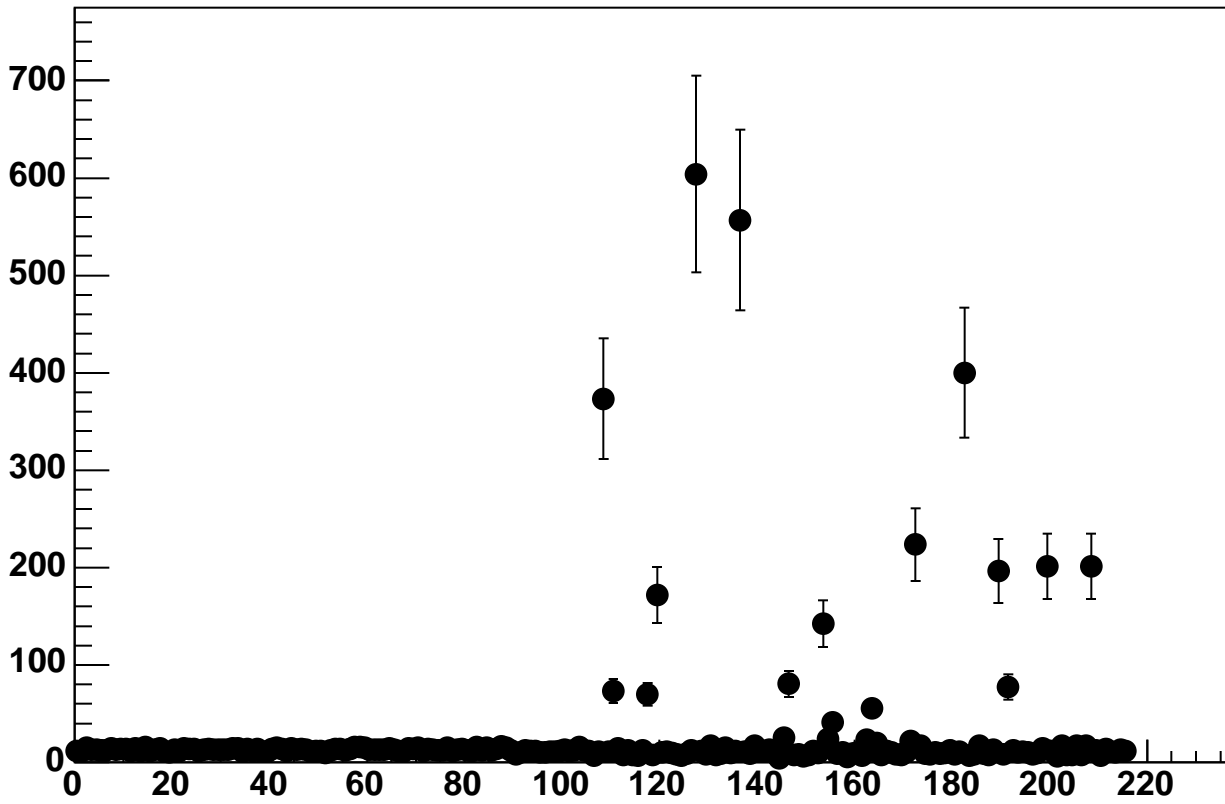
Enable 0, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



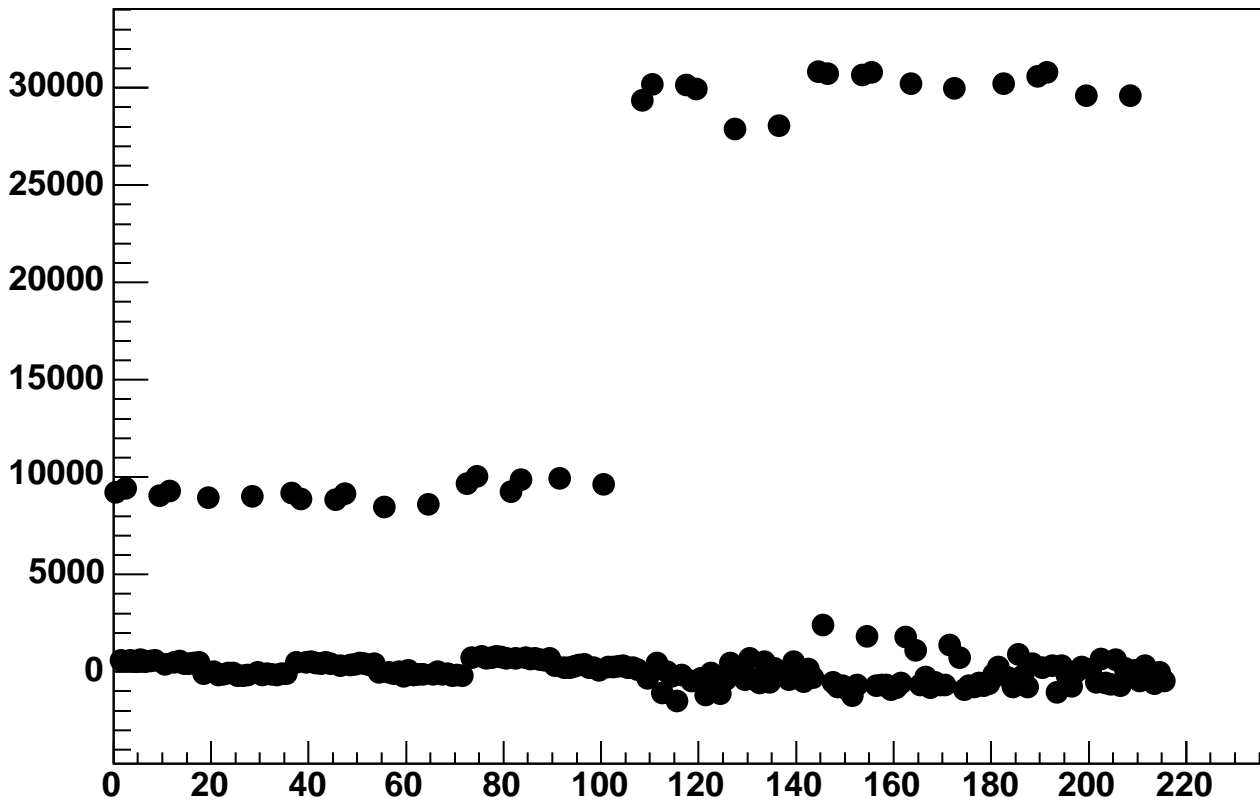
Enable 0, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



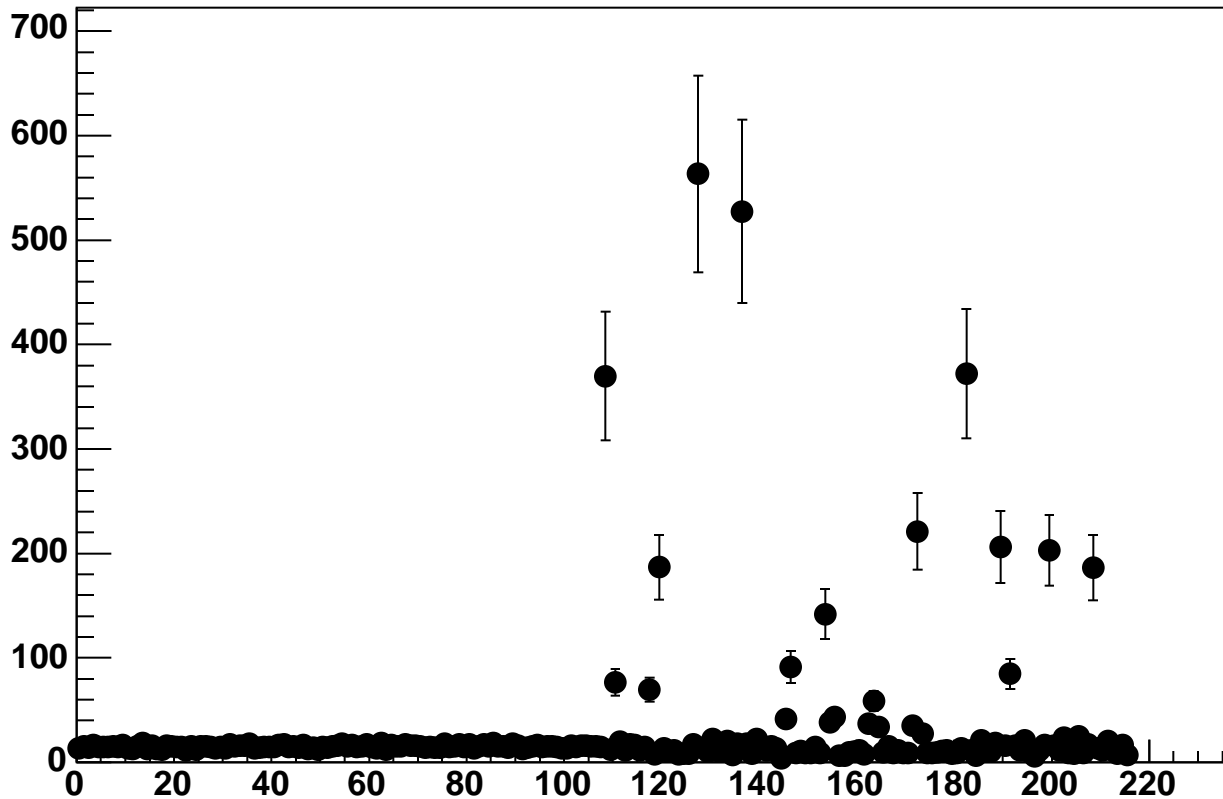
Enable 0, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



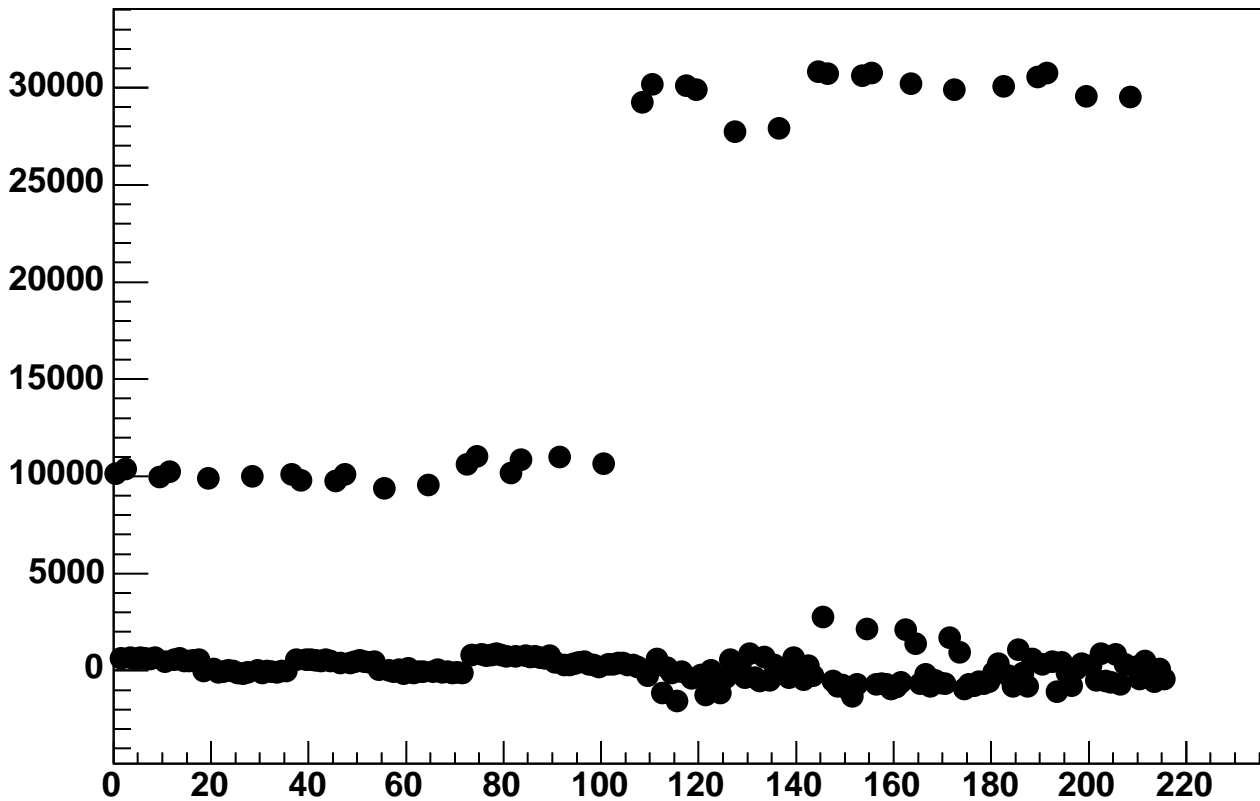
Enable 0, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



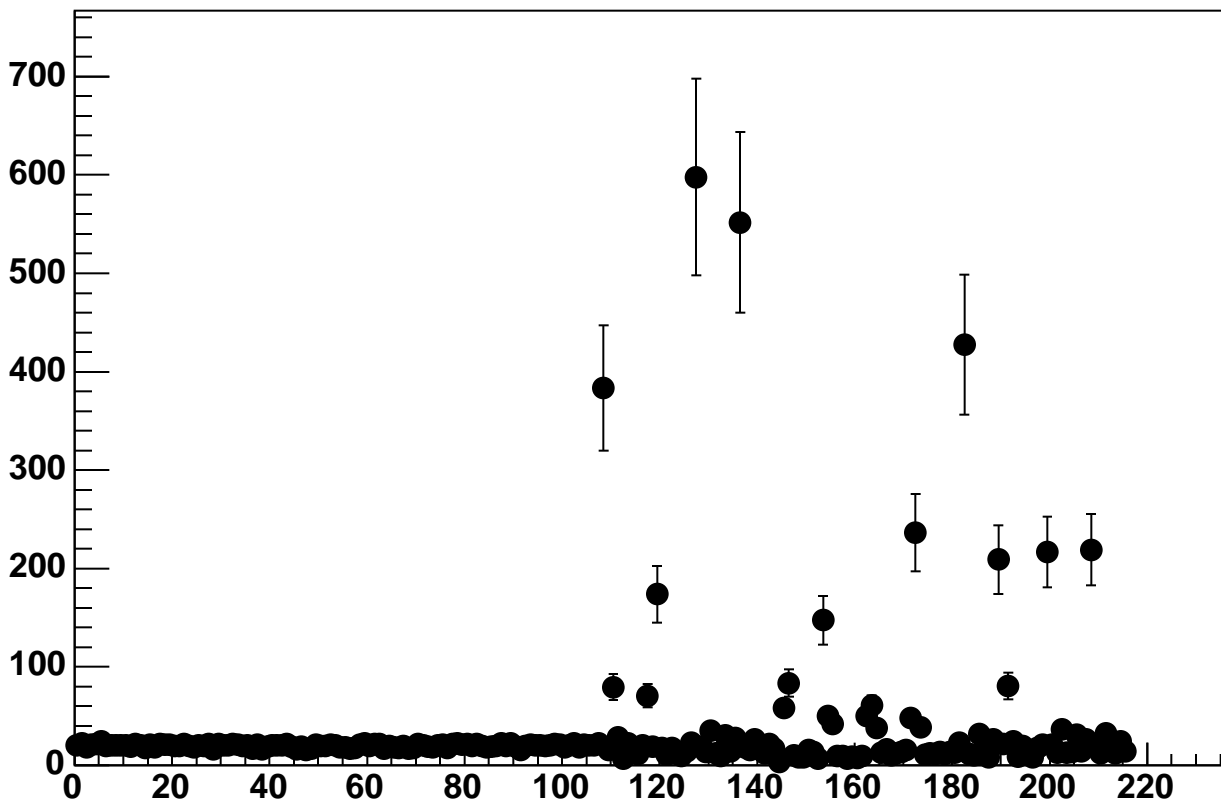
Enable 0, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



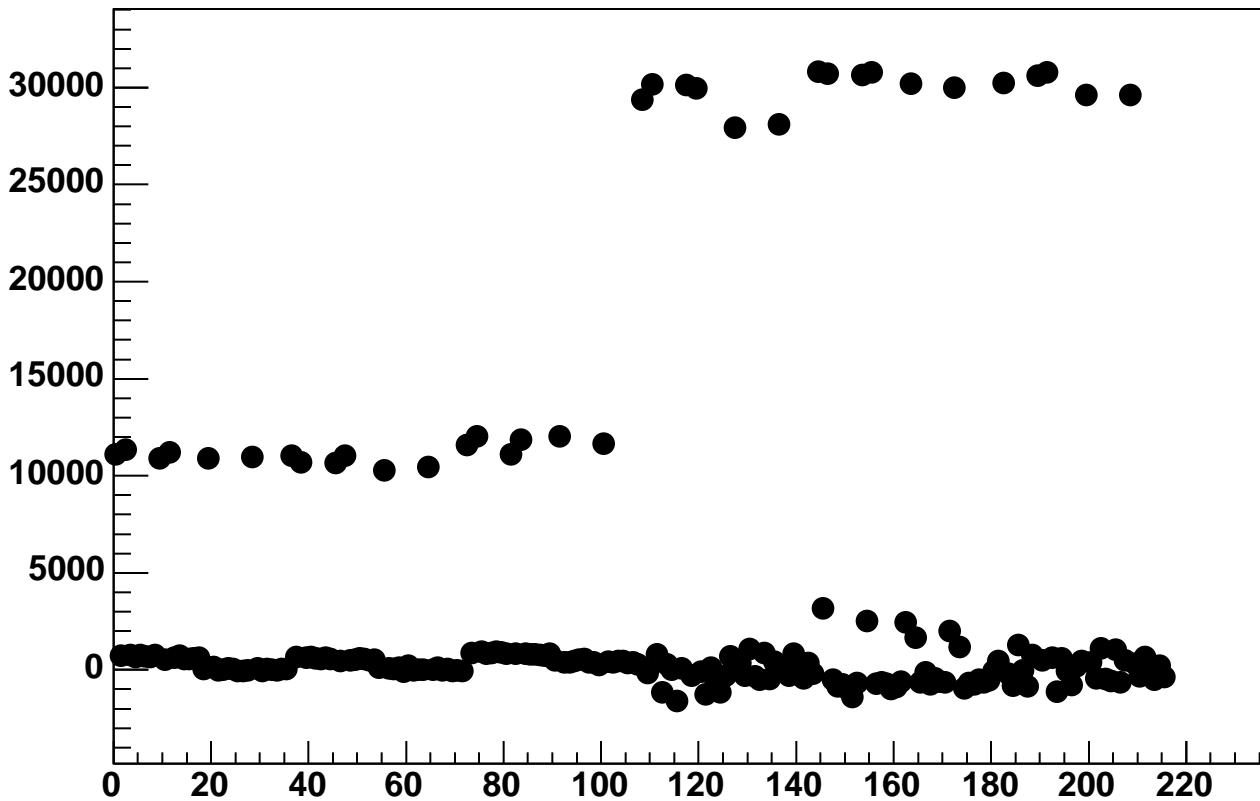
Enable 0, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



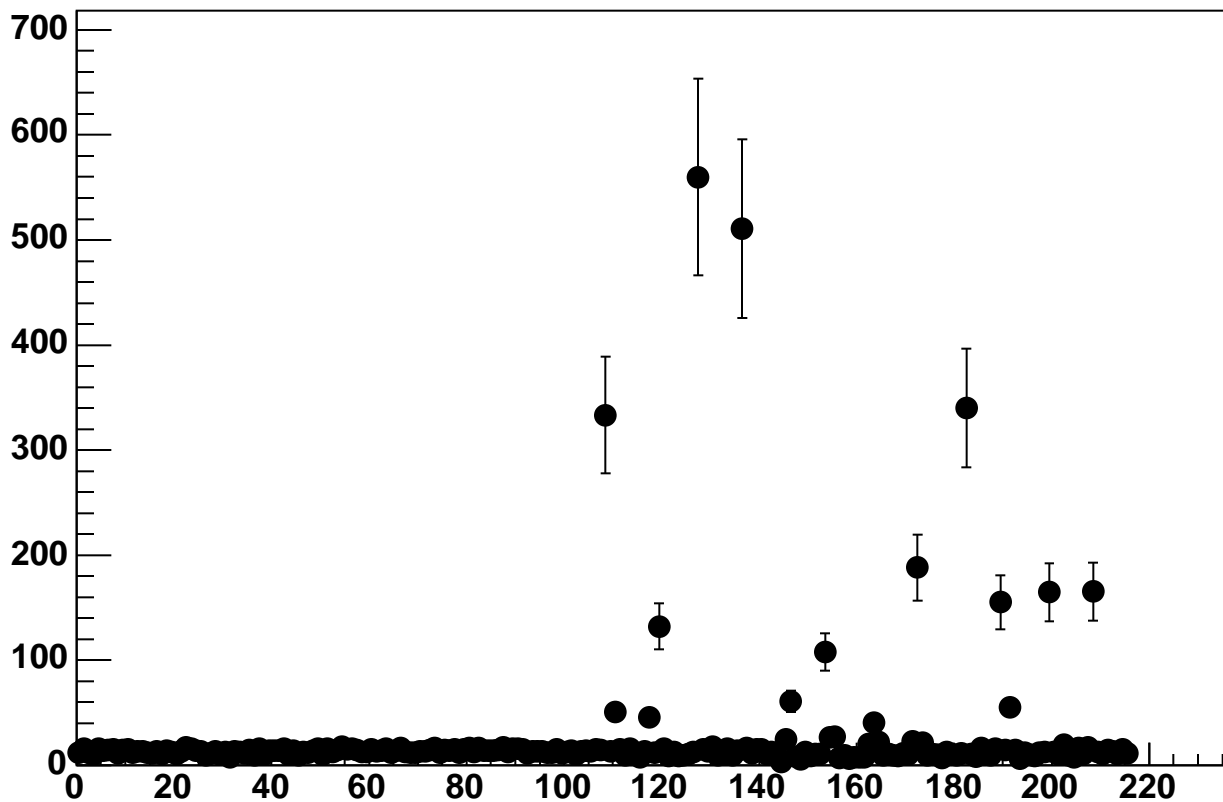
Enable 0, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



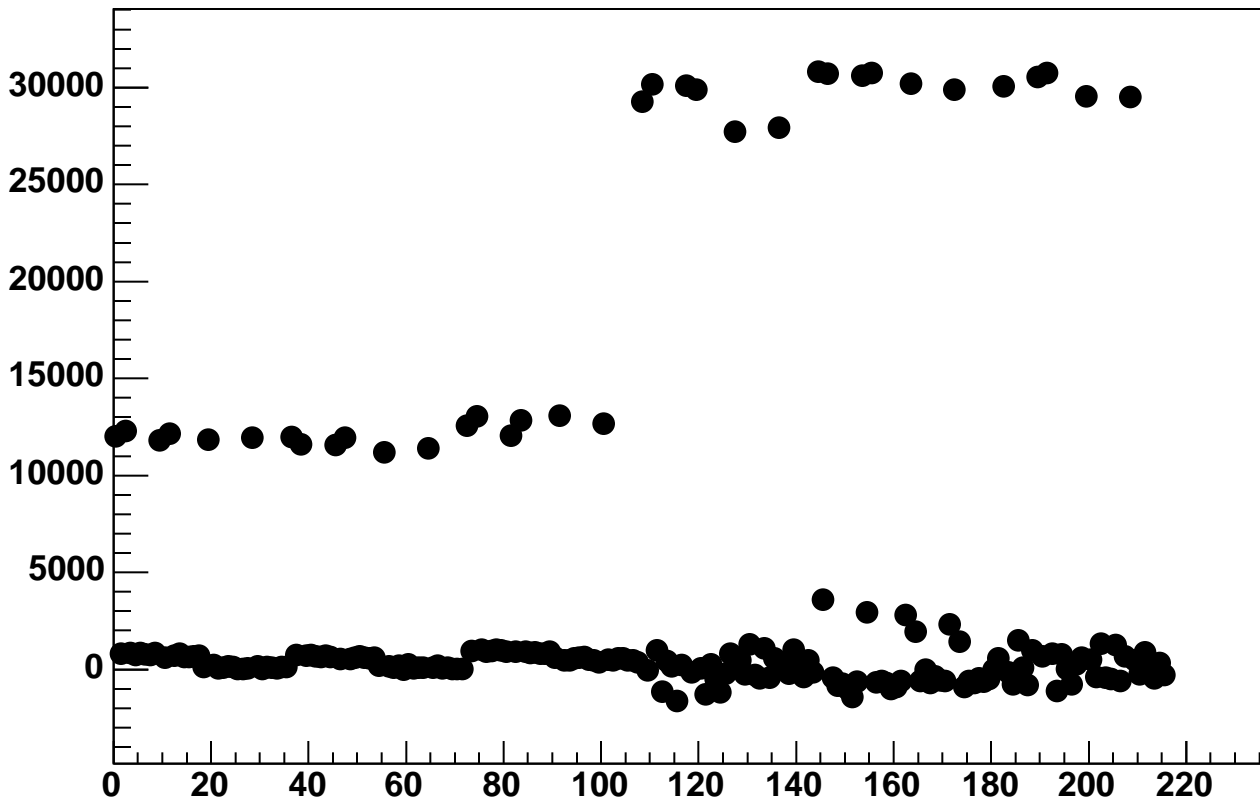
Enable 0, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



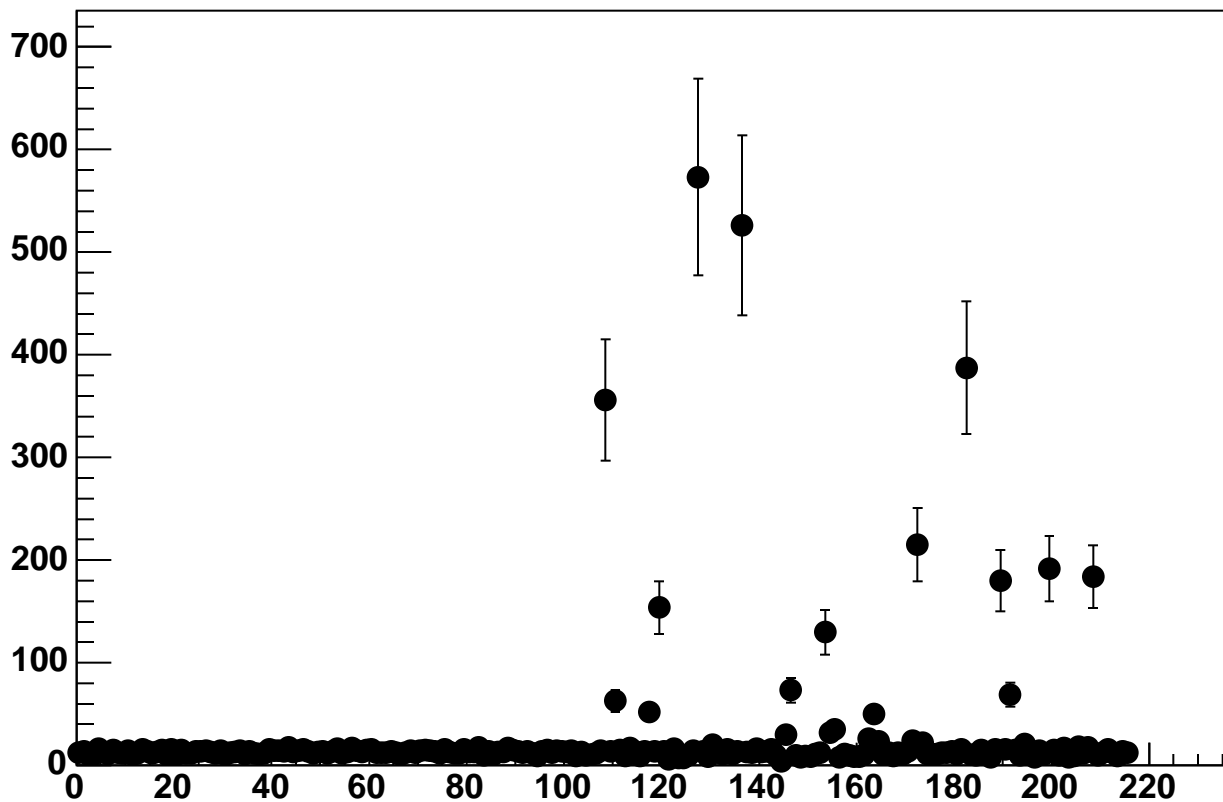
Enable 0, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



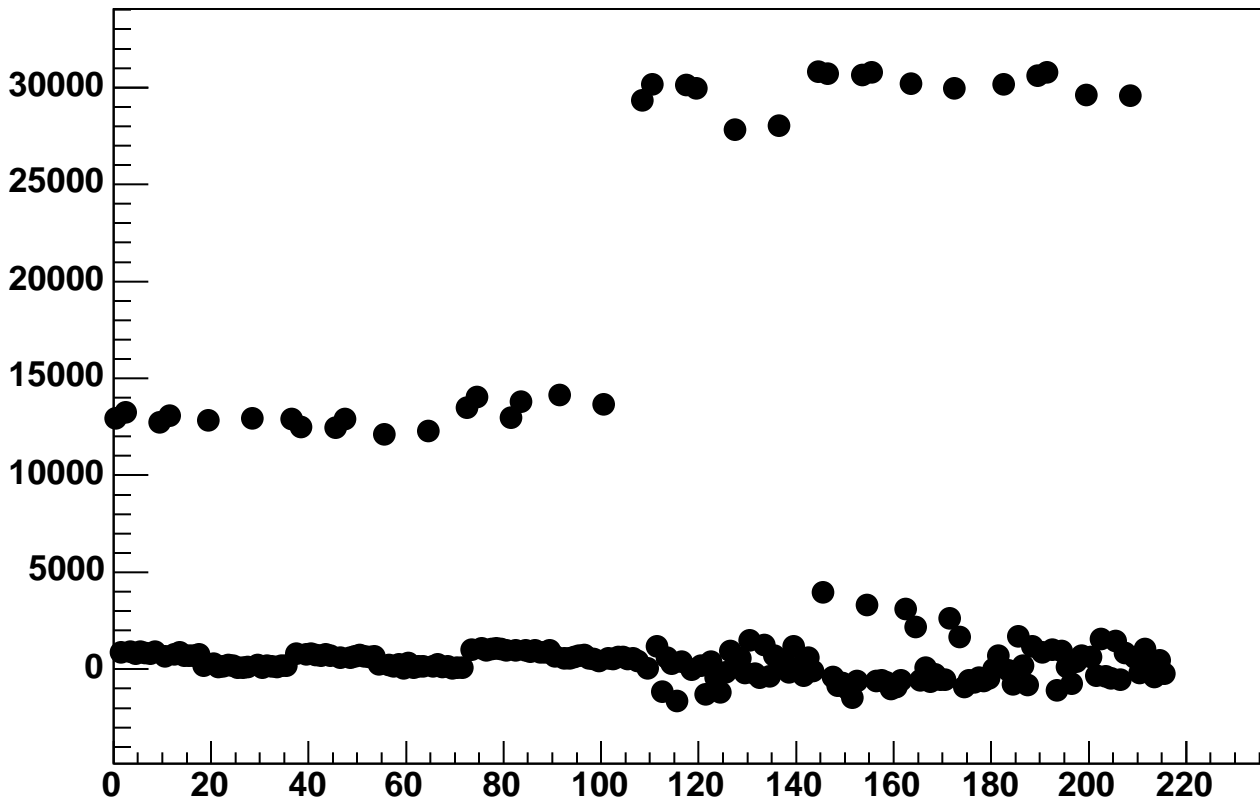
Enable 0, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



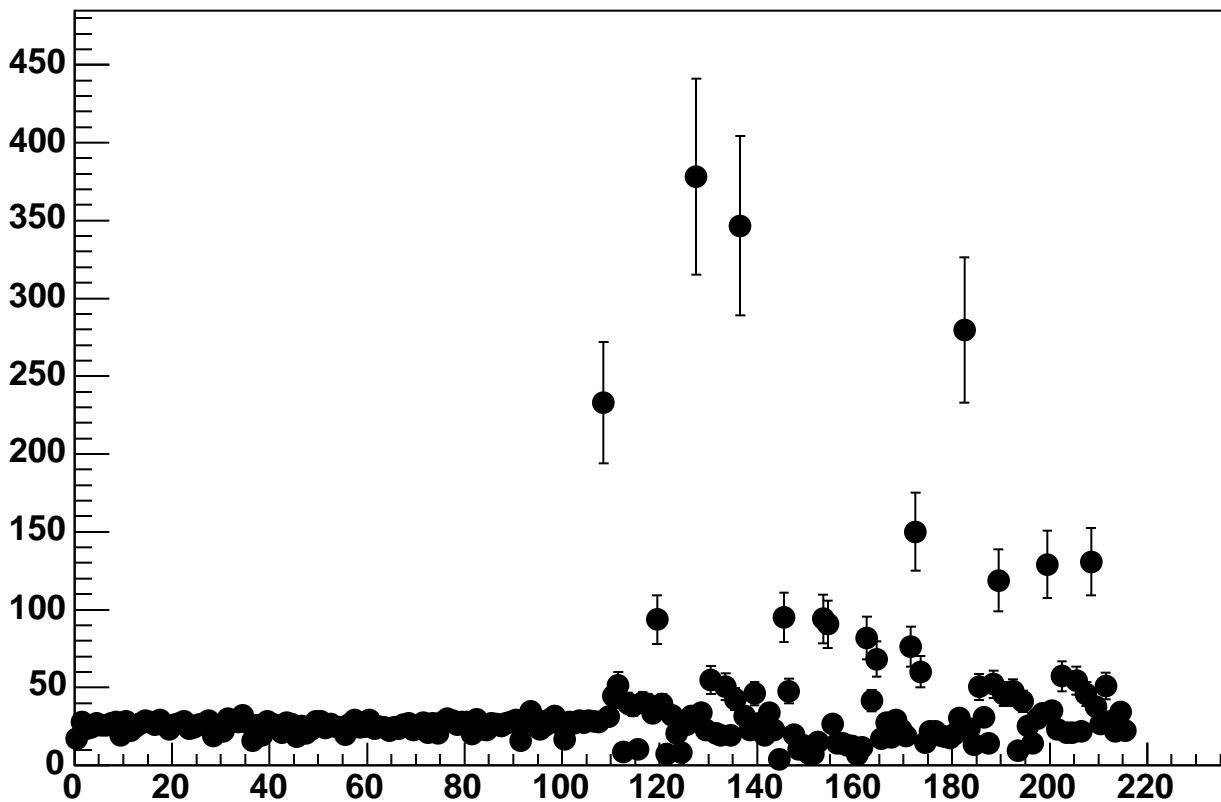
Enable 0, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



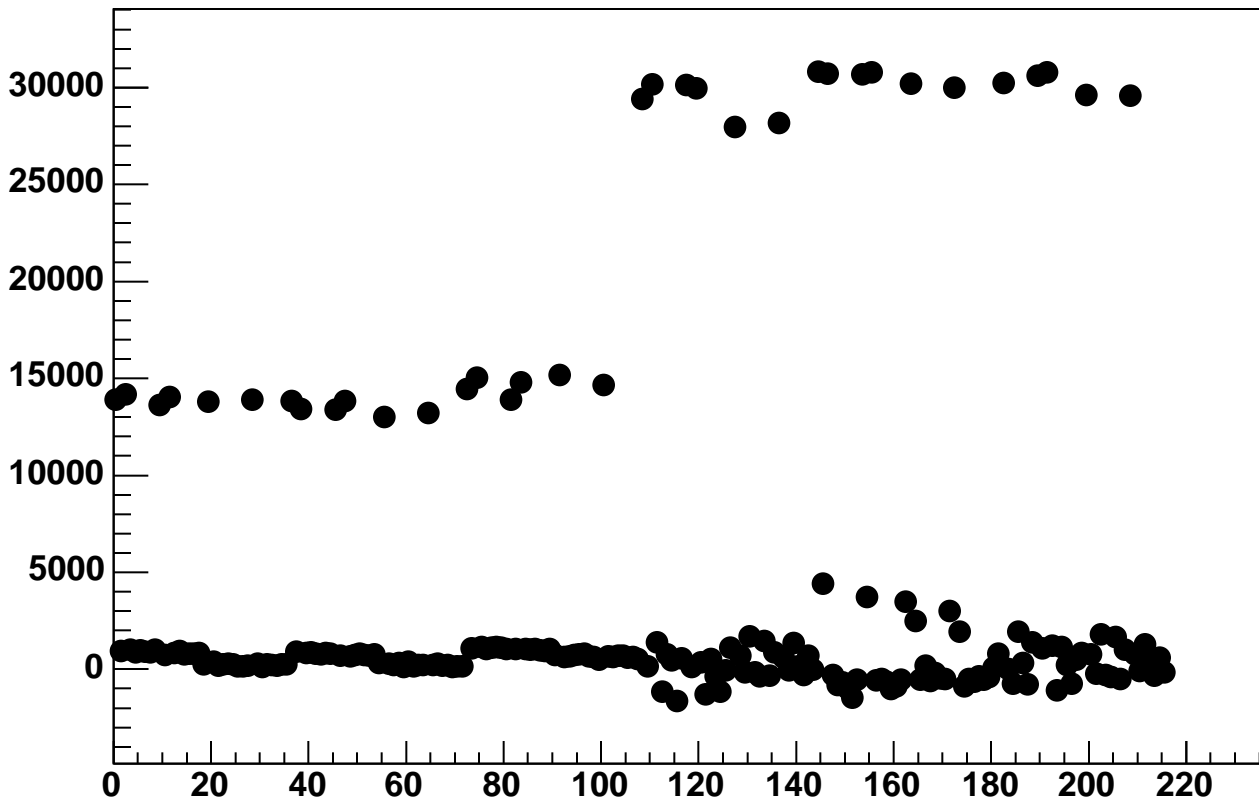
Enable 0, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



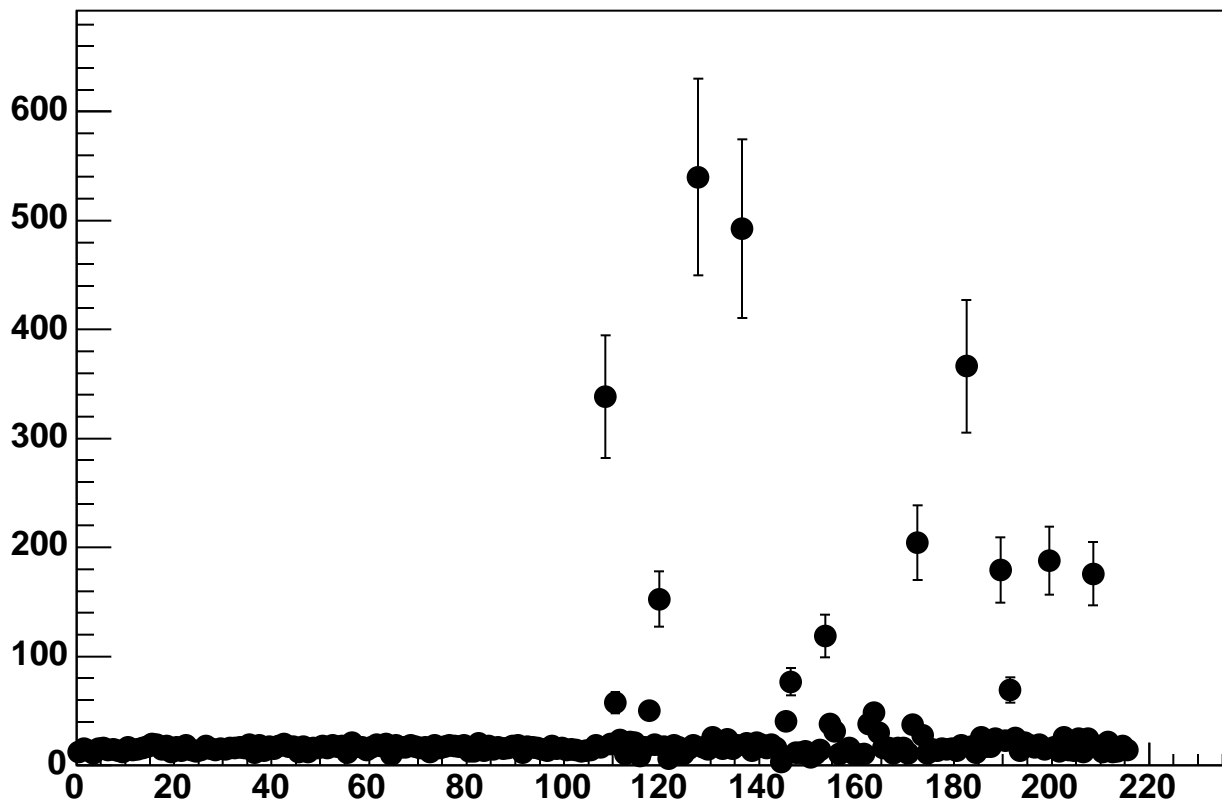
Enable 0, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



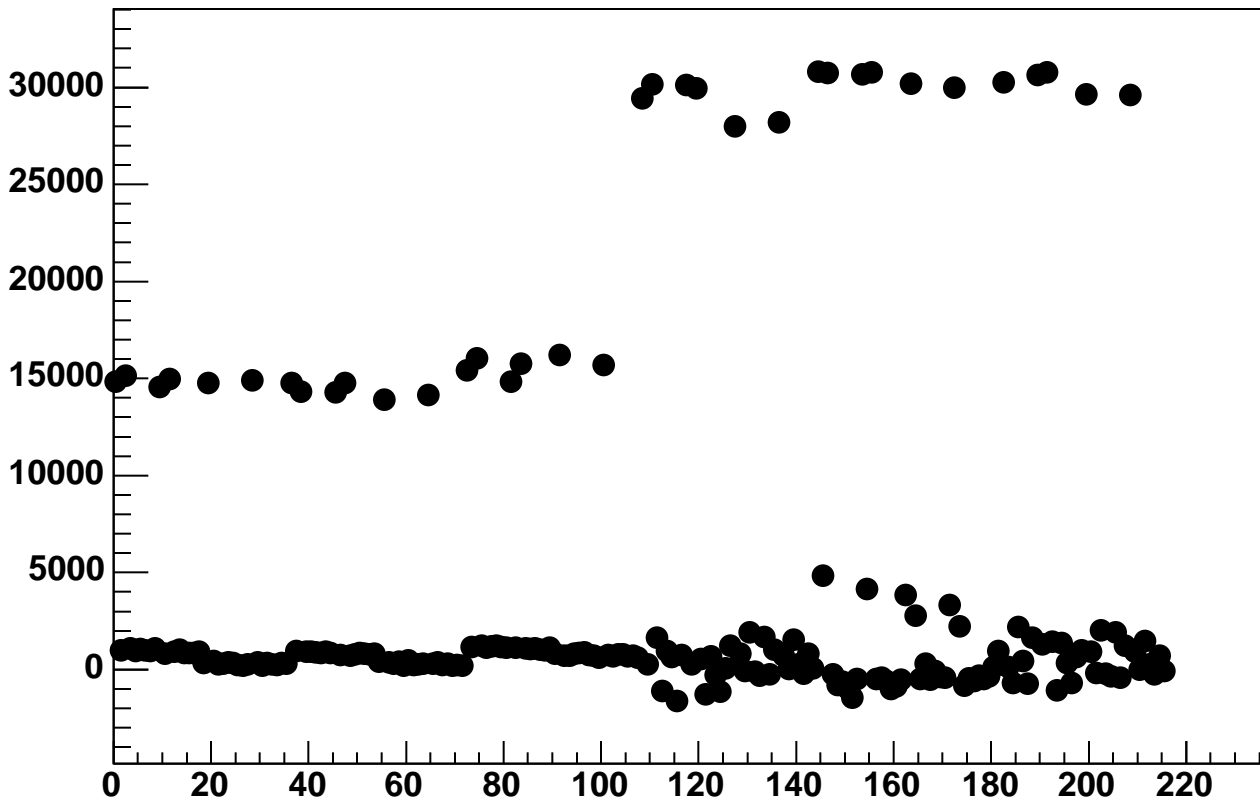
Enable 0, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



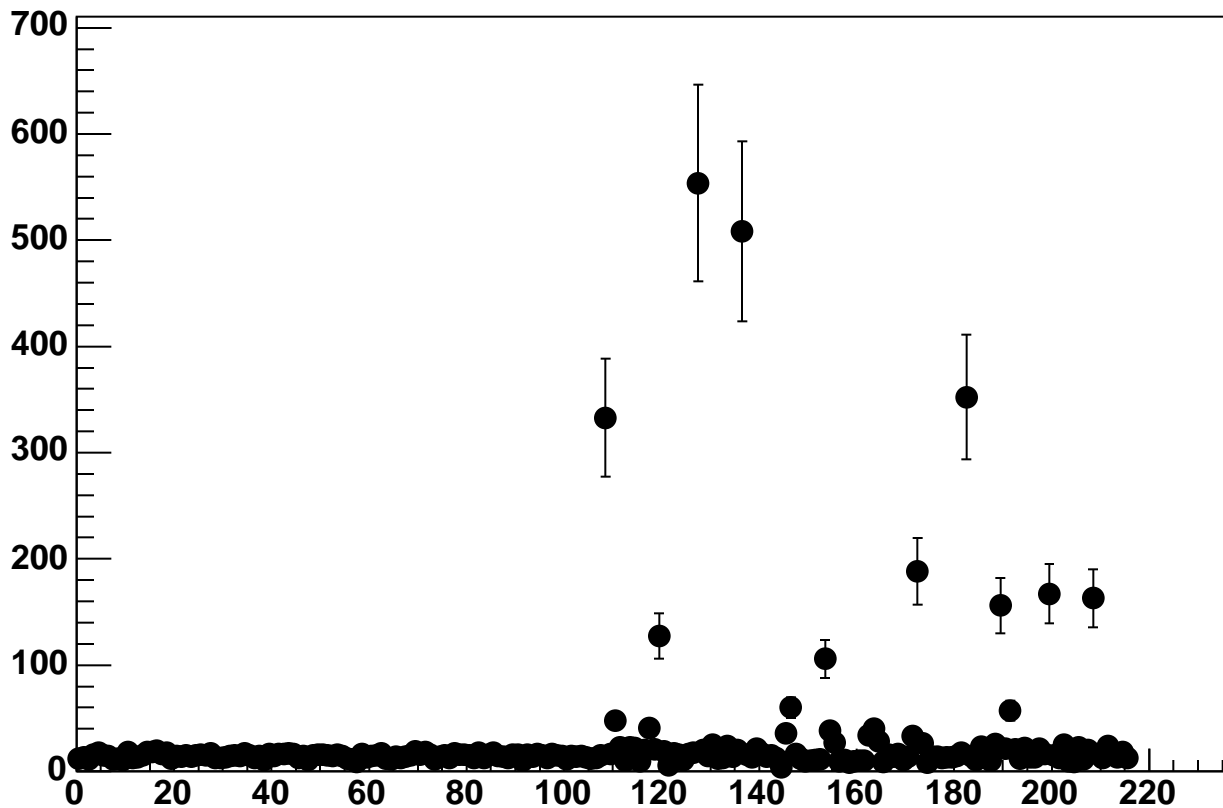
Enable 0, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



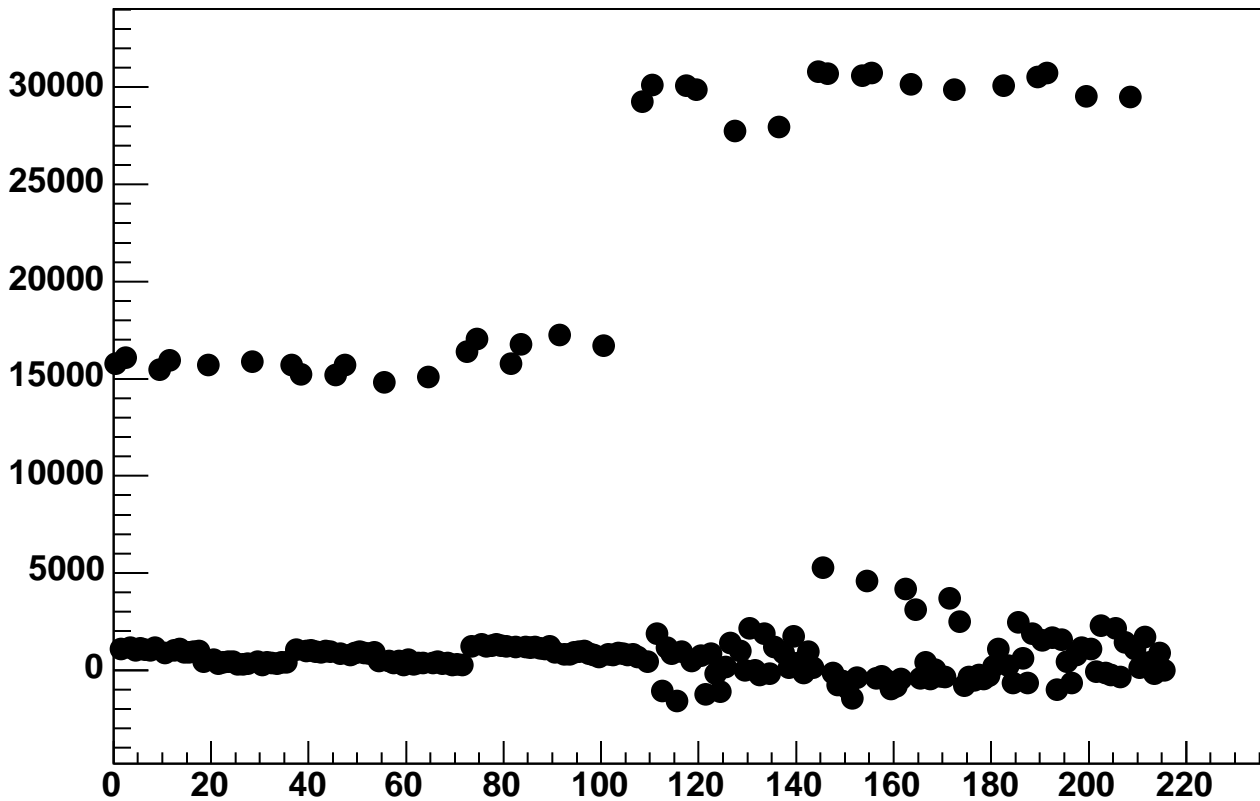
Enable 0, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



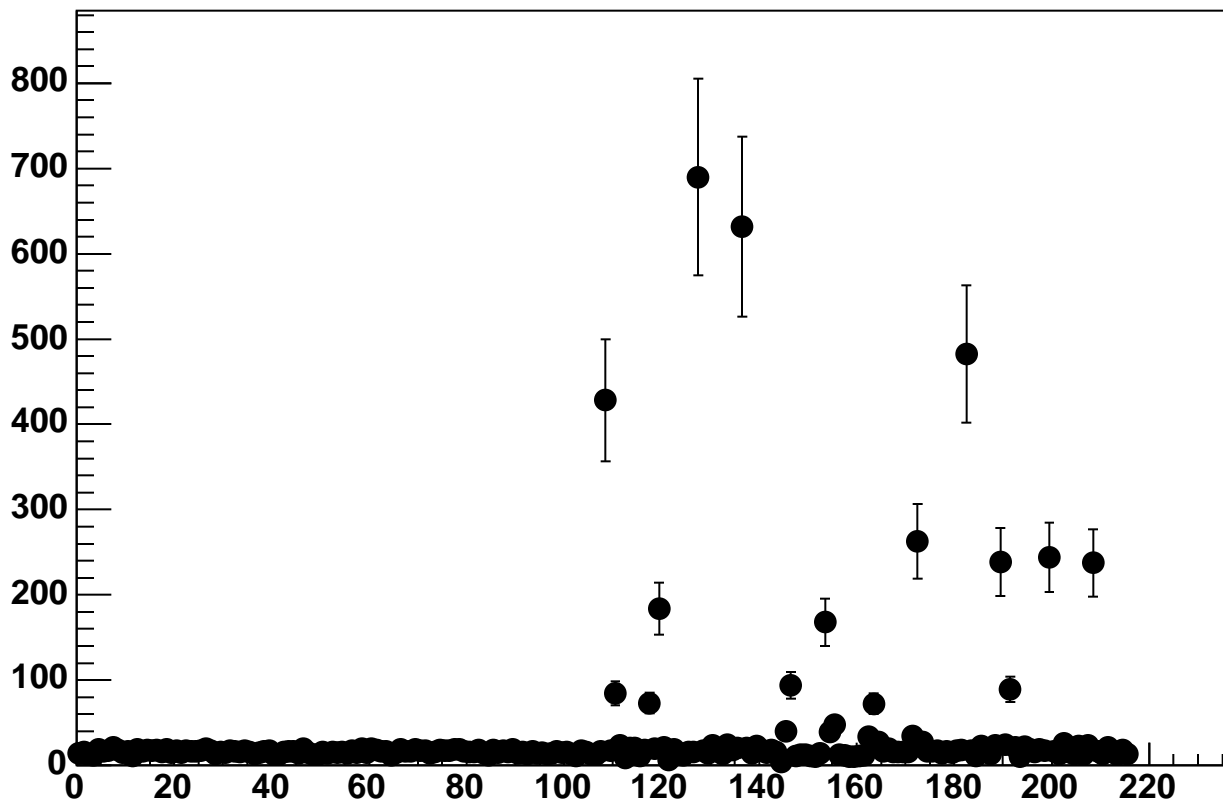
Enable 0, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



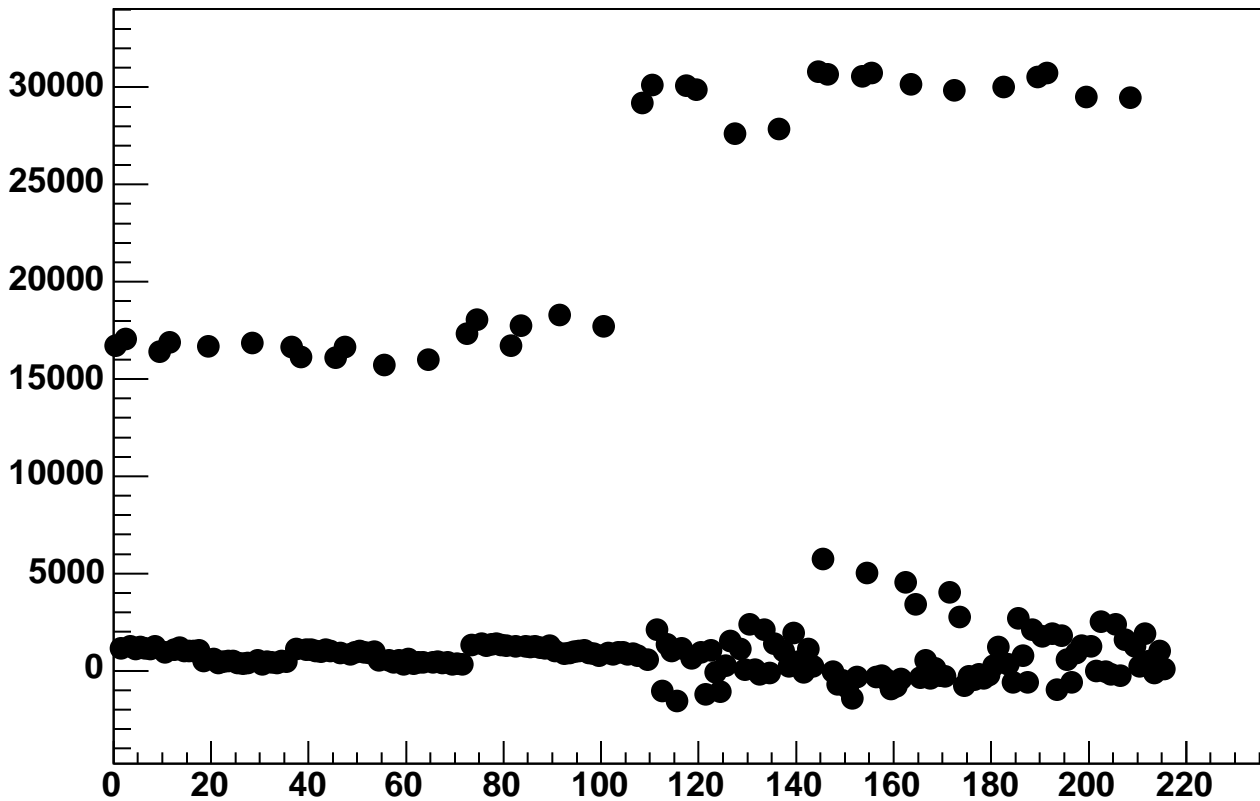
Enable 0, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



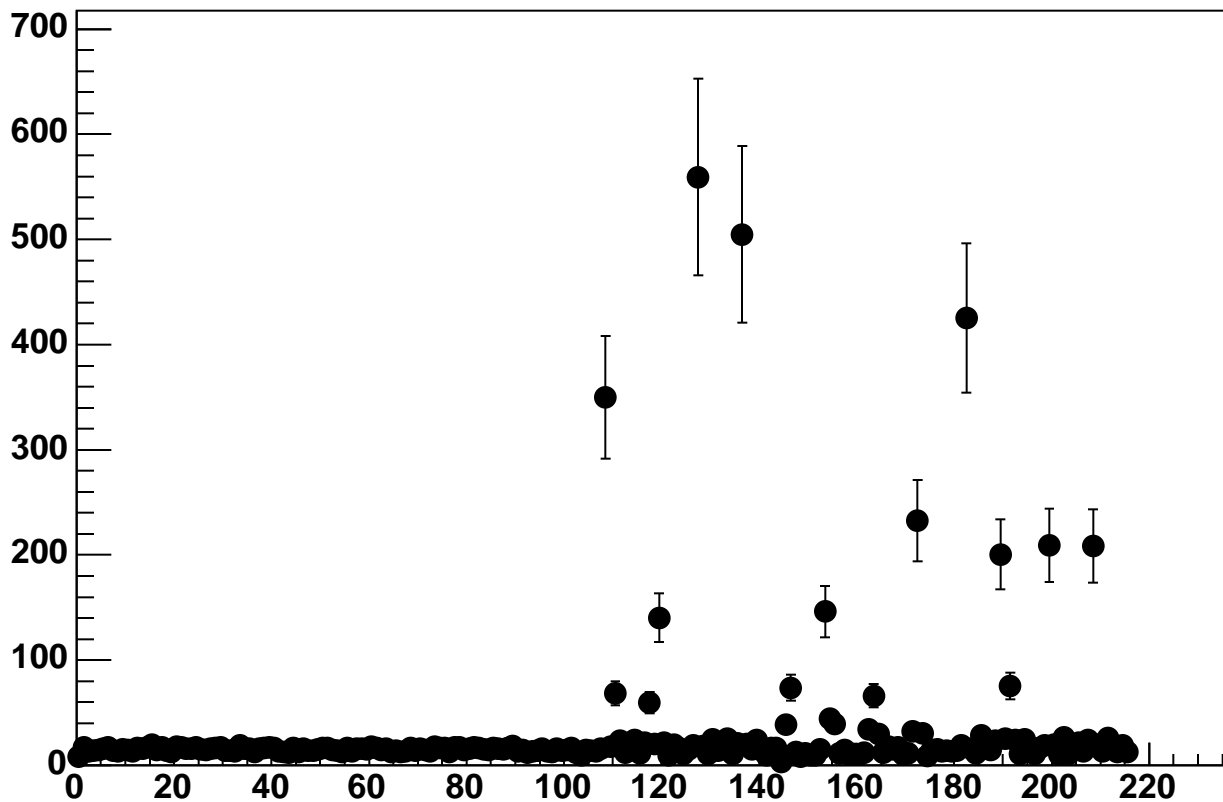
Enable 0, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



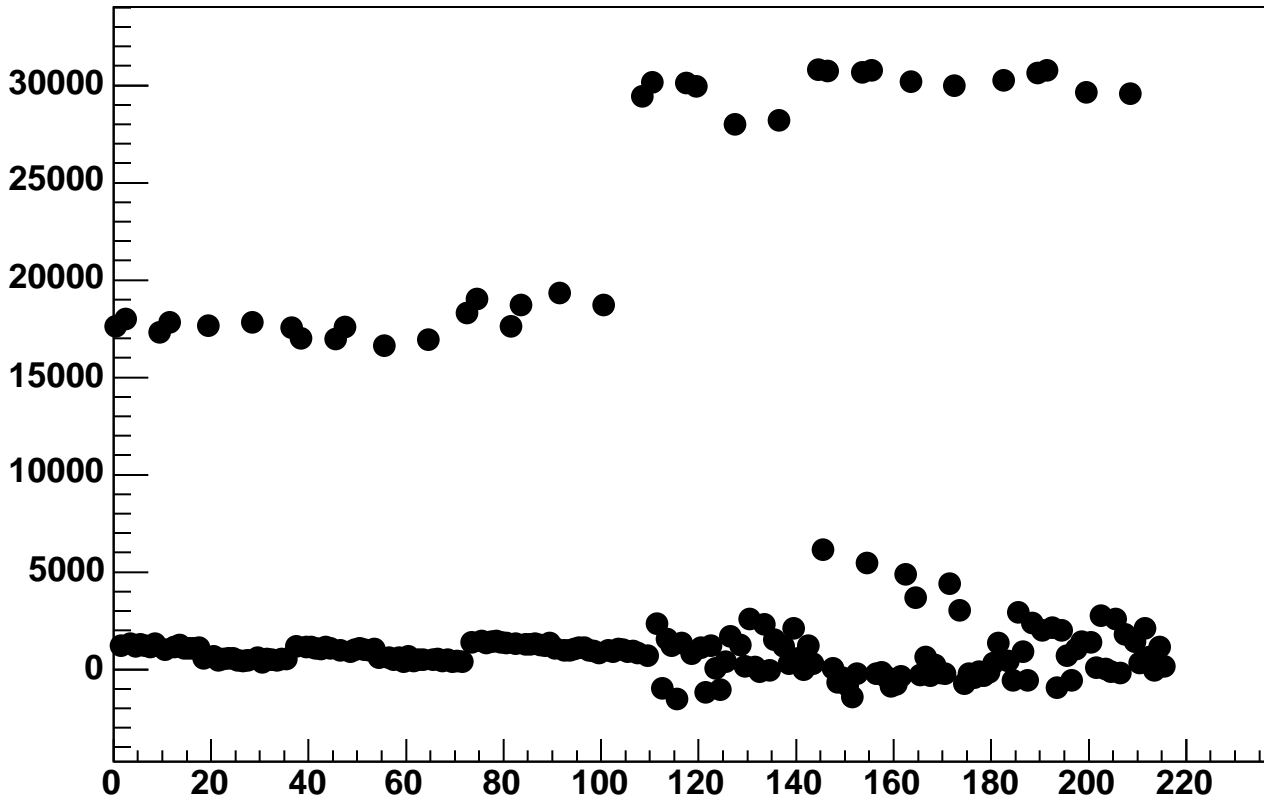
Enable 0, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



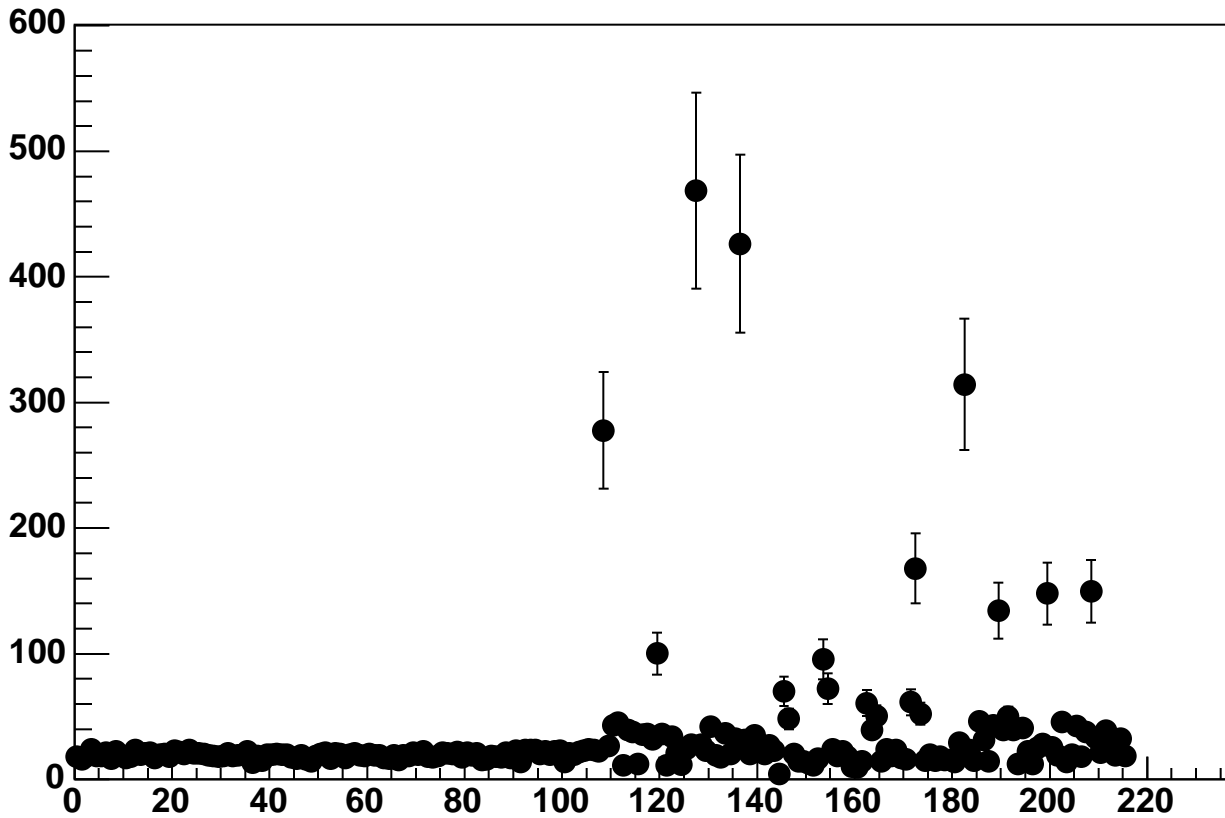
Enable 0, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



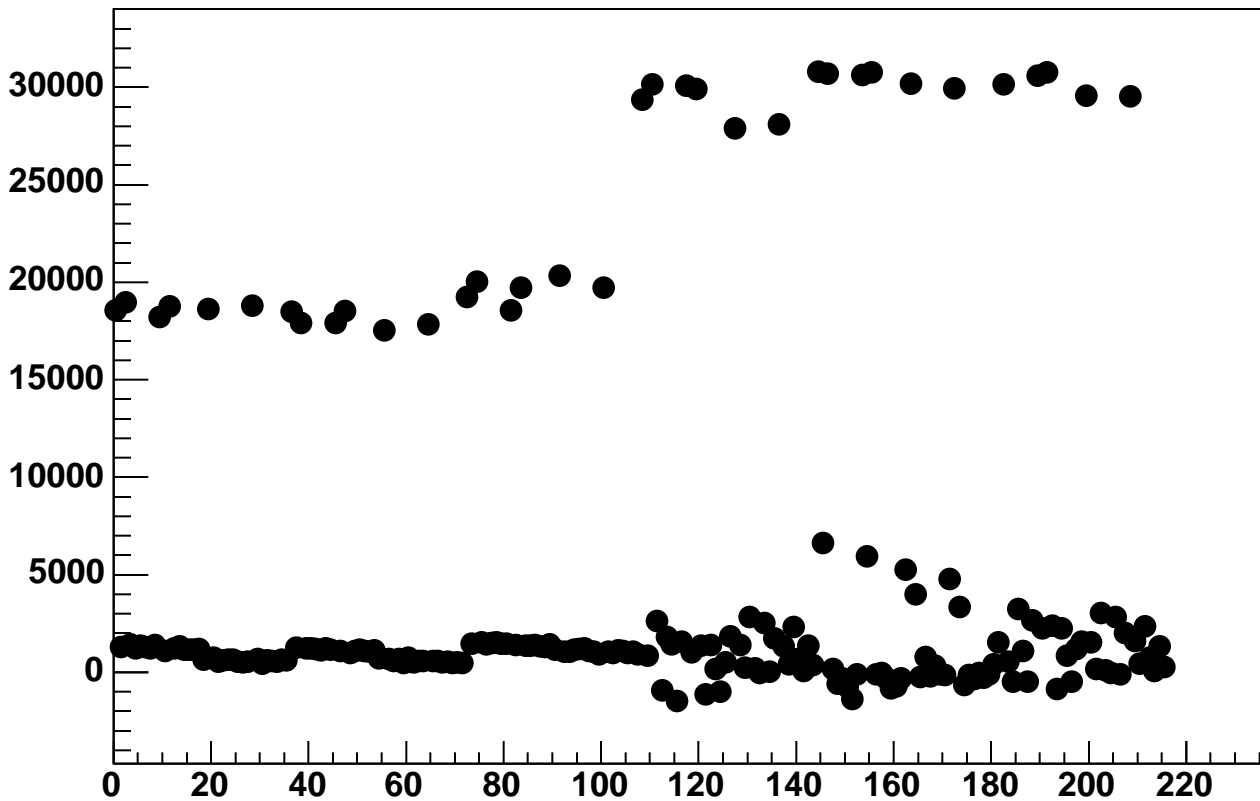
Enable 0, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



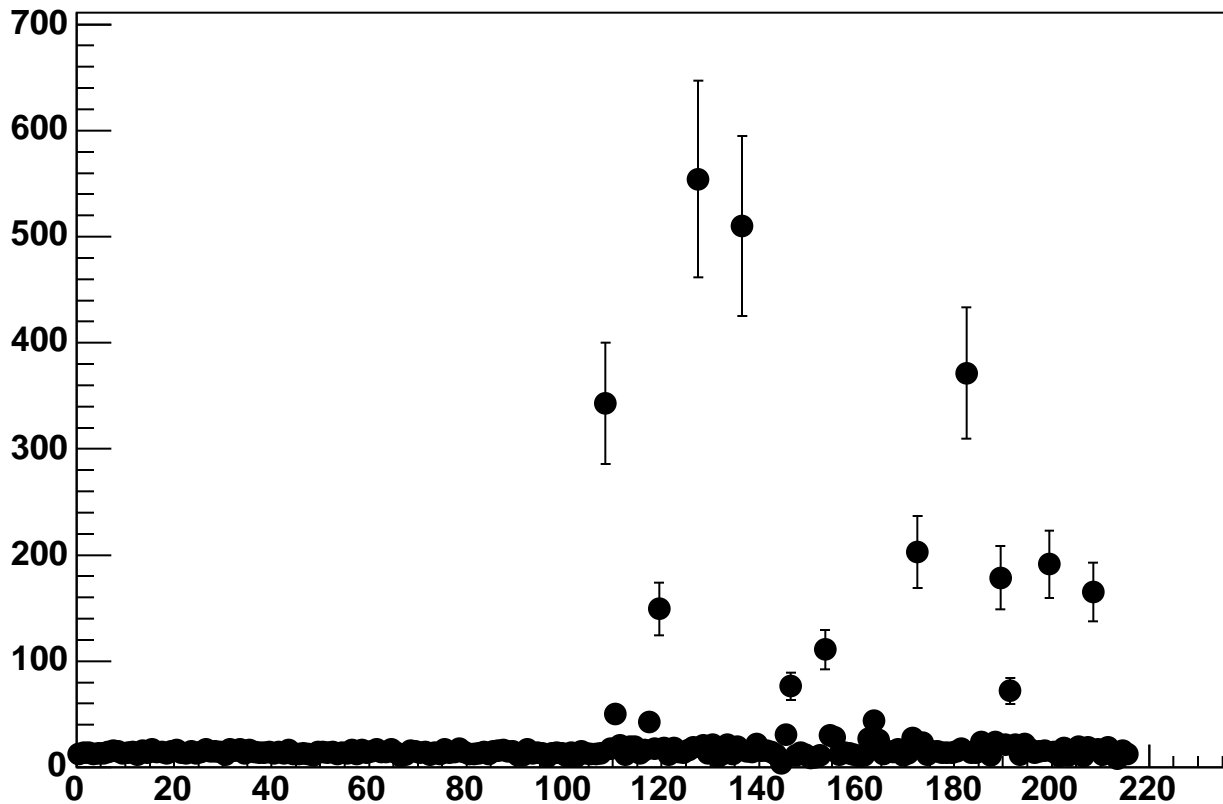
Enable 0, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



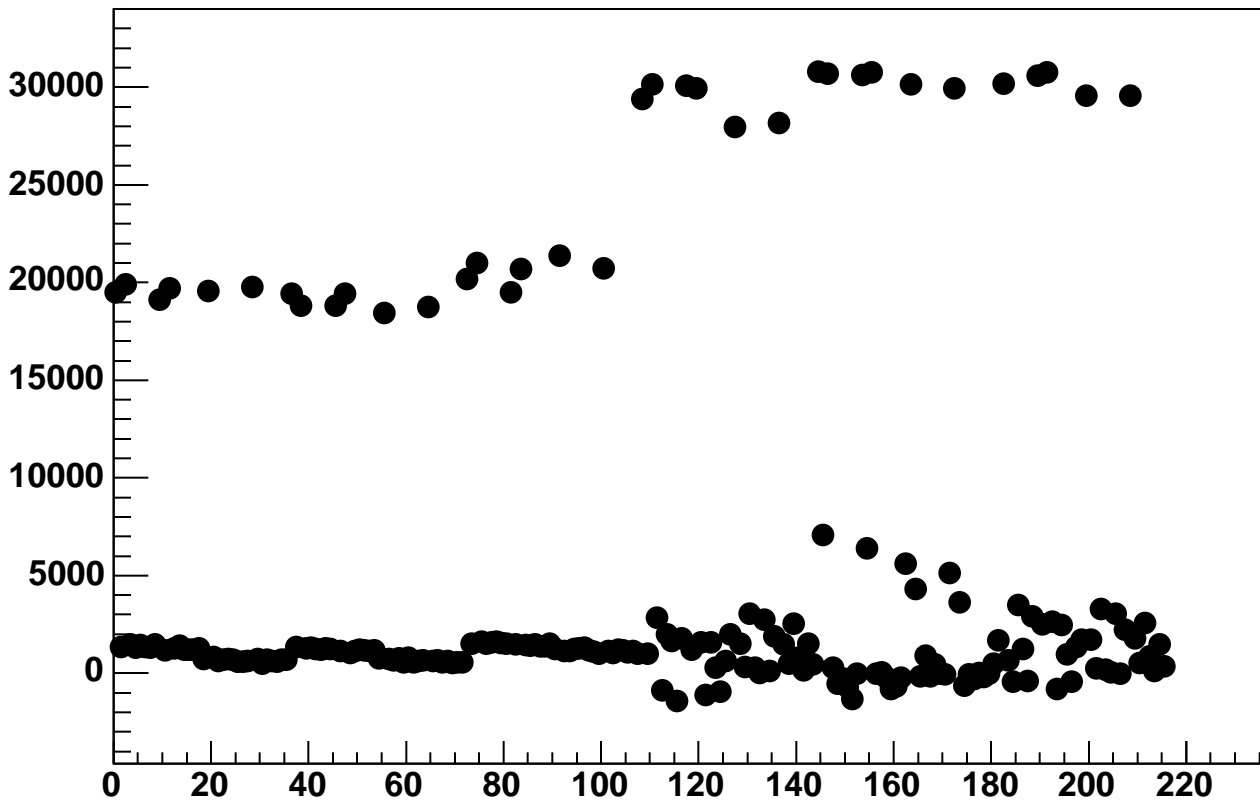
Enable 0, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



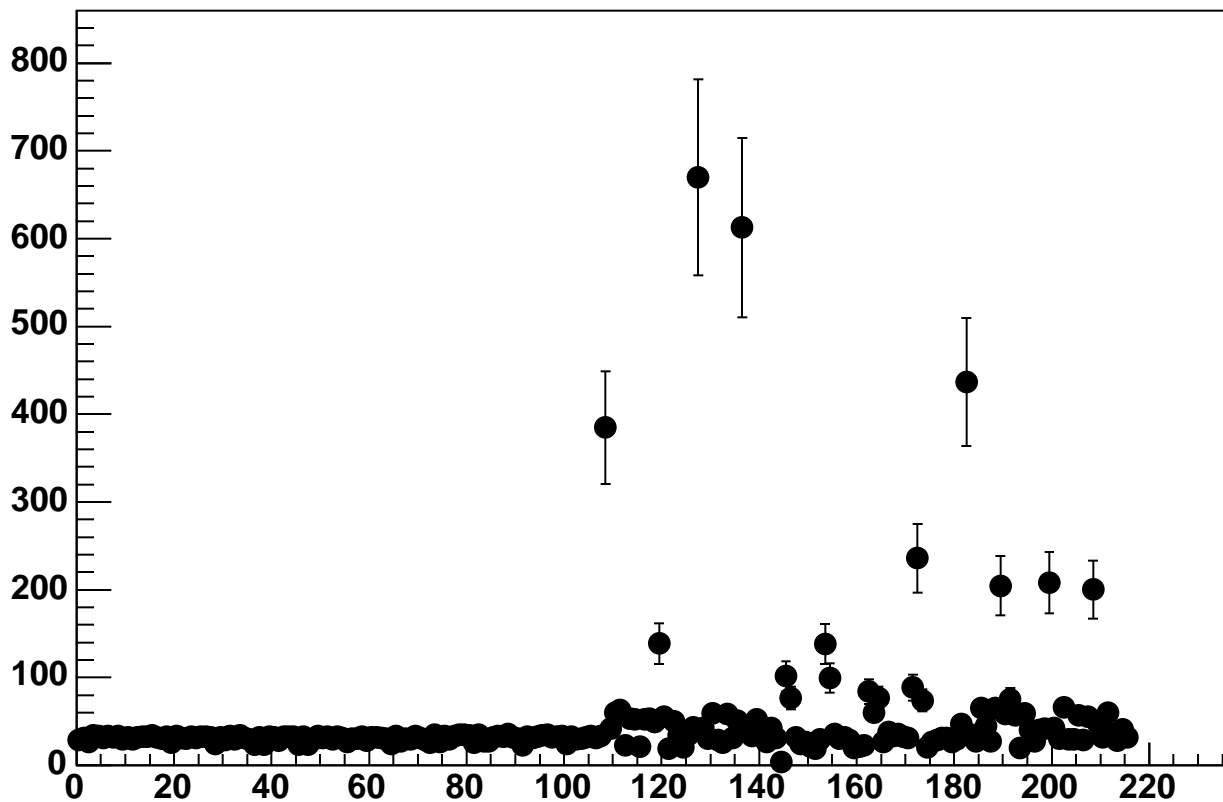
Enable 0, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



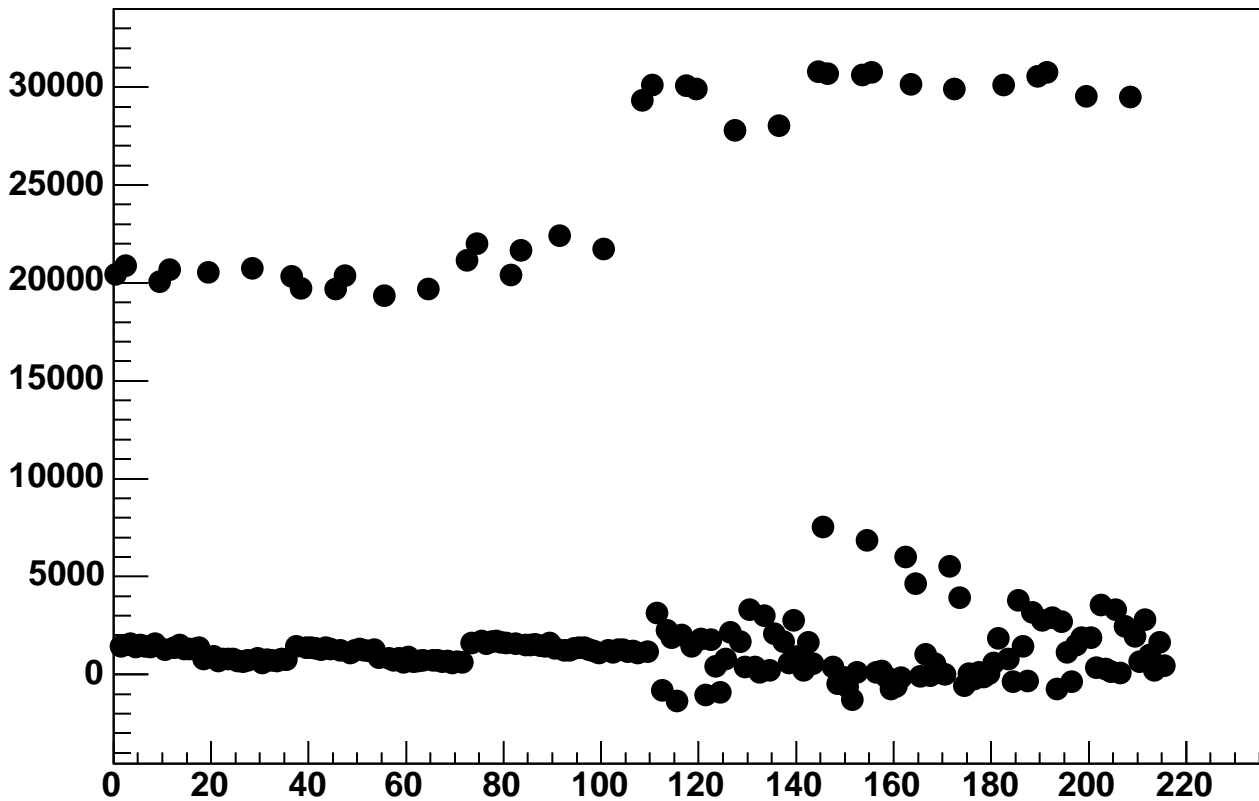
Enable 0, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



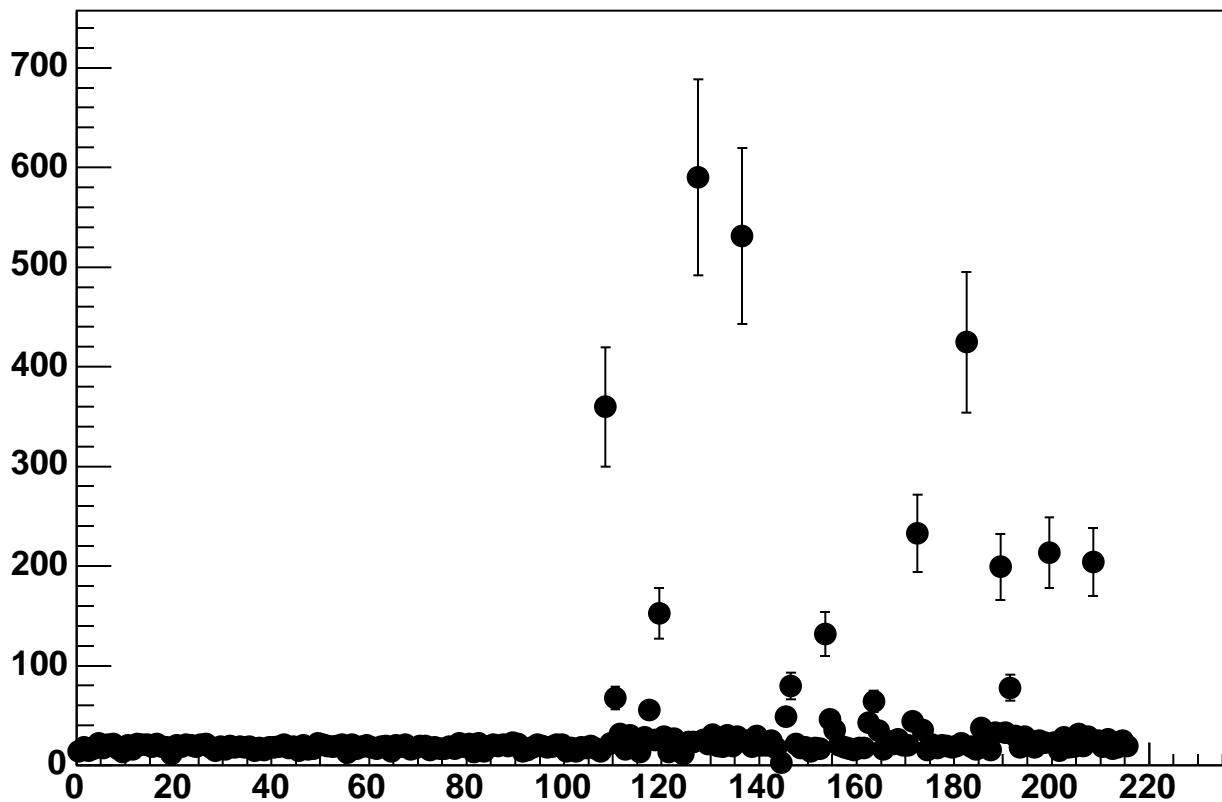
Enable 0, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



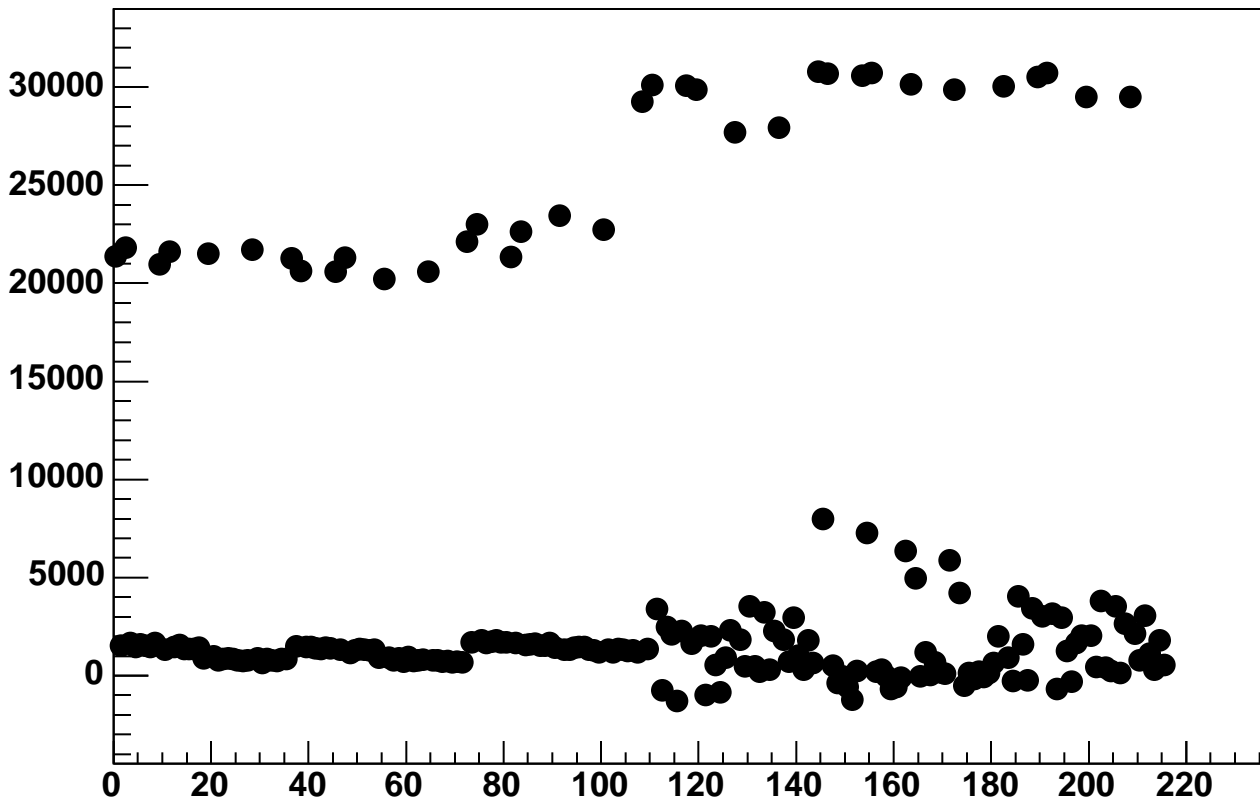
Enable 0, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



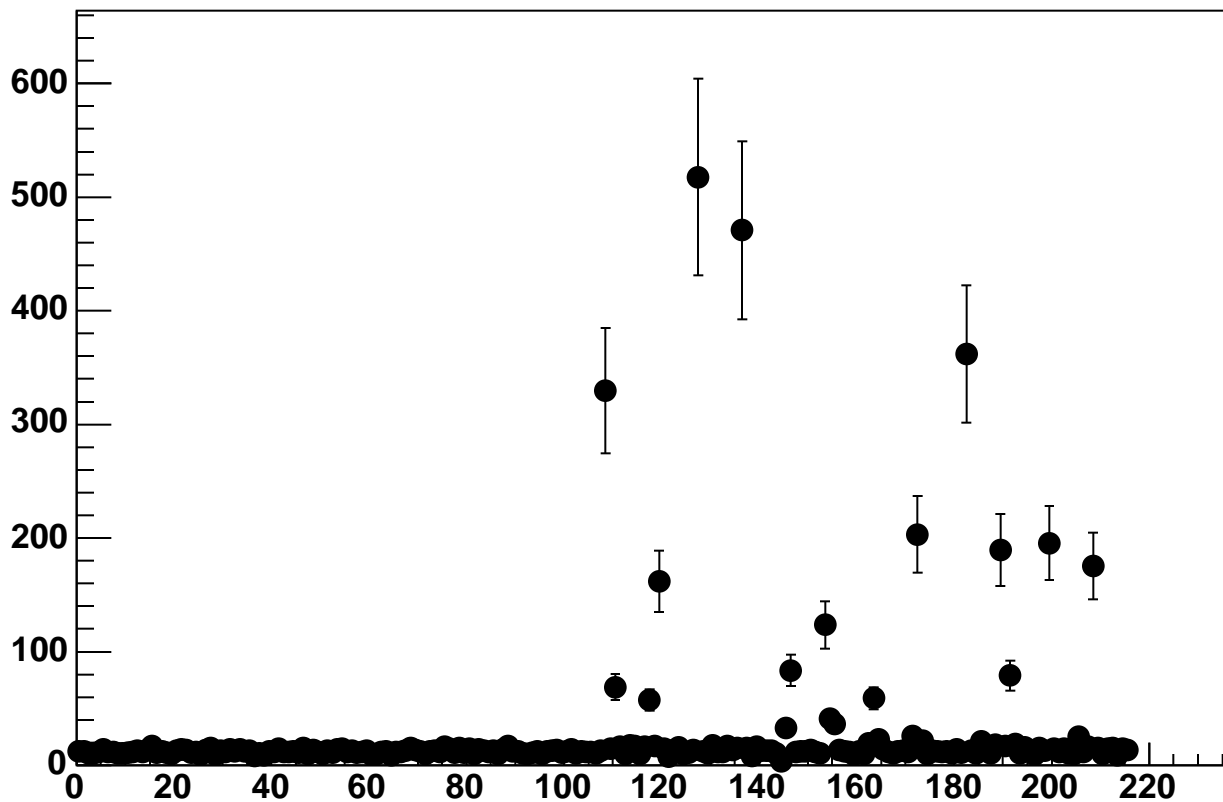
Enable 0, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



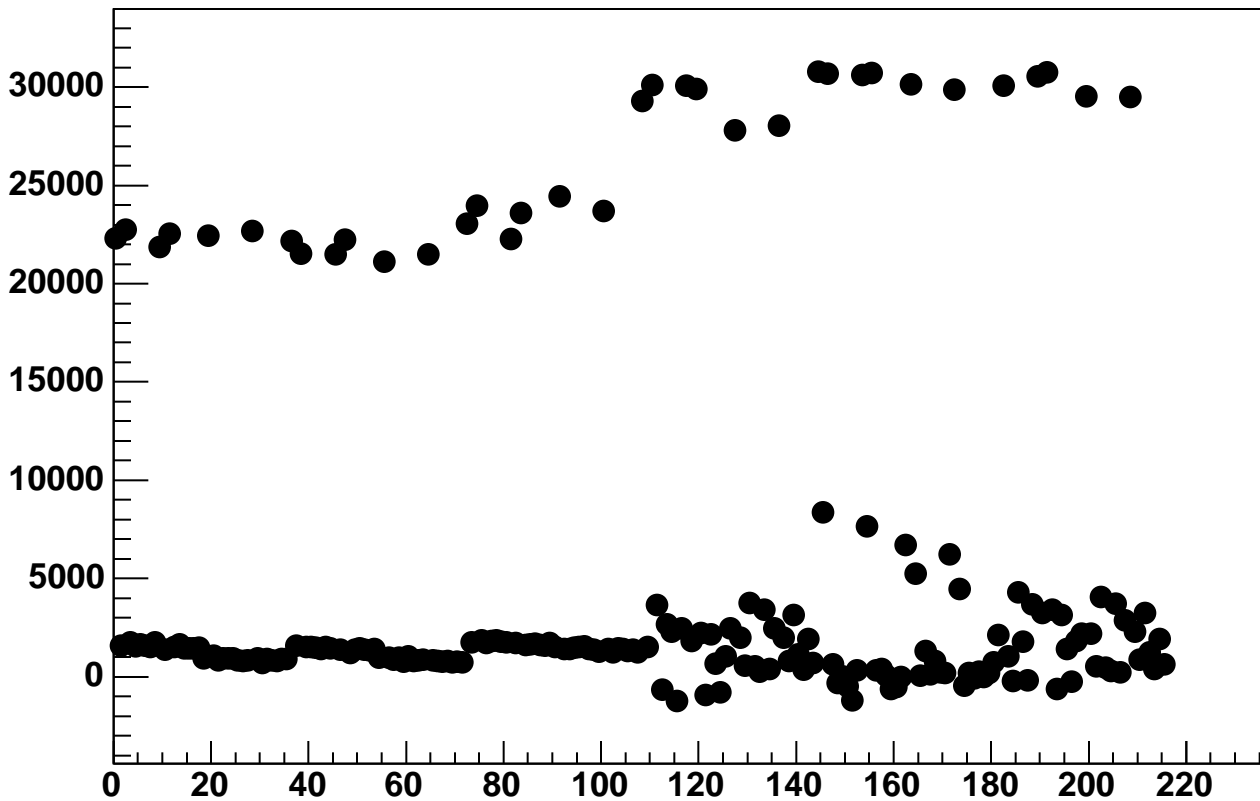
Enable 0, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



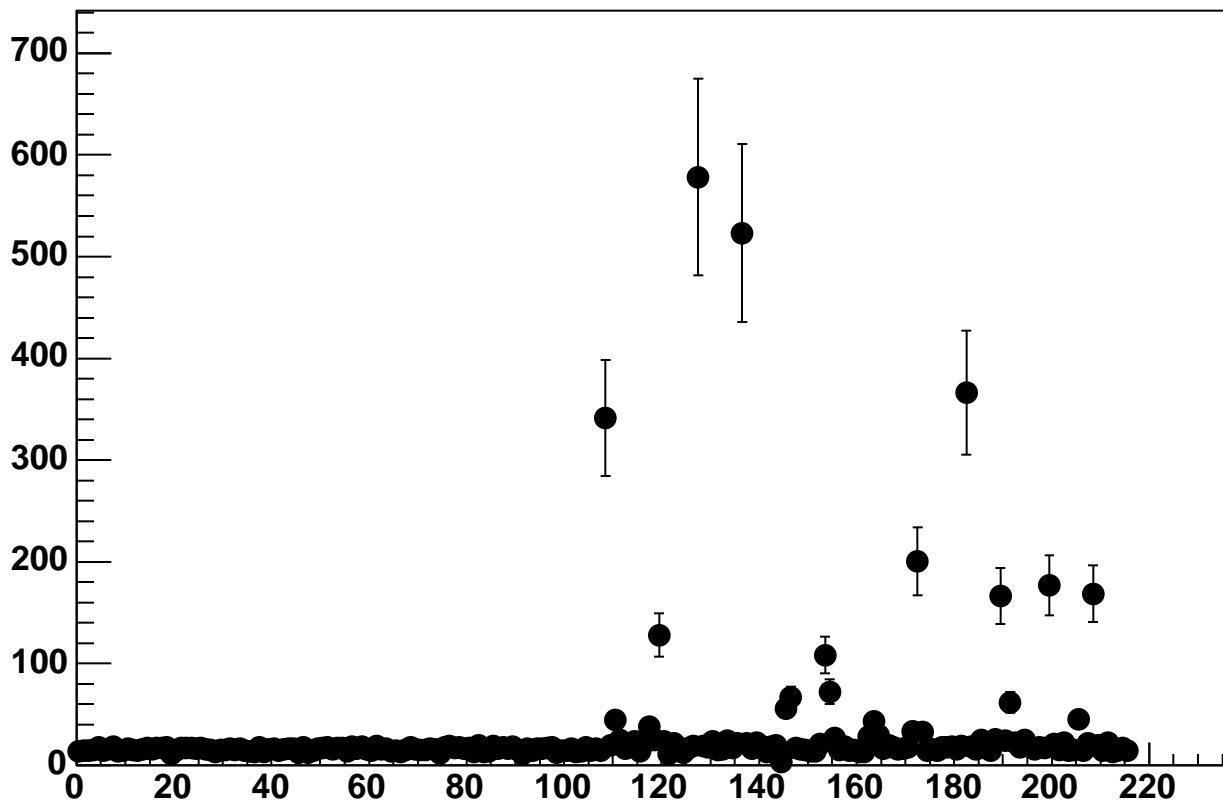
Enable 0, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



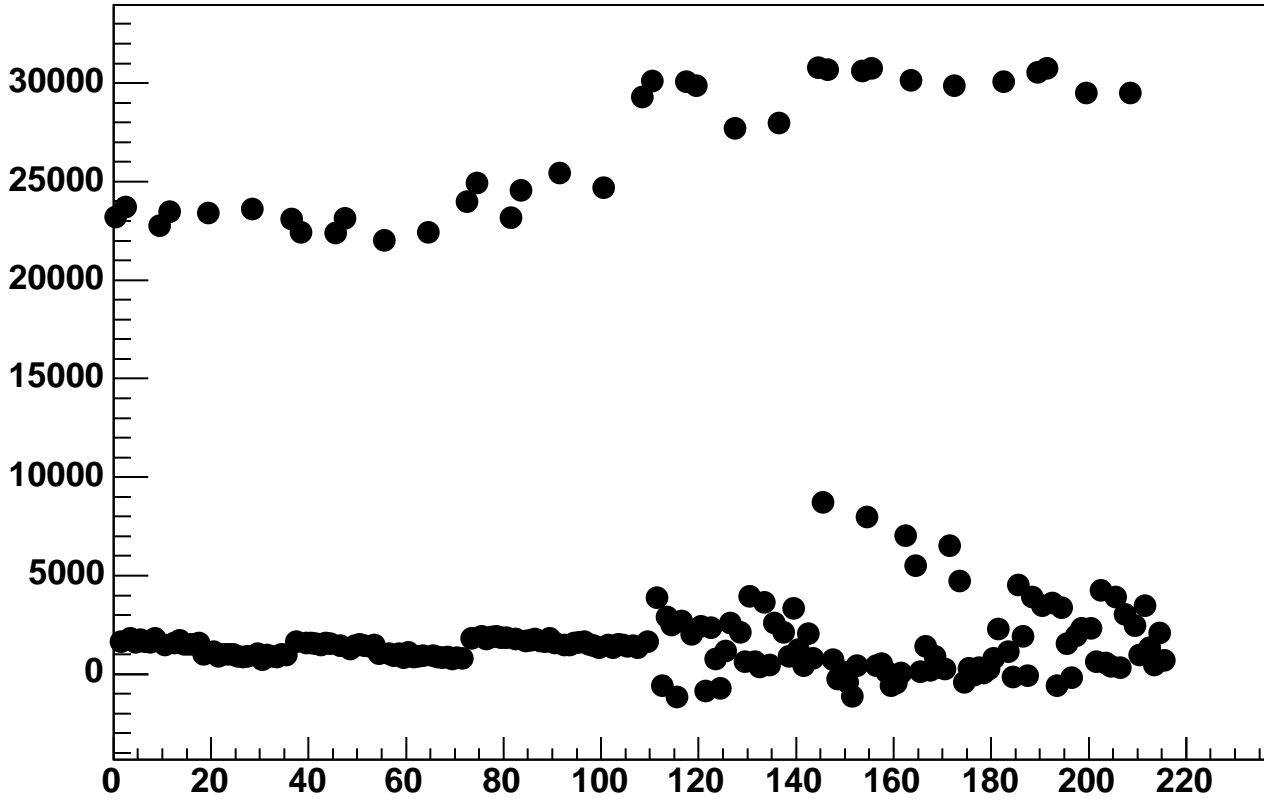
Enable 0, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



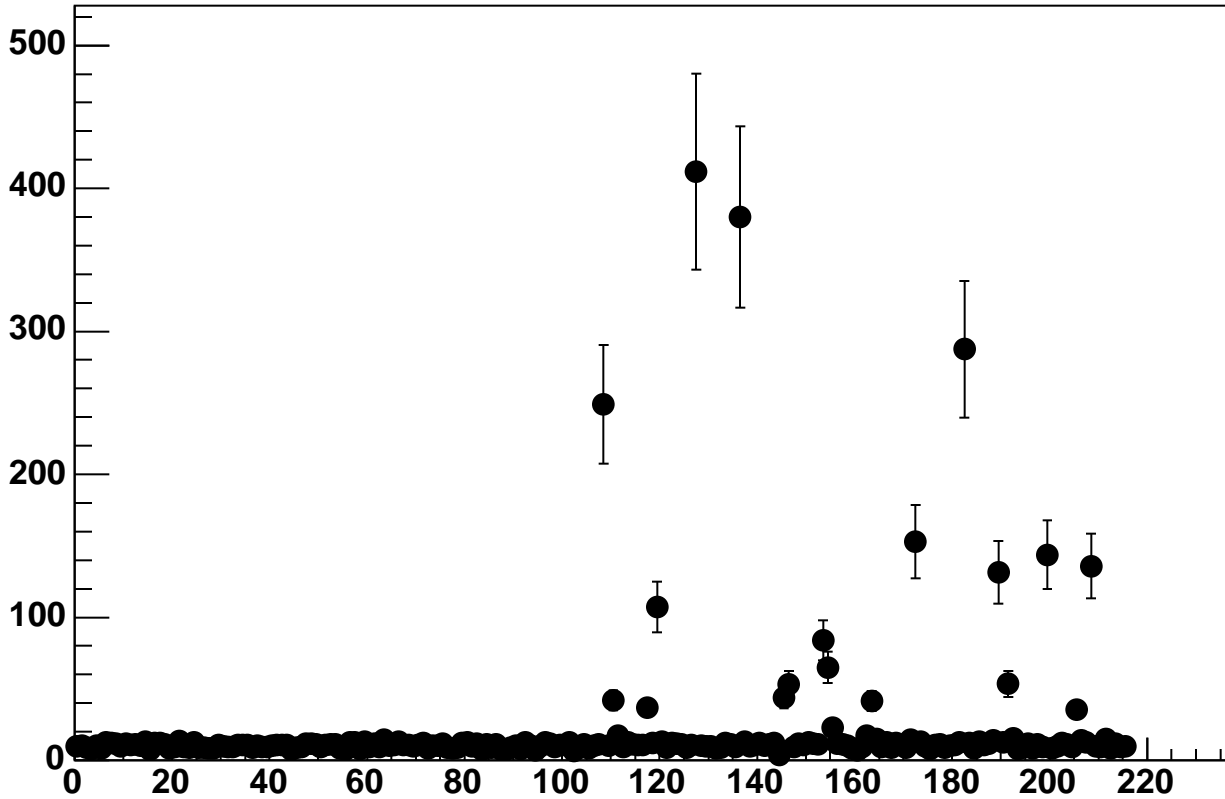
Enable 0, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



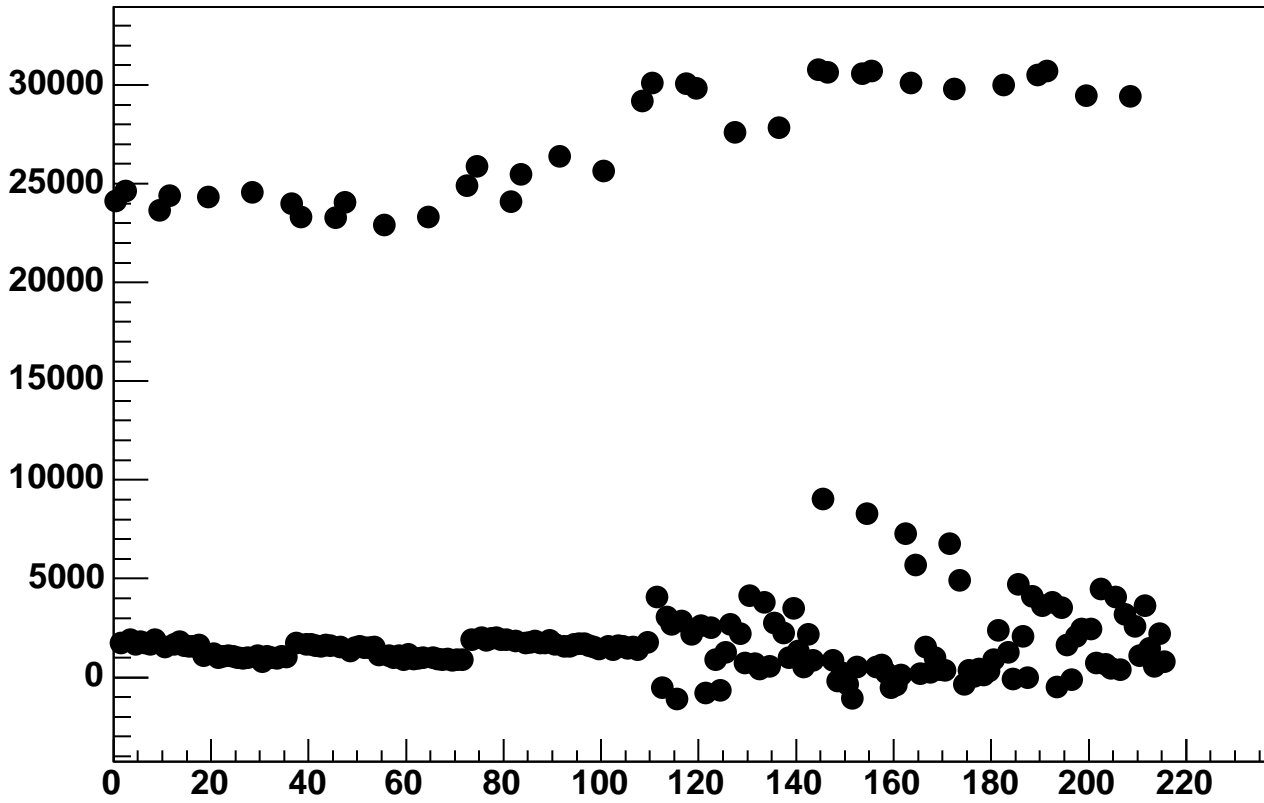
Enable 0, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



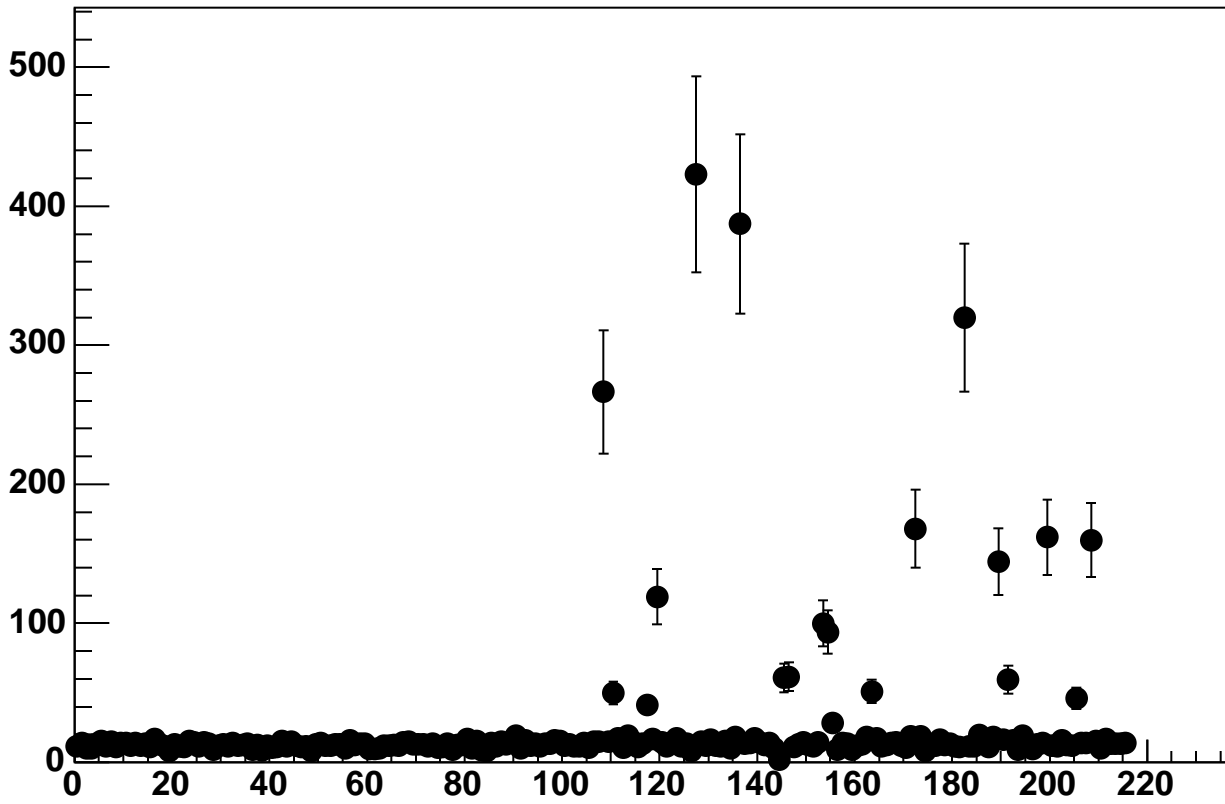
Enable 0, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



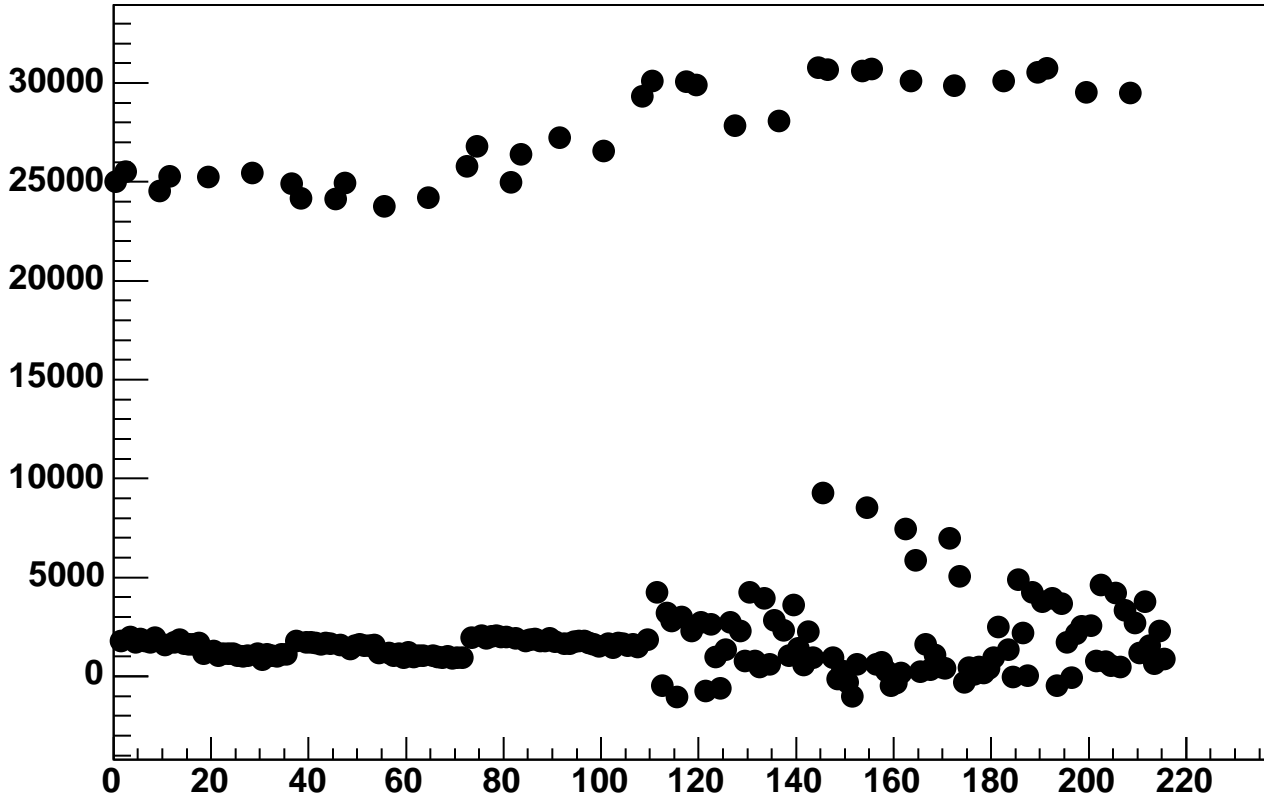
Enable 0, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



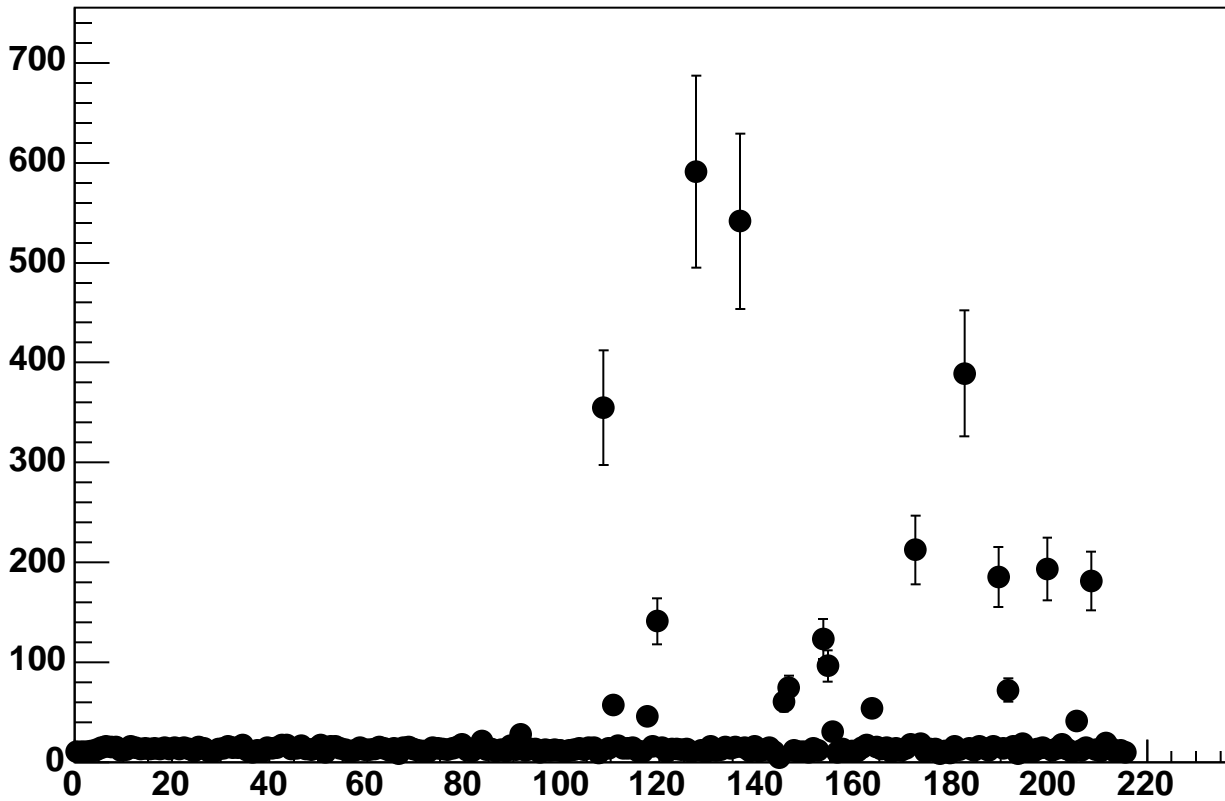
Enable 0, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



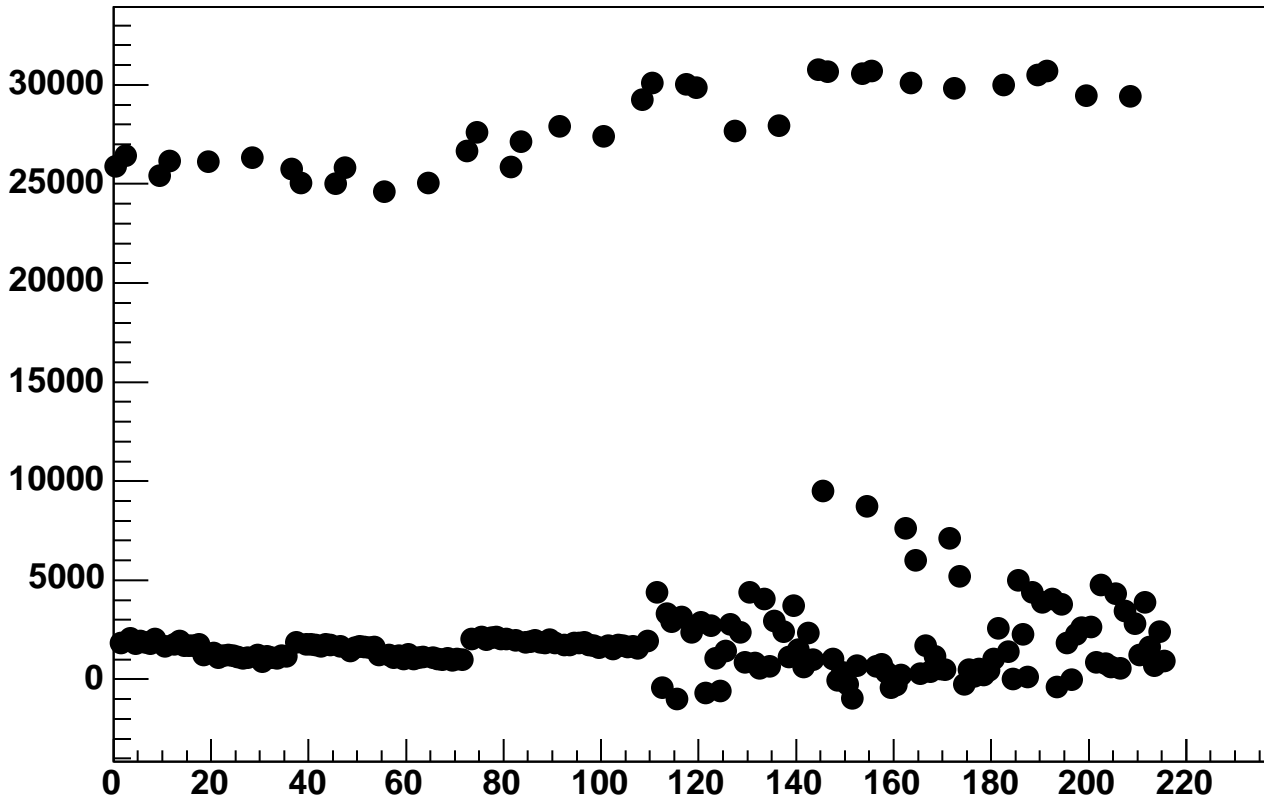
Enable 0, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



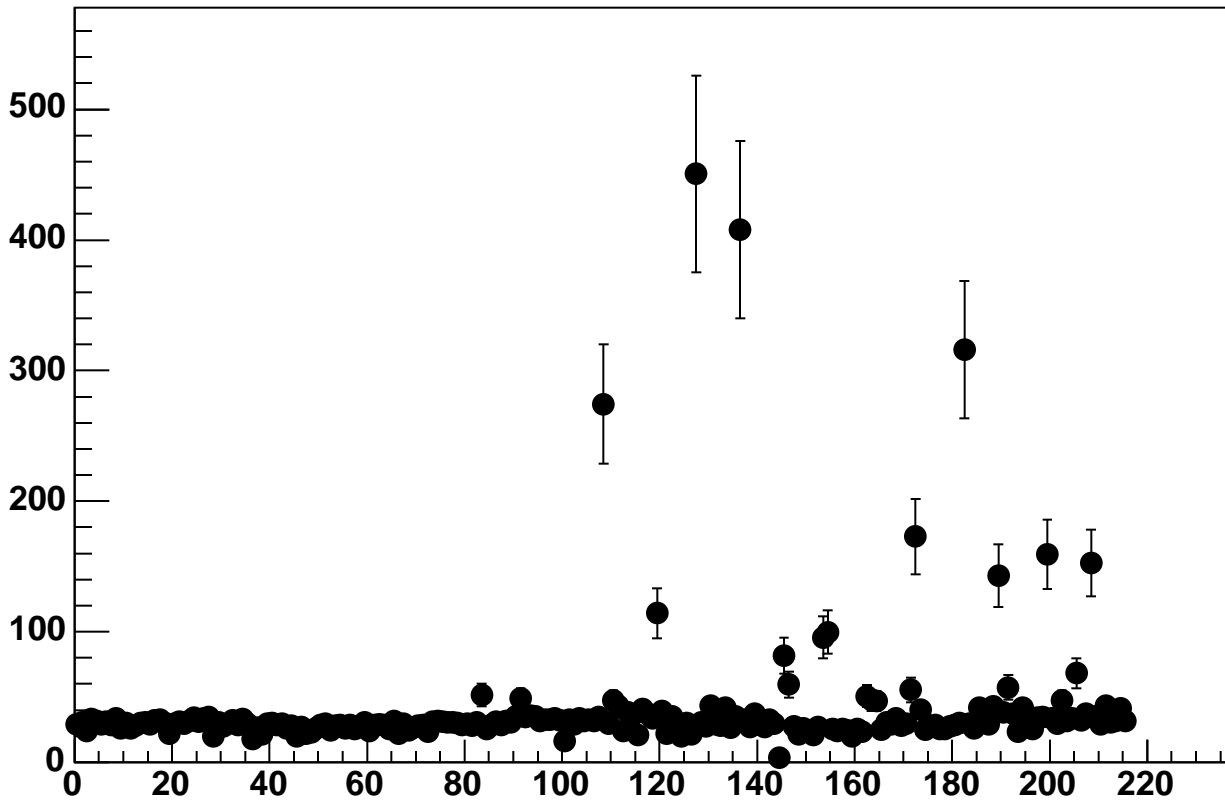
Enable 0, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



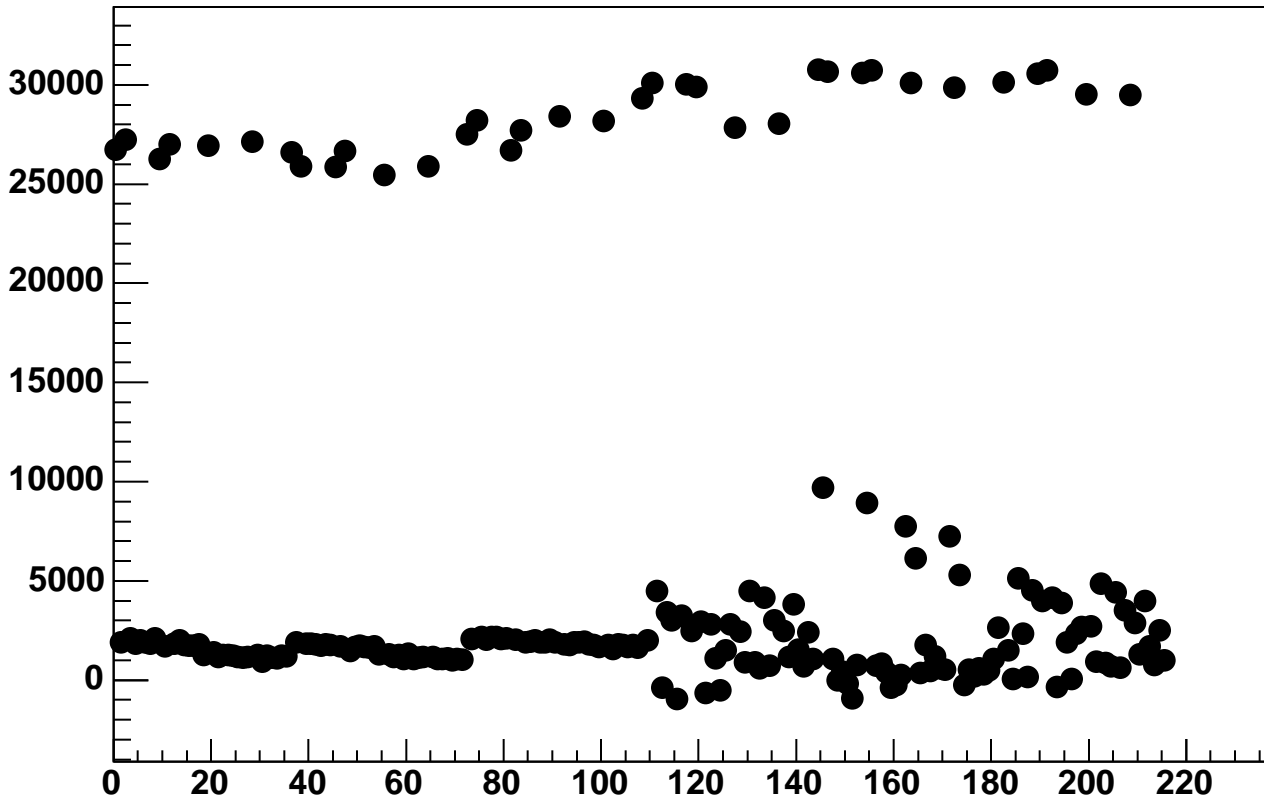
Enable 0, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



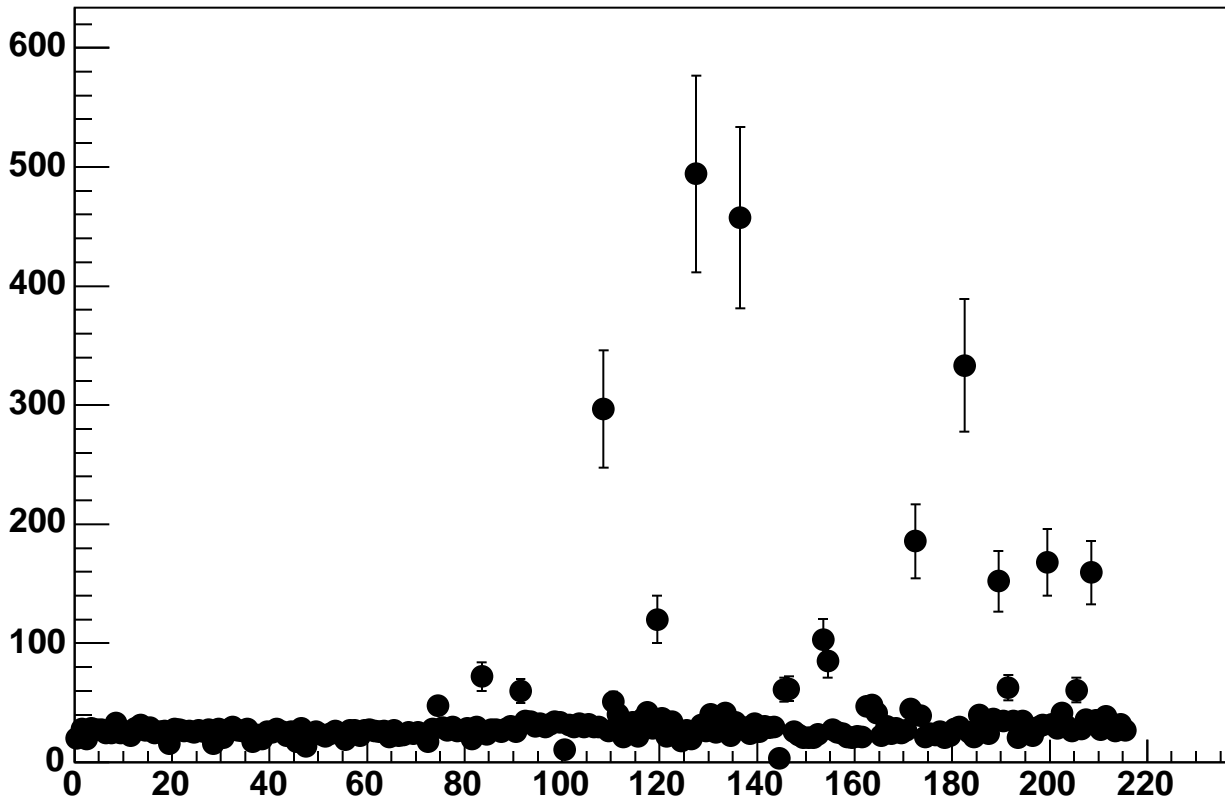
Enable 0, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



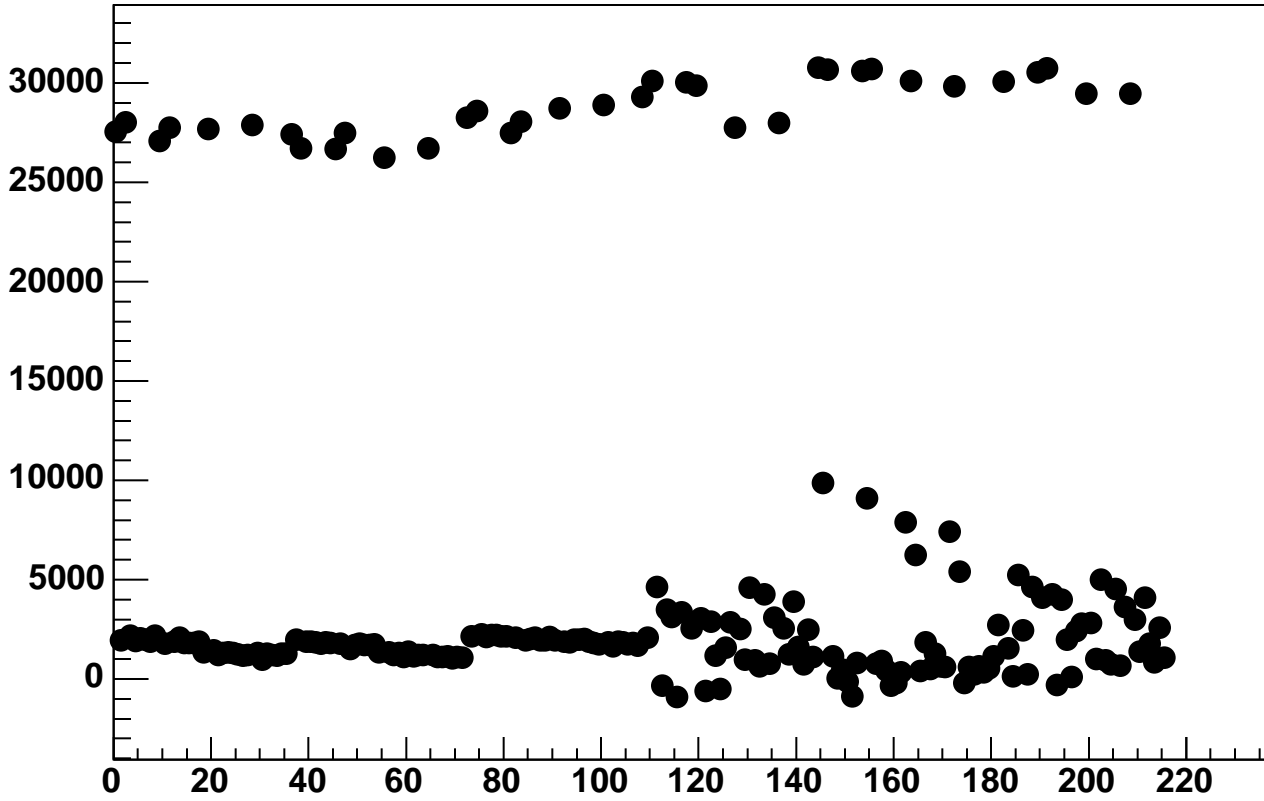
Enable 0, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



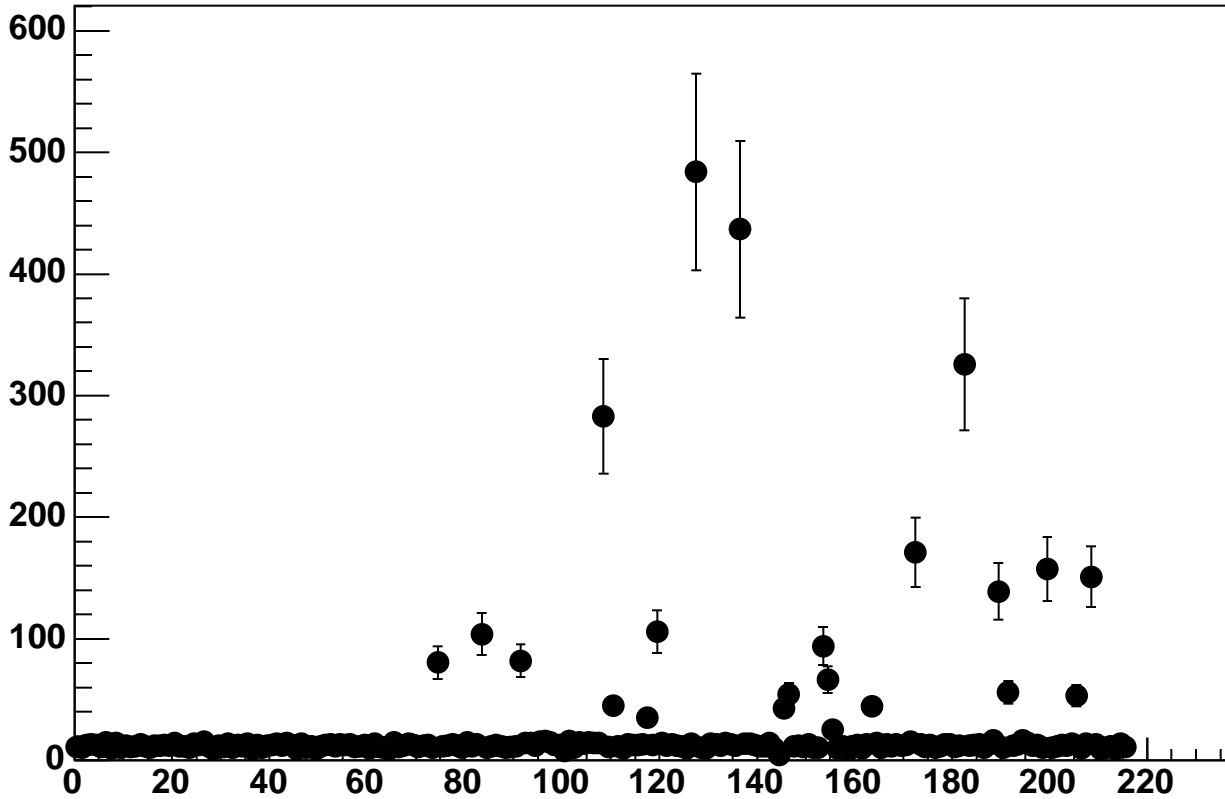
Enable 0, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



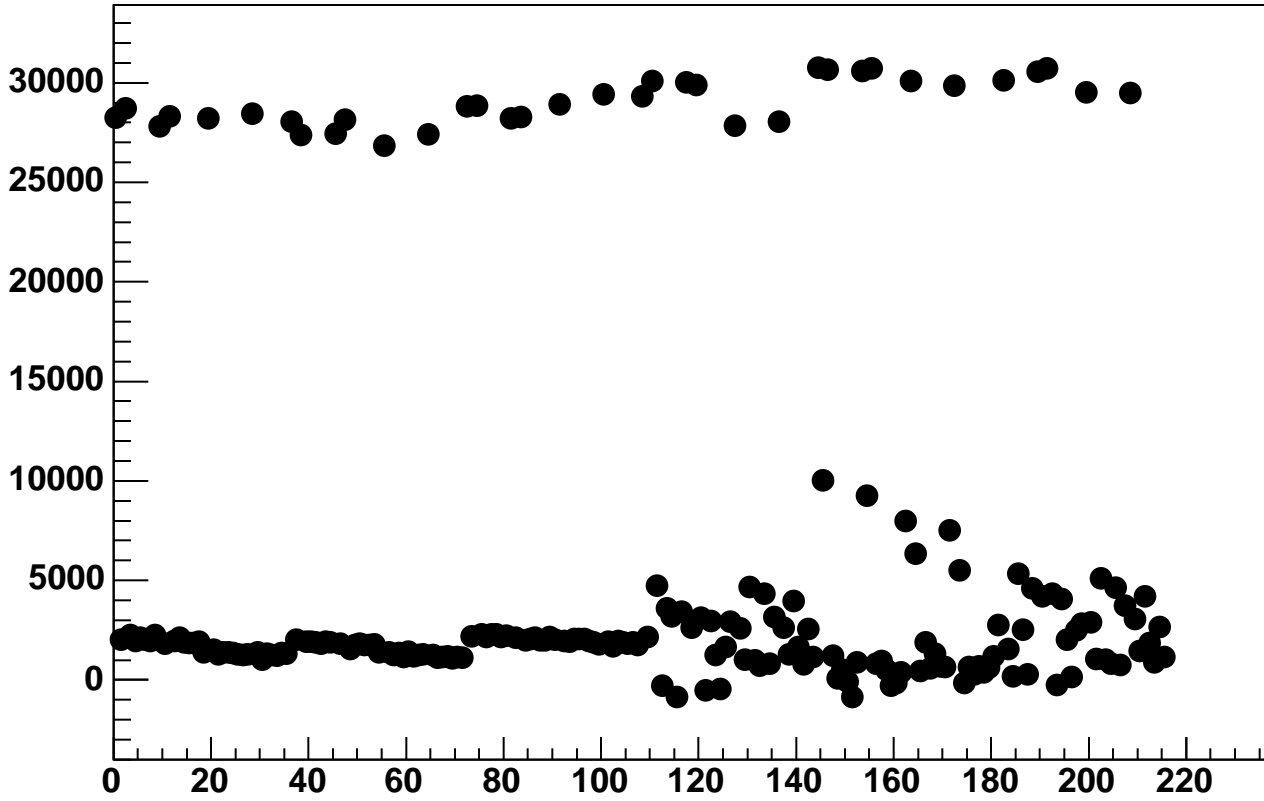
Enable 0, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



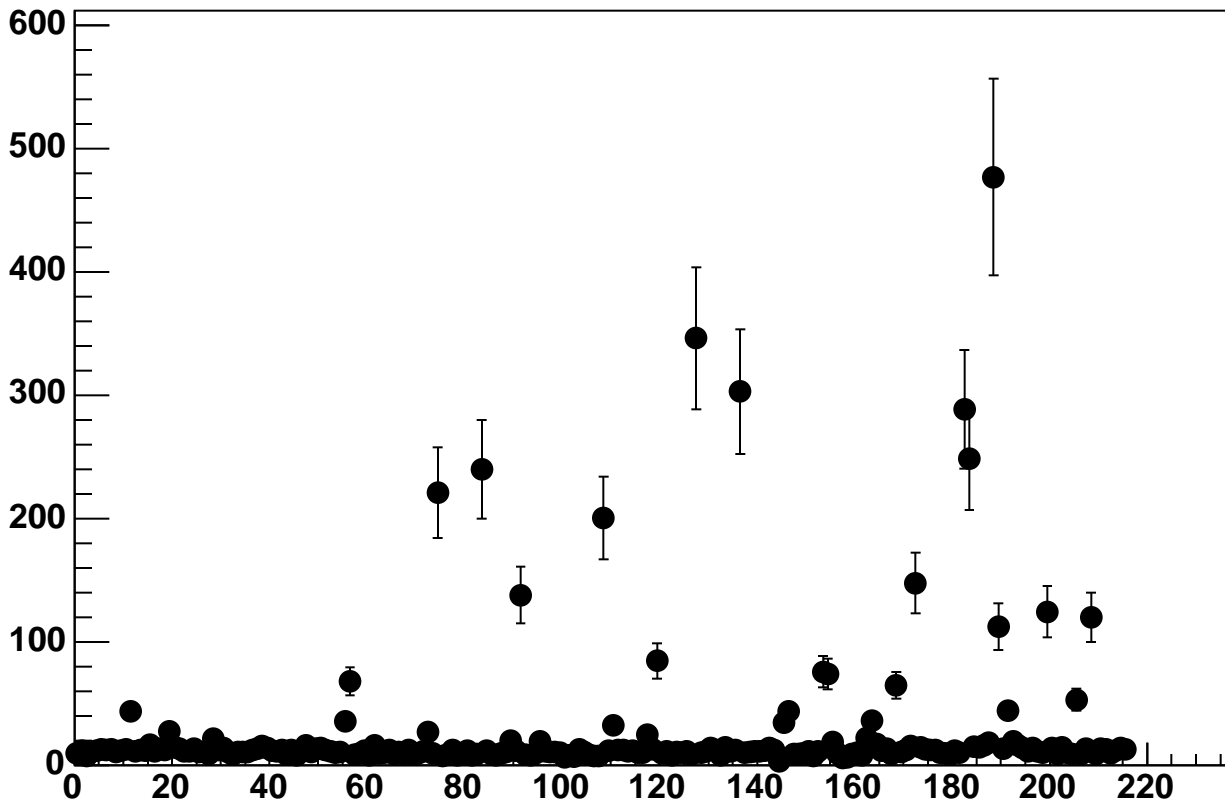
Enable 0, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



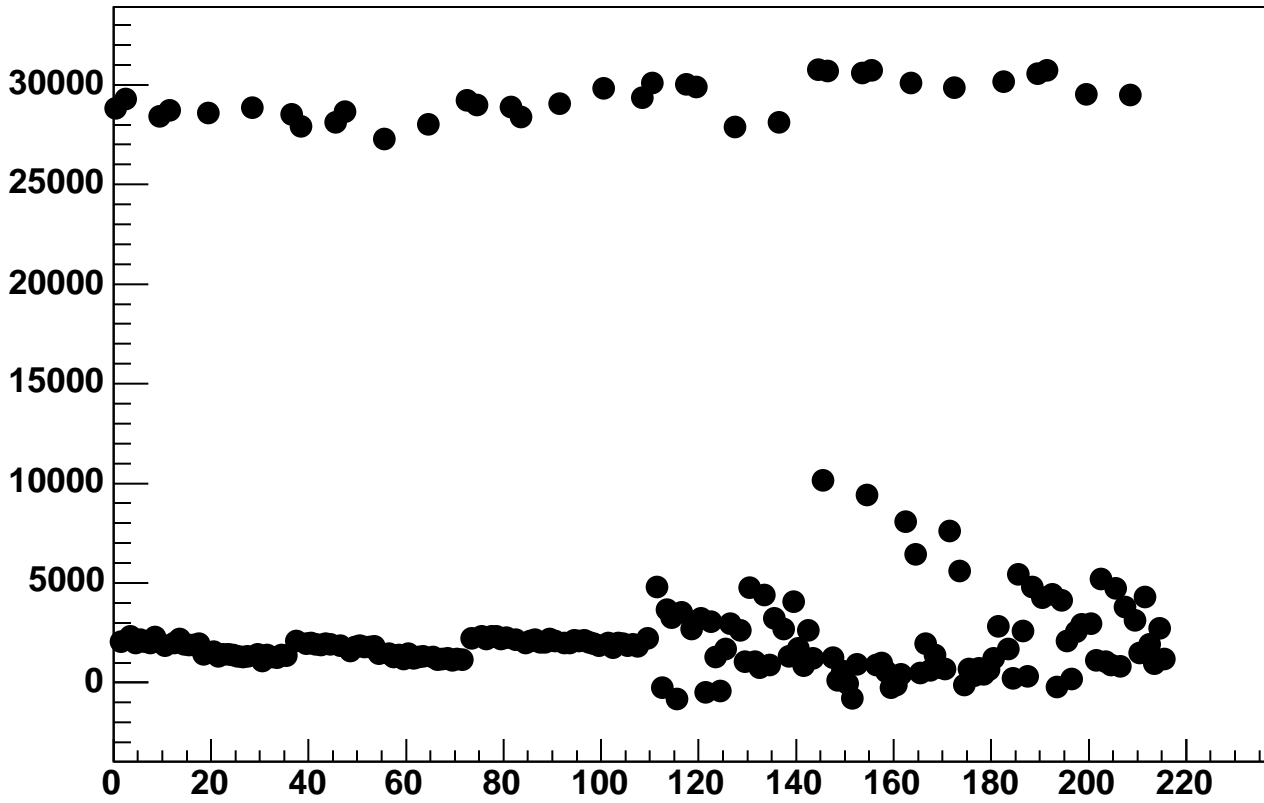
Enable 0, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



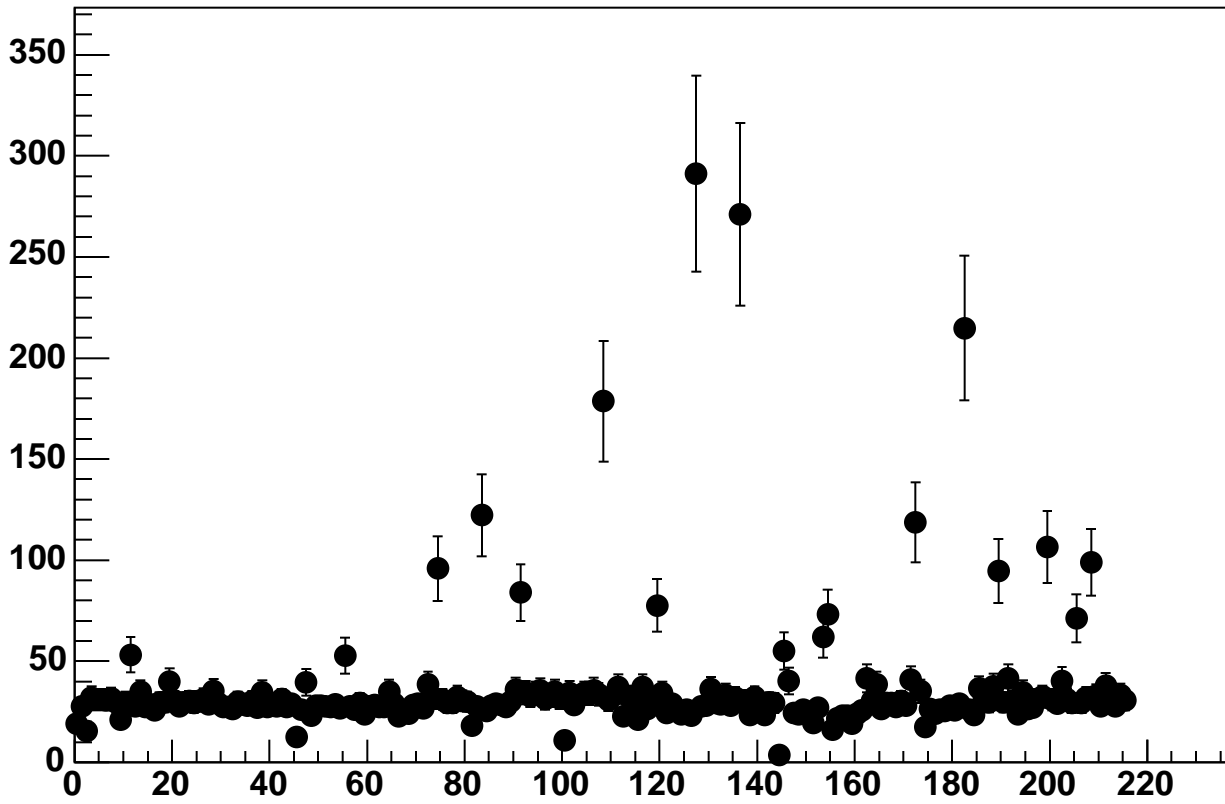
Enable 0, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



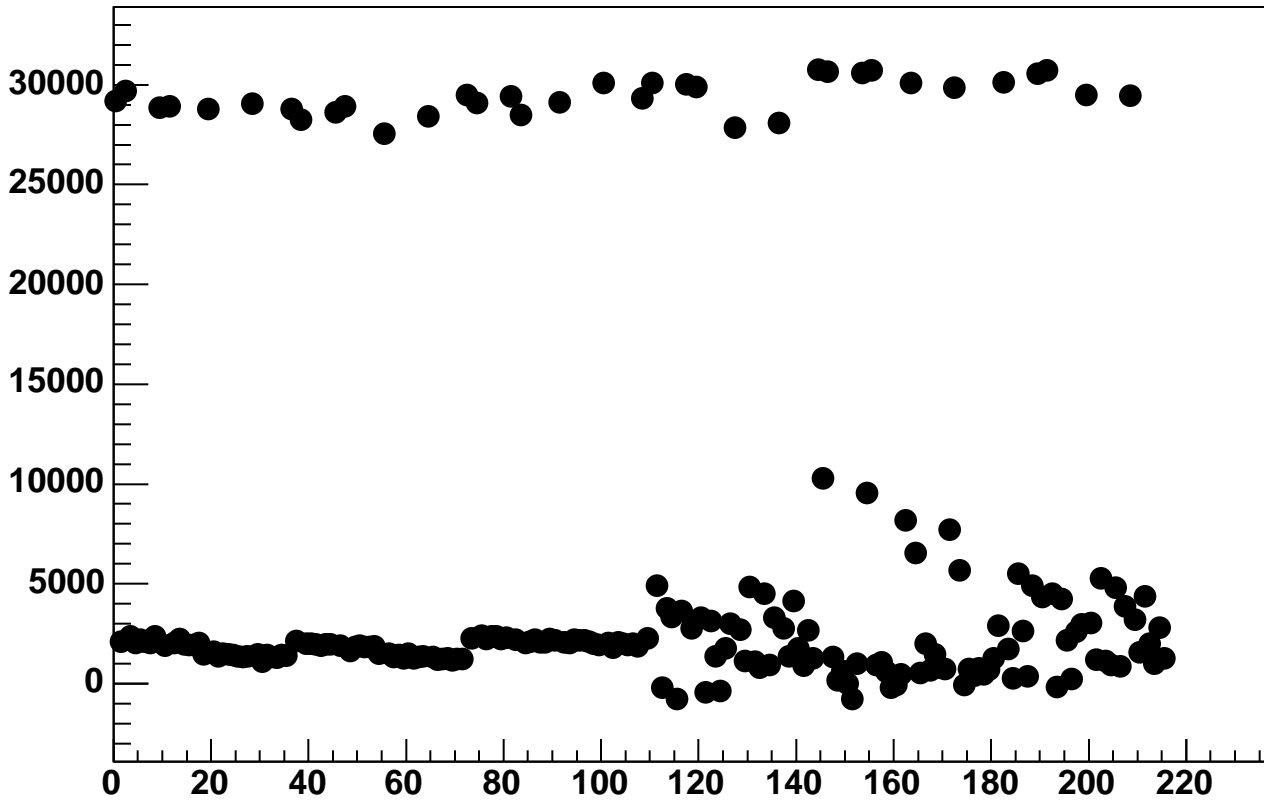
Enable 0, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



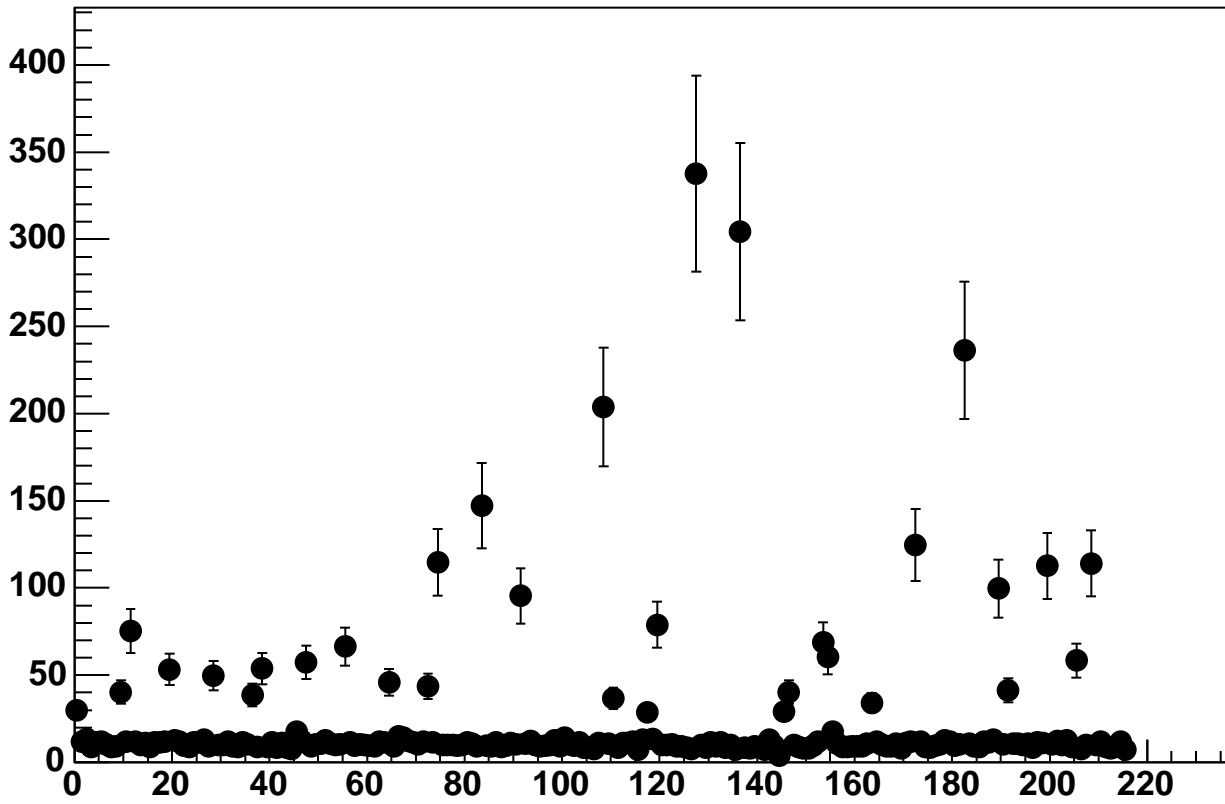
Enable 0, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



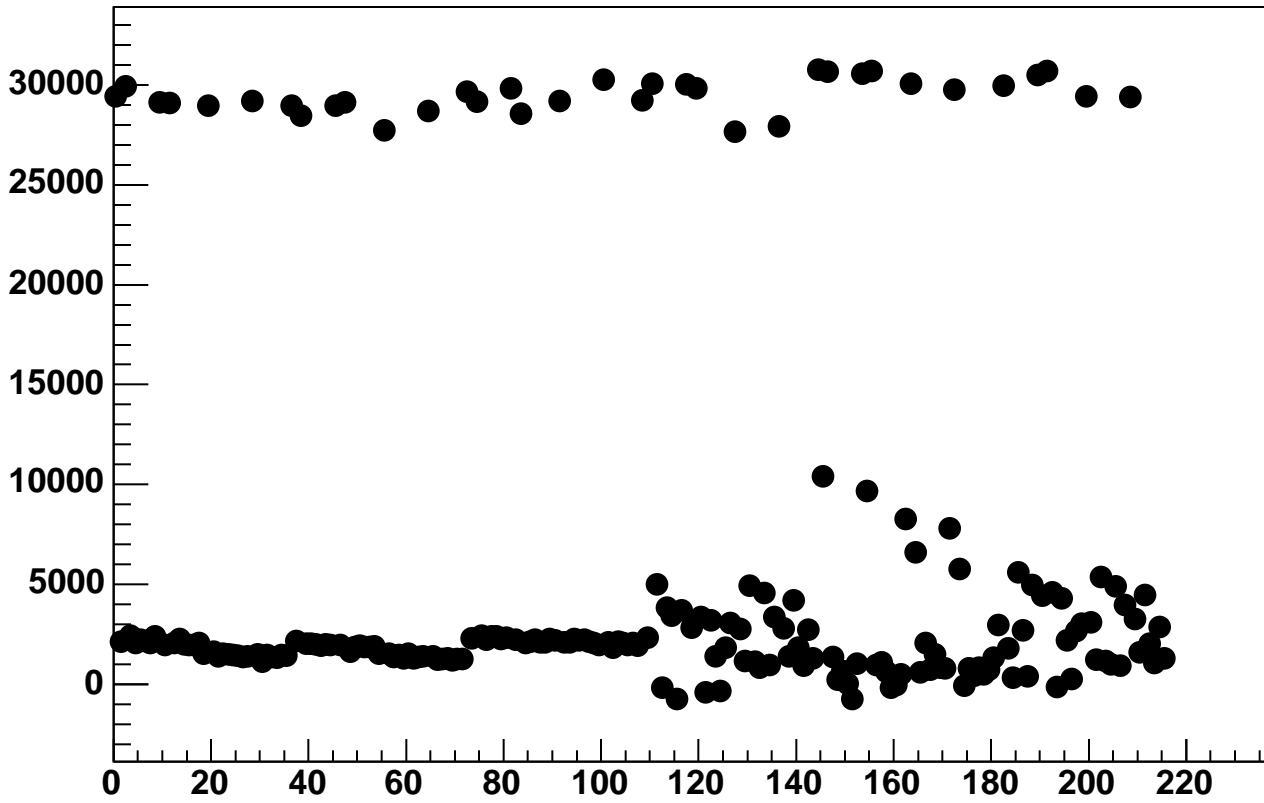
Enable 0, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



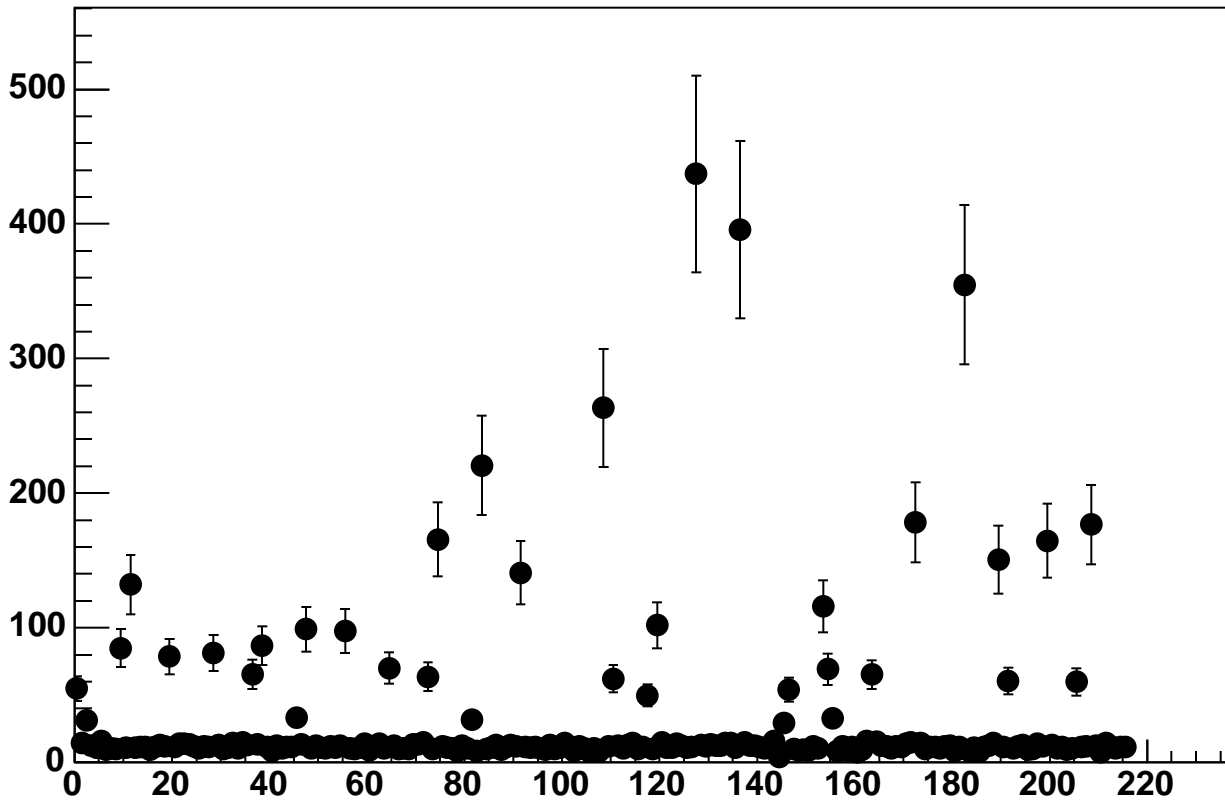
Enable 0, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



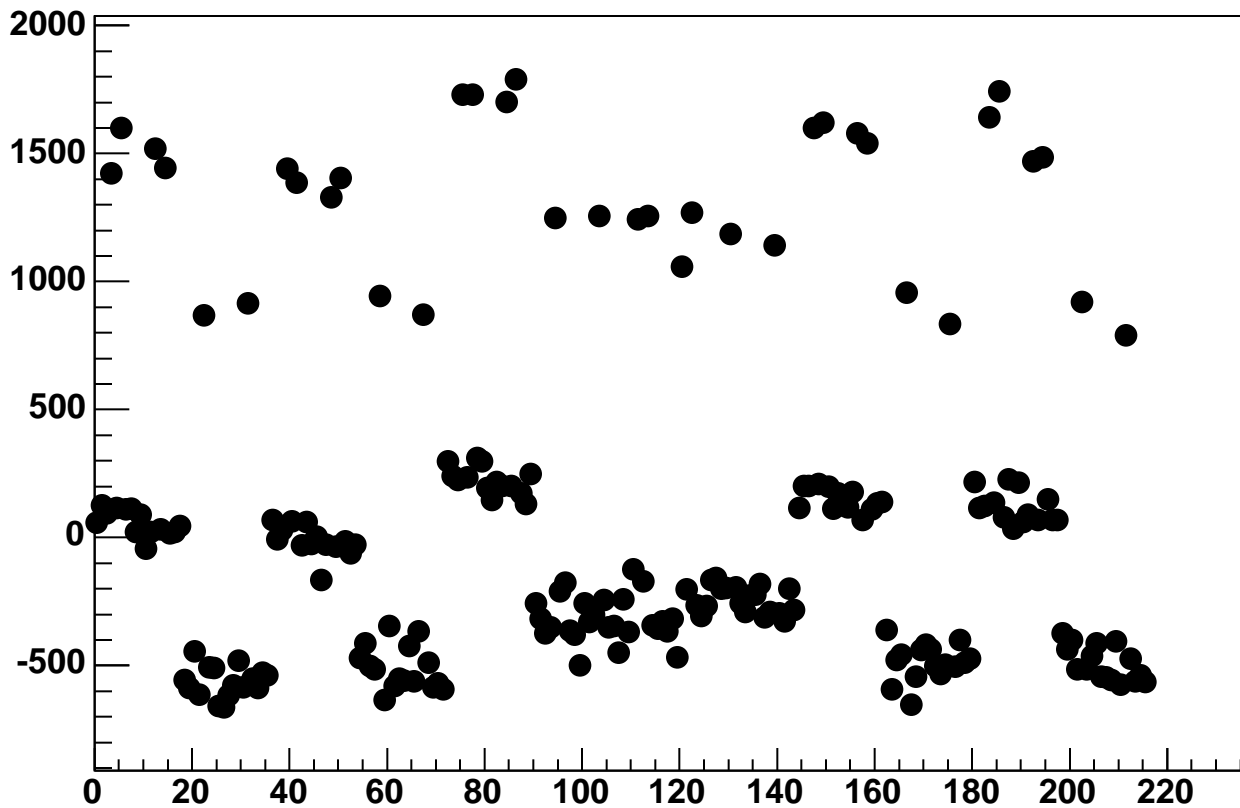
Enable 0, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



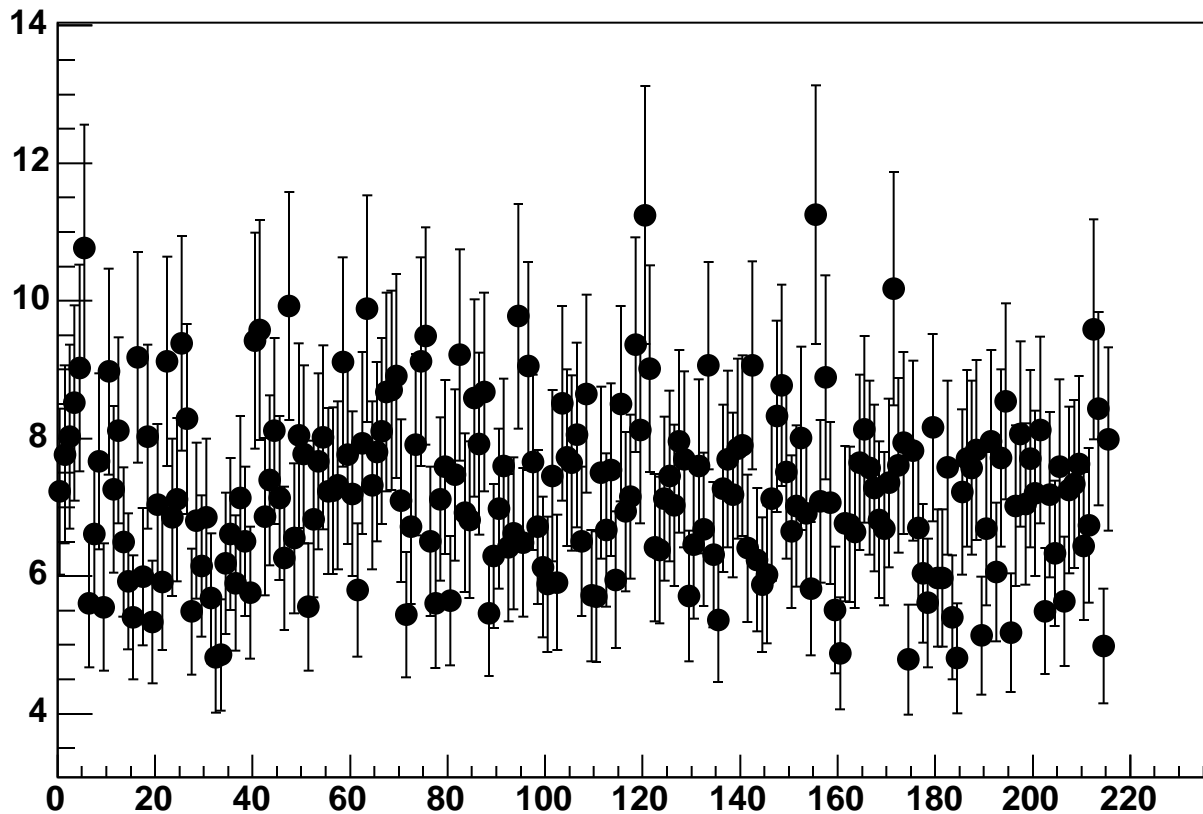
Enable 0, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



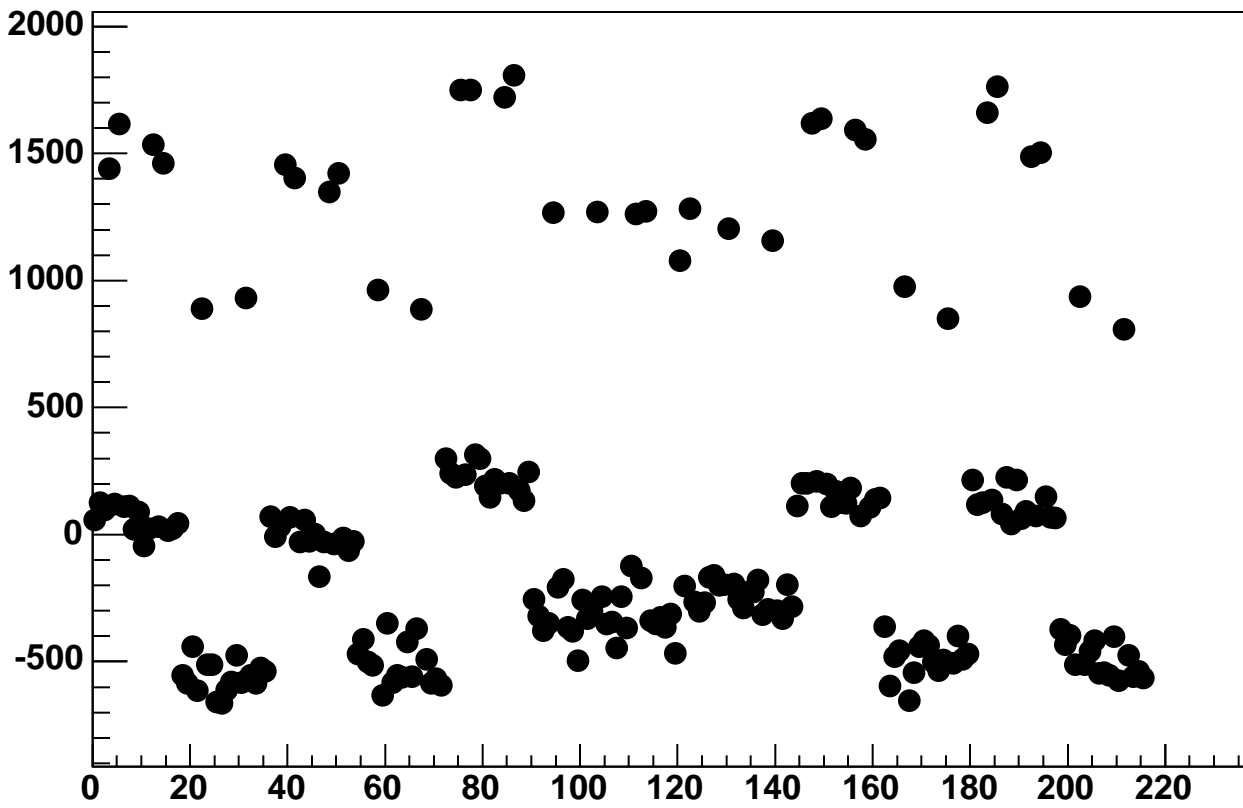
Enable 1, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



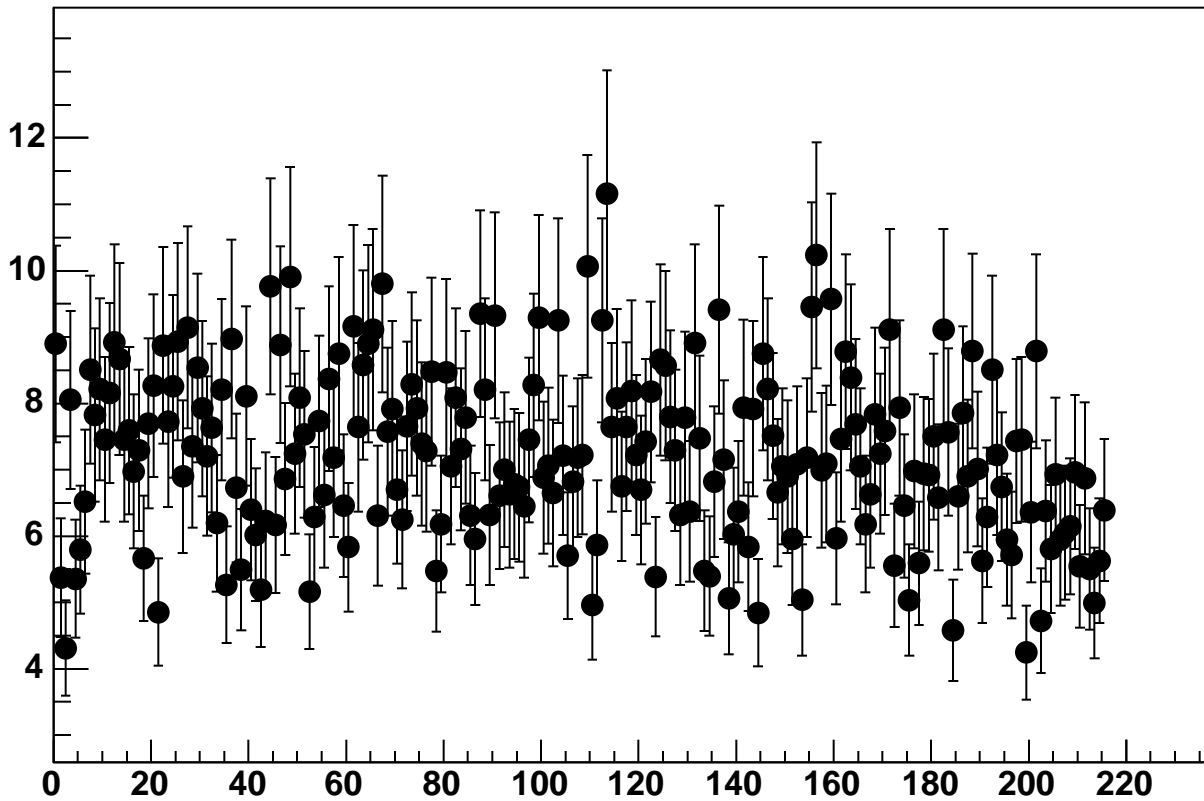
Enable 1, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



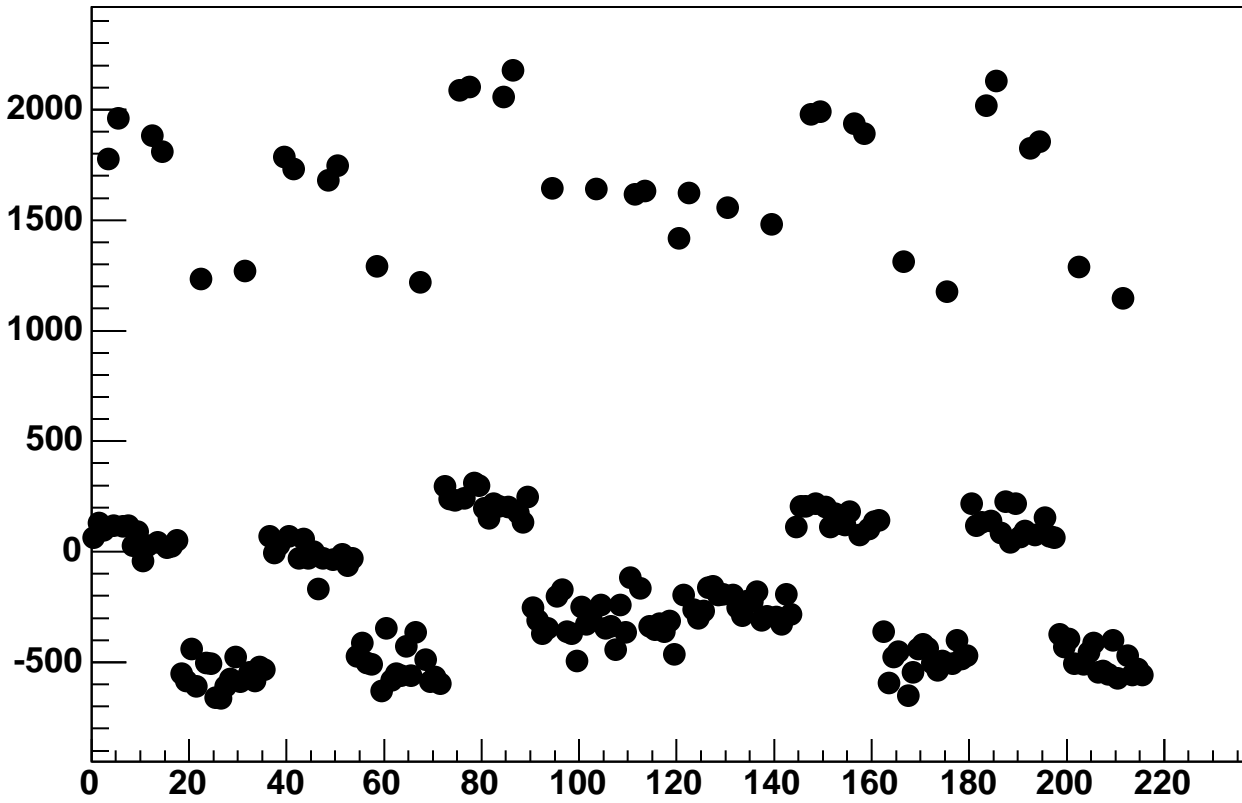
Enable 1, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



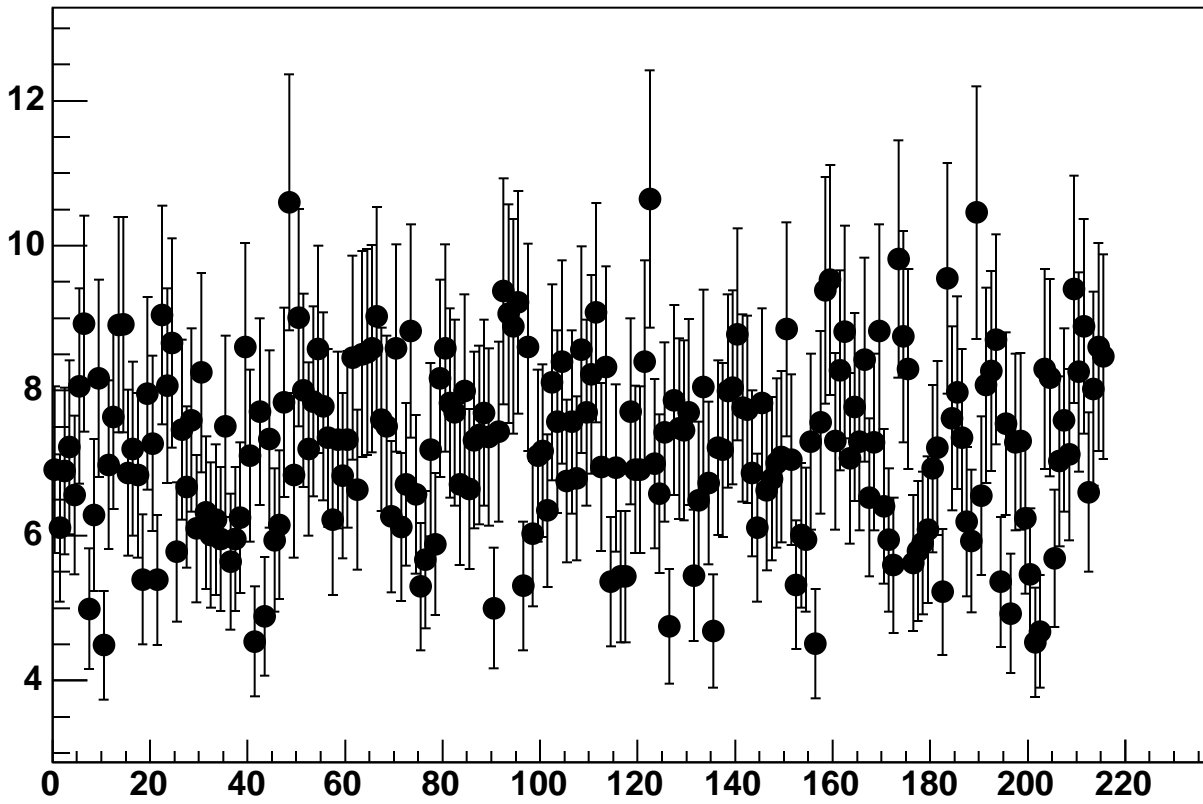
Enable 1, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



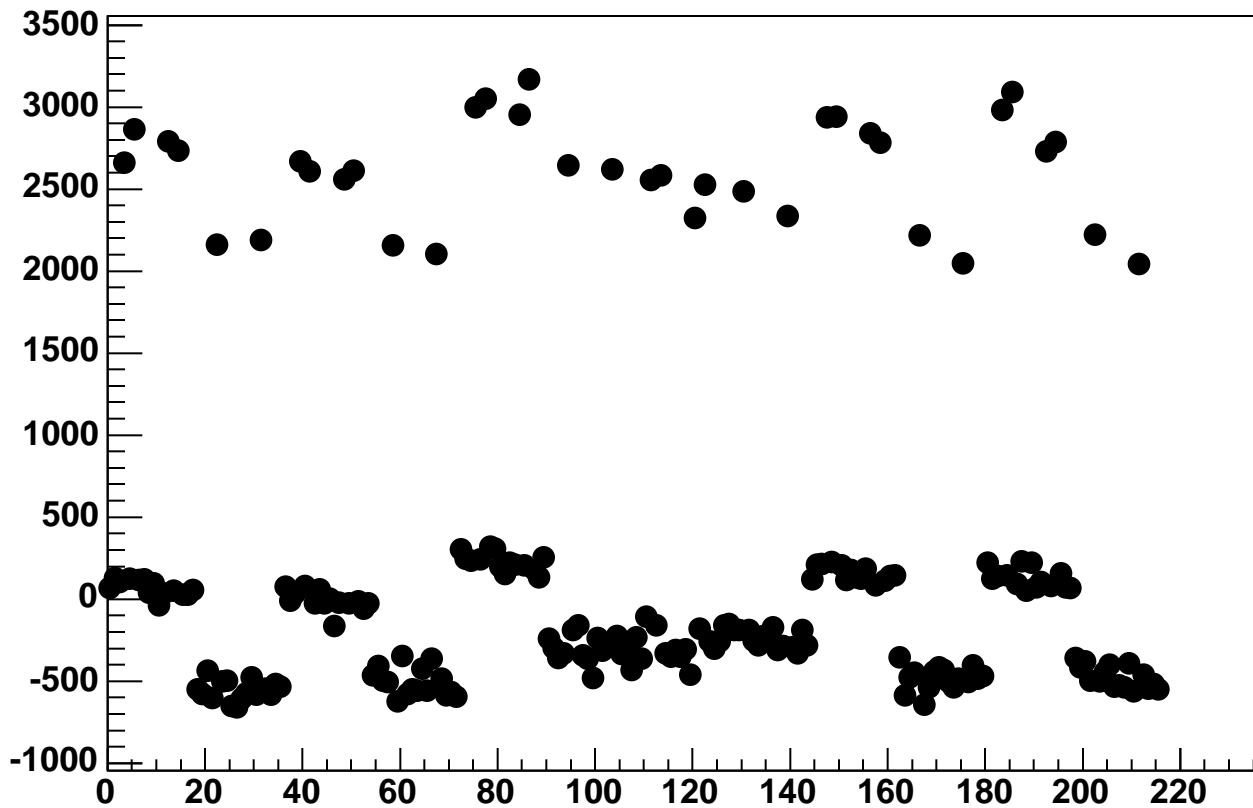
Enable 1, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



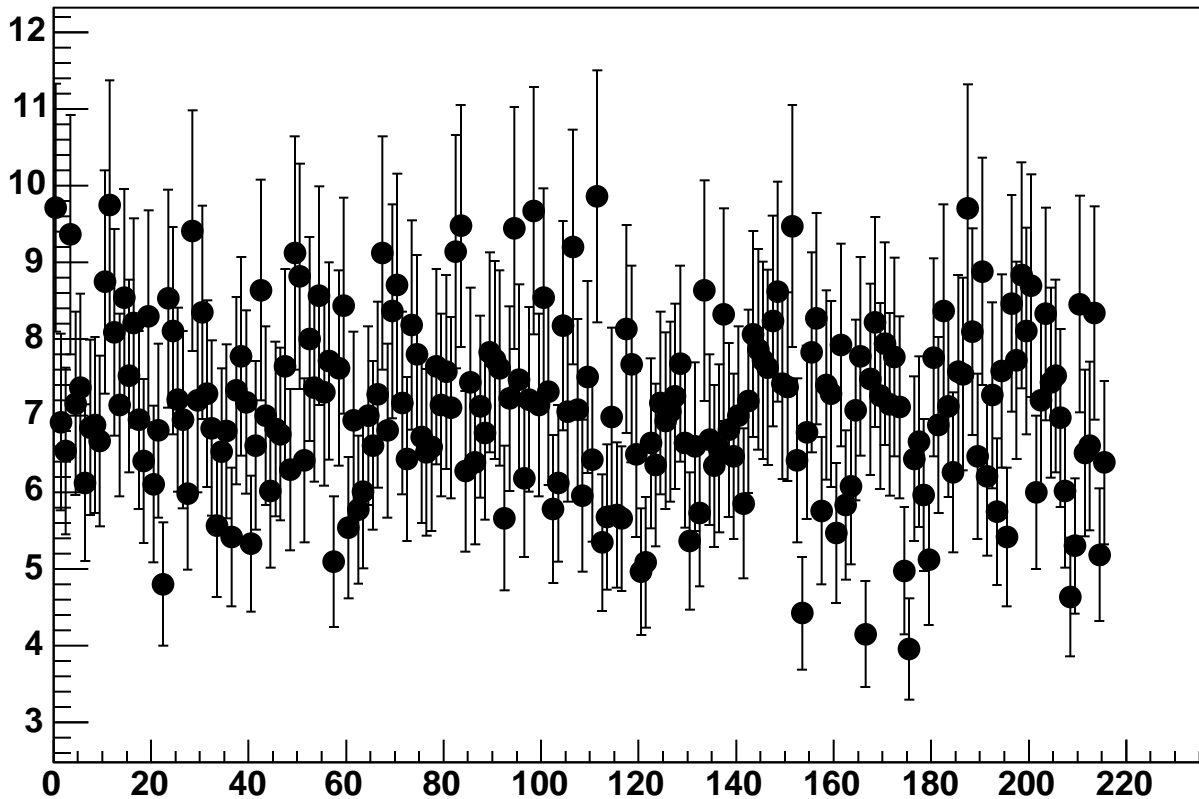
Enable 1, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



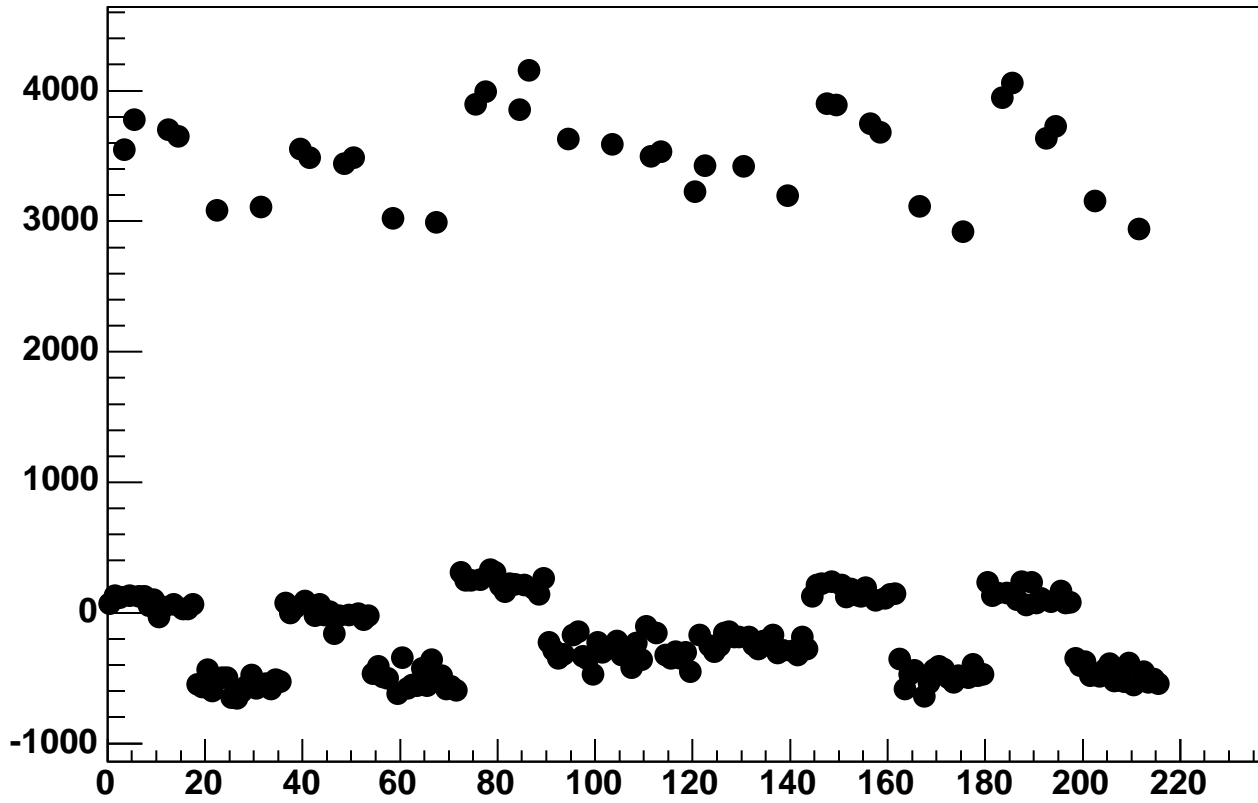
Enable 1, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



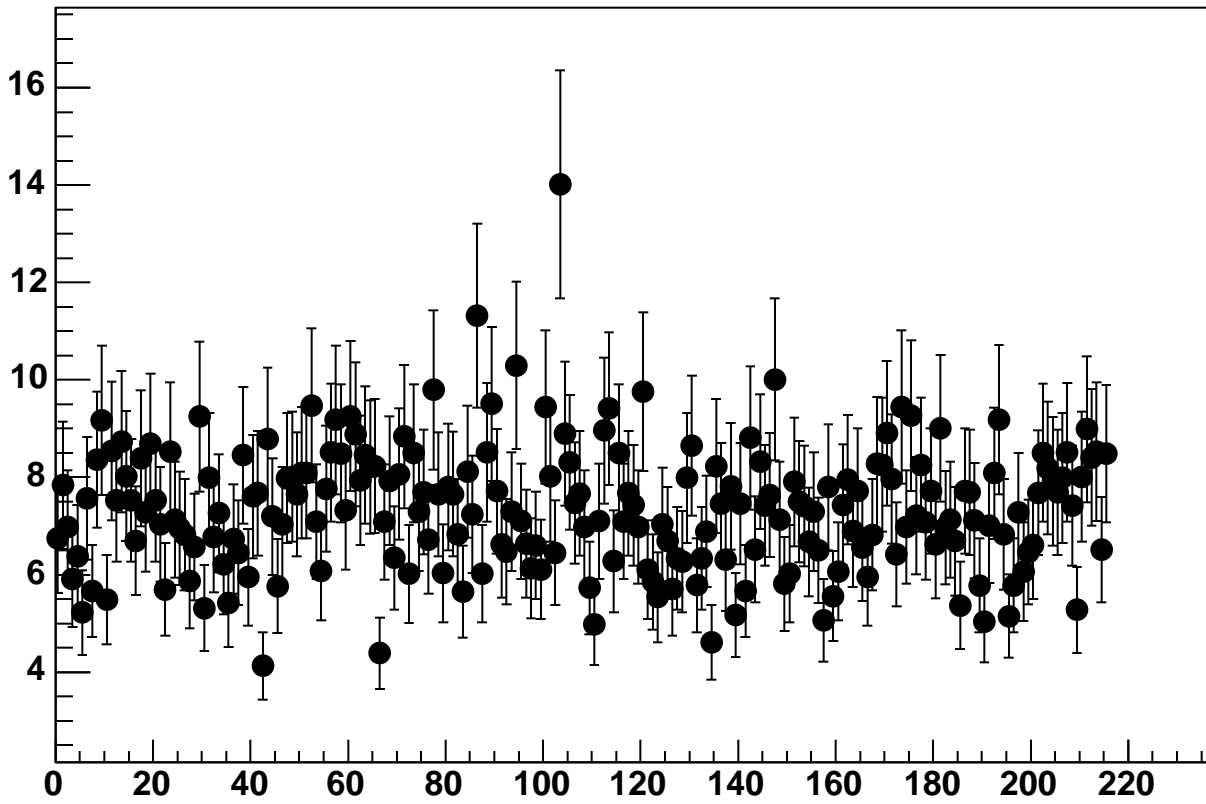
Enable 1, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



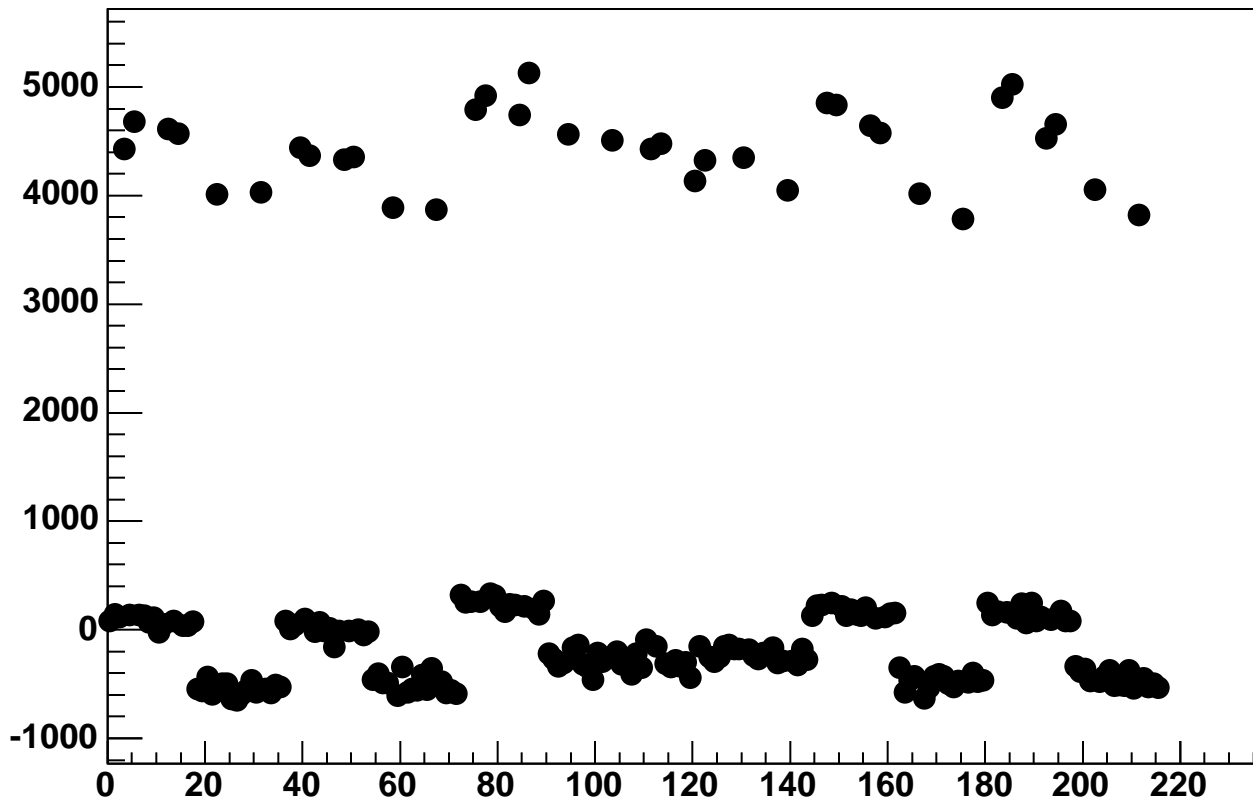
Enable 1, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



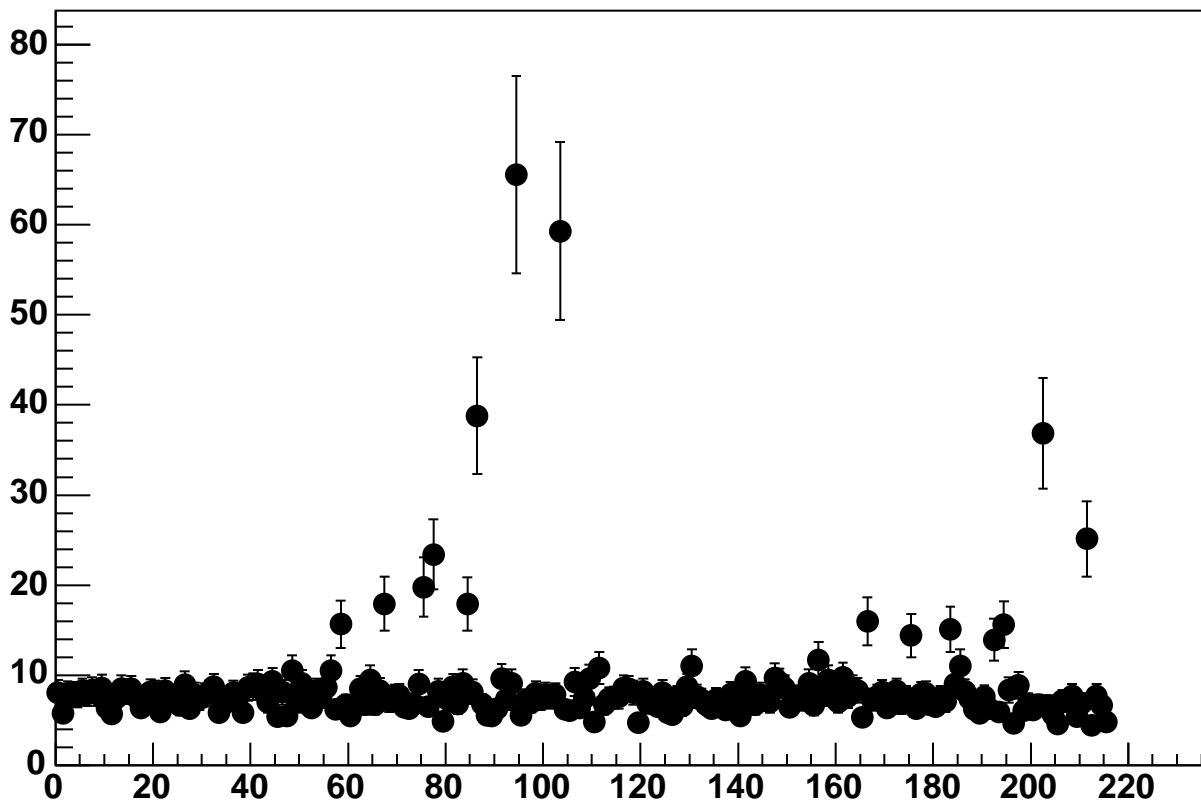
Enable 1, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



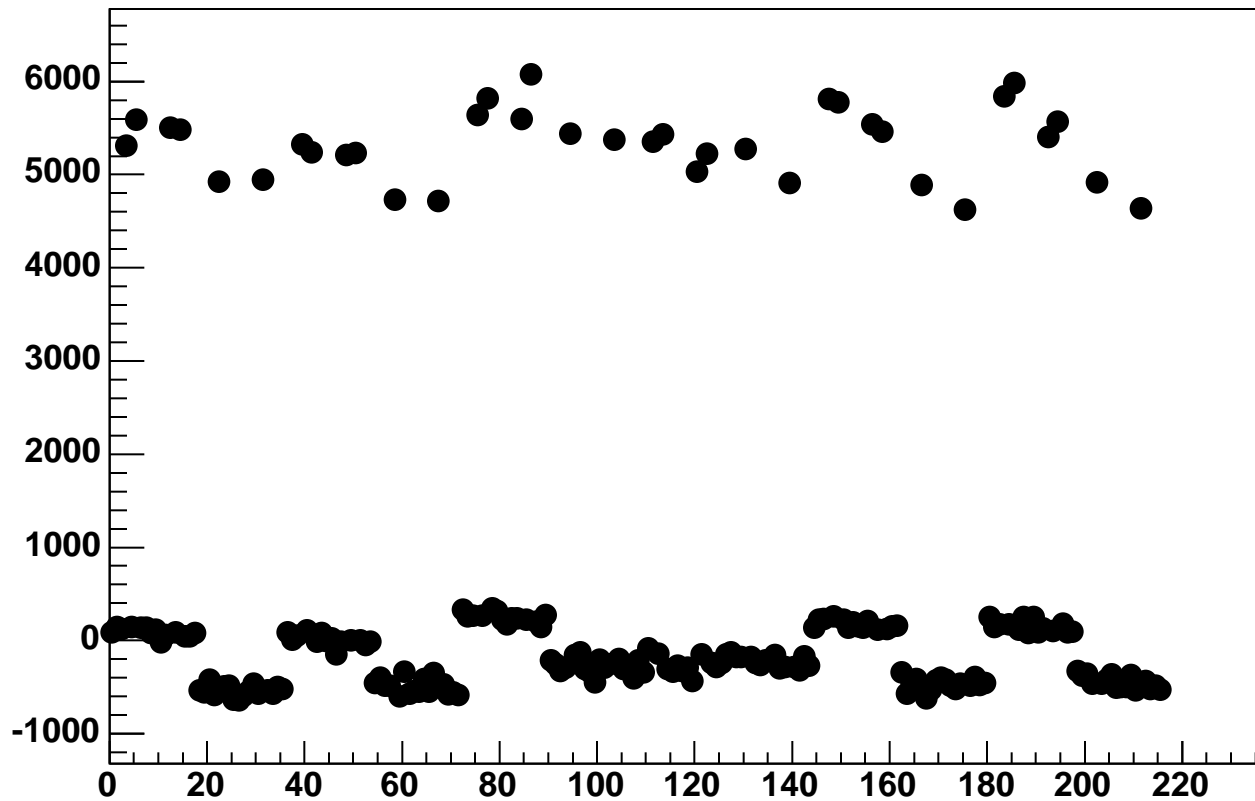
Enable 1, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



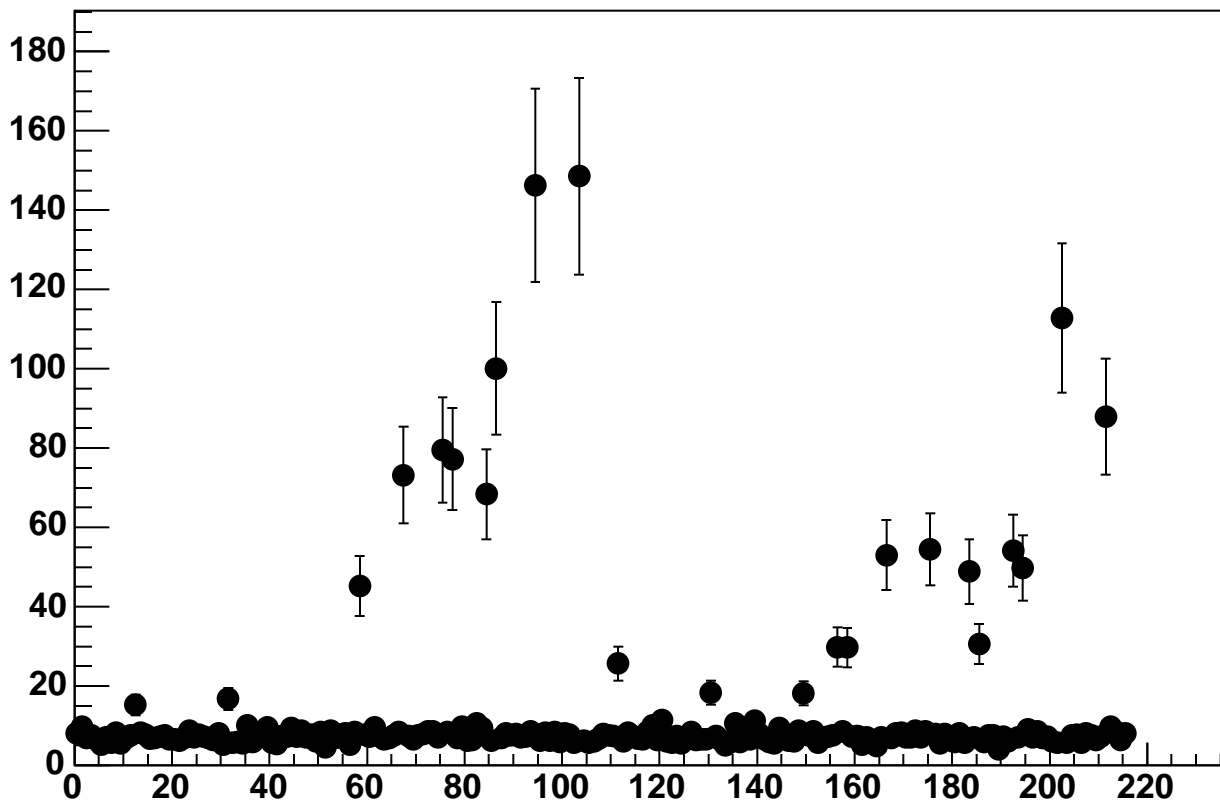
Enable 1, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



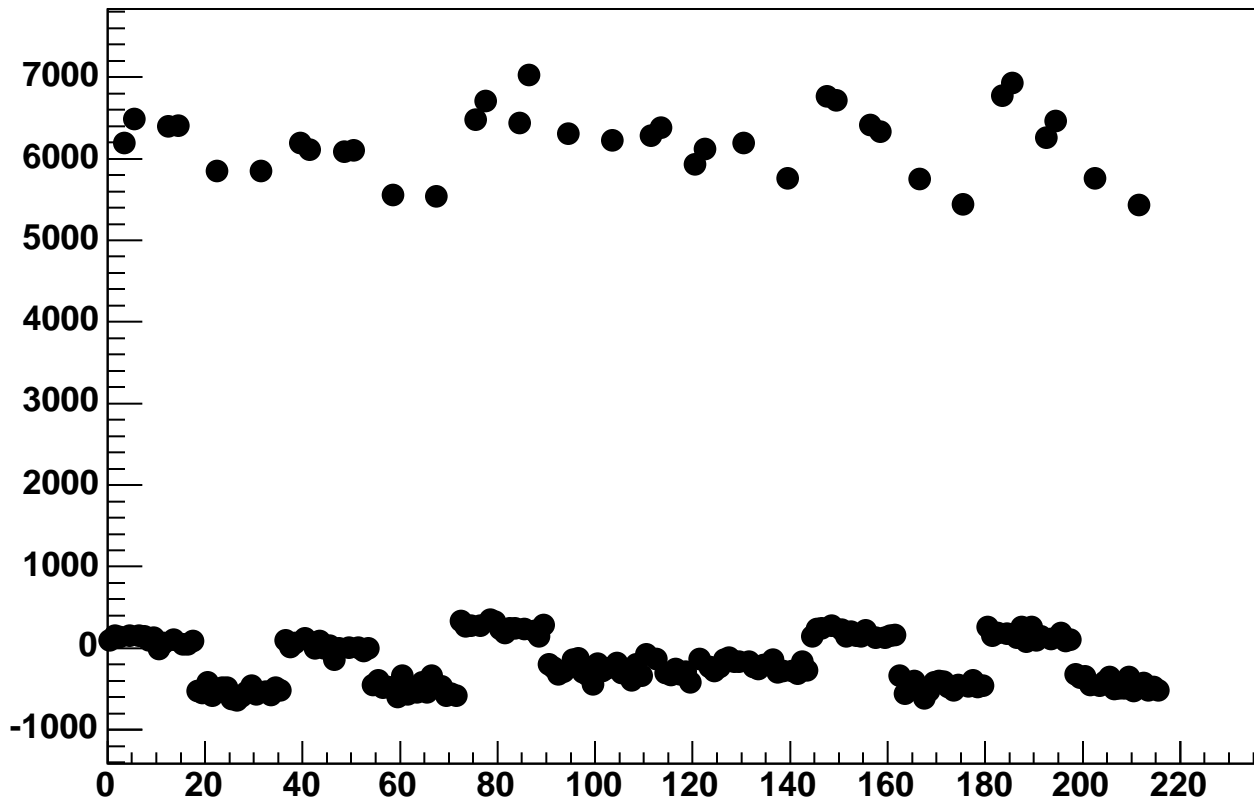
Enable 1, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



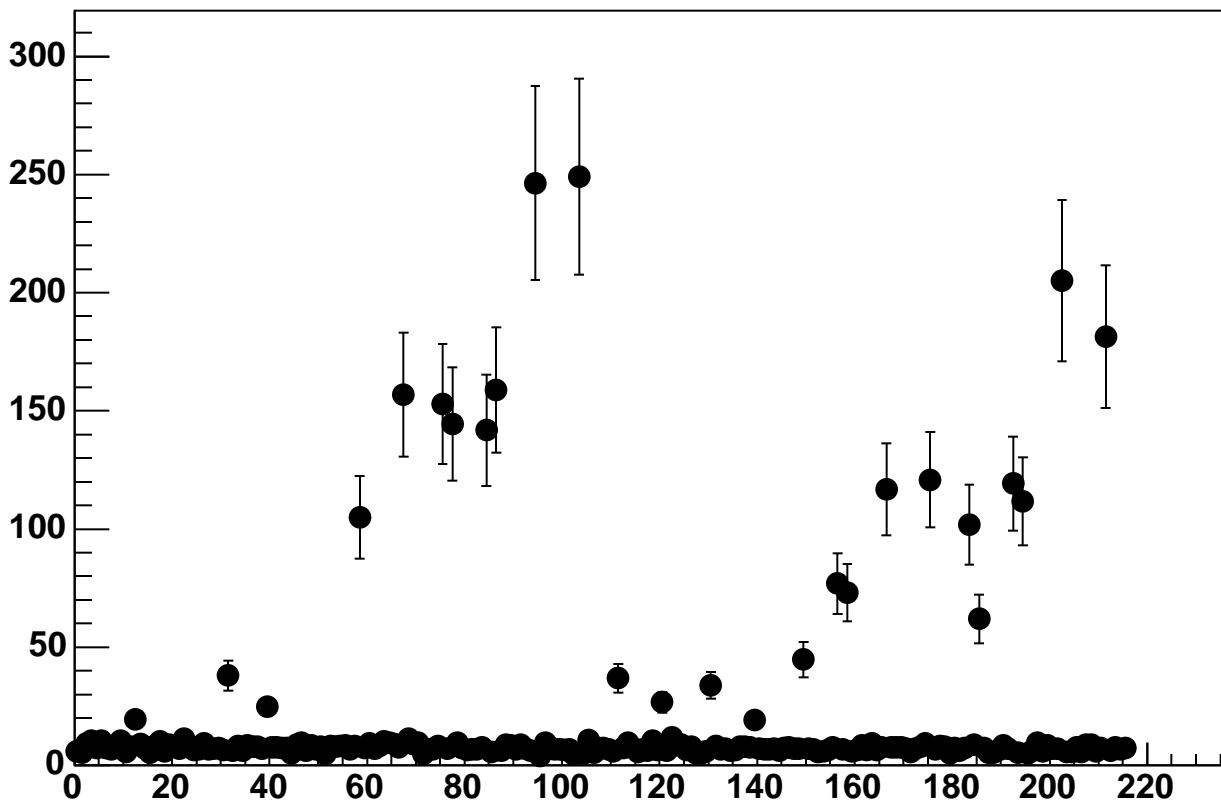
Enable 1, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



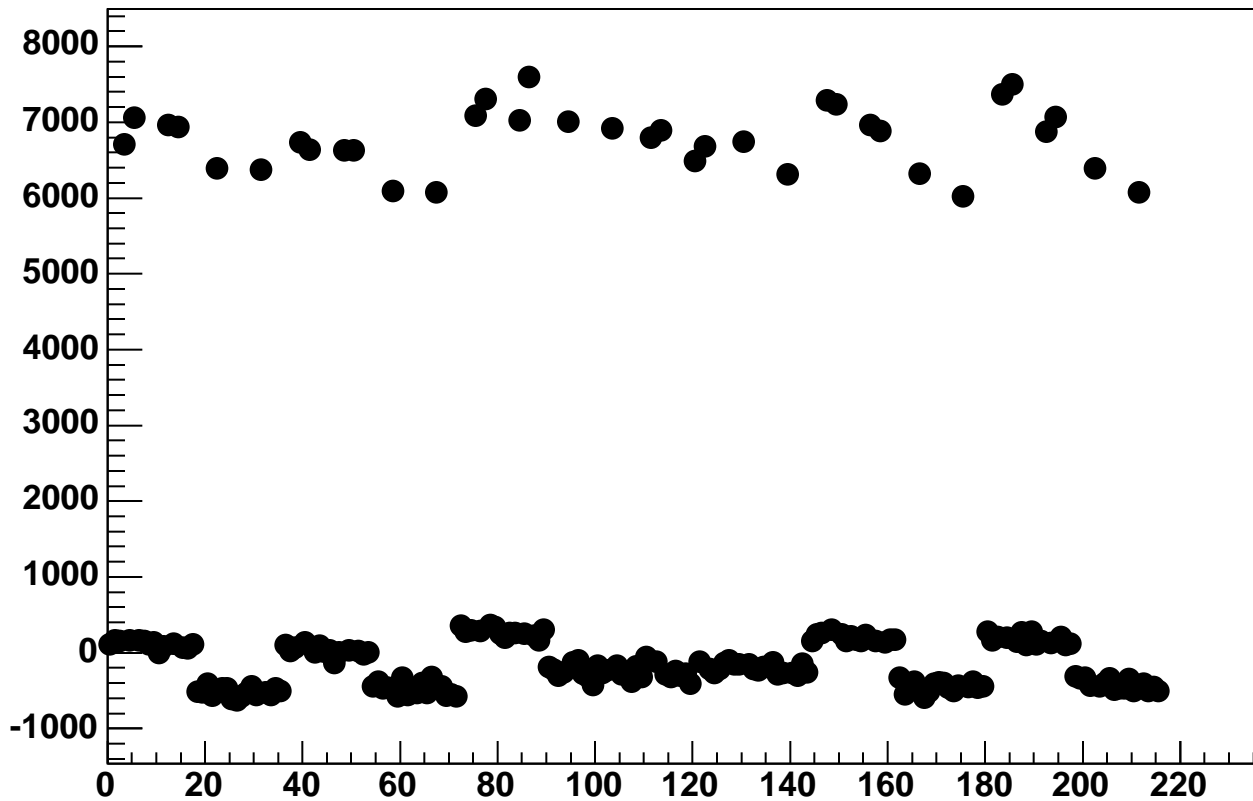
Enable 1, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



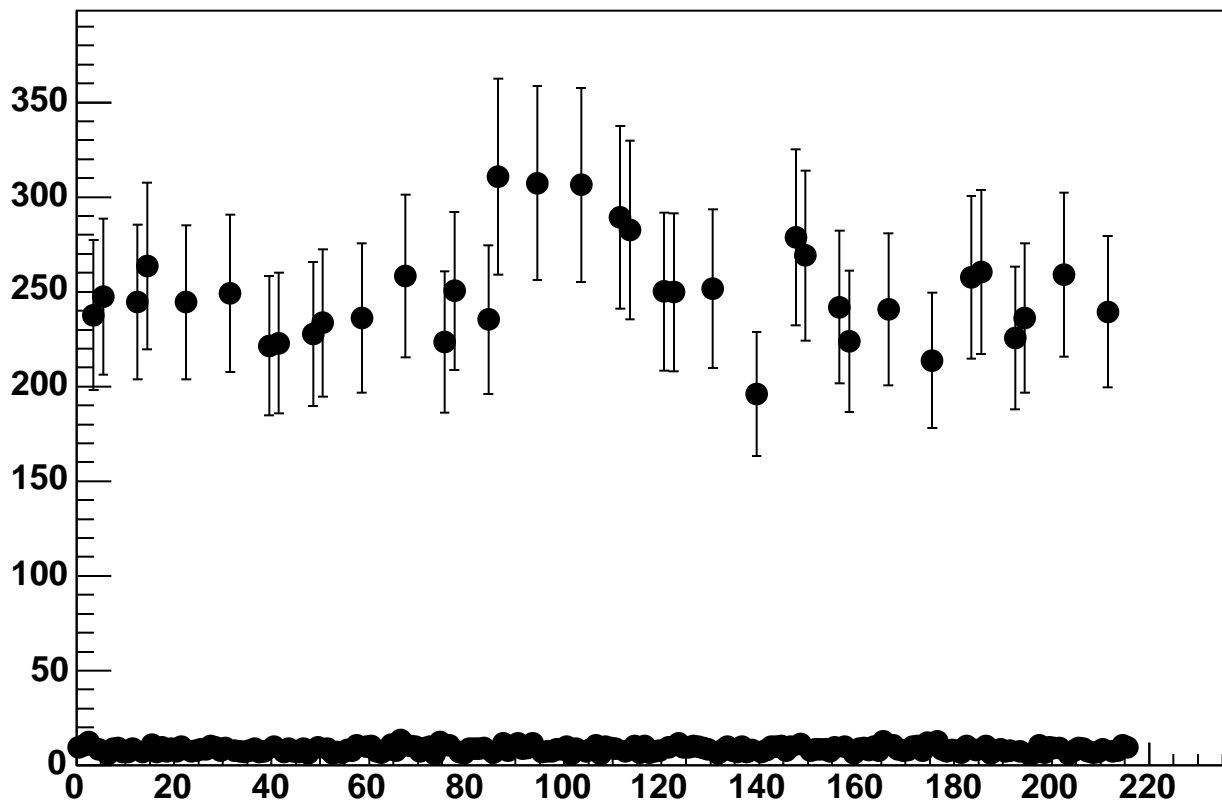
Enable 1, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



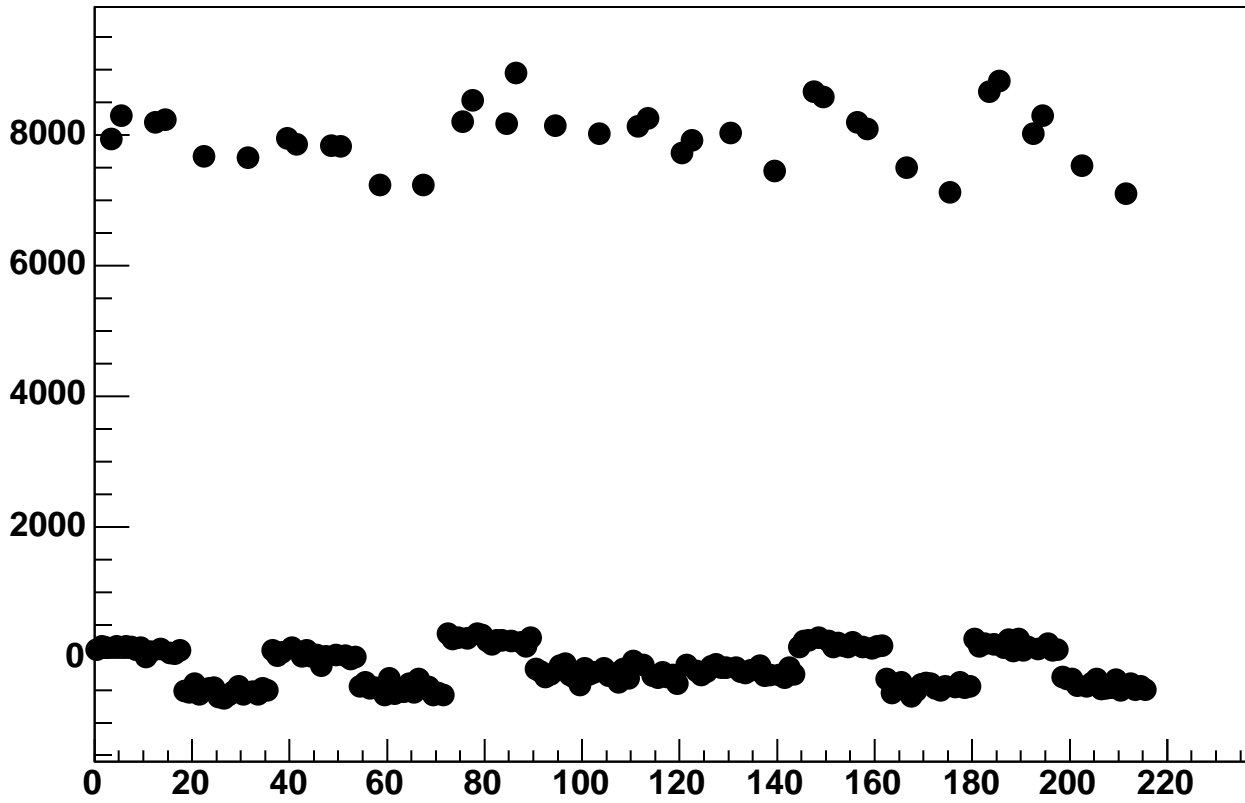
Enable 1, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



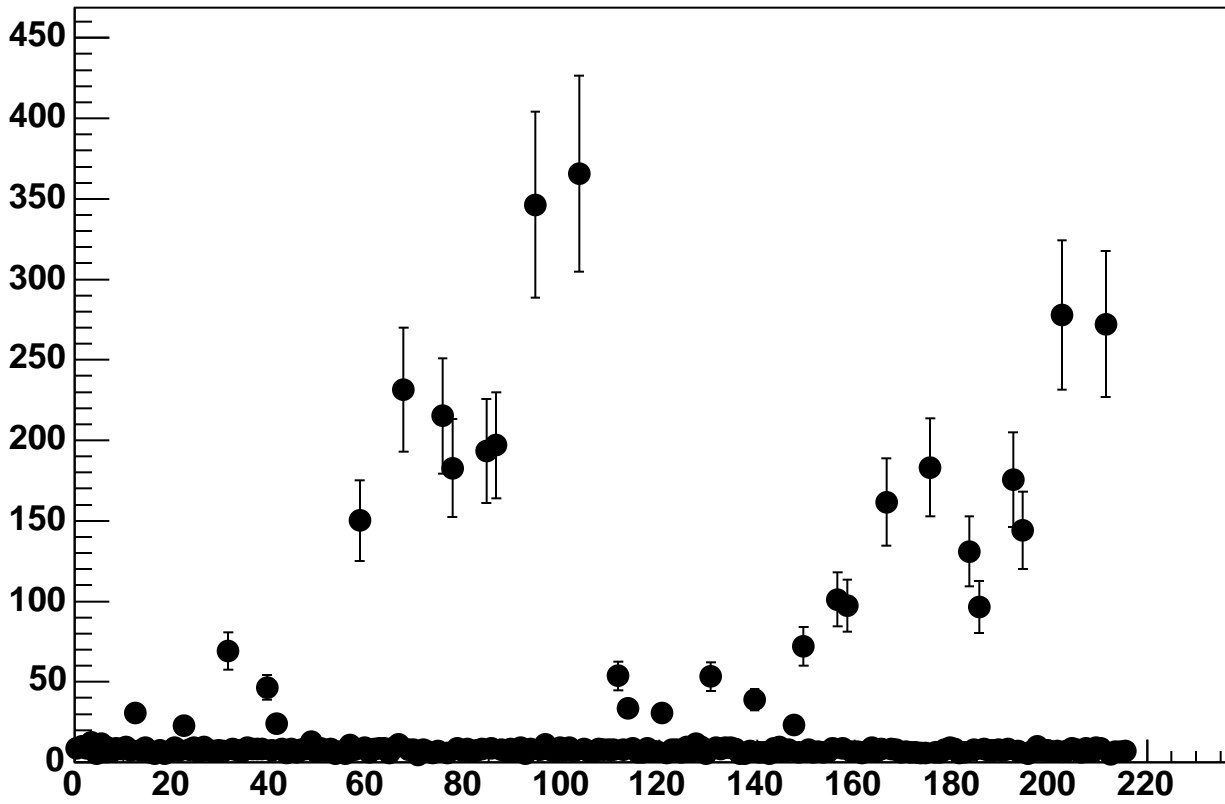
Enable 1, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



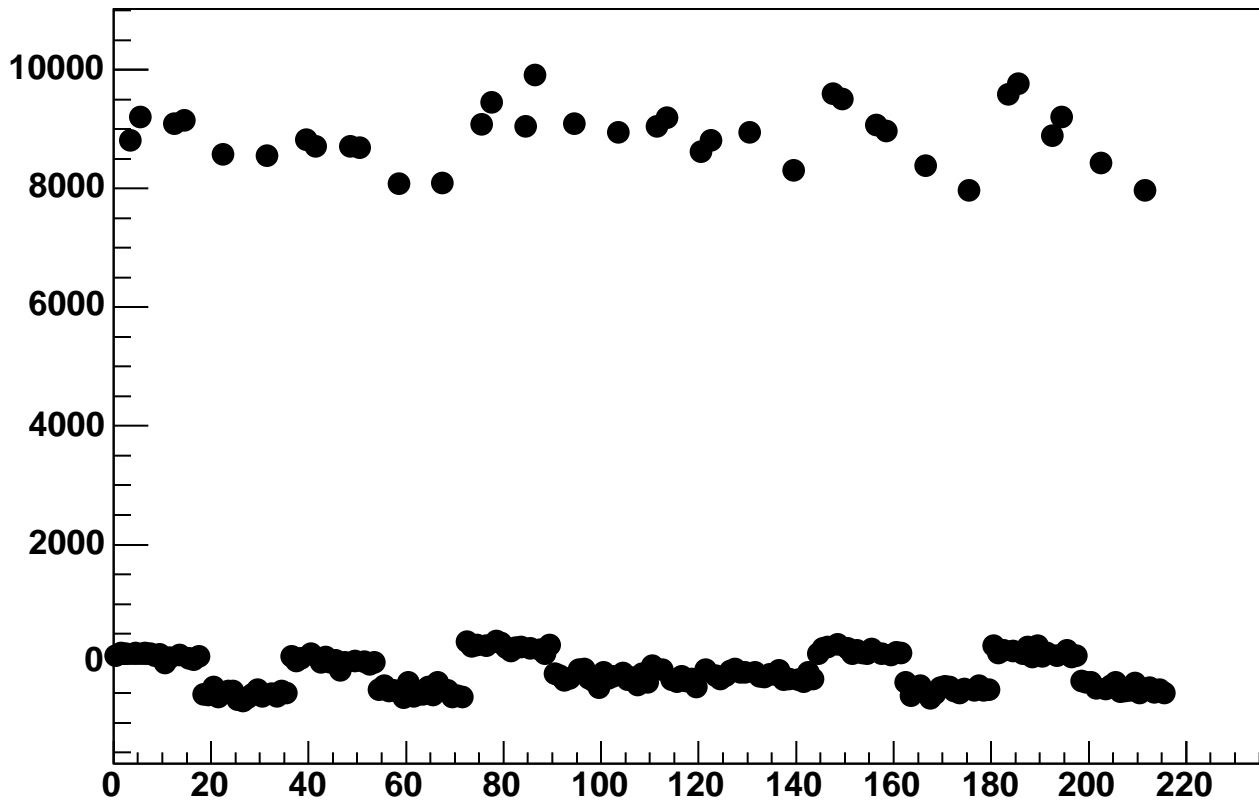
Enable 1, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



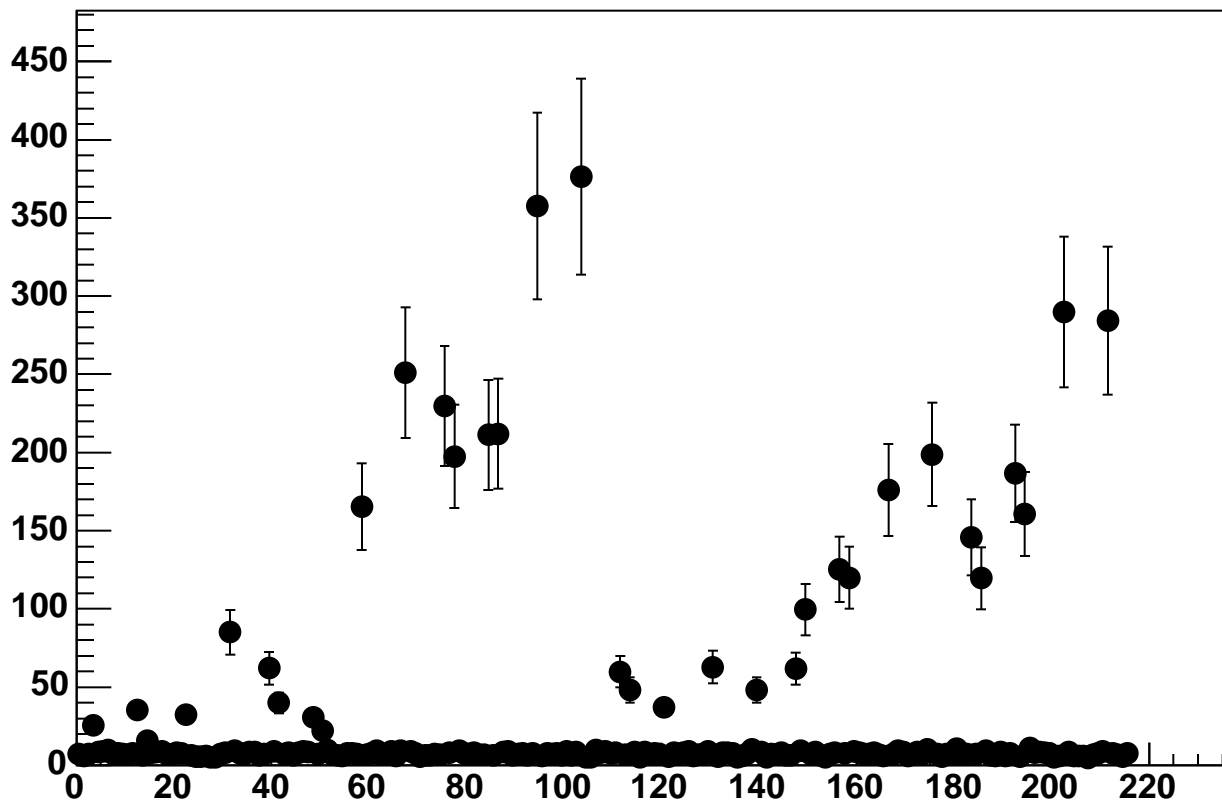
Enable 1, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



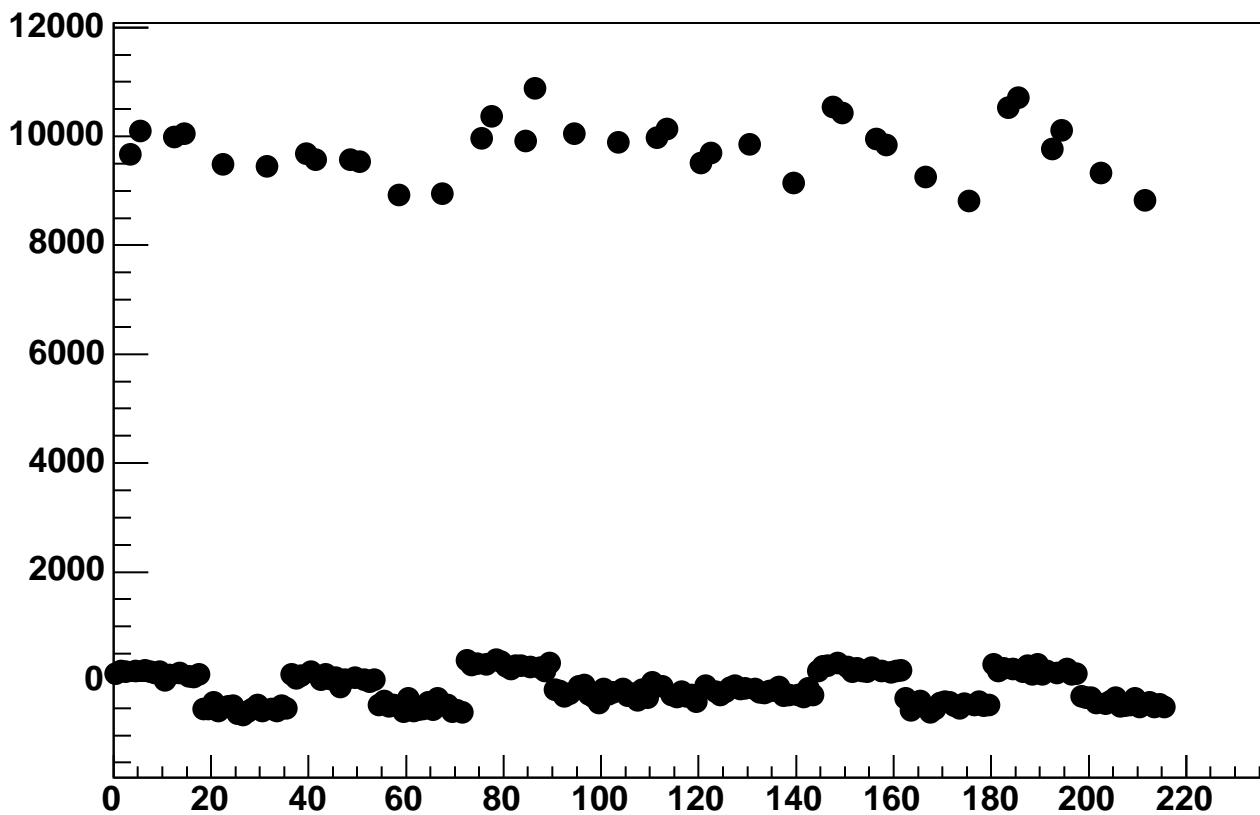
Enable 1, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



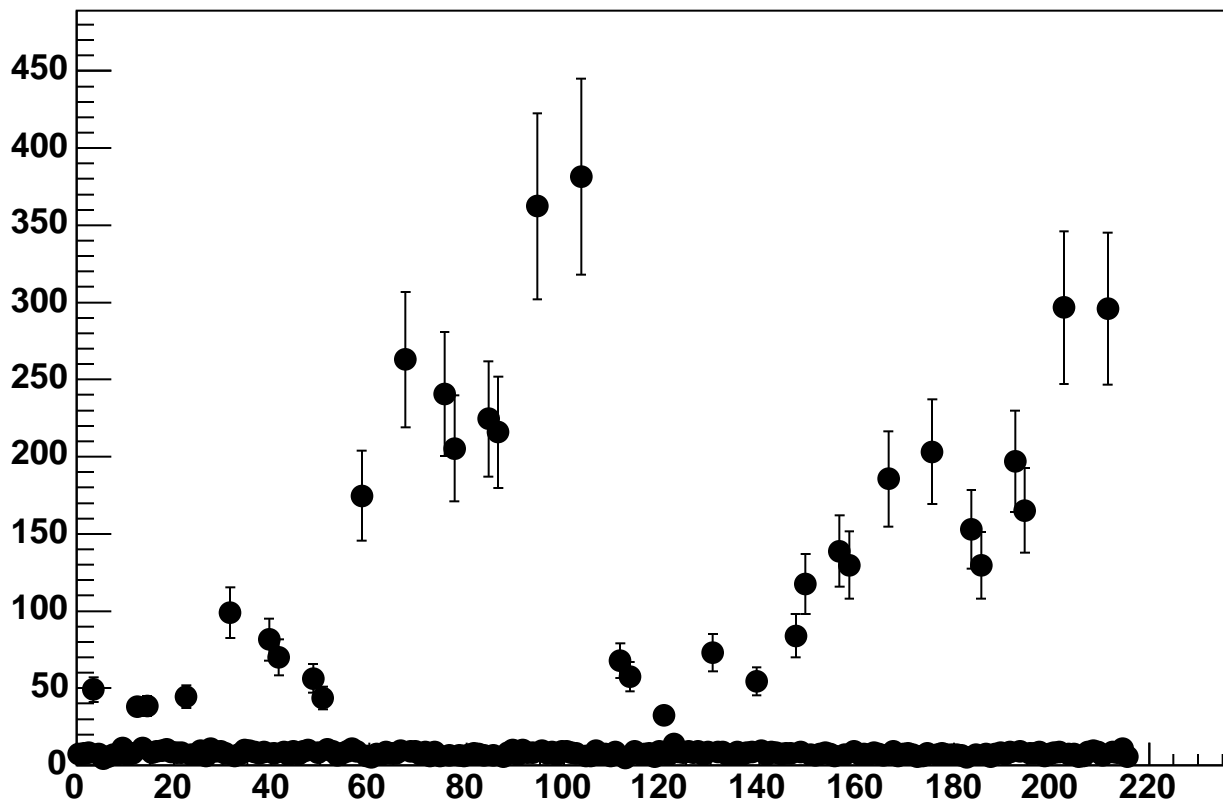
Enable 1, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



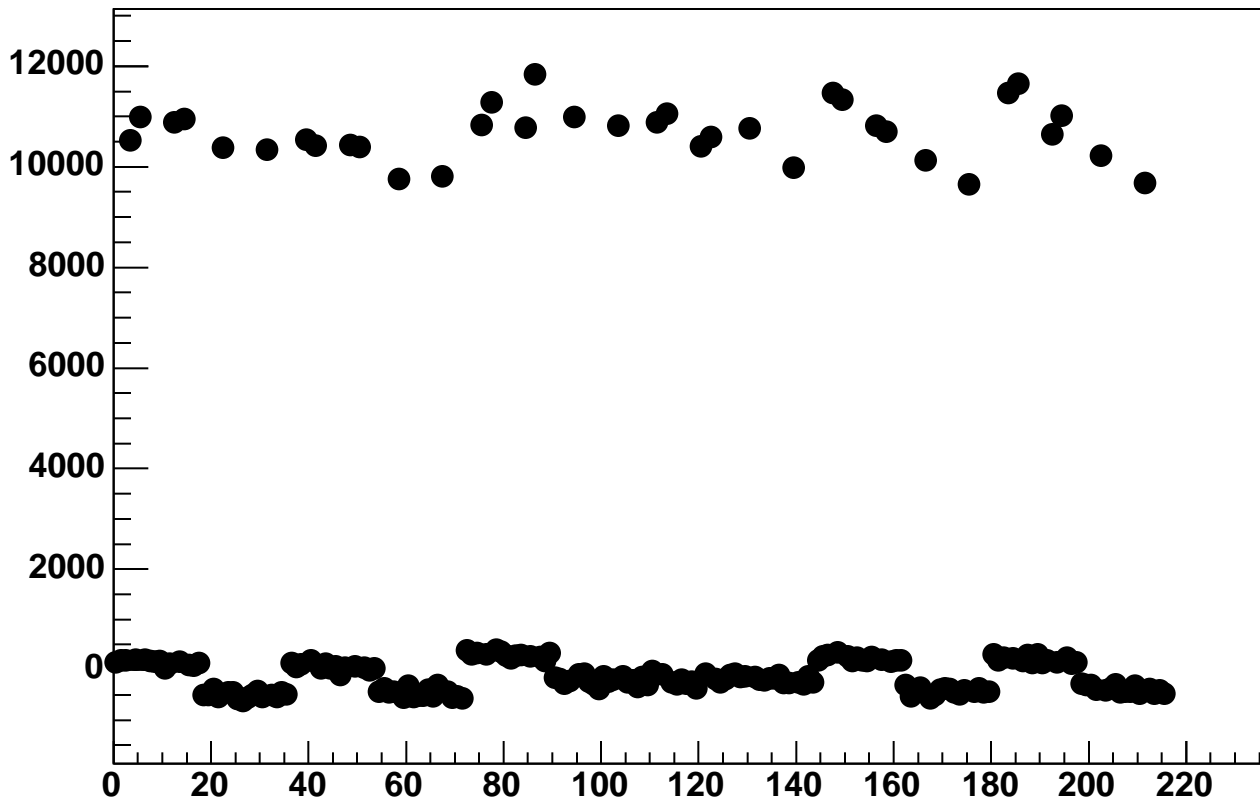
Enable 1, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



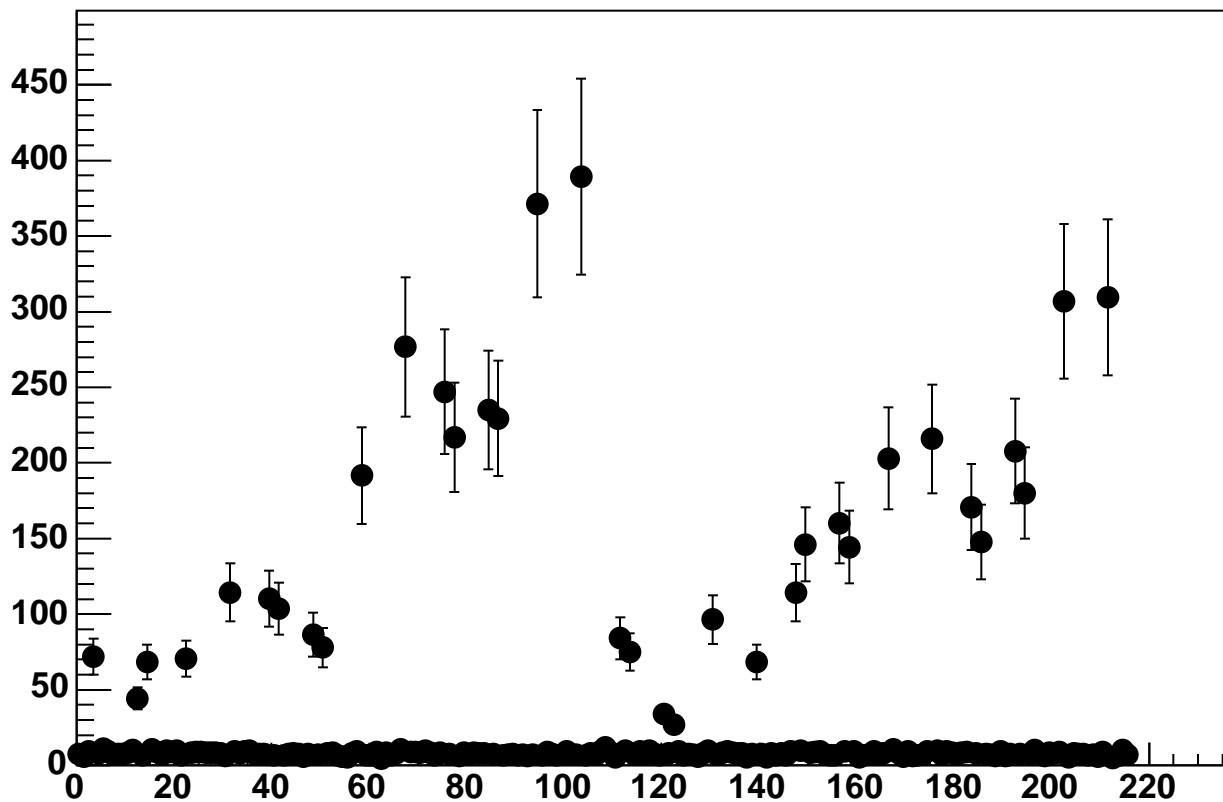
Enable 1, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



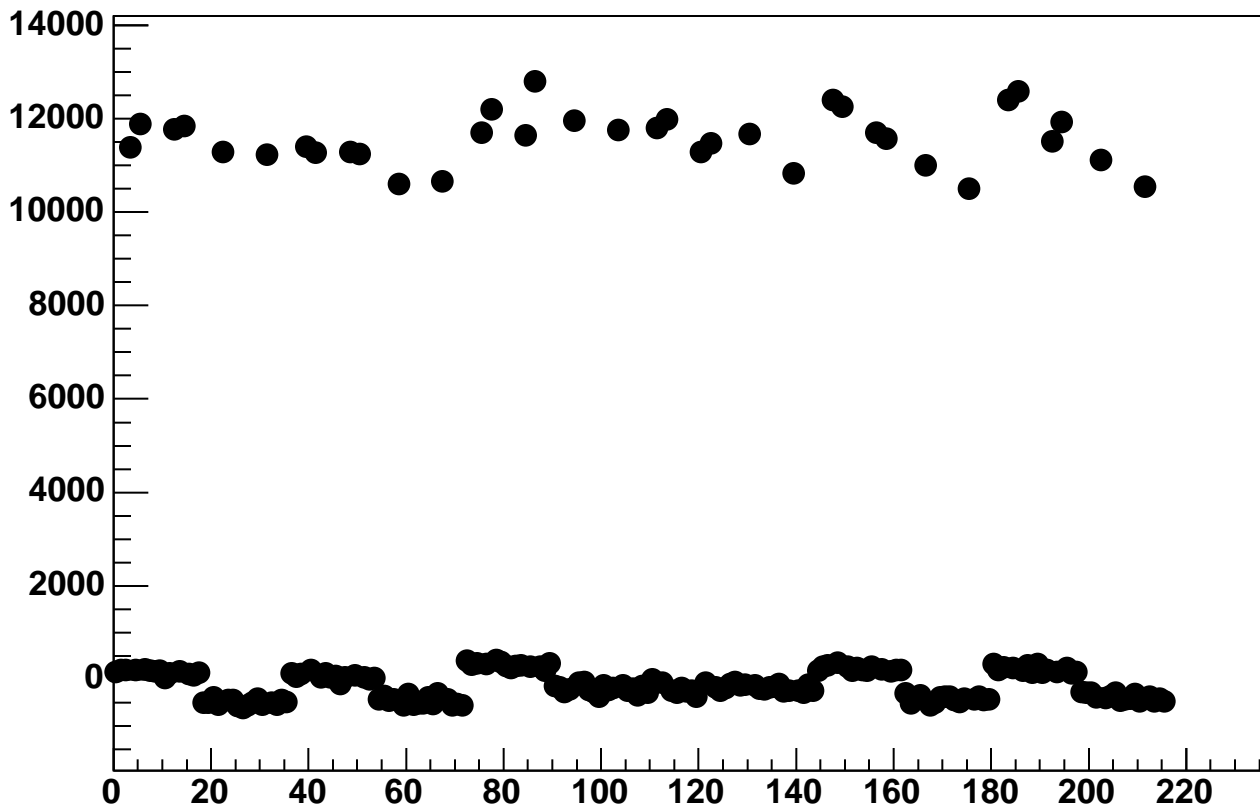
Enable 1, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



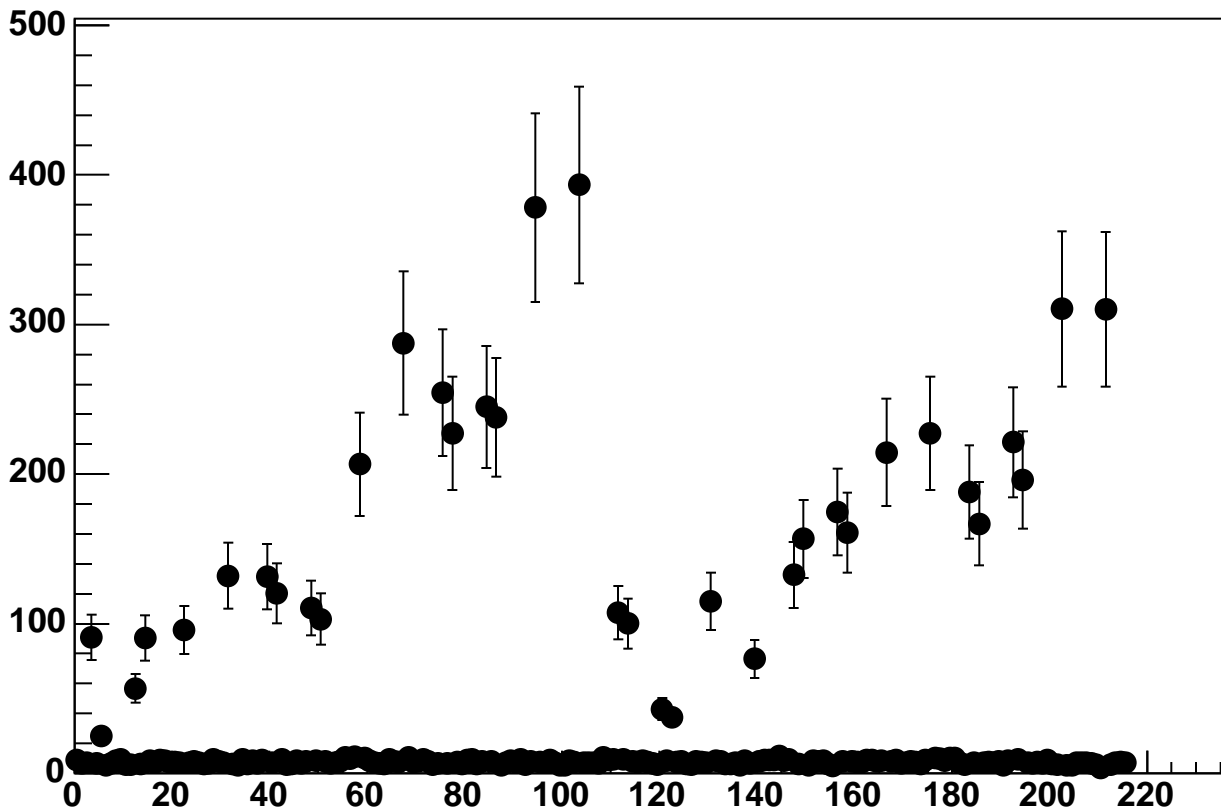
Enable 1, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



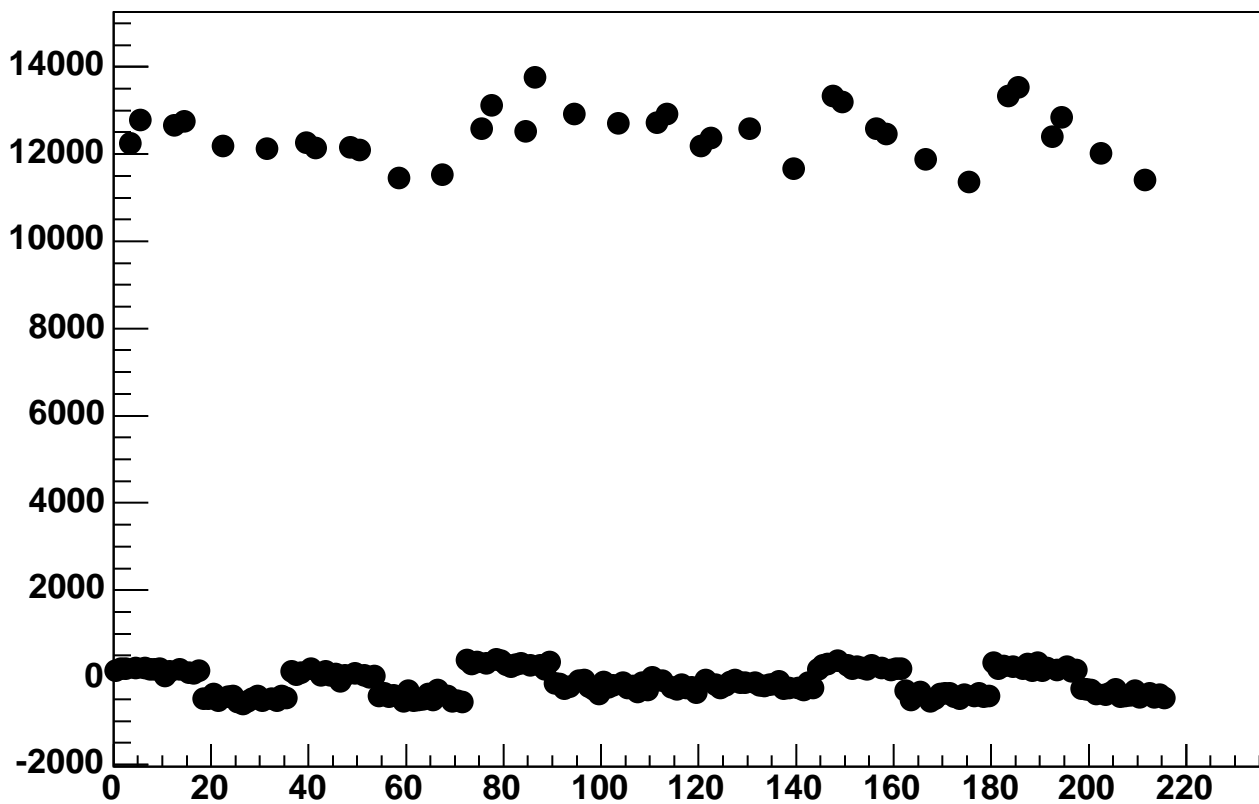
Enable 1, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



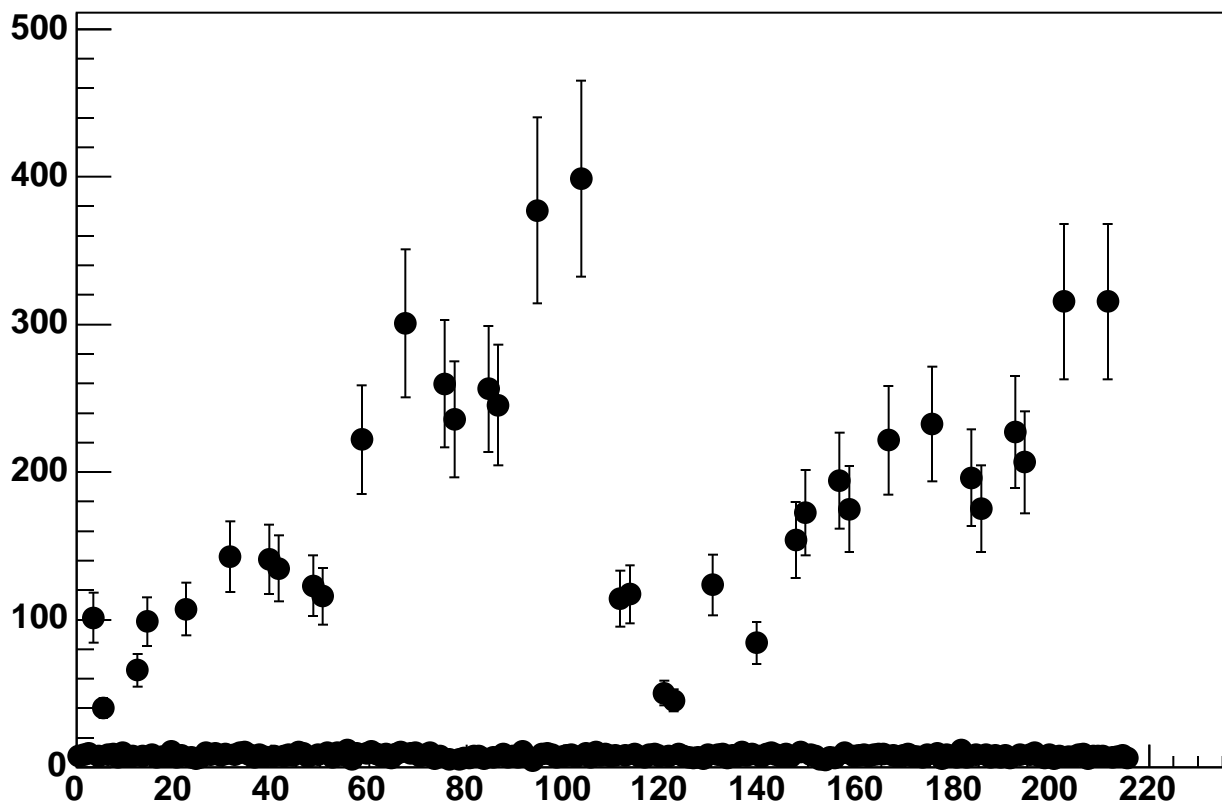
Enable 1, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



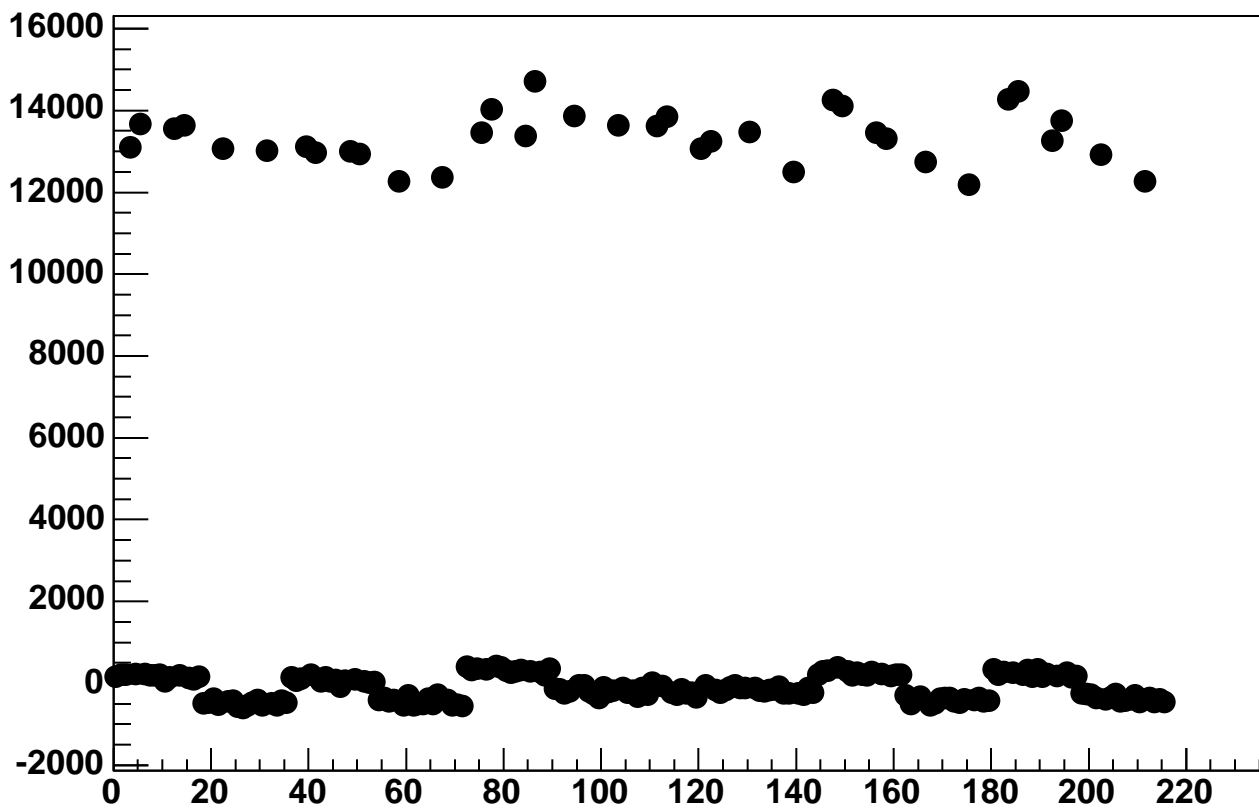
Enable 1, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



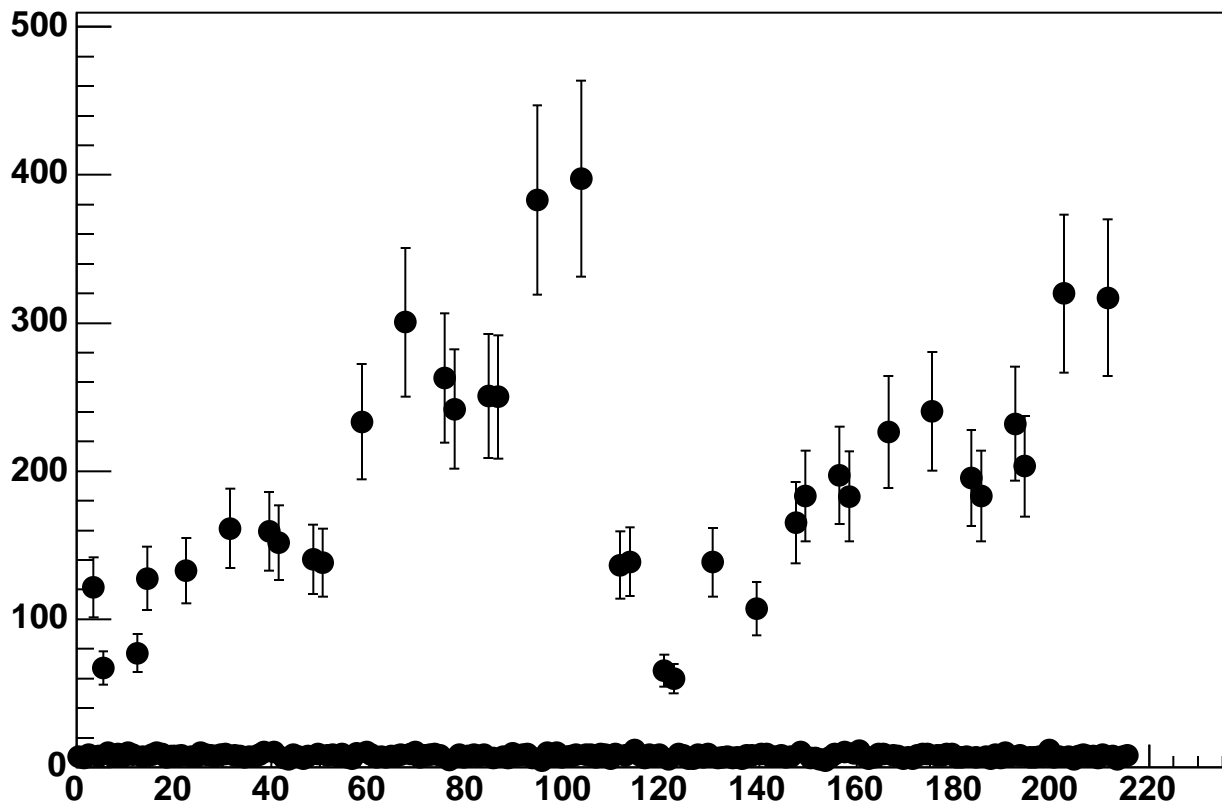
Enable 1, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



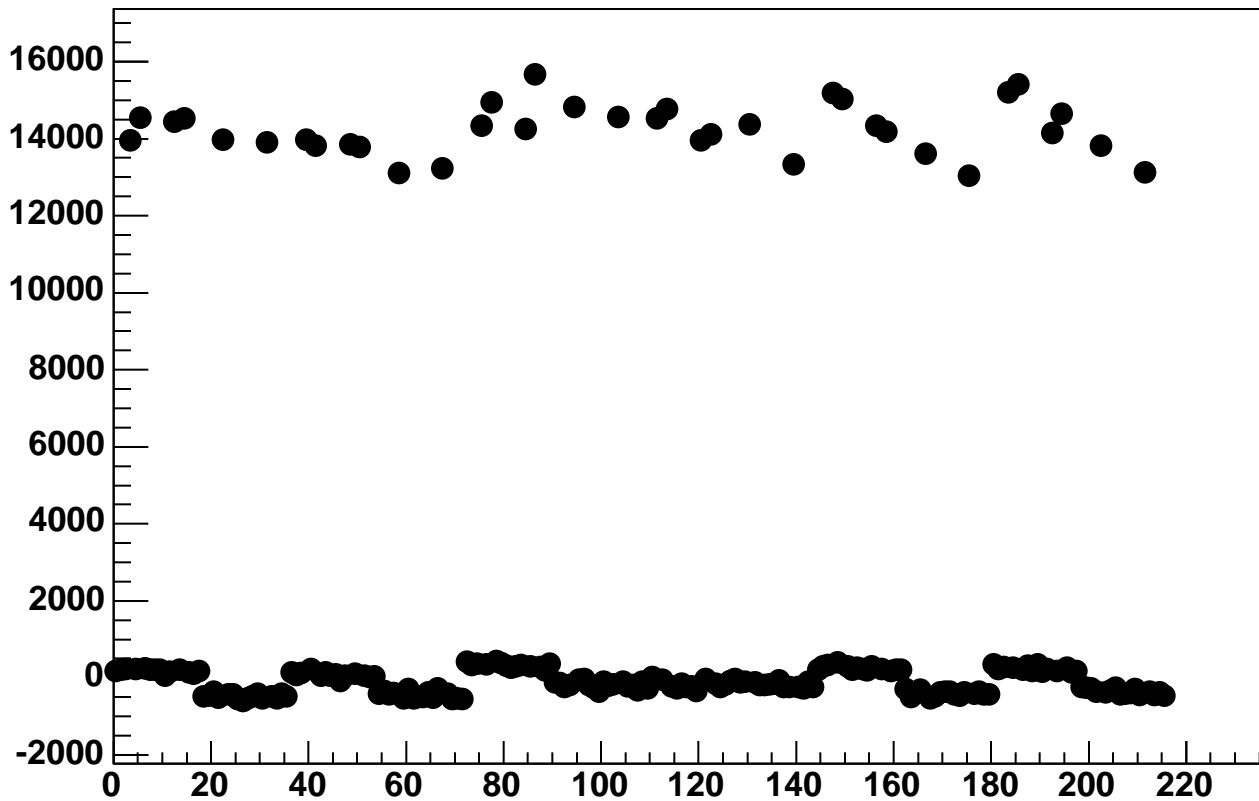
Enable 1, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



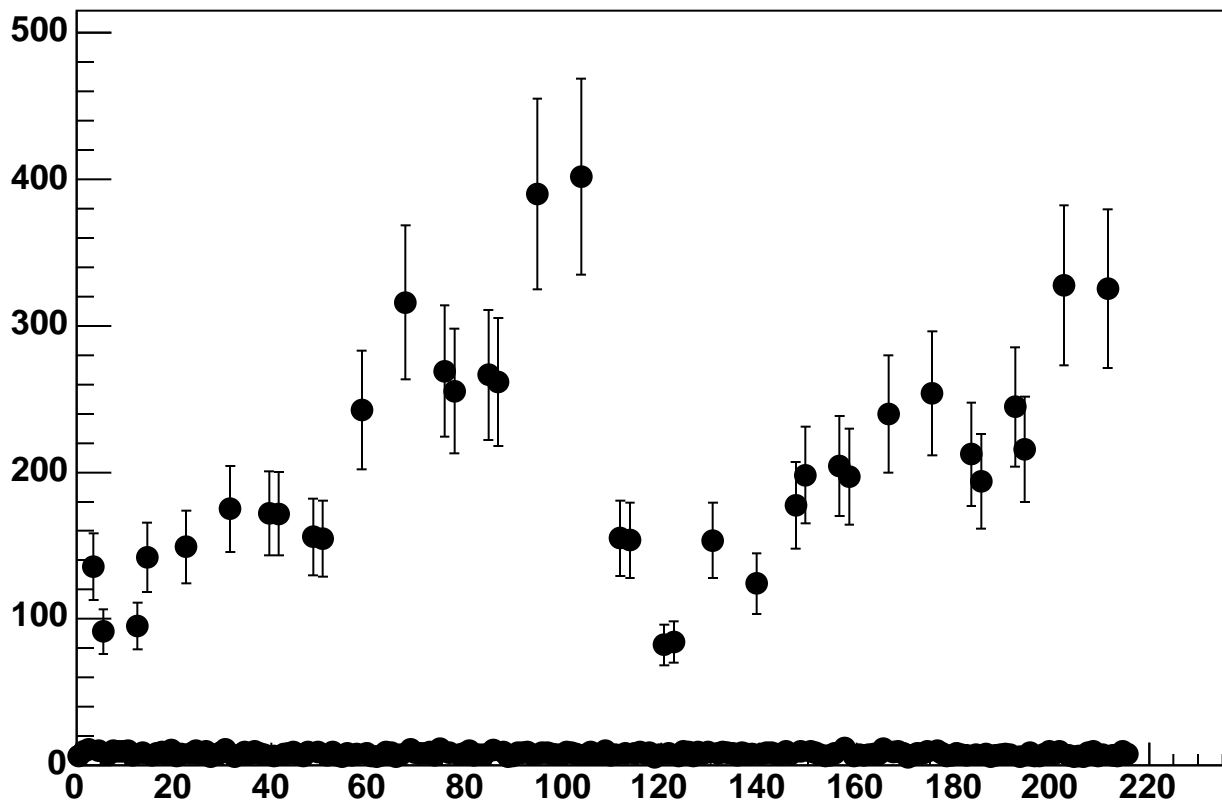
Enable 1, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



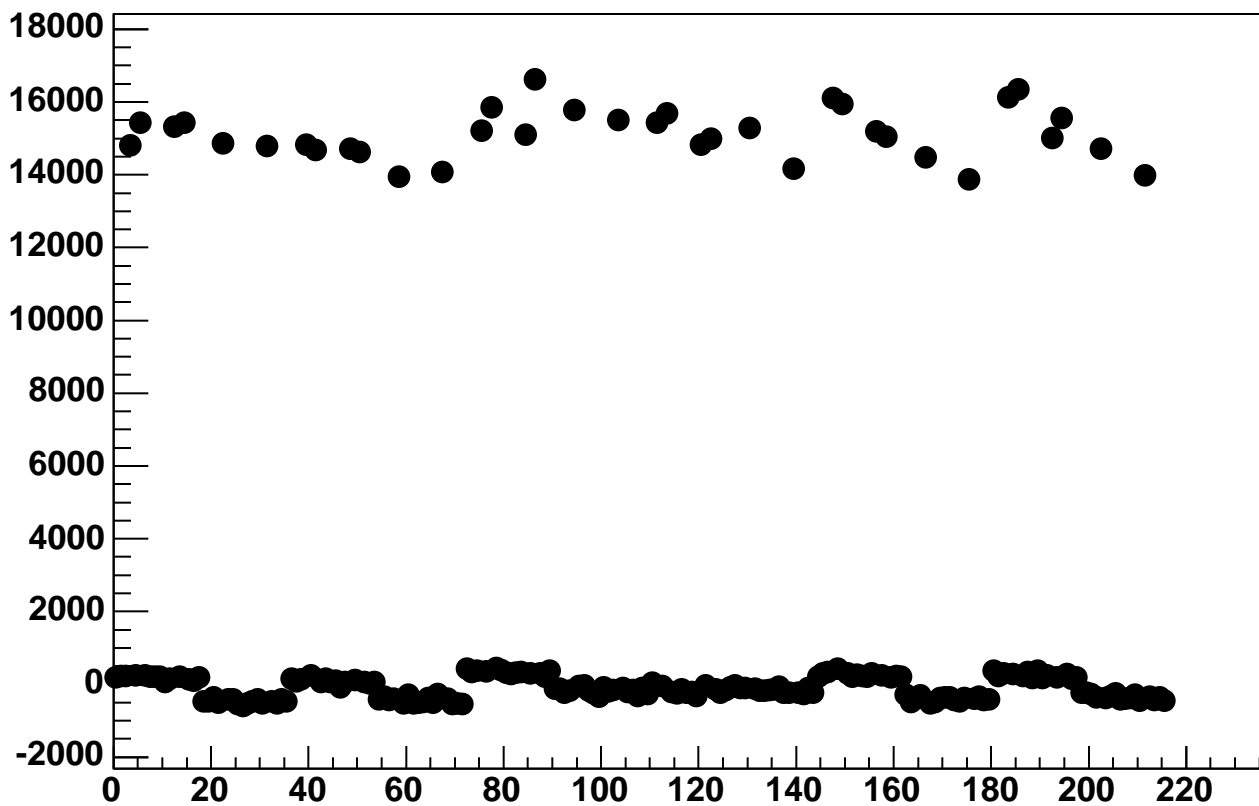
Enable 1, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



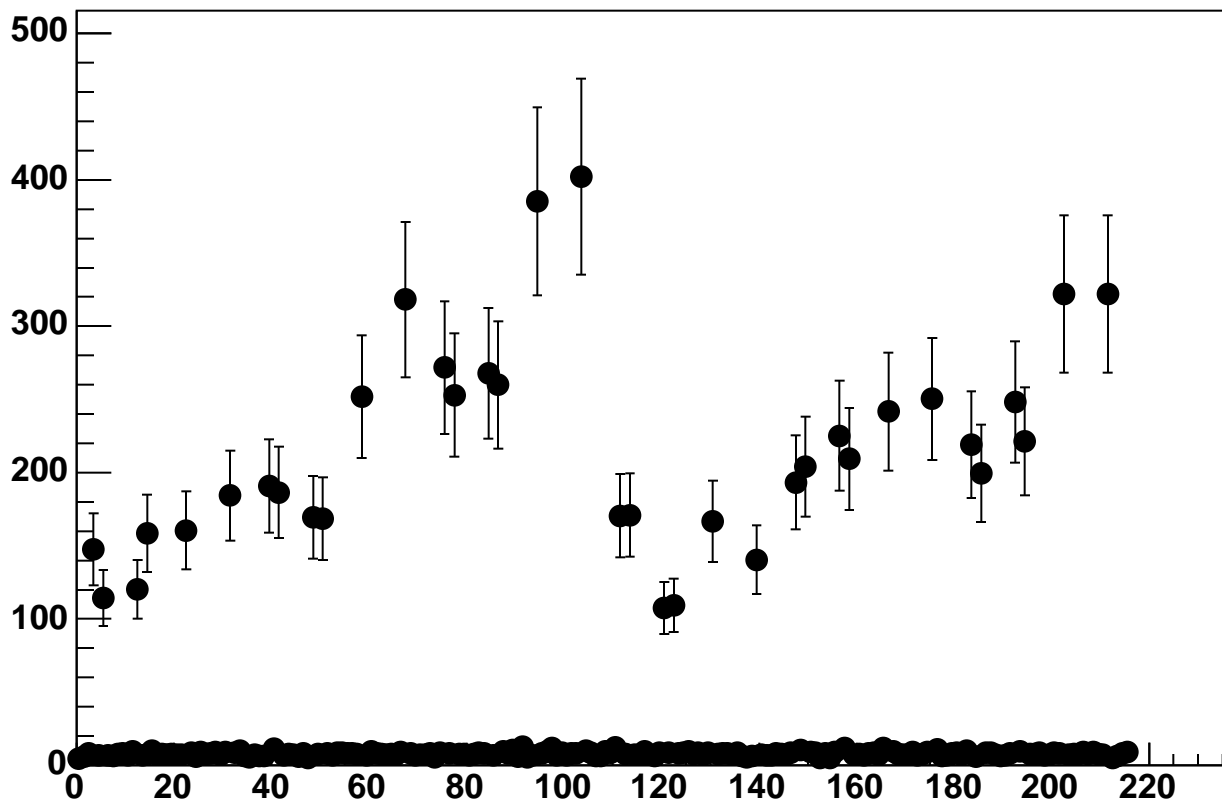
Enable 1, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



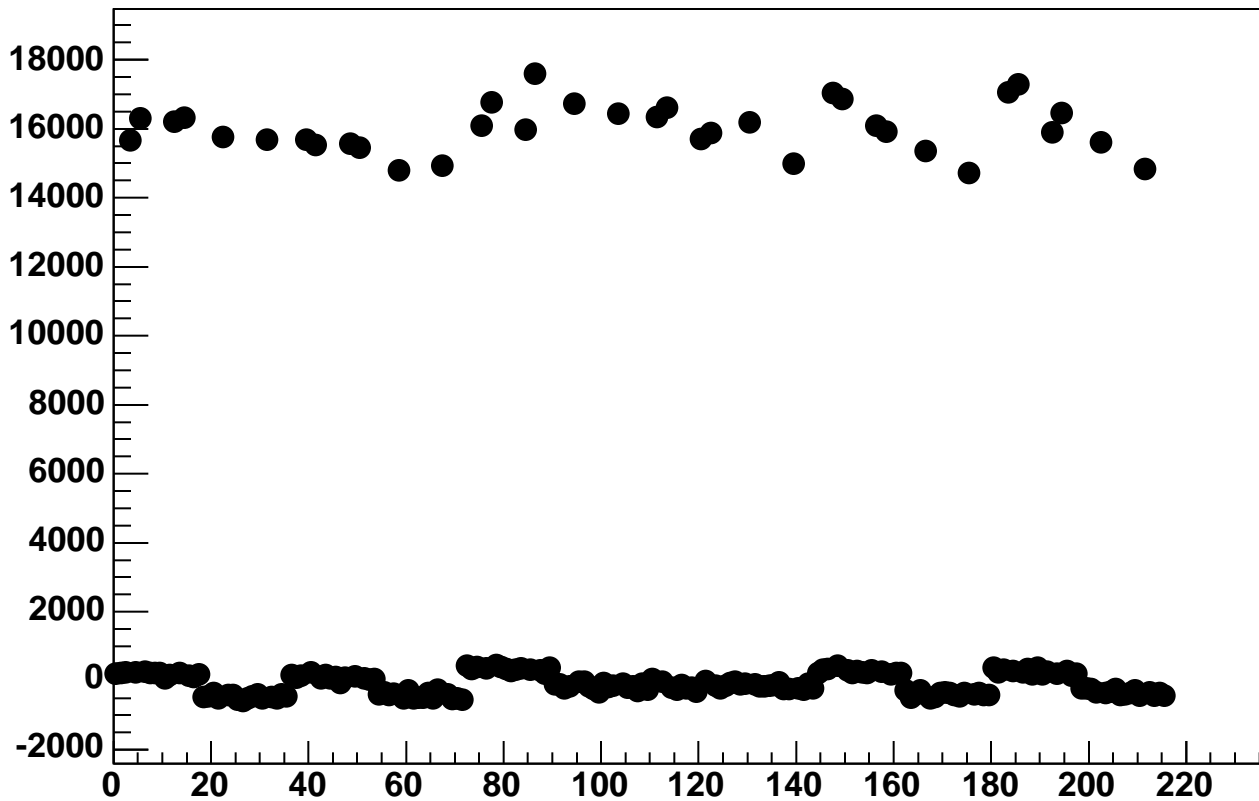
Enable 1, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



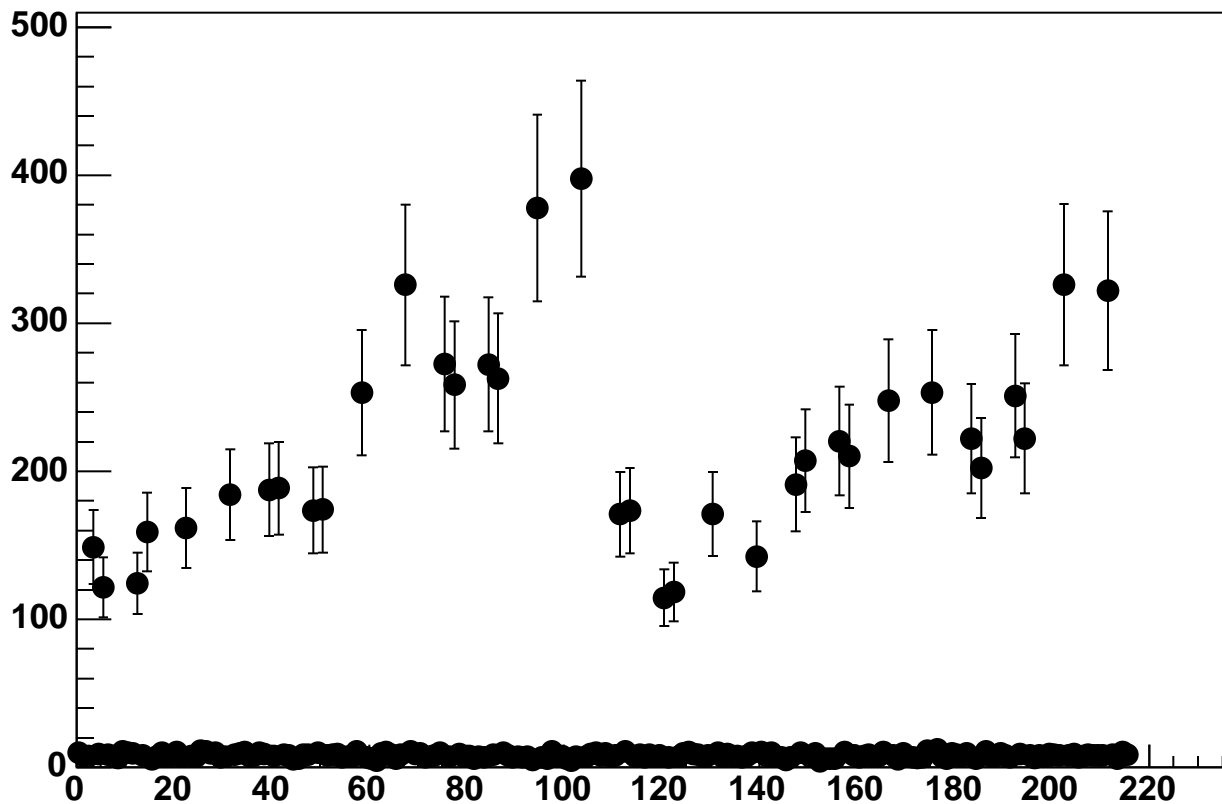
Enable 1, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



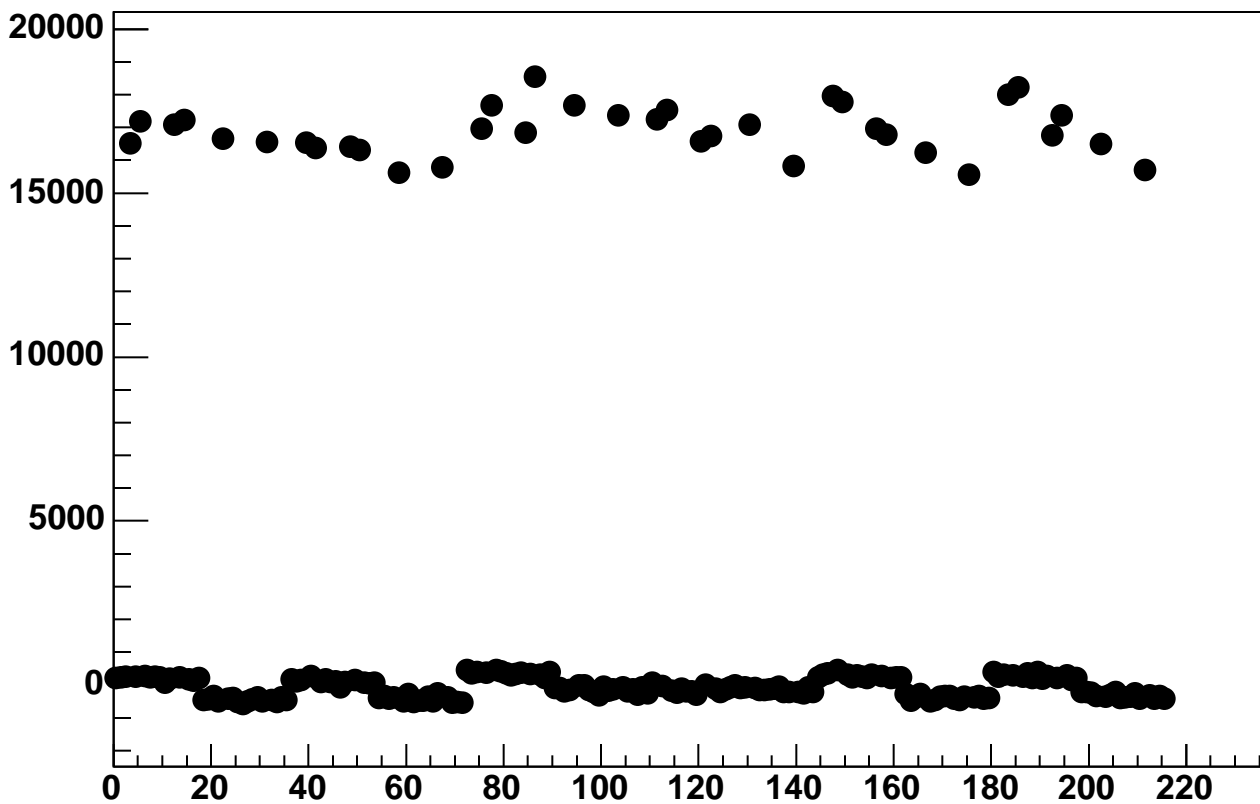
Enable 1, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



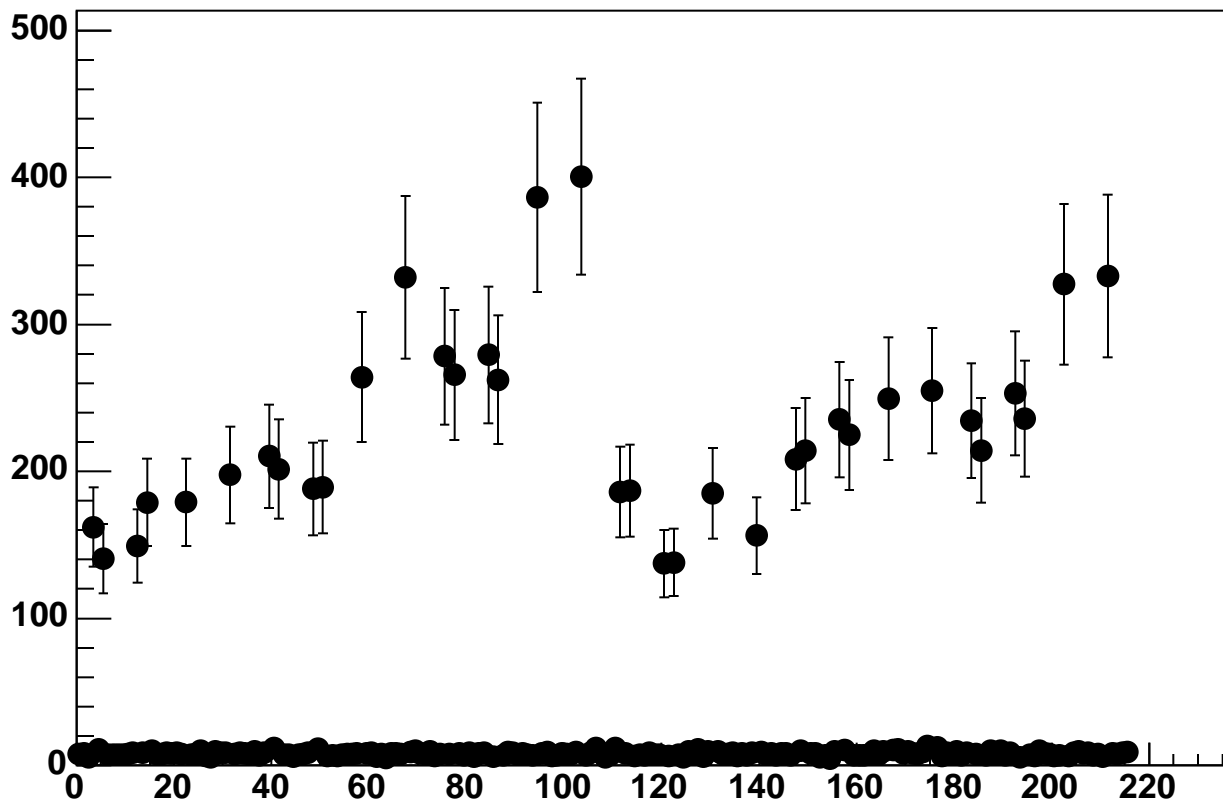
Enable 1, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



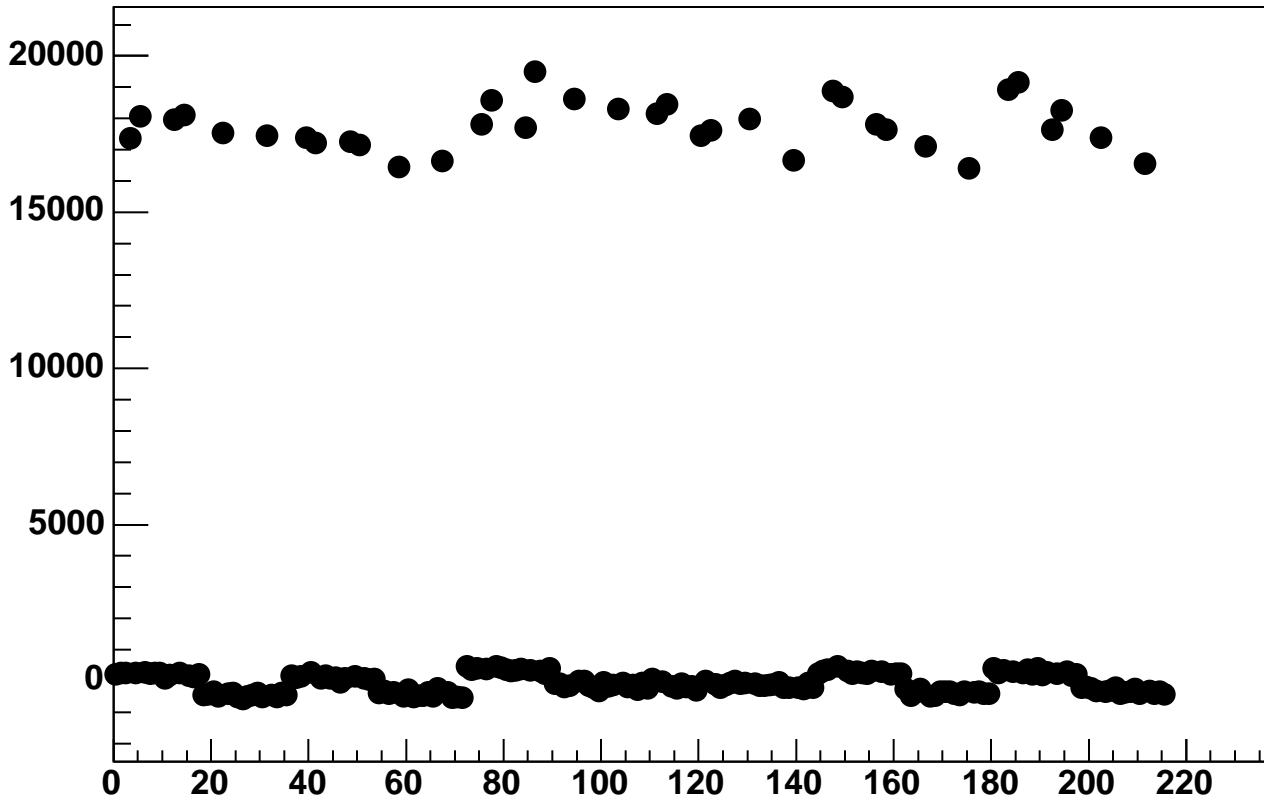
Enable 1, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



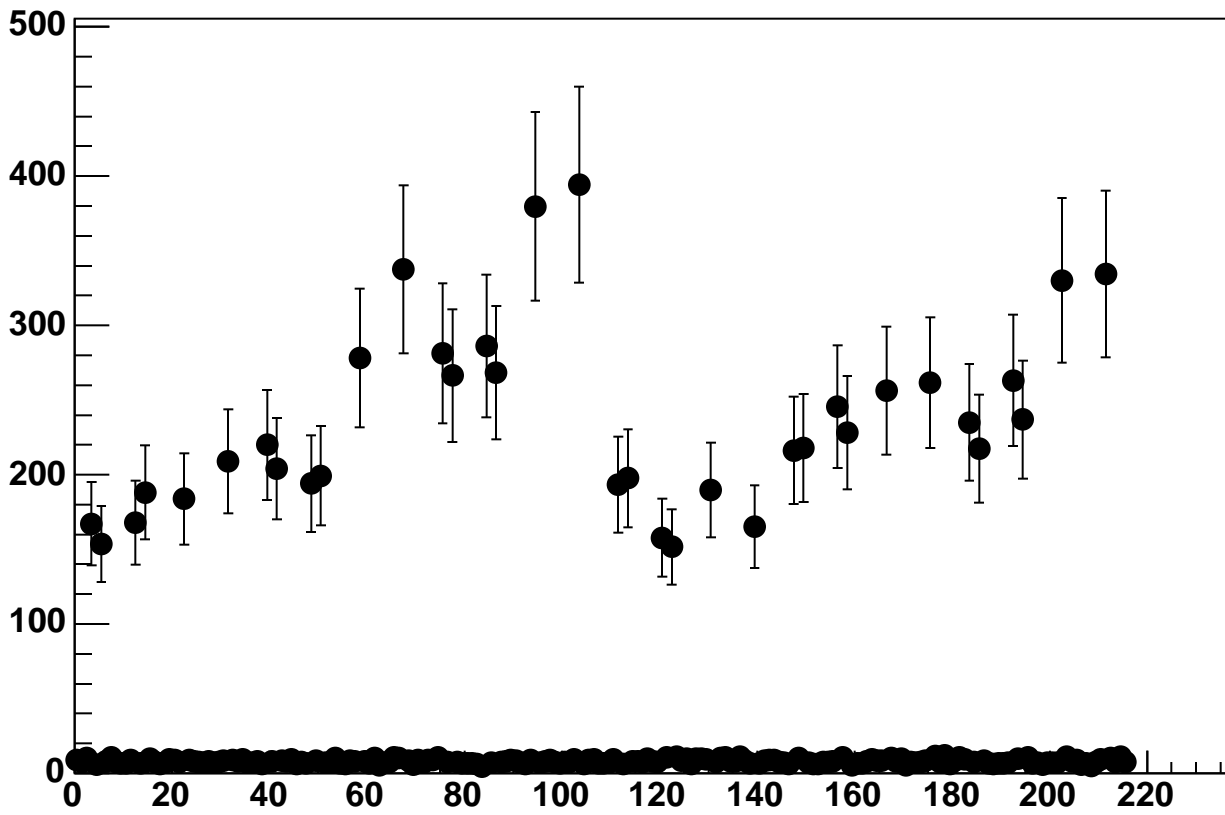
Enable 1, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



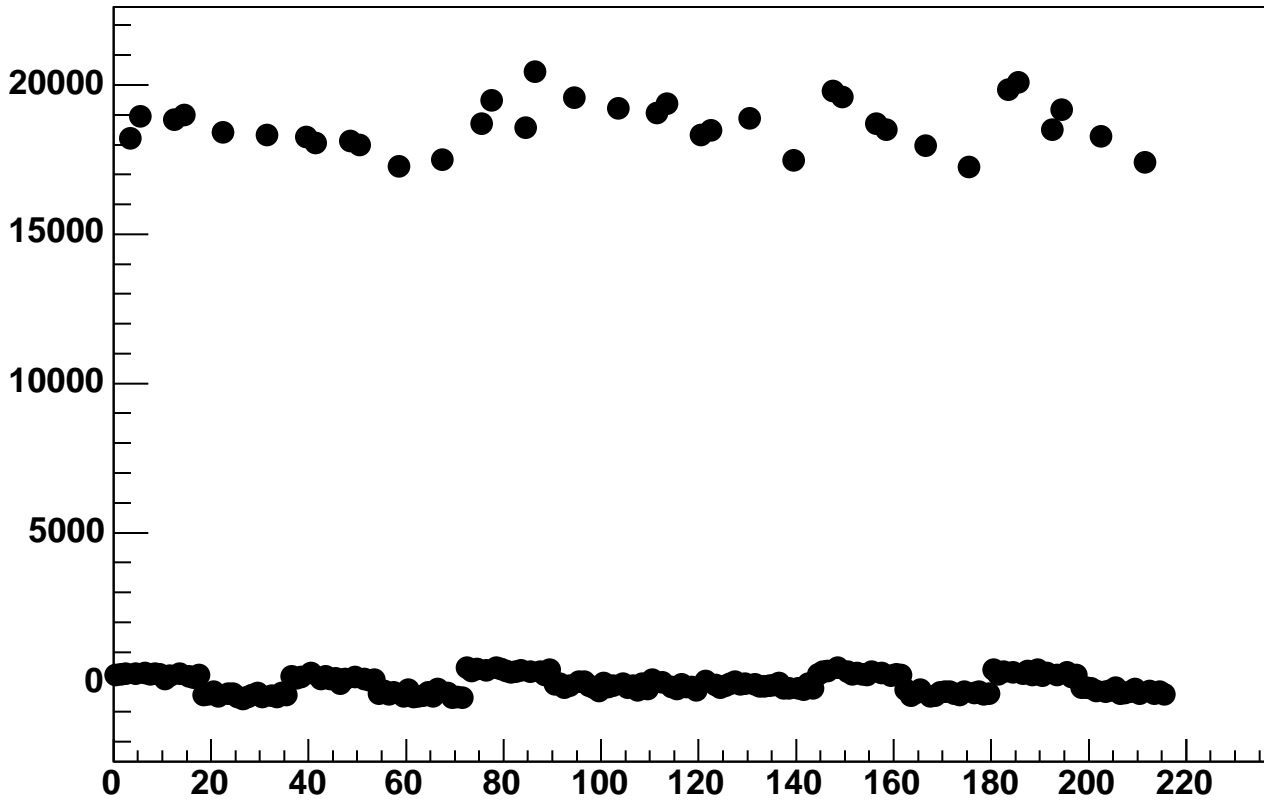
Enable 1, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



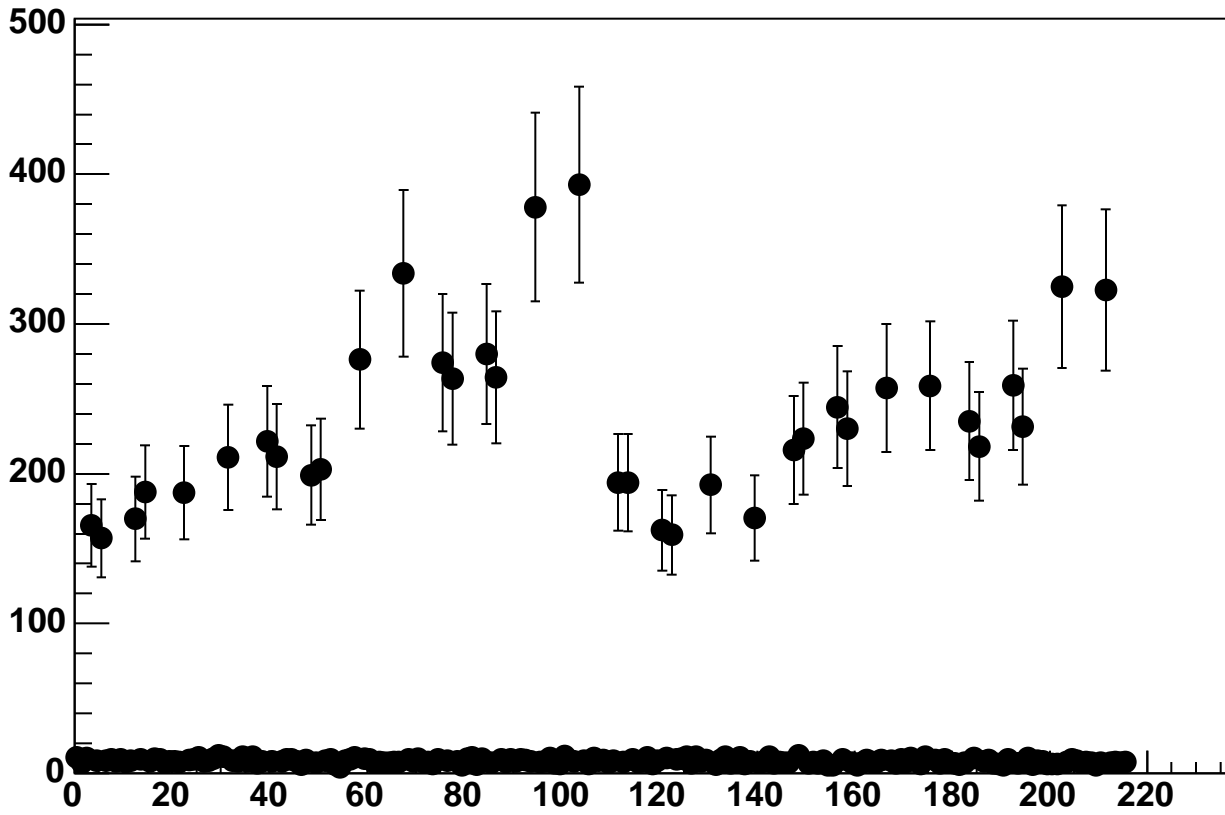
Enable 1, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



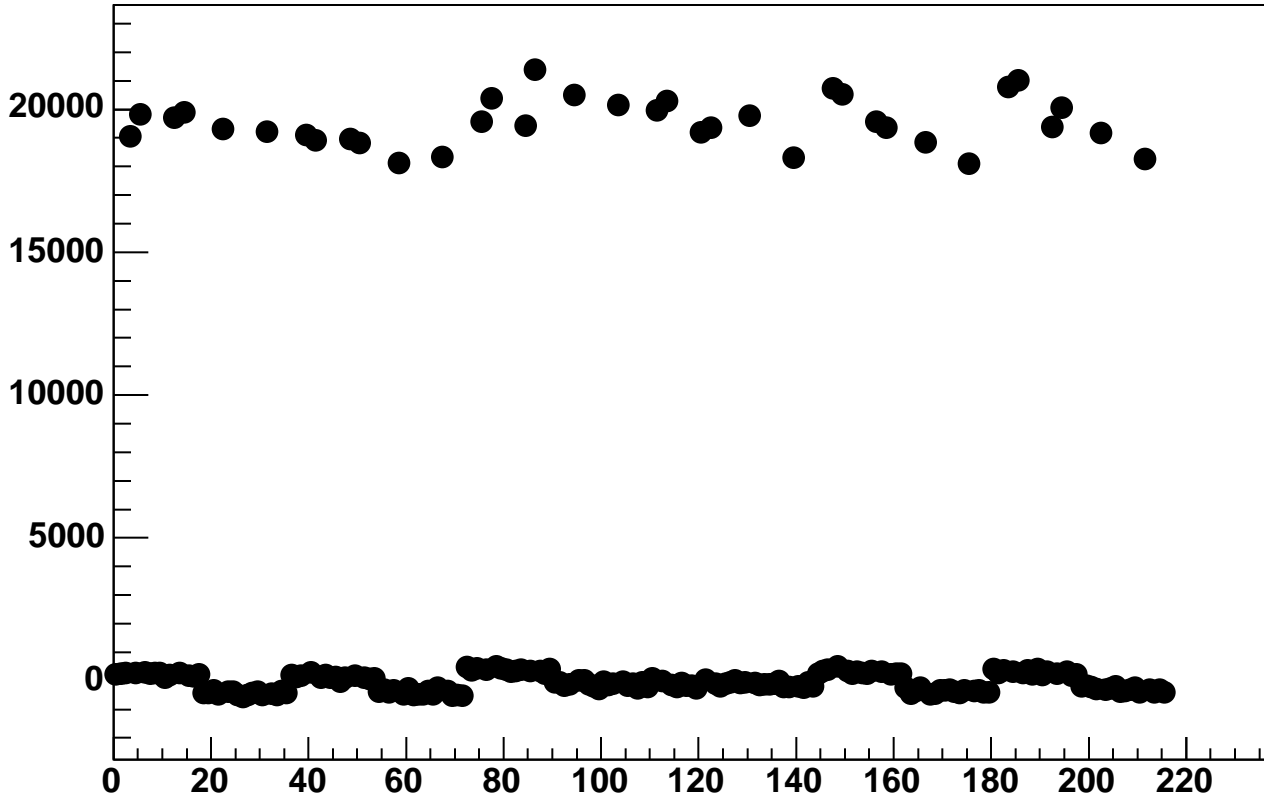
Enable 1, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



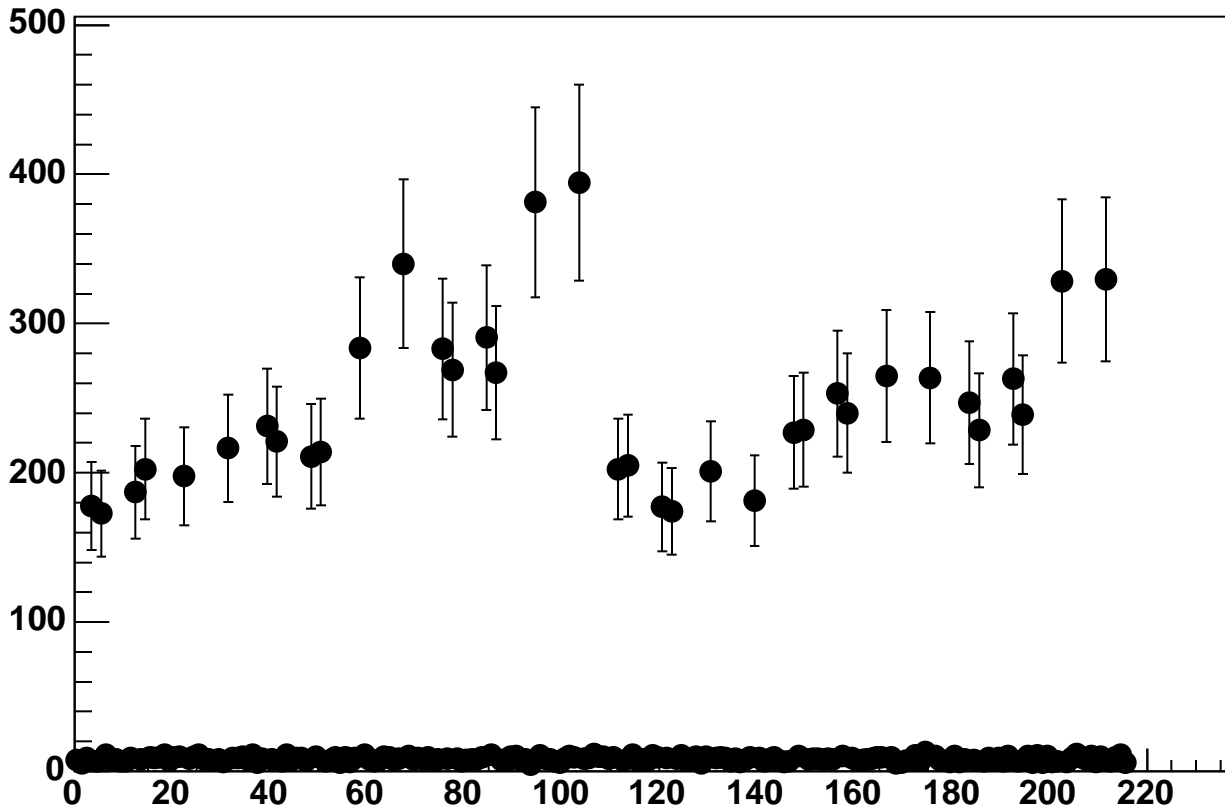
Enable 1, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



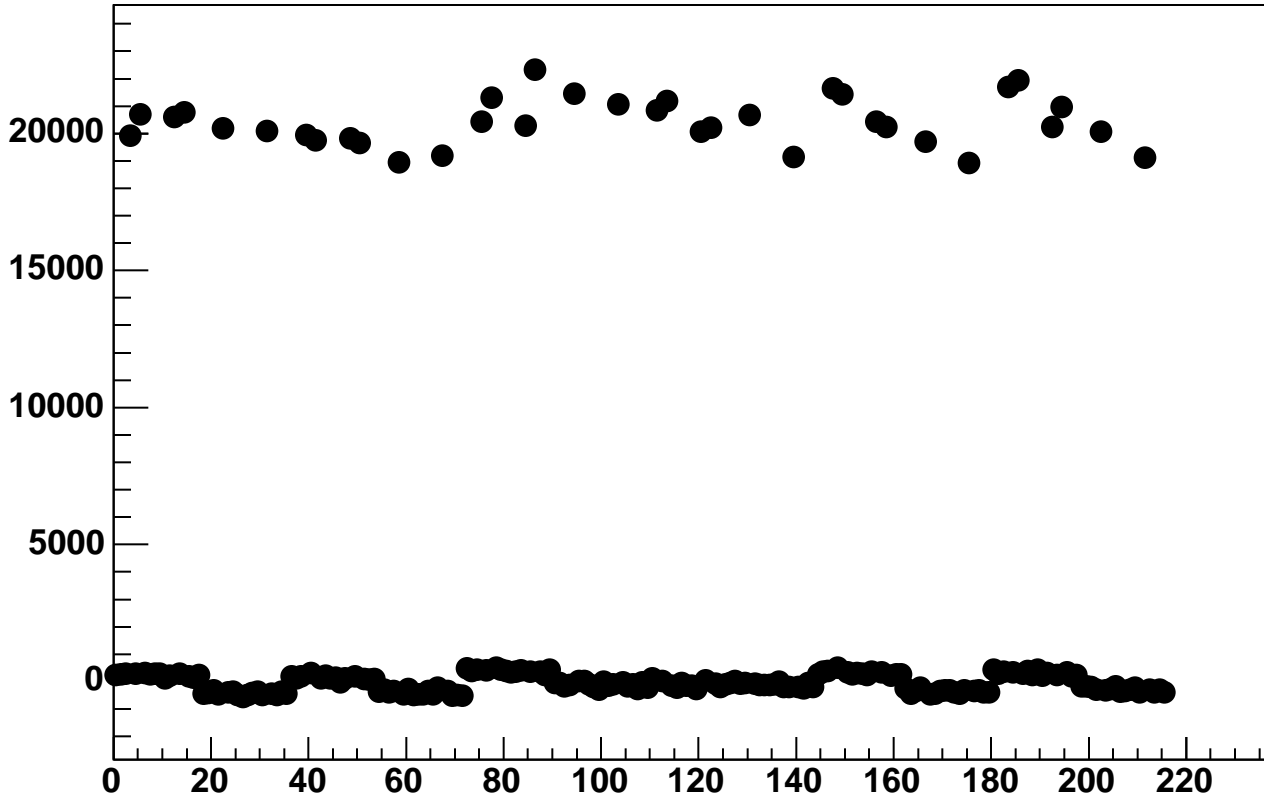
Enable 1, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



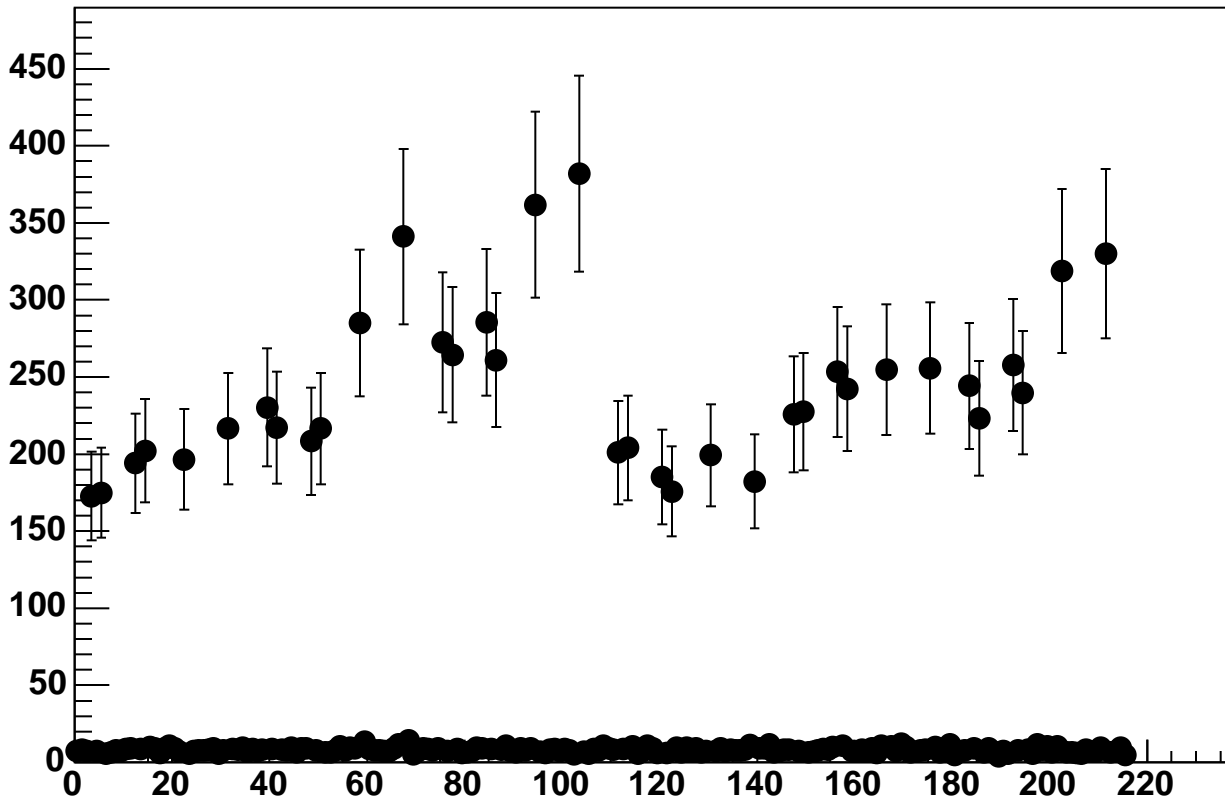
Enable 1, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



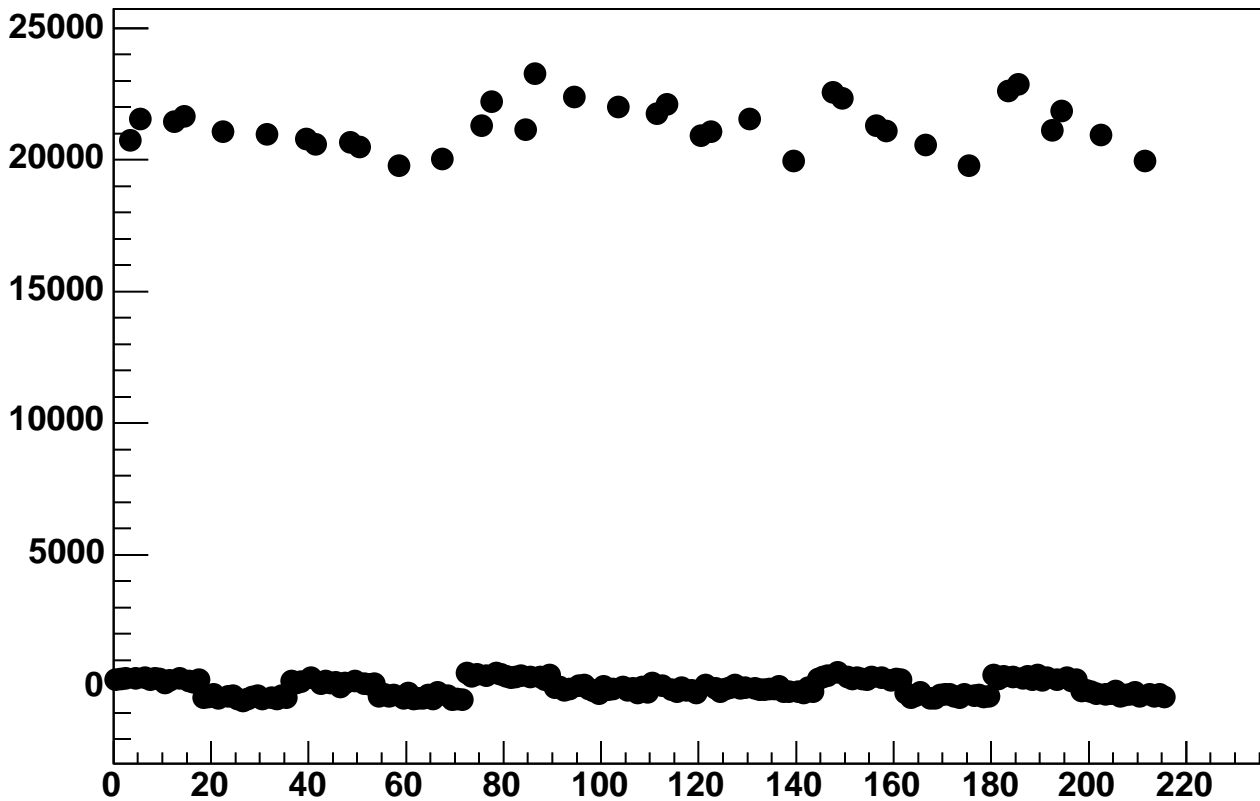
Enable 1, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



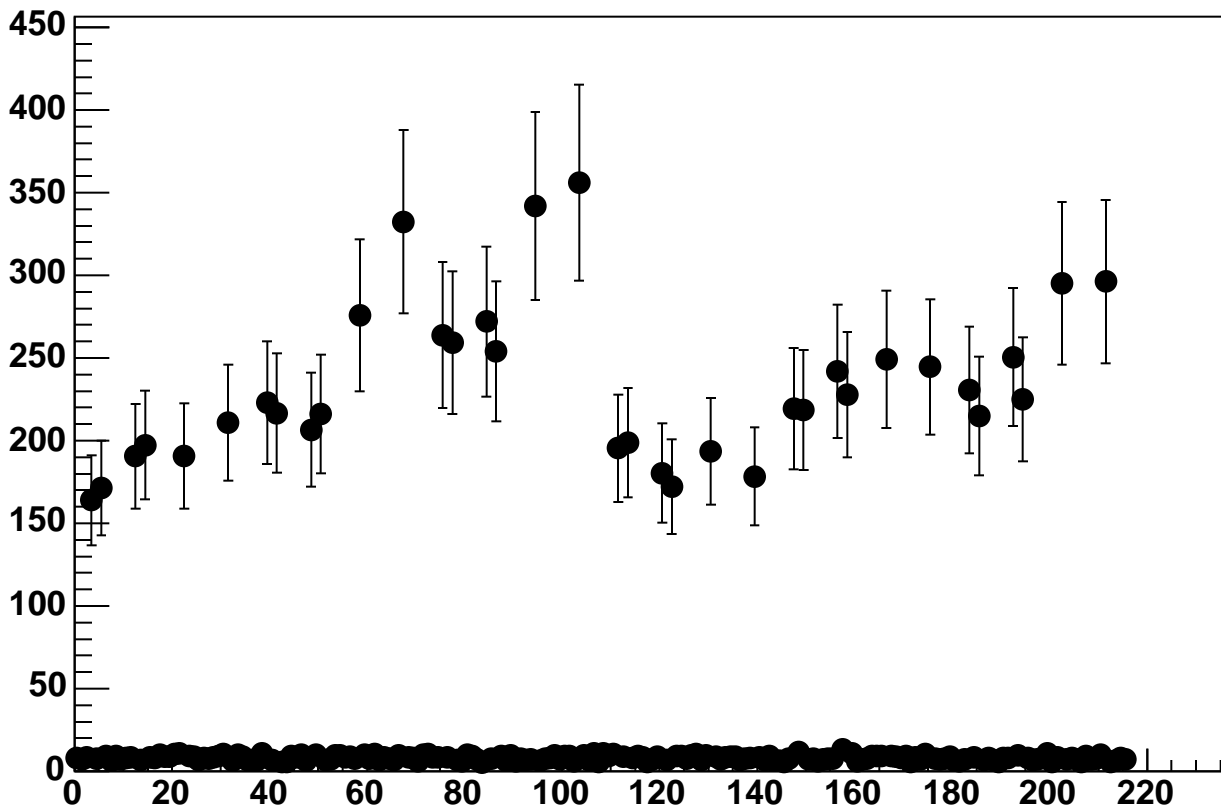
Enable 1, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



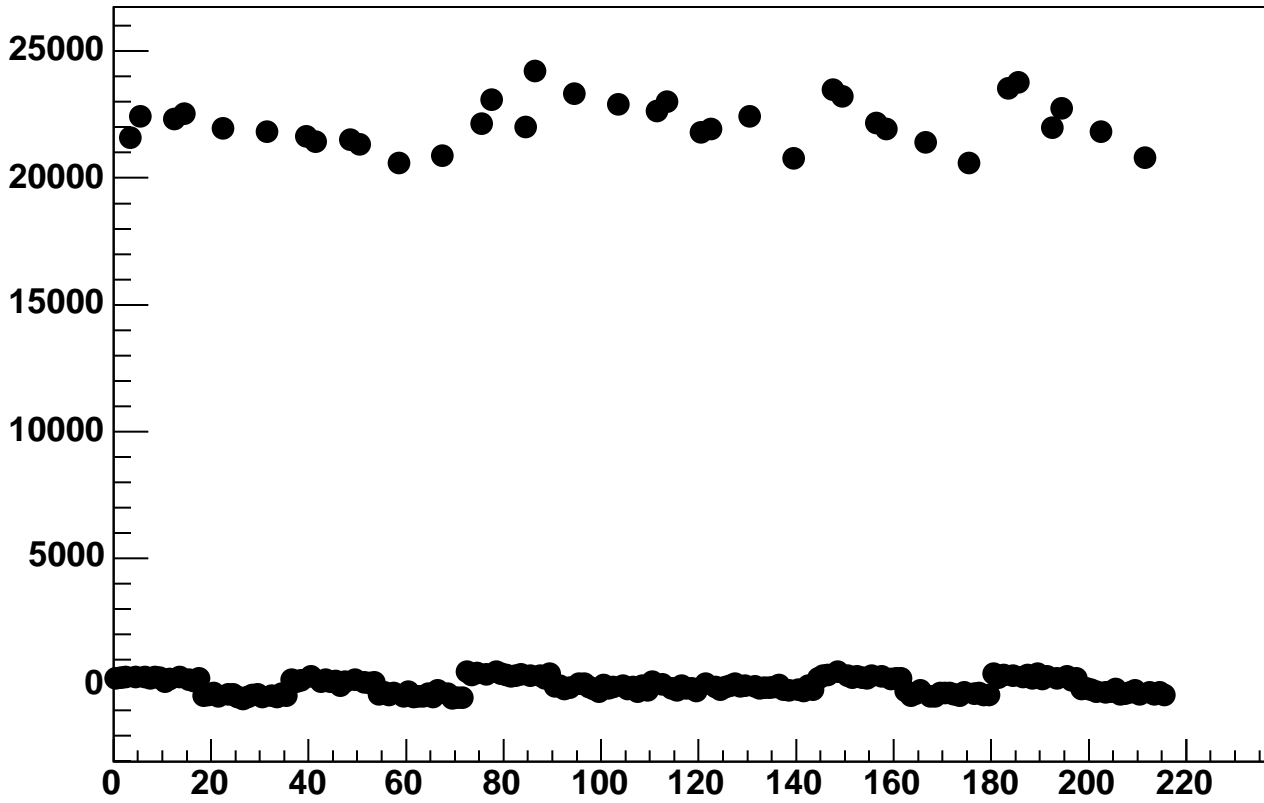
Enable 1, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



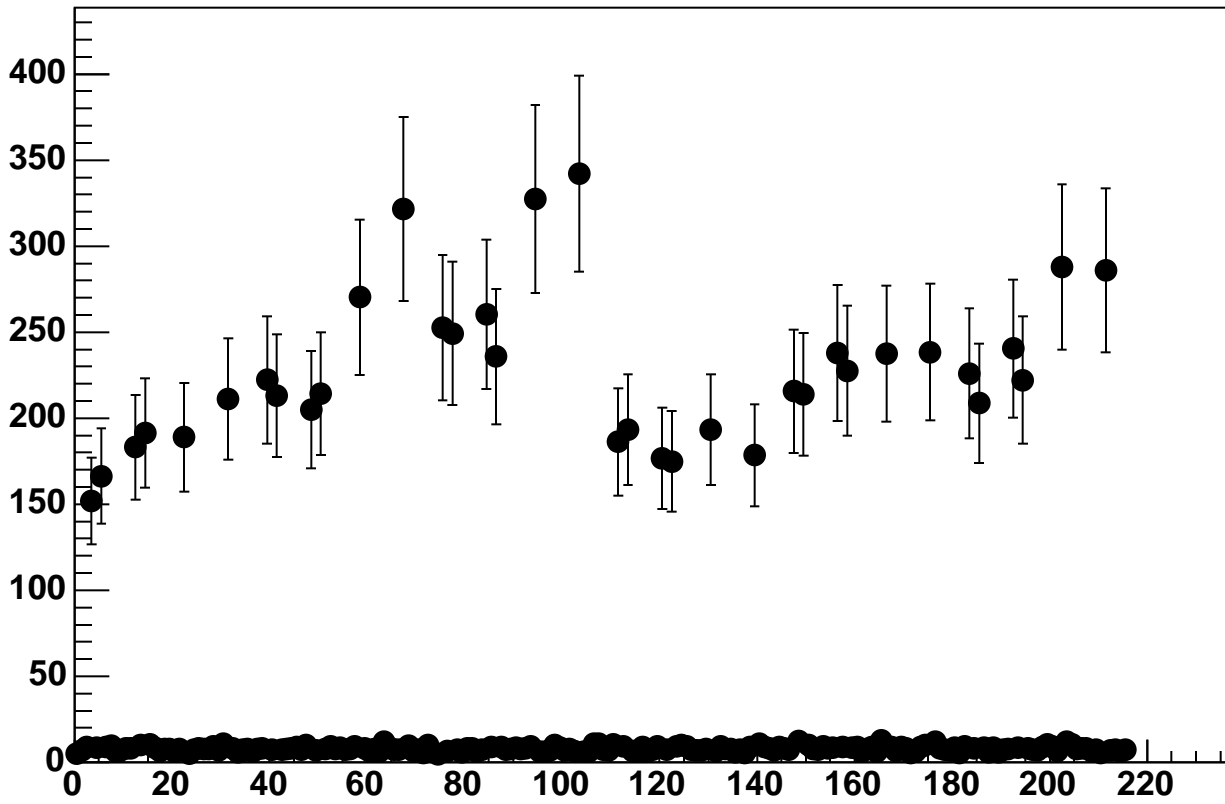
Enable 1, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



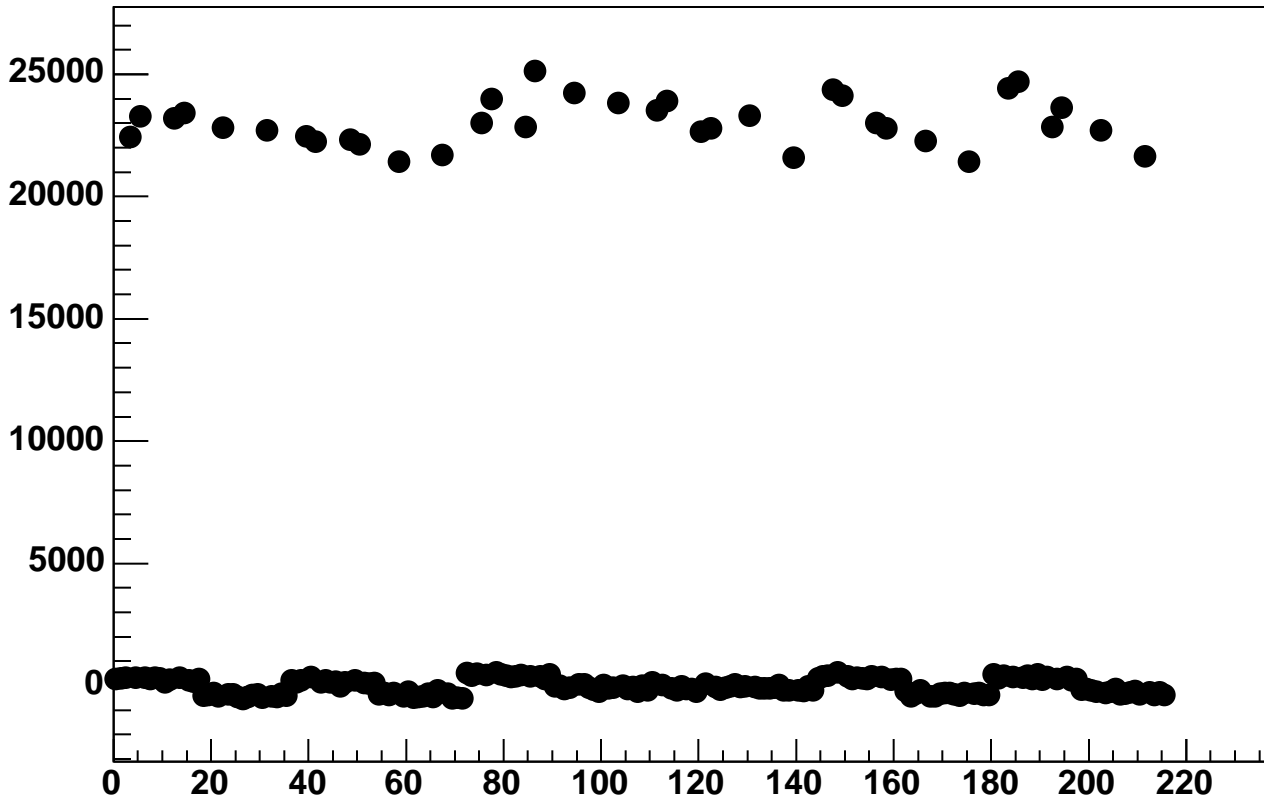
Enable 1, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



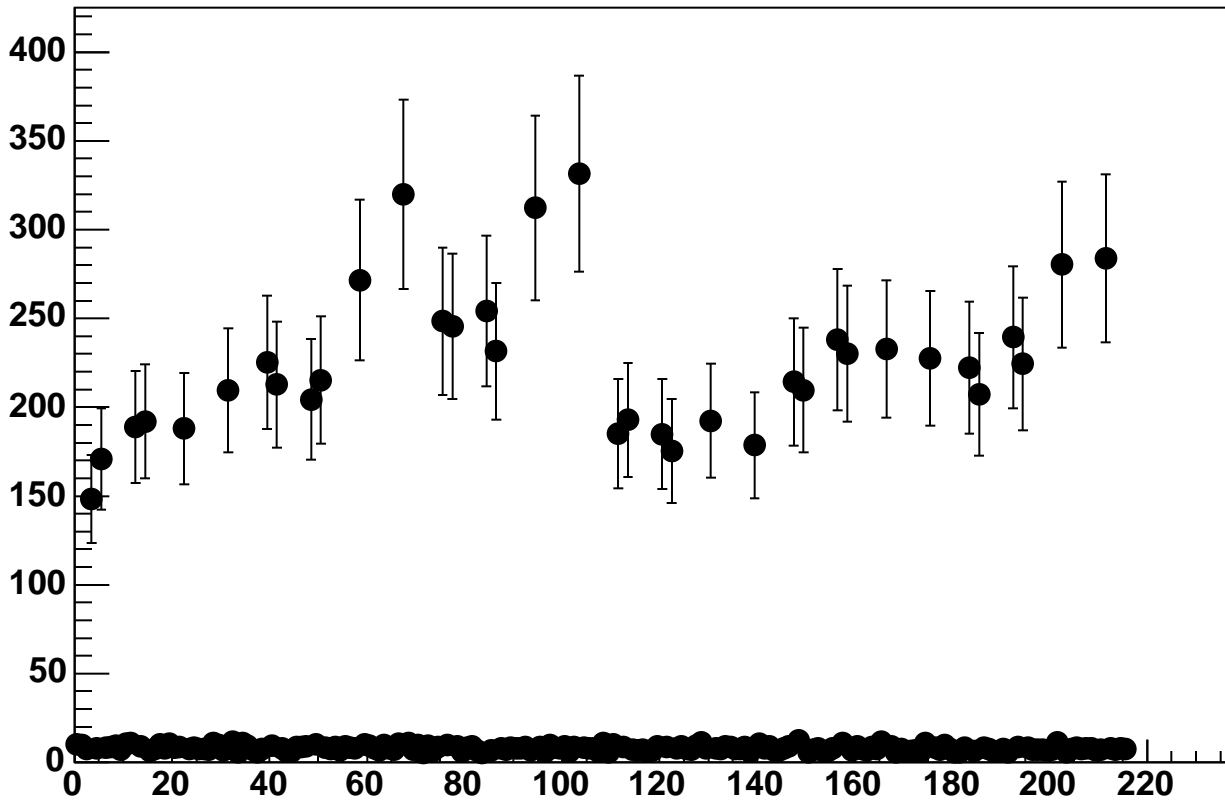
Enable 1, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



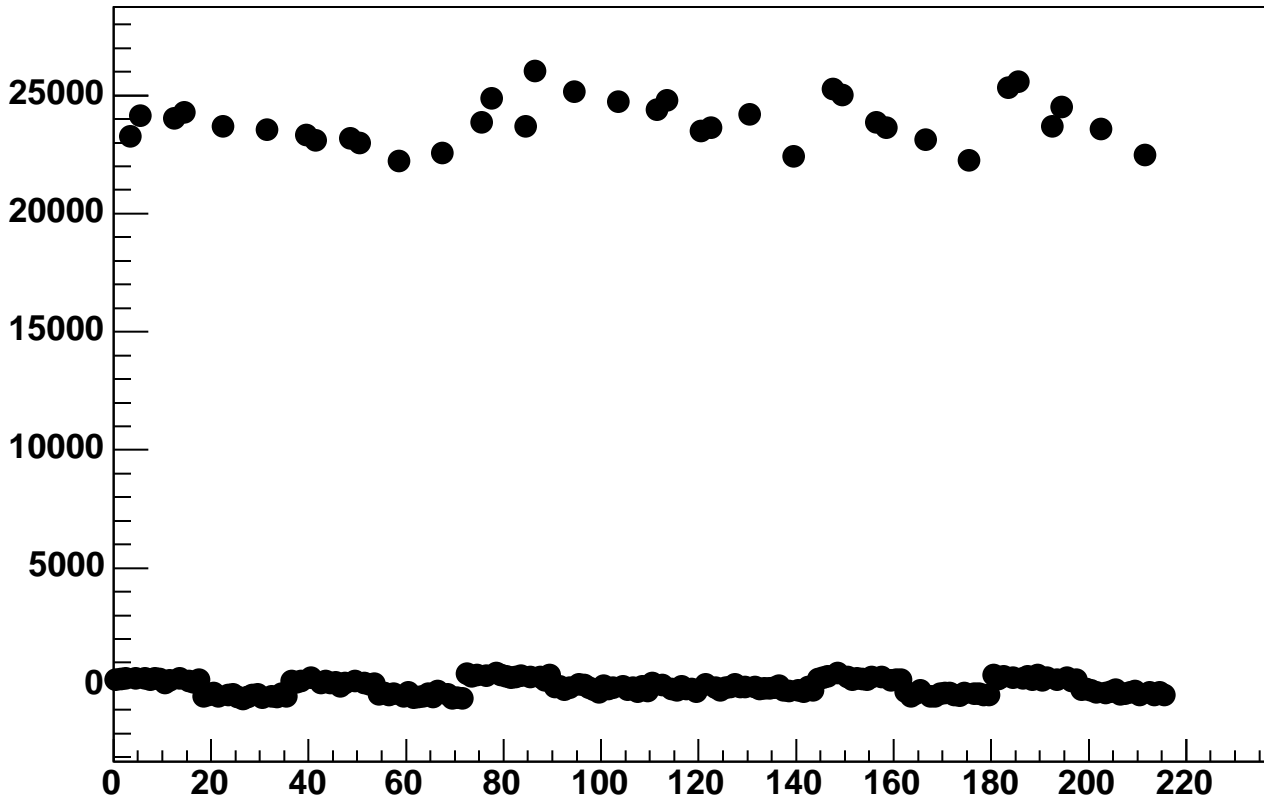
Enable 1, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



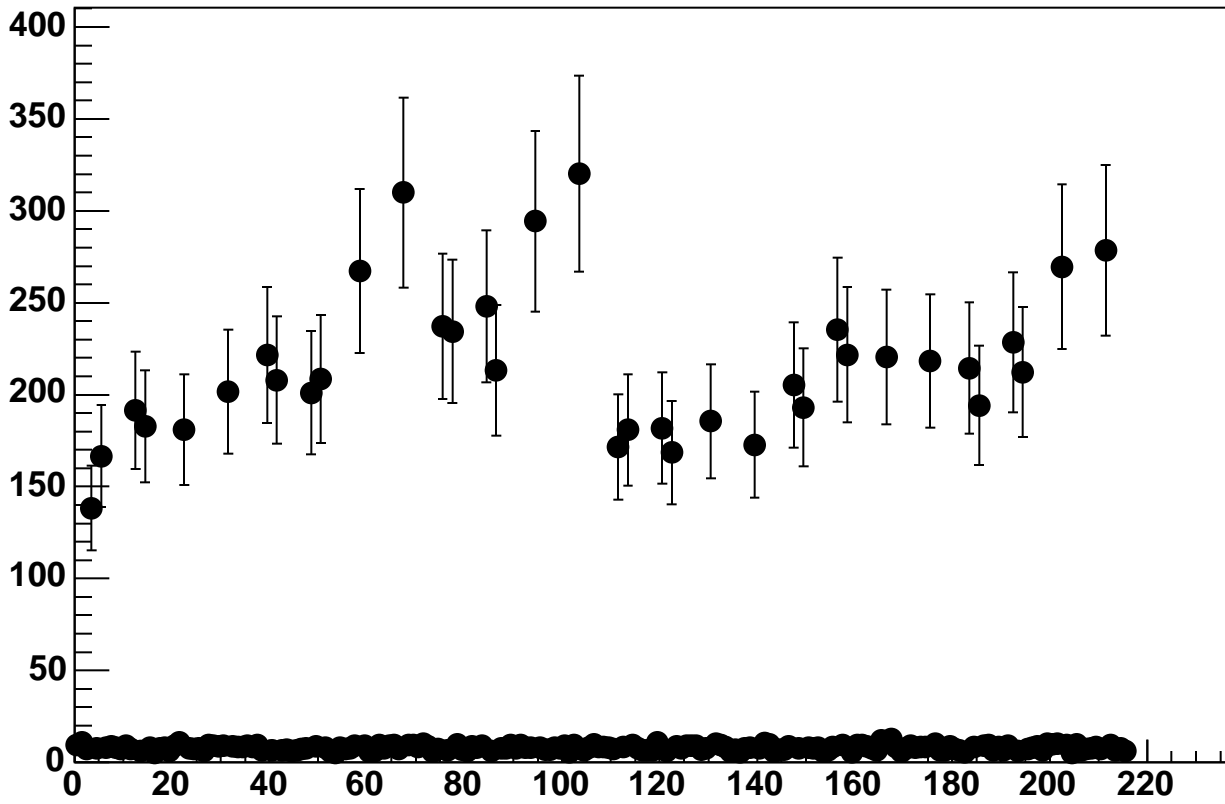
Enable 1, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



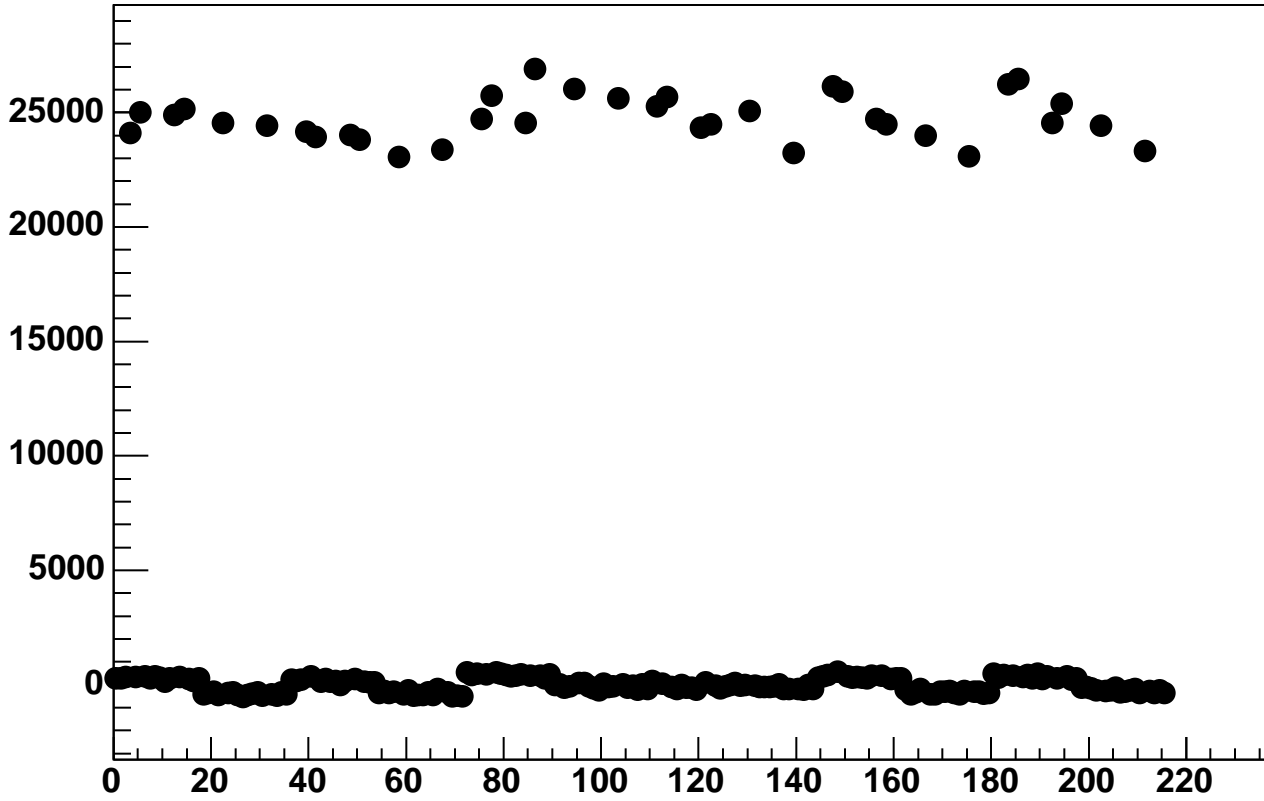
Enable 1, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



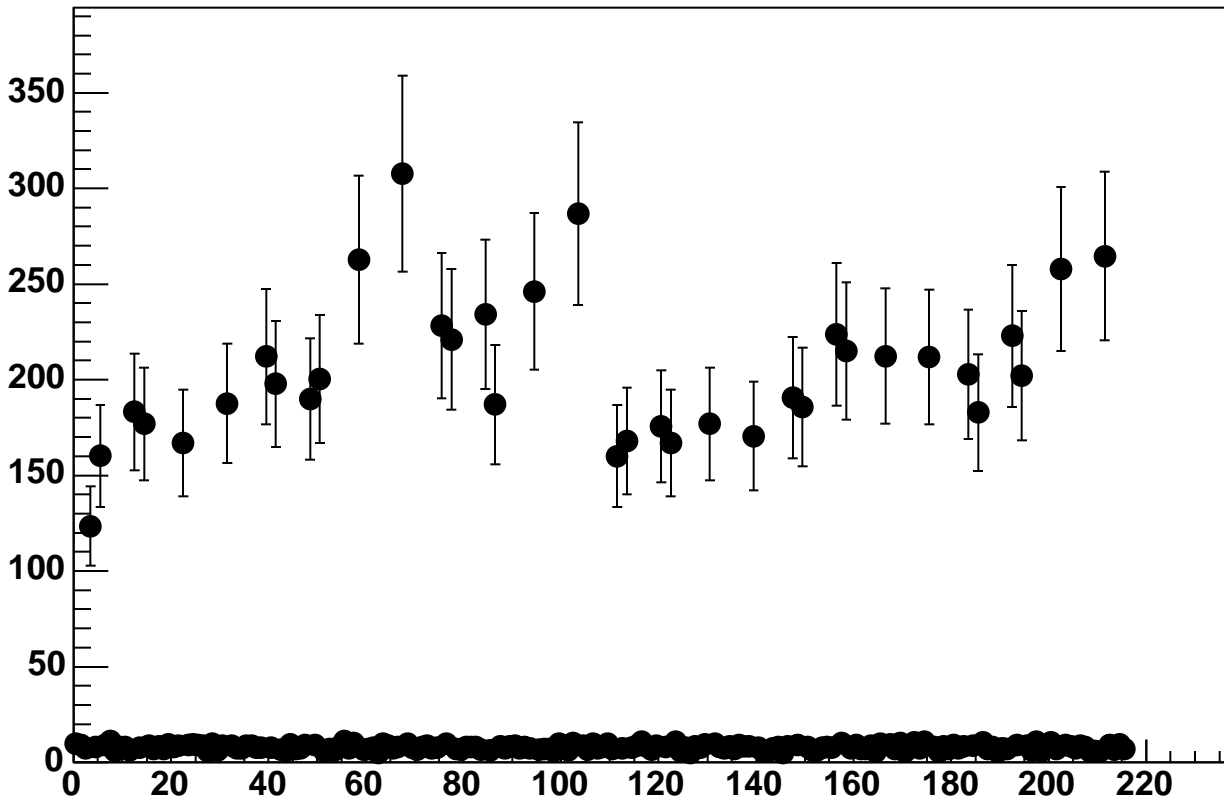
Enable 1, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



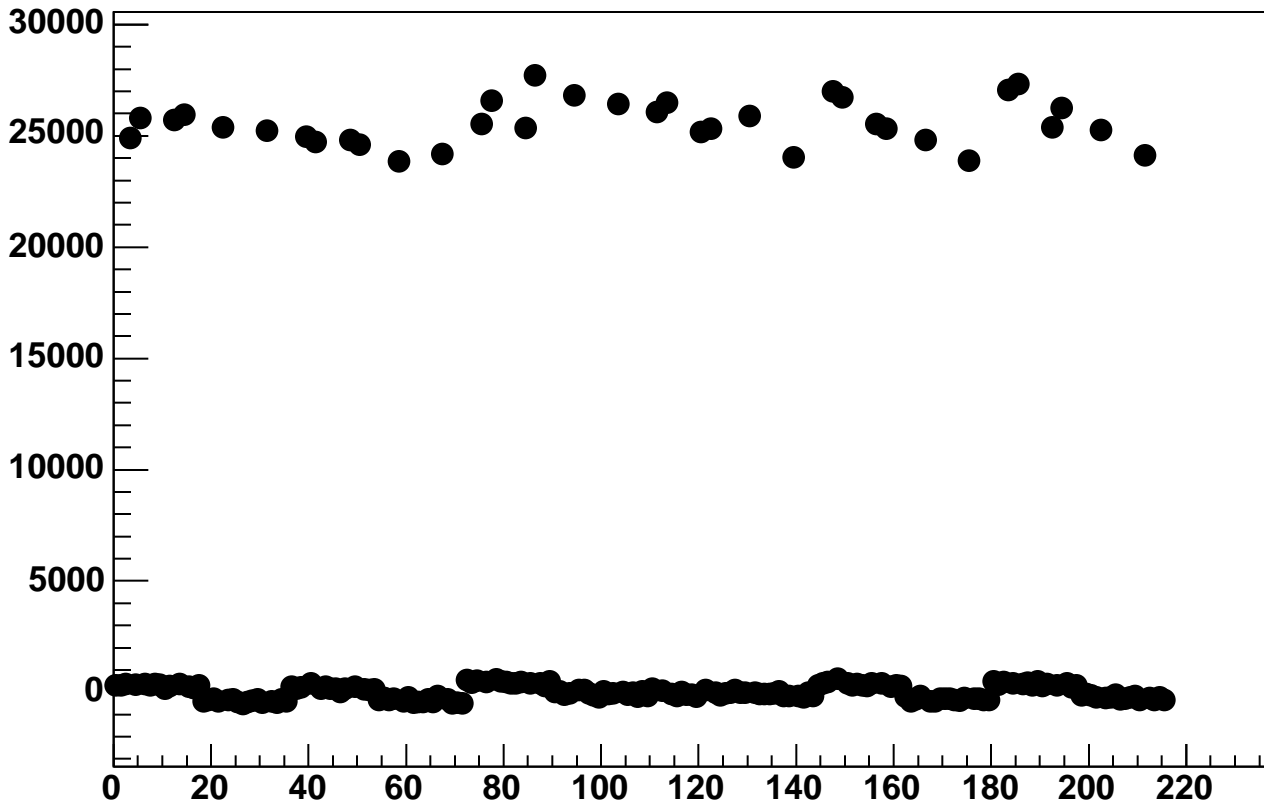
Enable 1, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



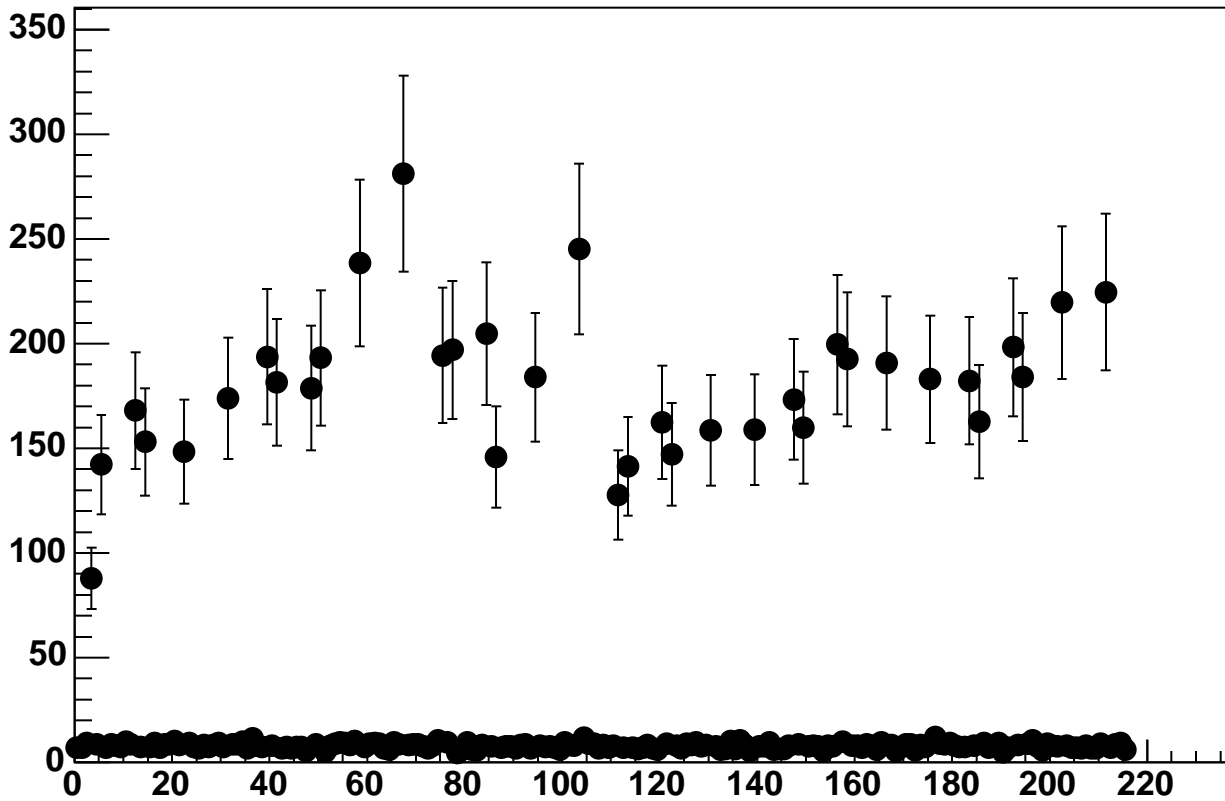
Enable 1, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



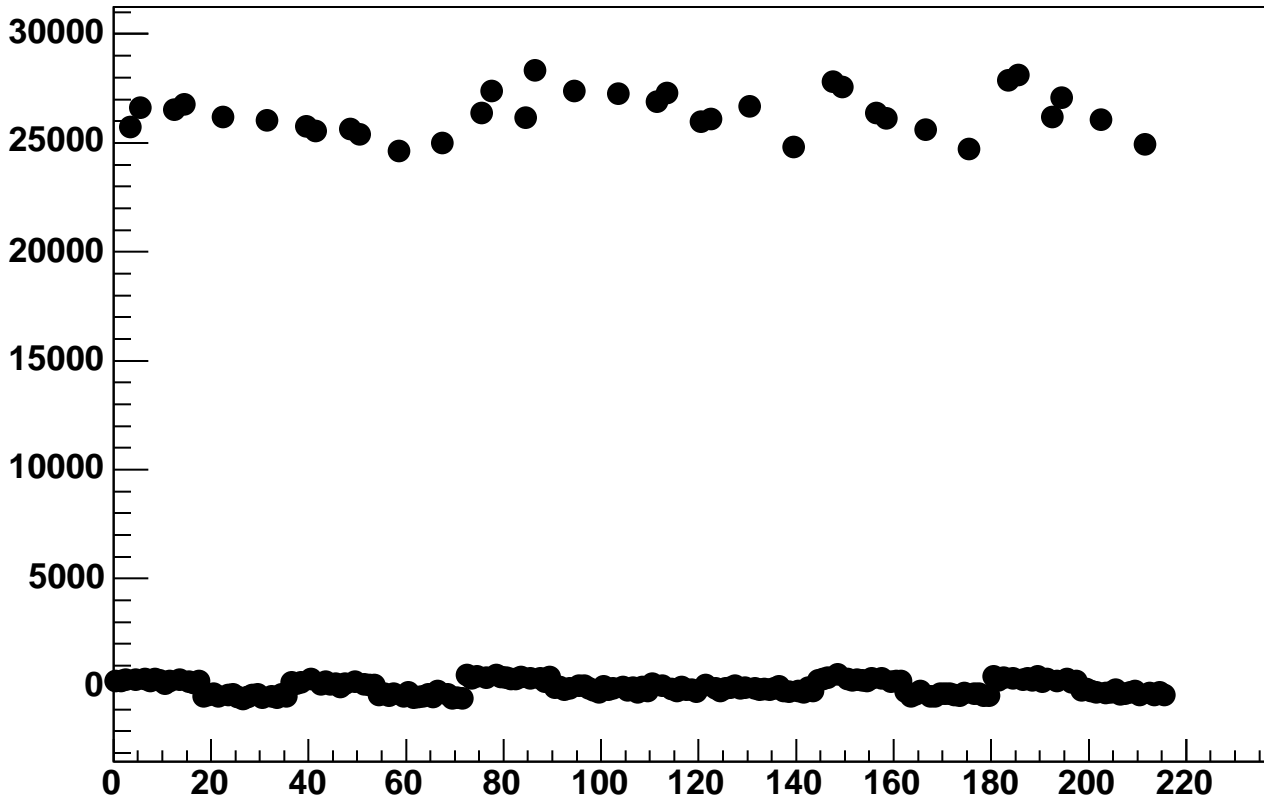
Enable 1, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



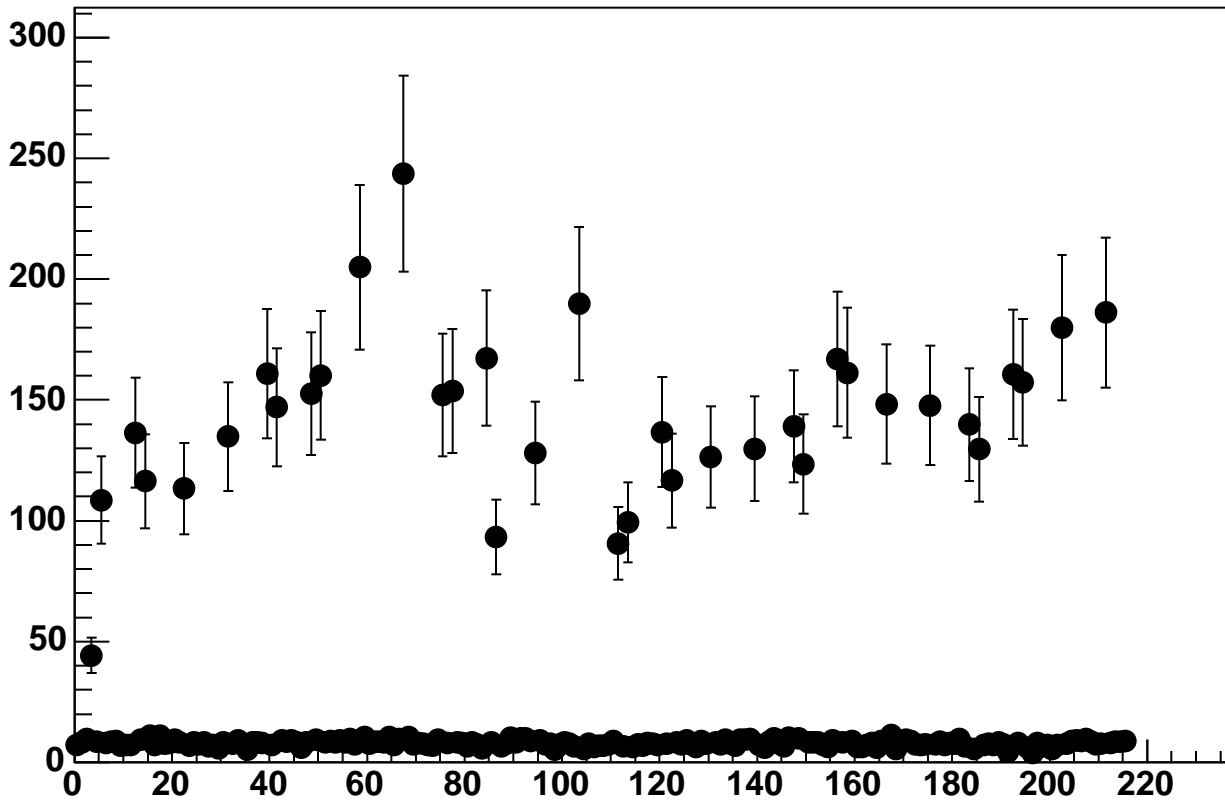
Enable 1, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



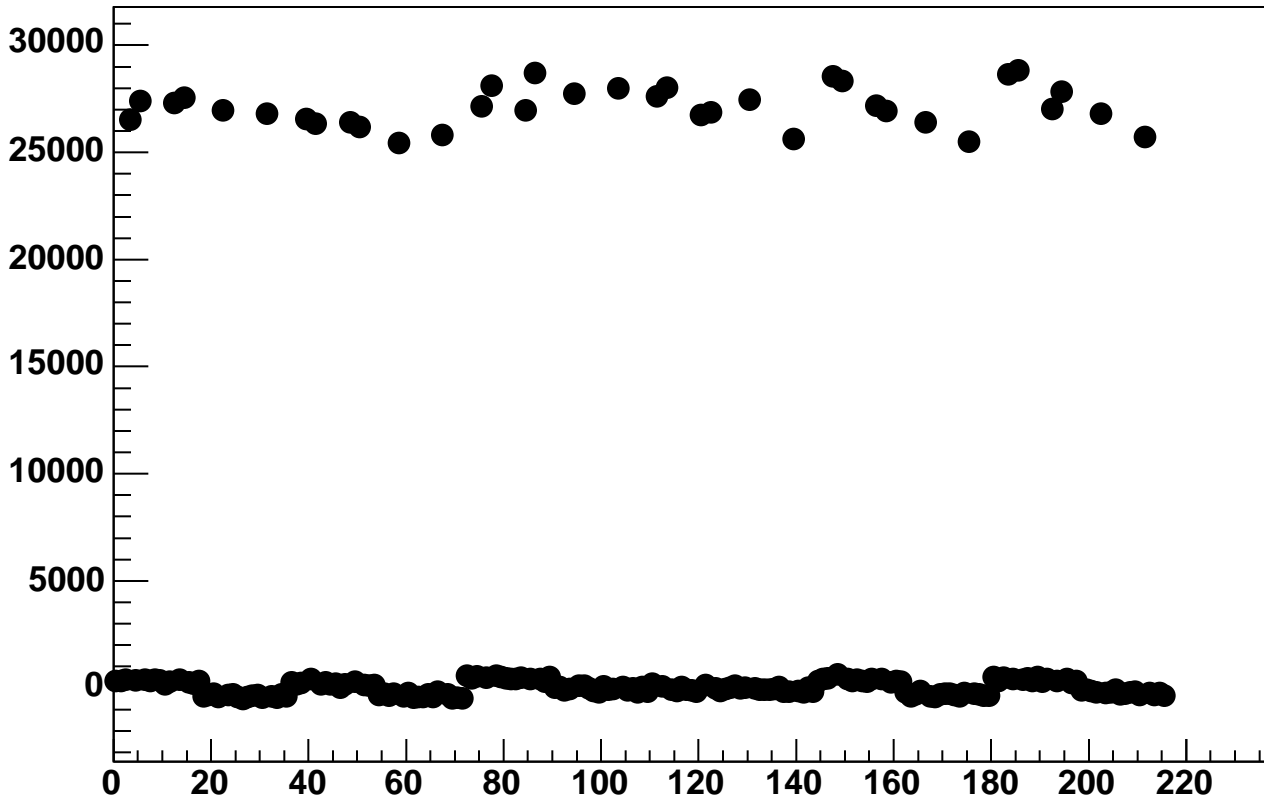
Enable 1, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



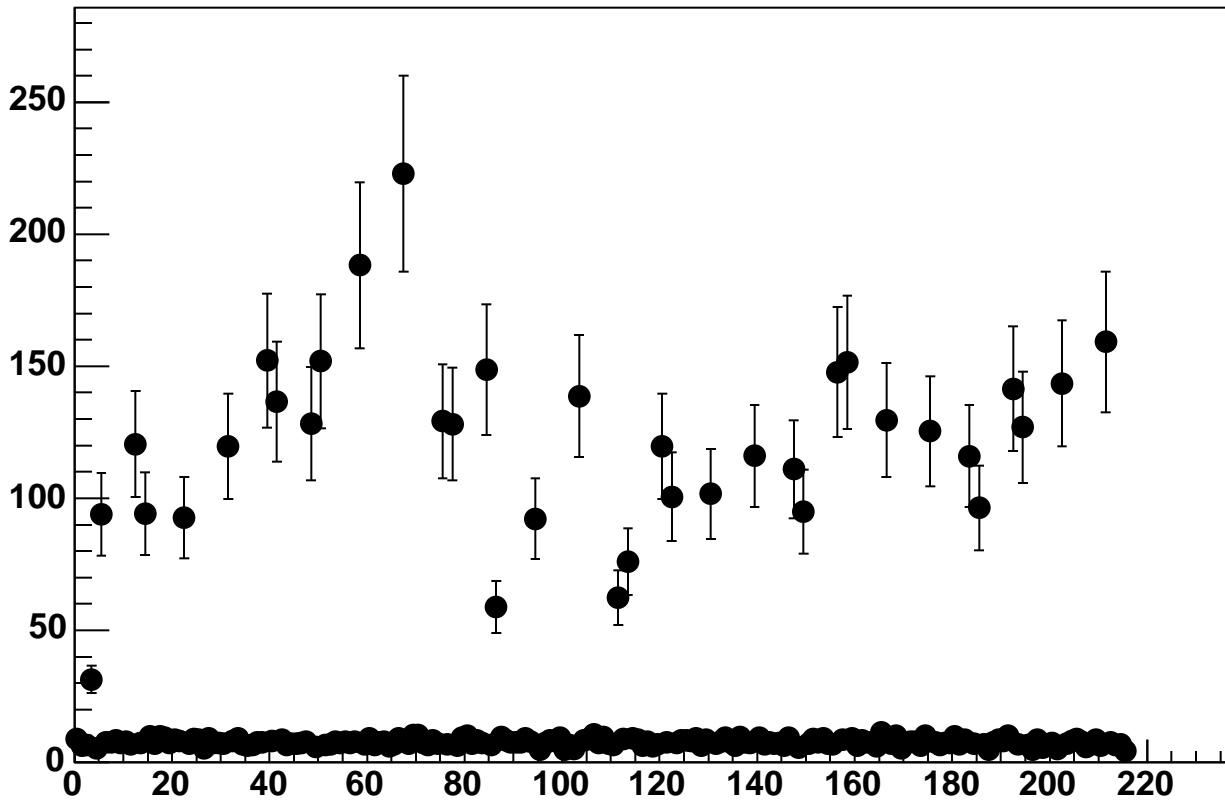
Enable 1, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



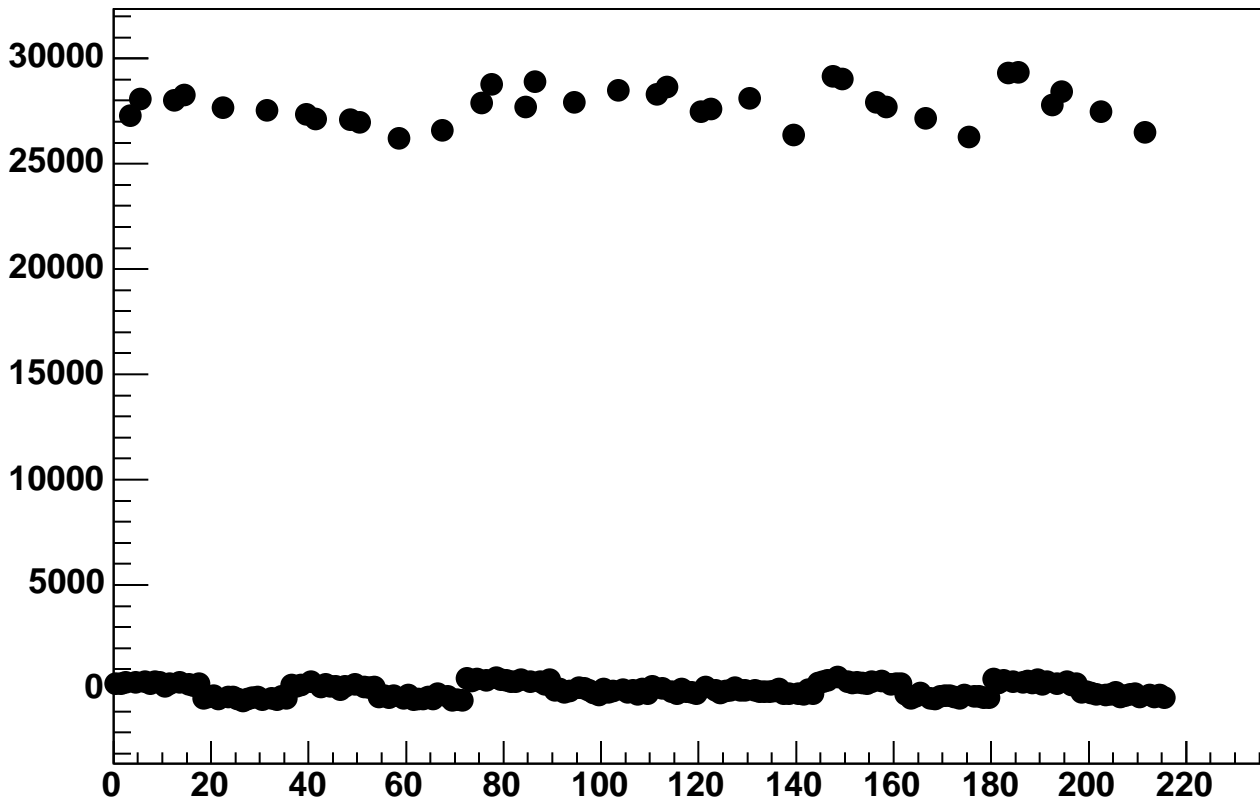
Enable 1, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



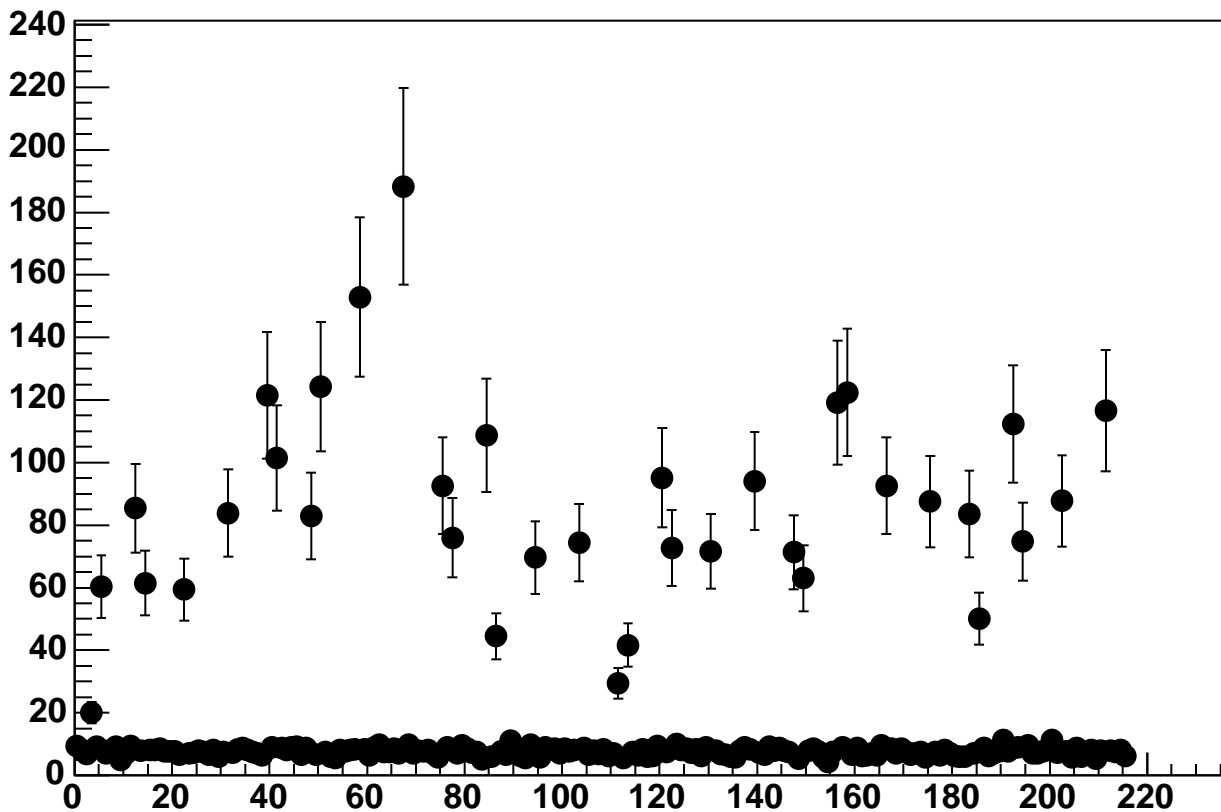
Enable 1, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



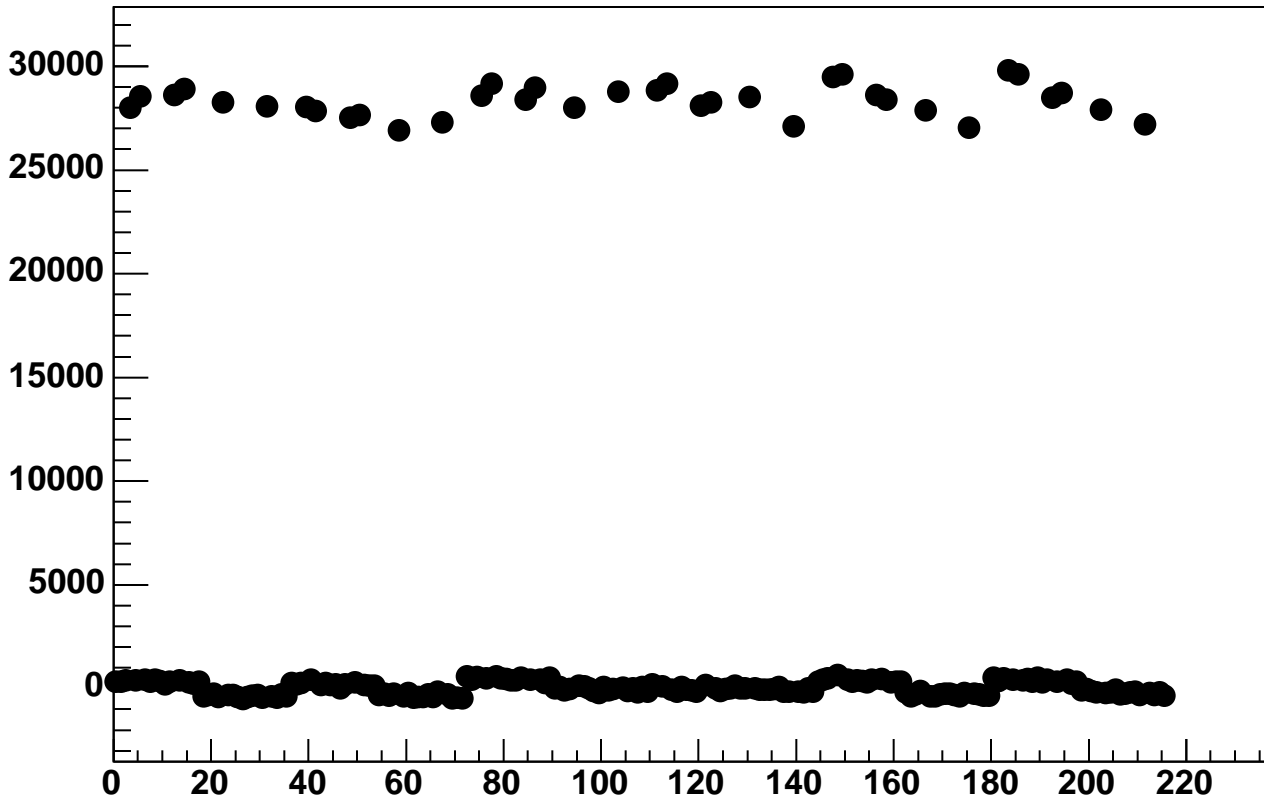
Enable 1, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



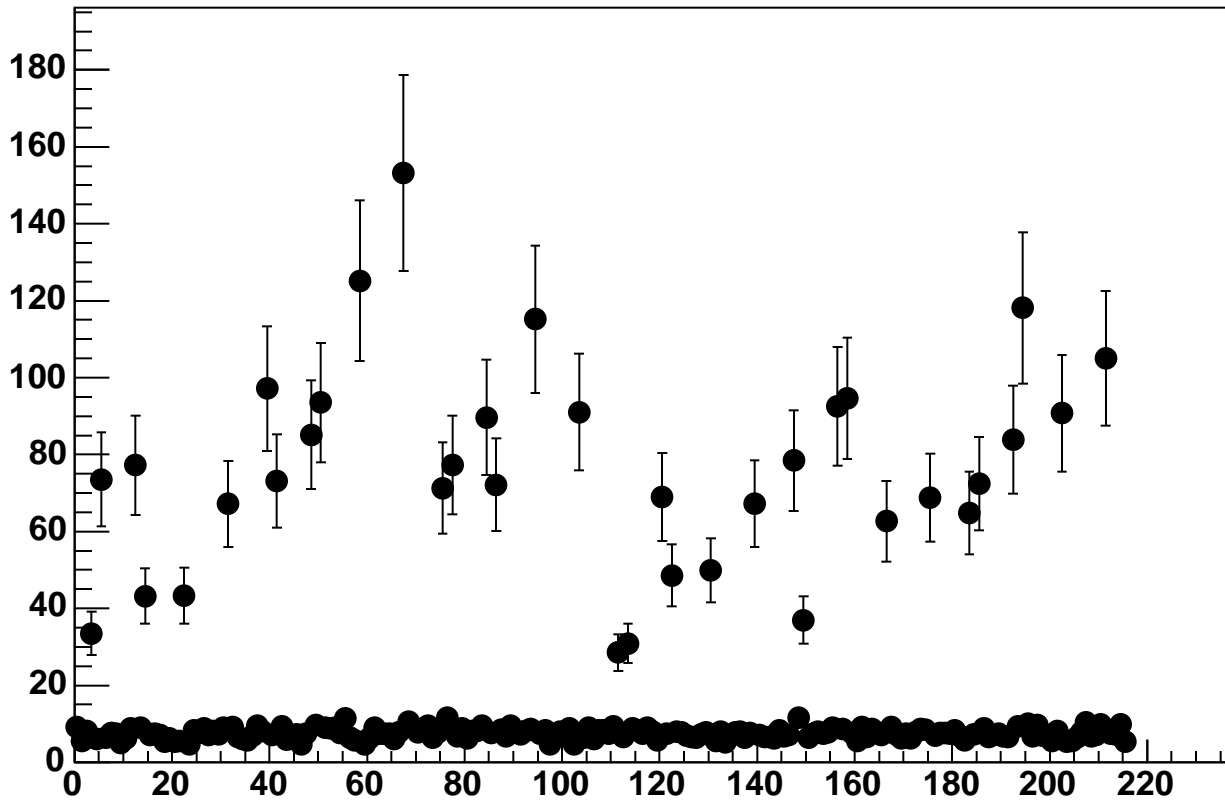
Enable 1, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



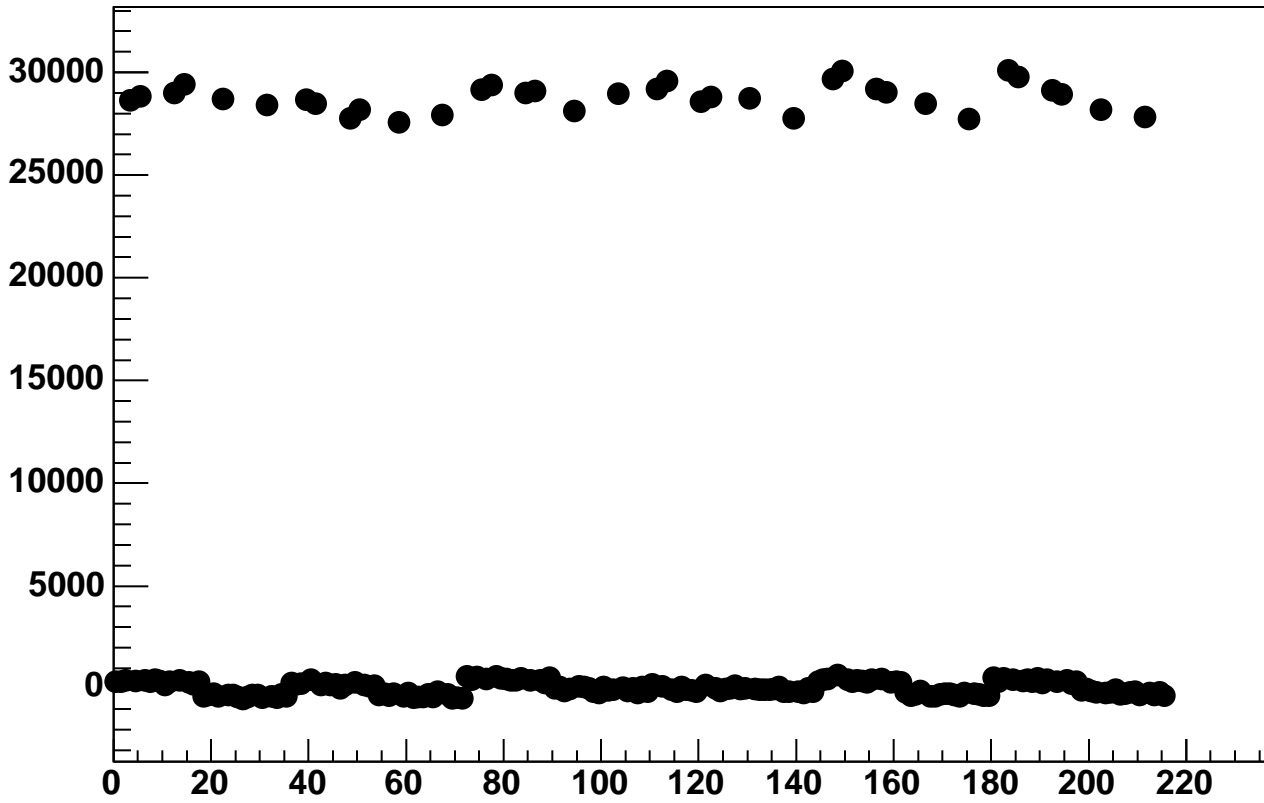
Enable 1, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



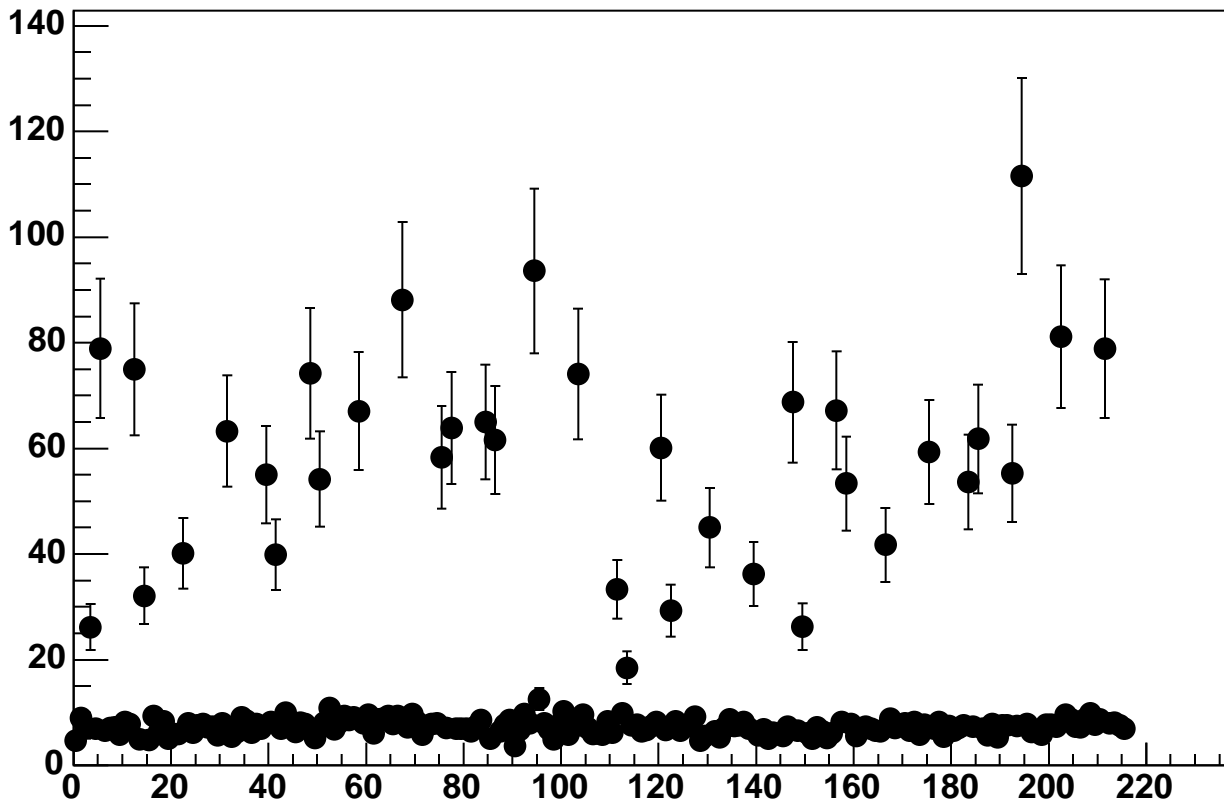
Enable 1, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



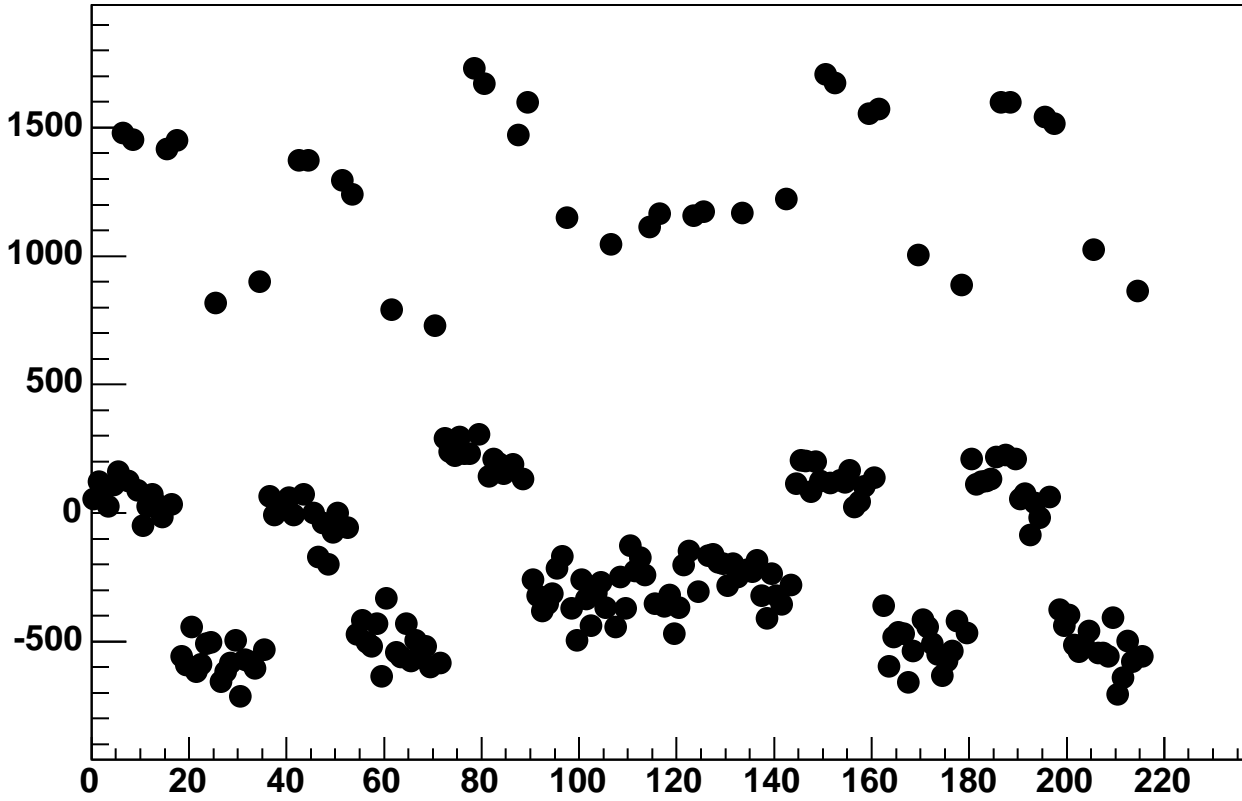
Enable 1, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



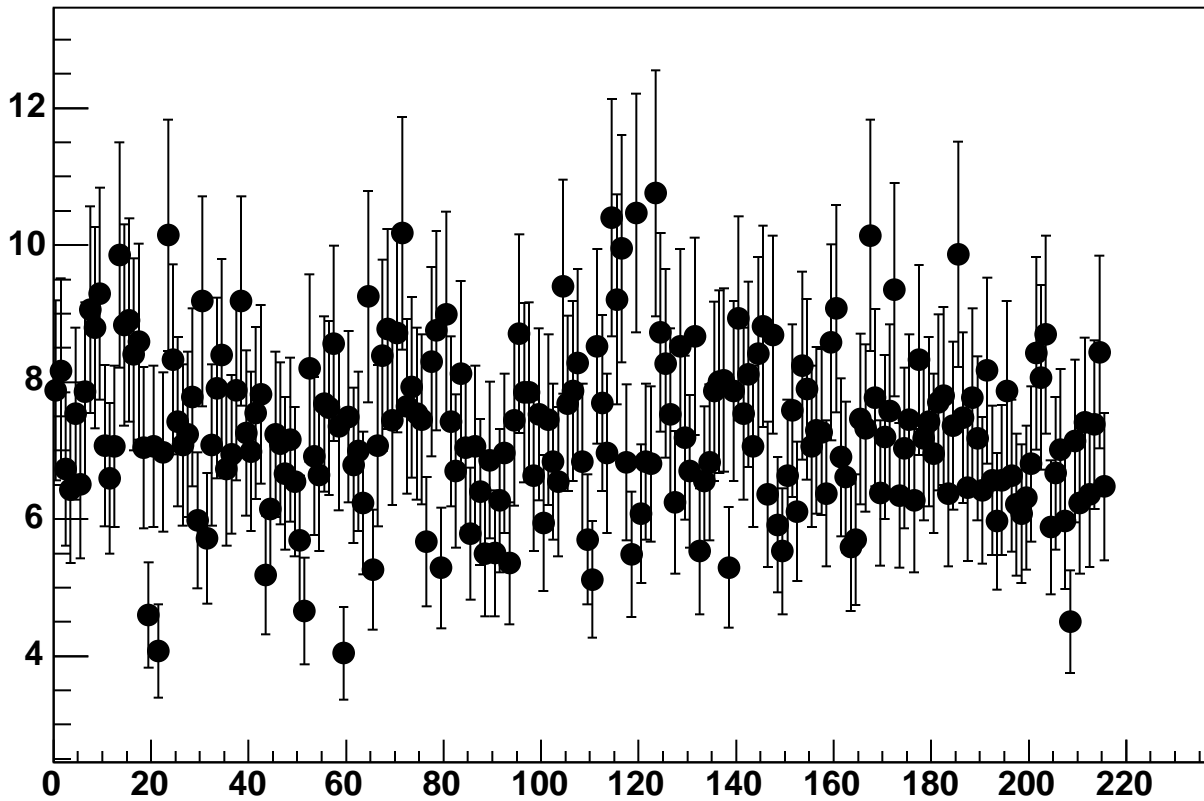
Enable 1, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



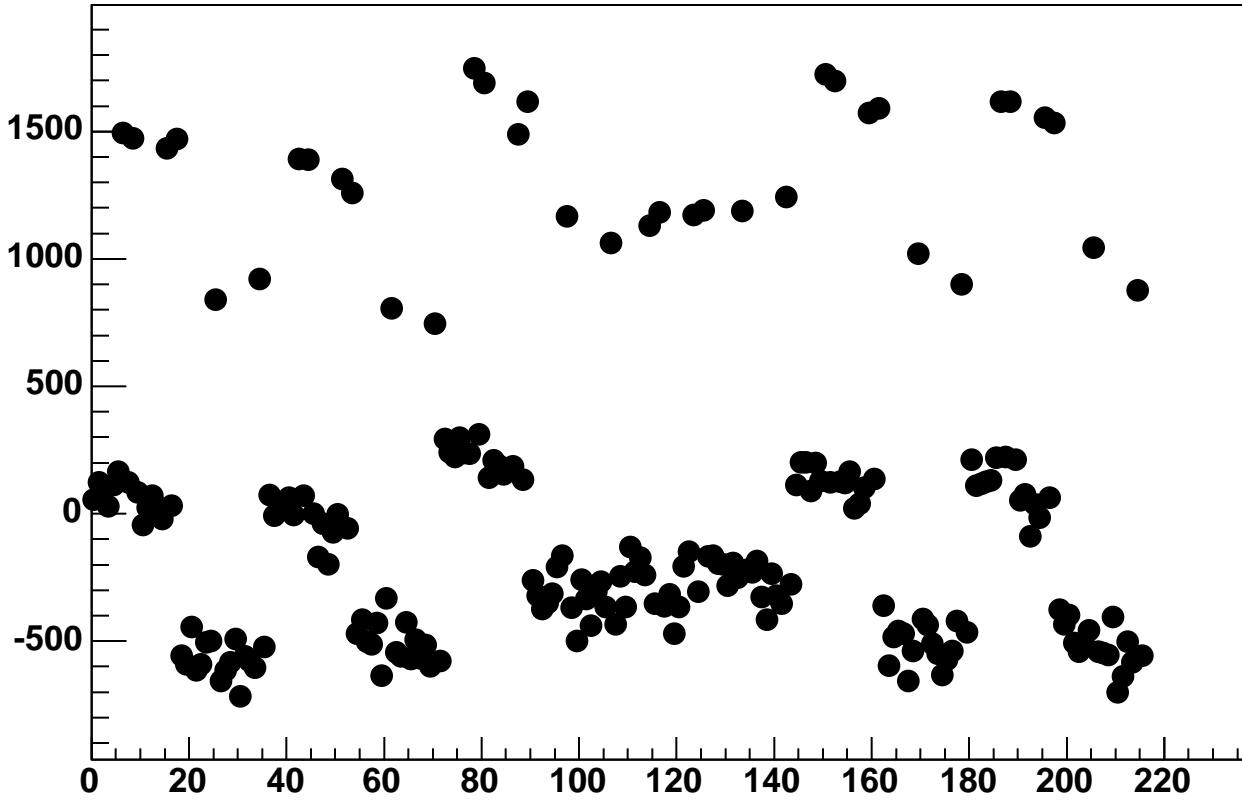
Enable 2, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



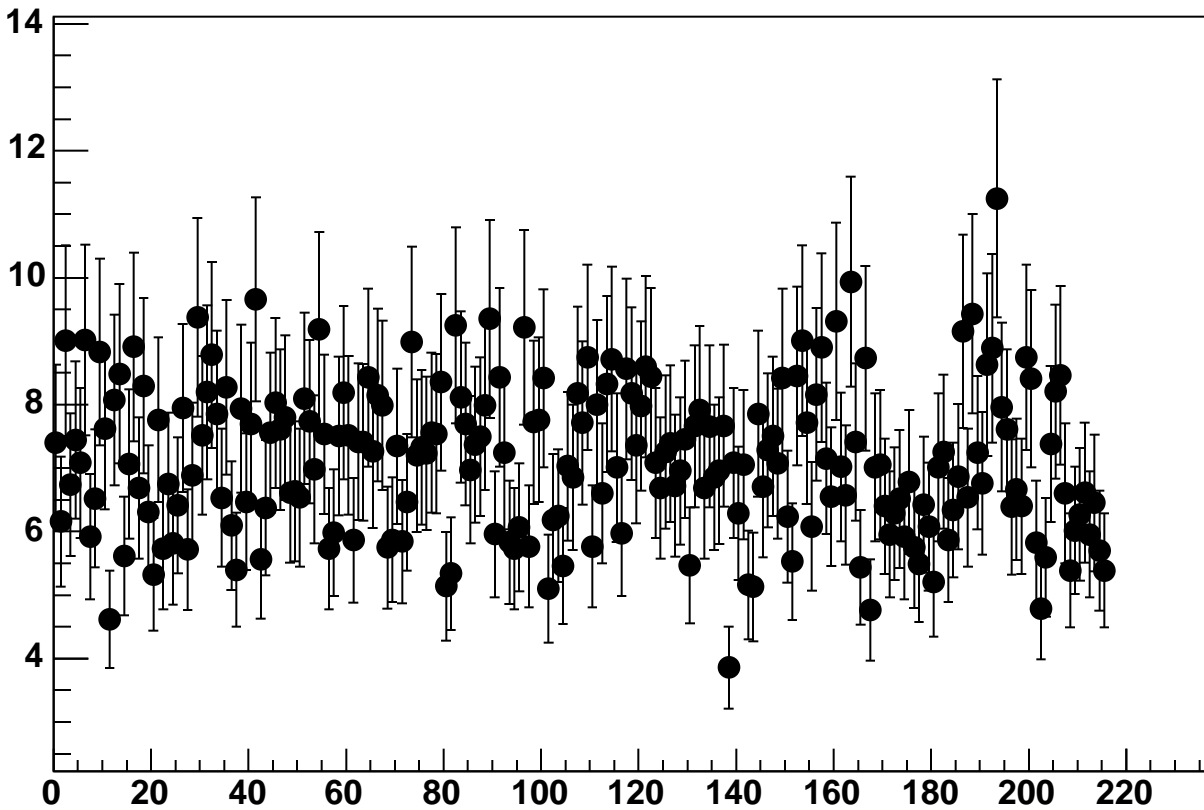
Enable 2, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



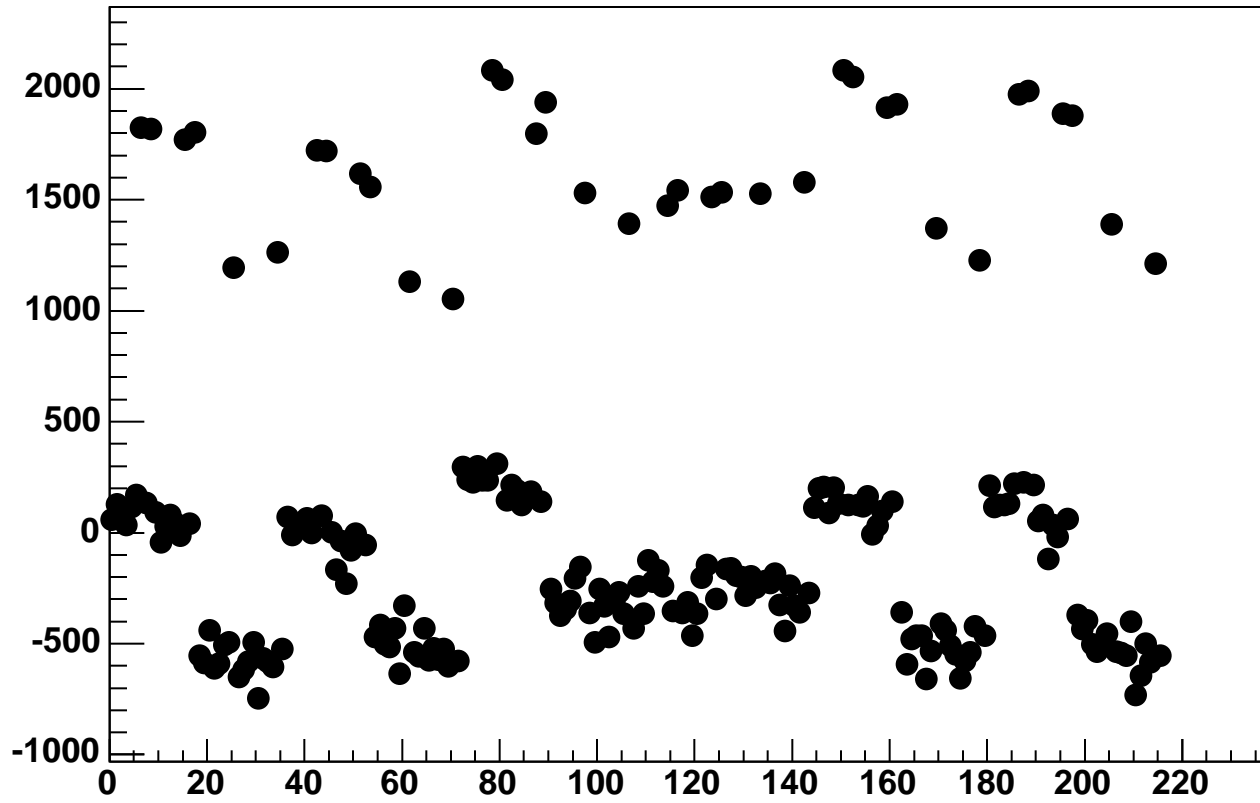
Enable 2, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



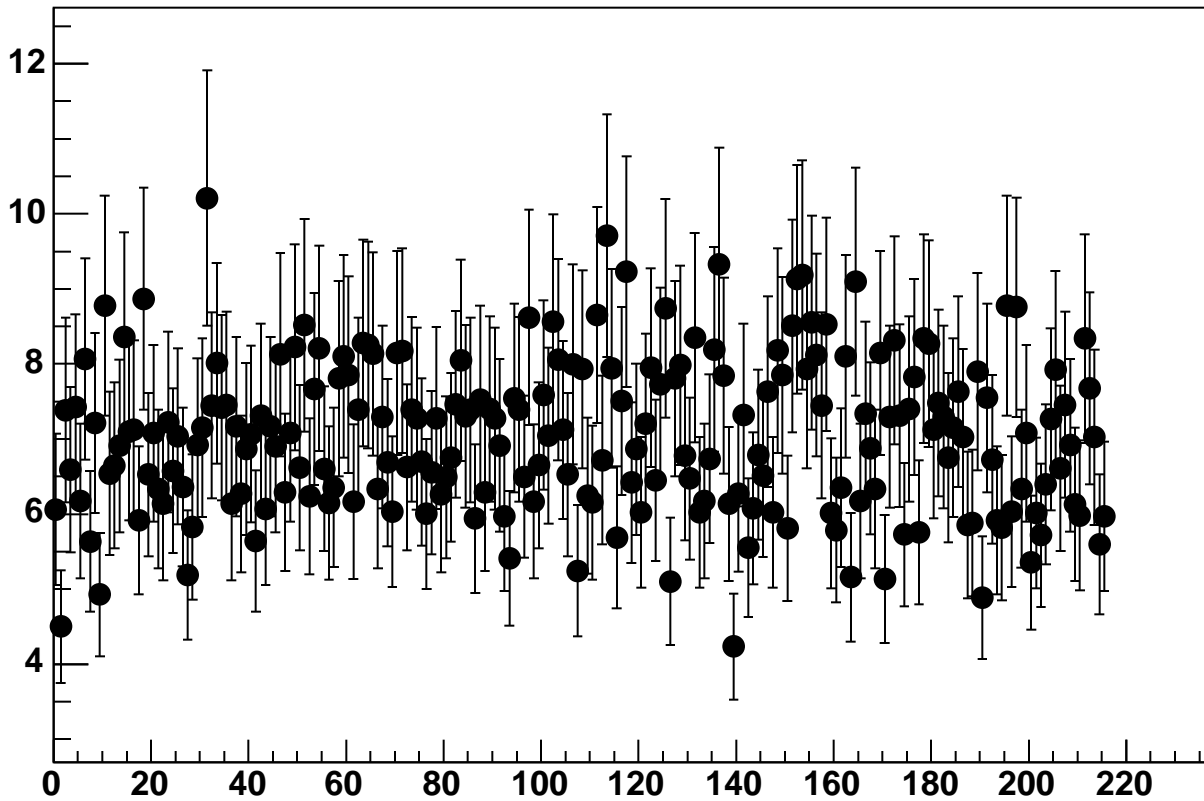
Enable 2, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



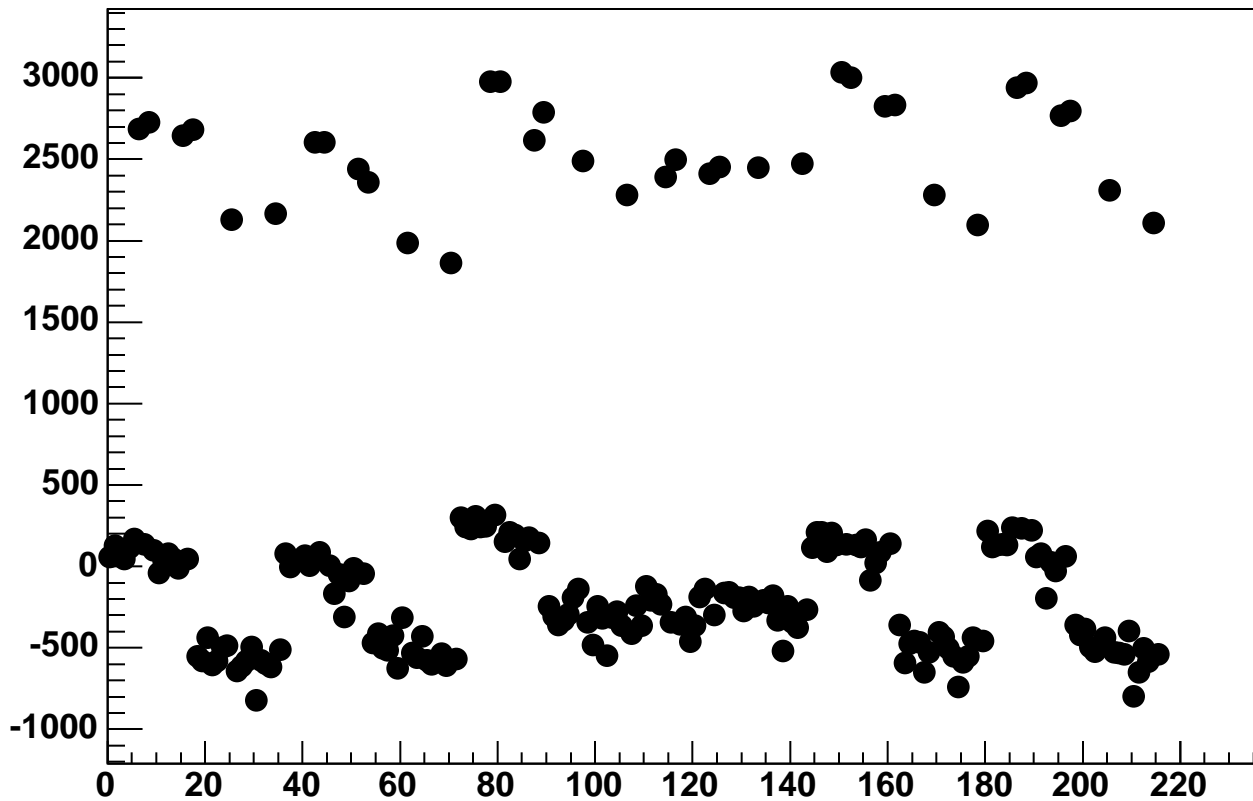
Enable 2, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



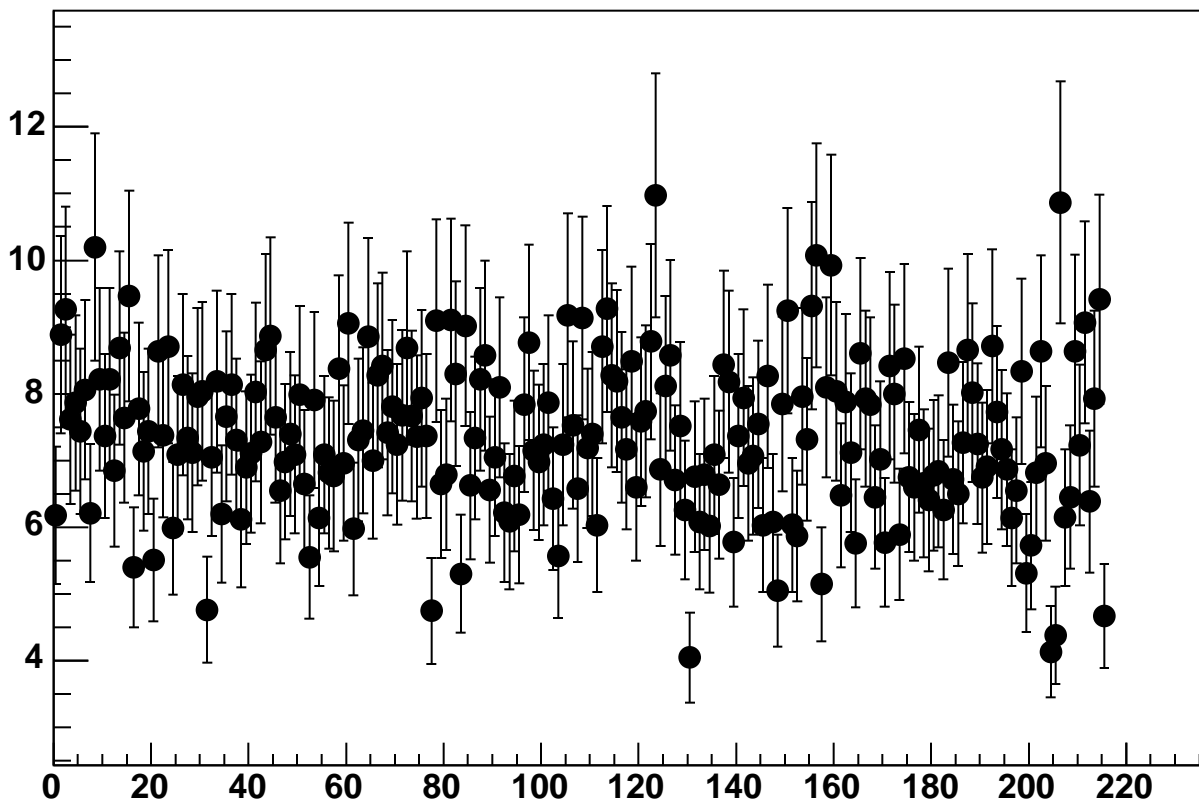
Enable 2, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



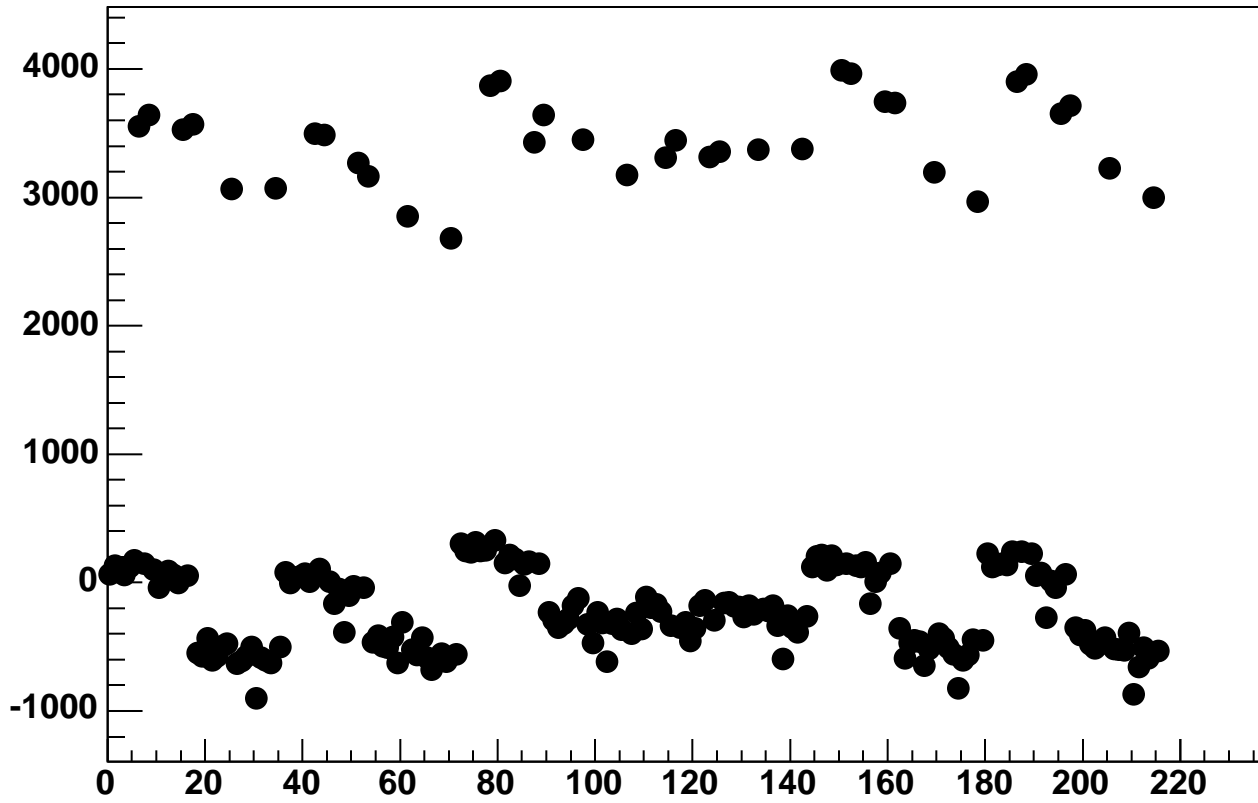
Enable 2, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



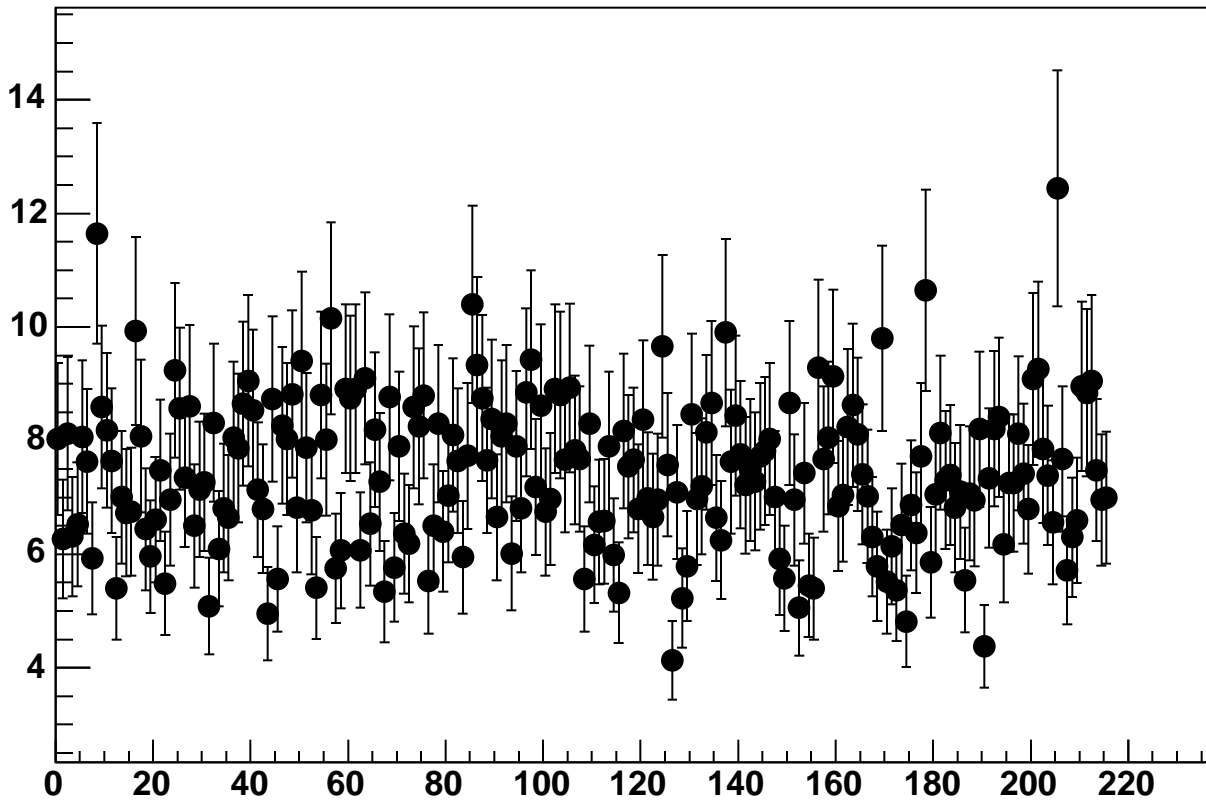
Enable 2, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



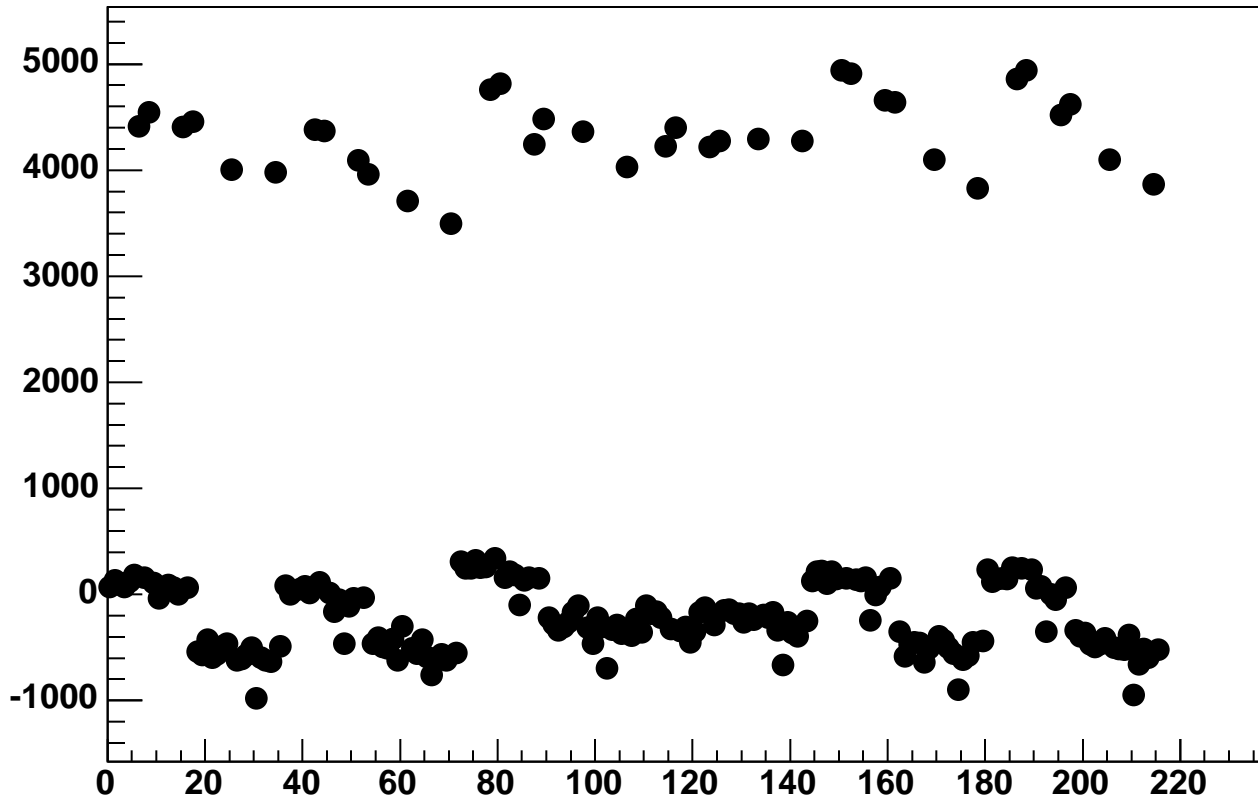
Enable 2, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



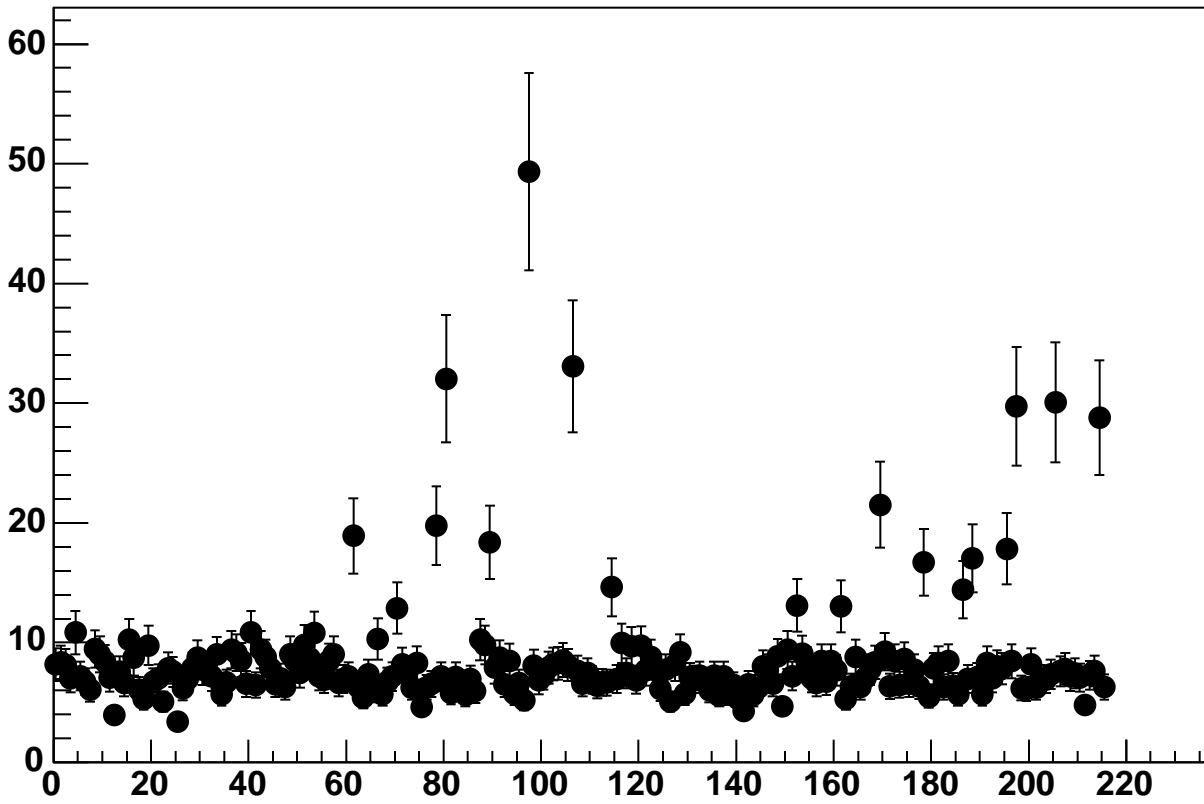
Enable 2, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



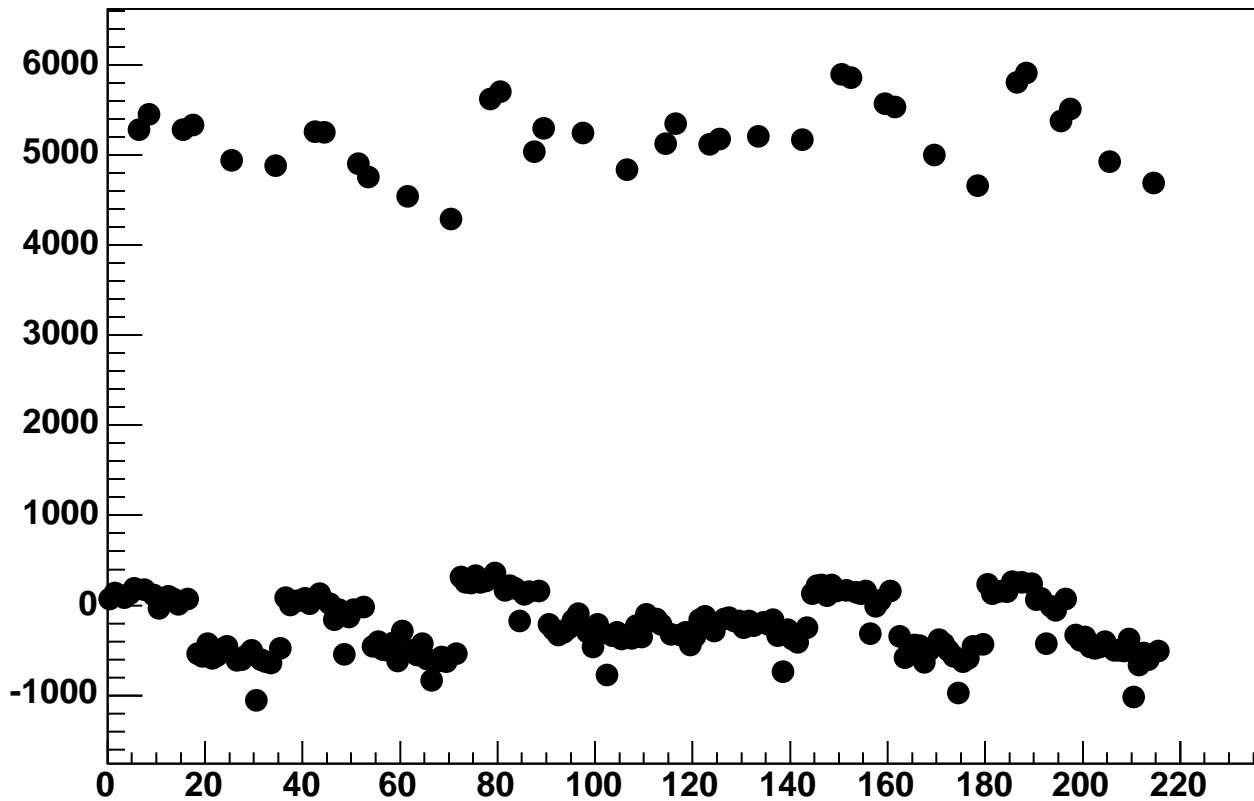
Enable 2, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



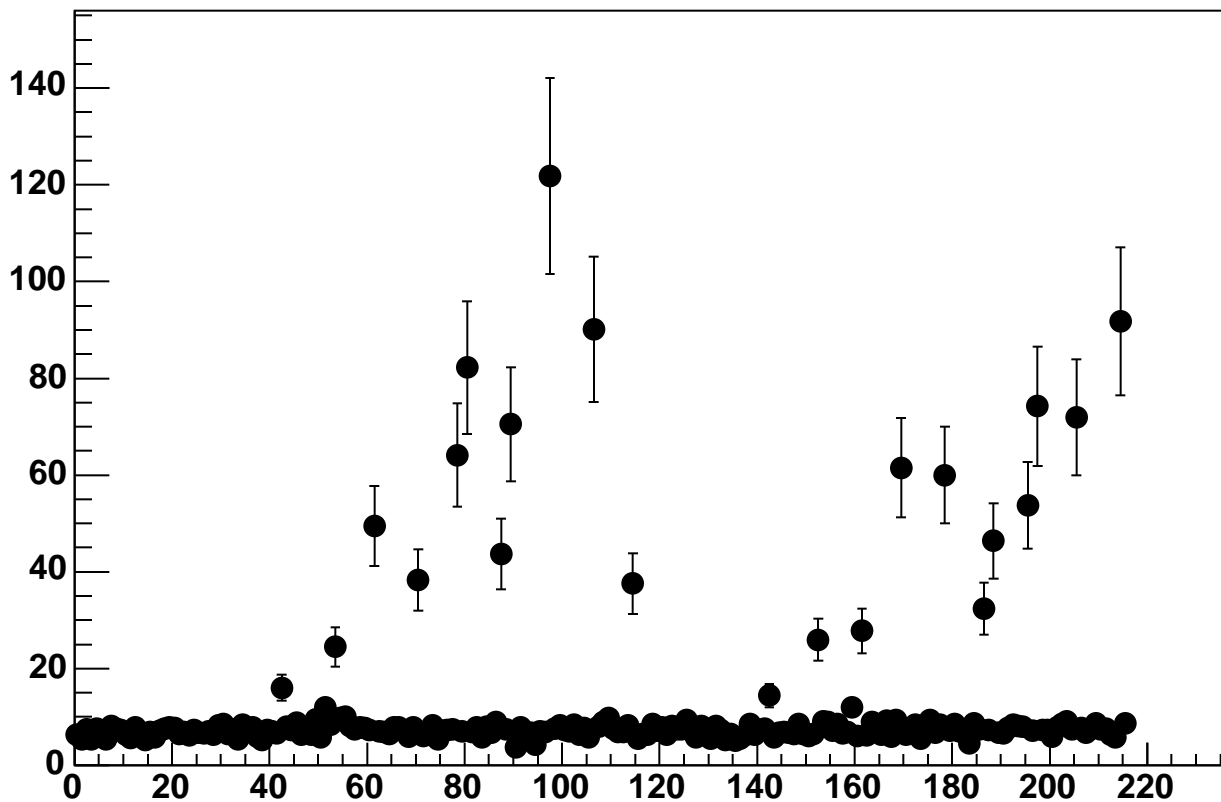
Enable 2, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



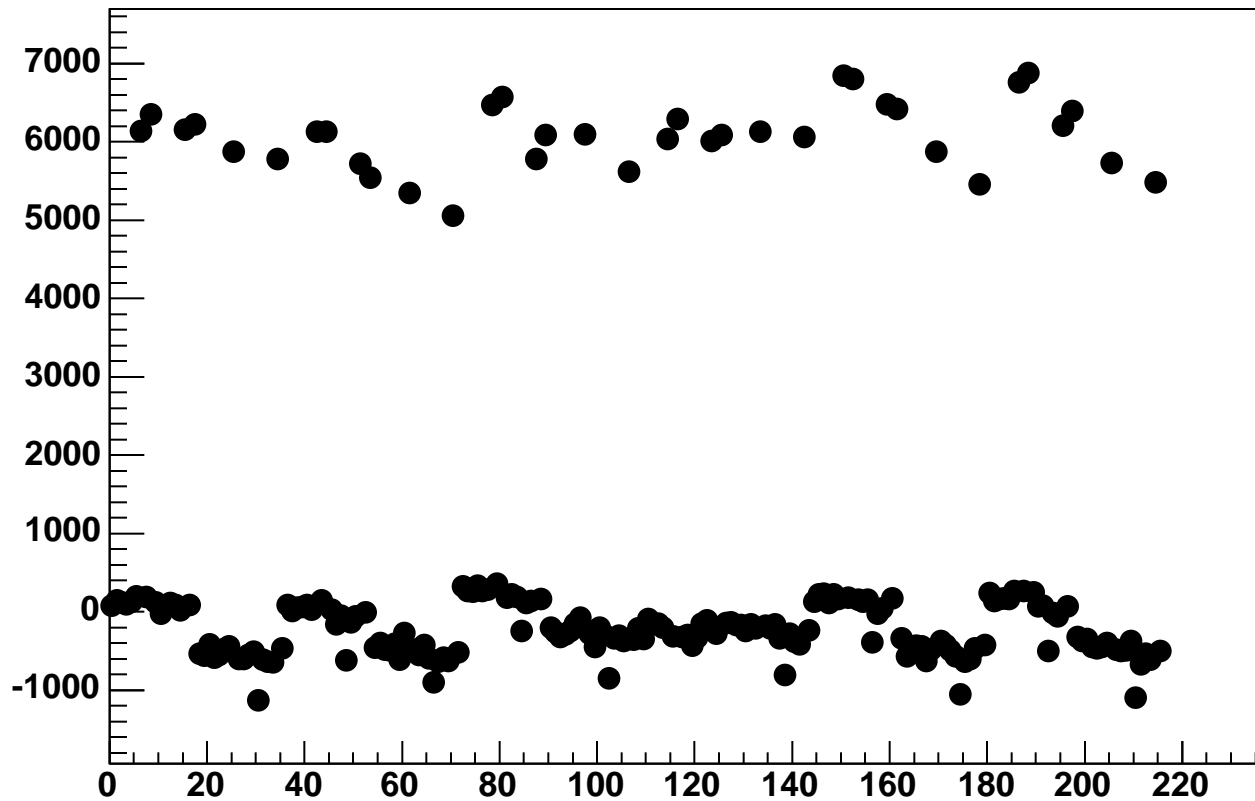
Enable 2, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



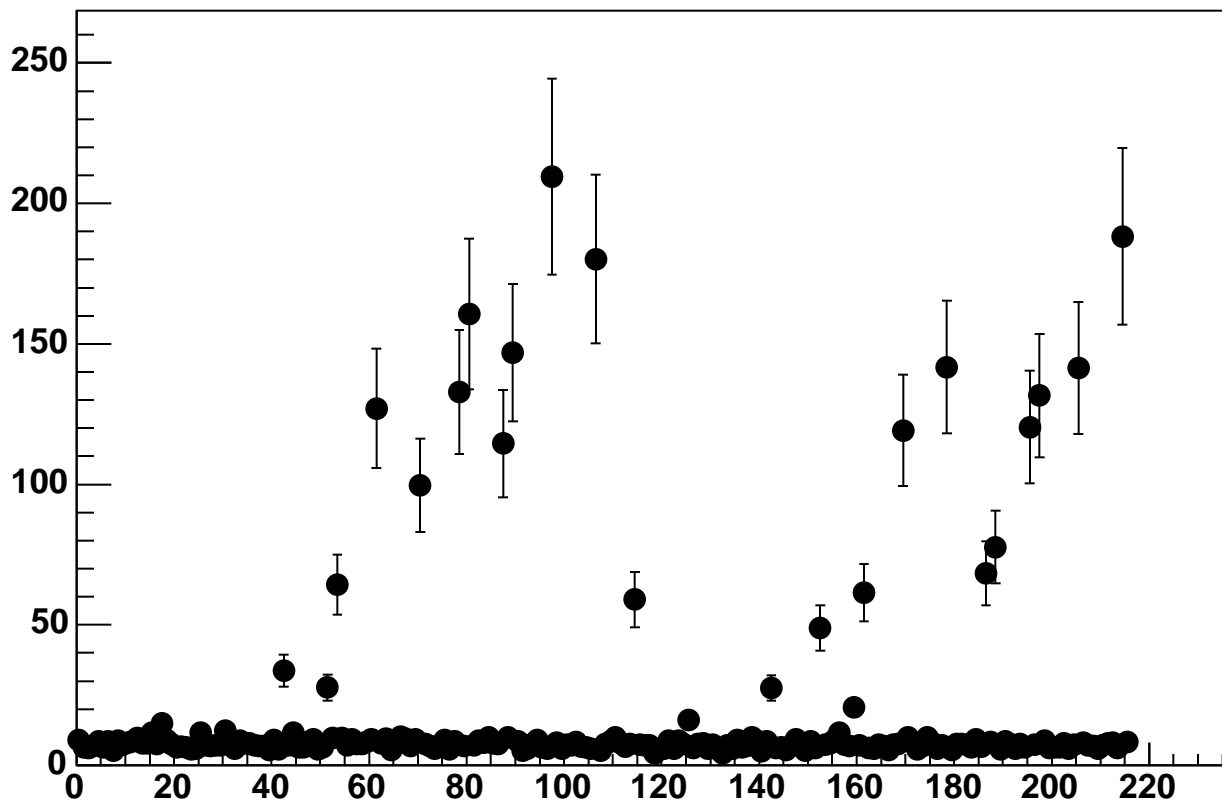
Enable 2, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



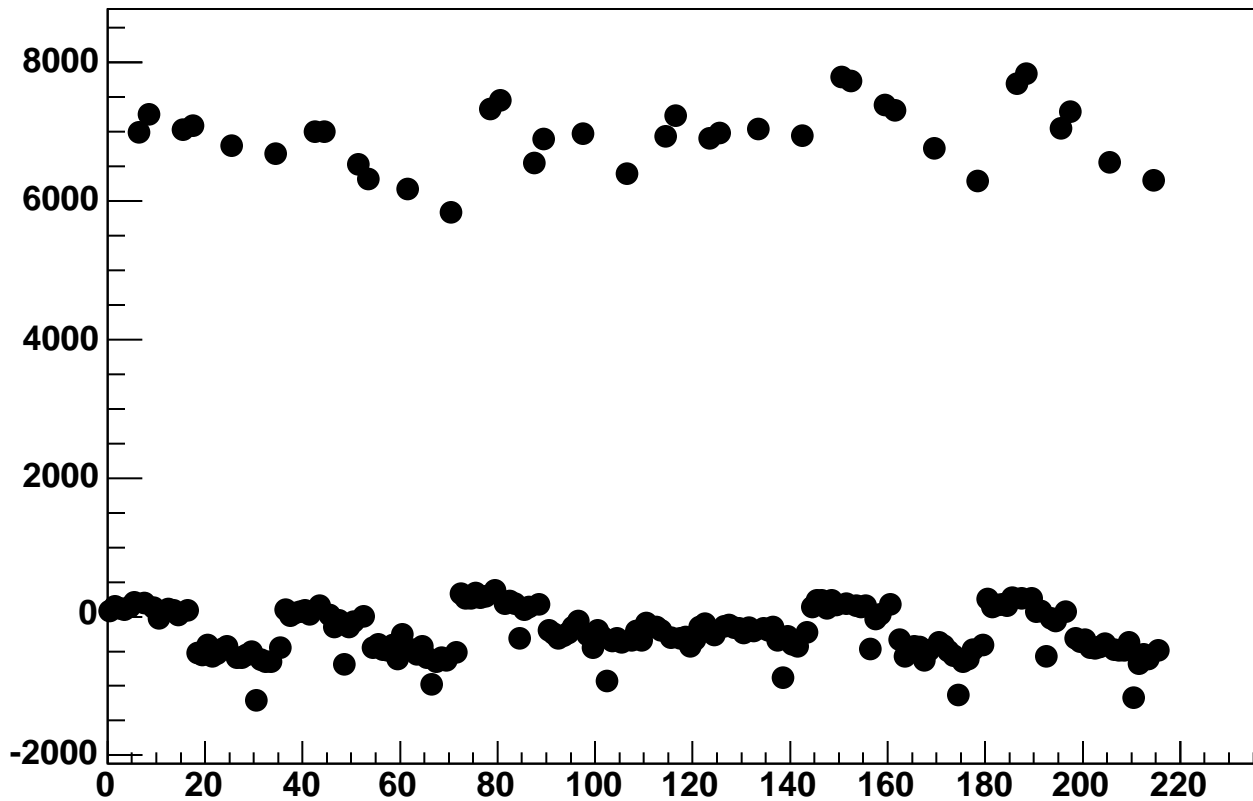
Enable 2, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



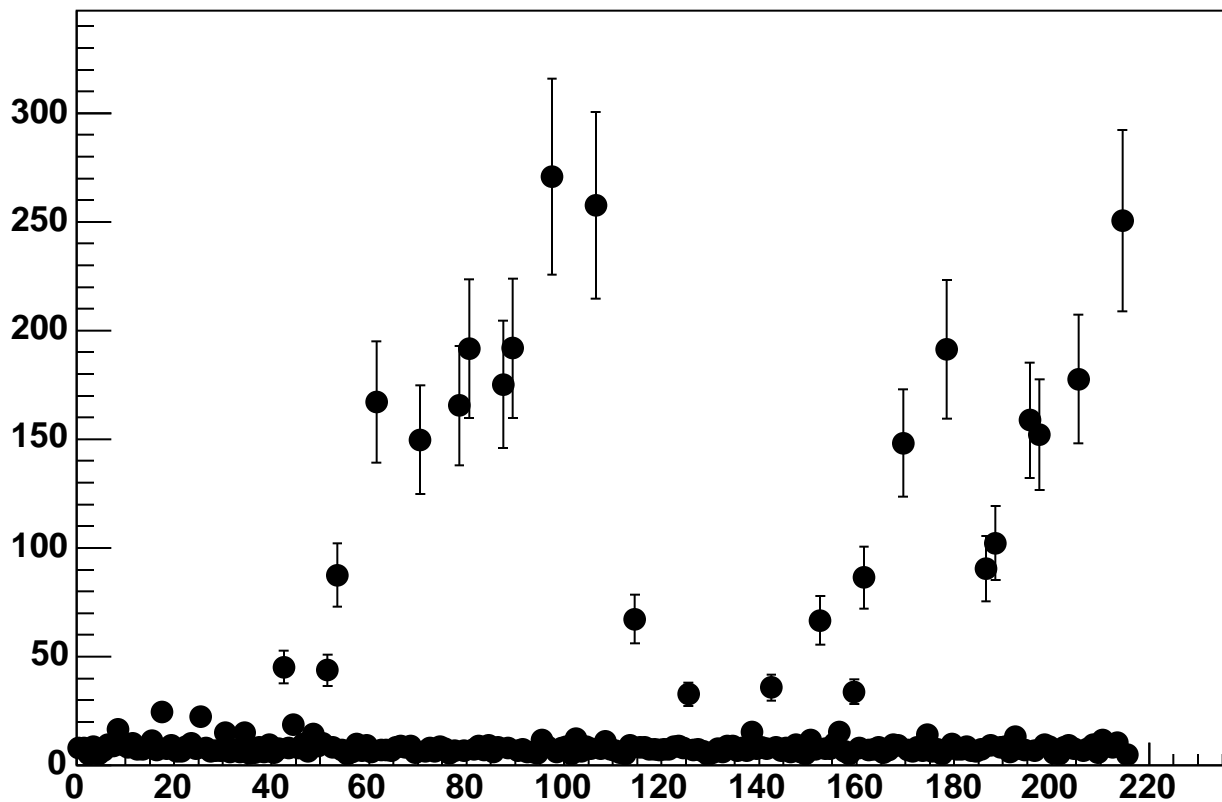
Enable 2, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



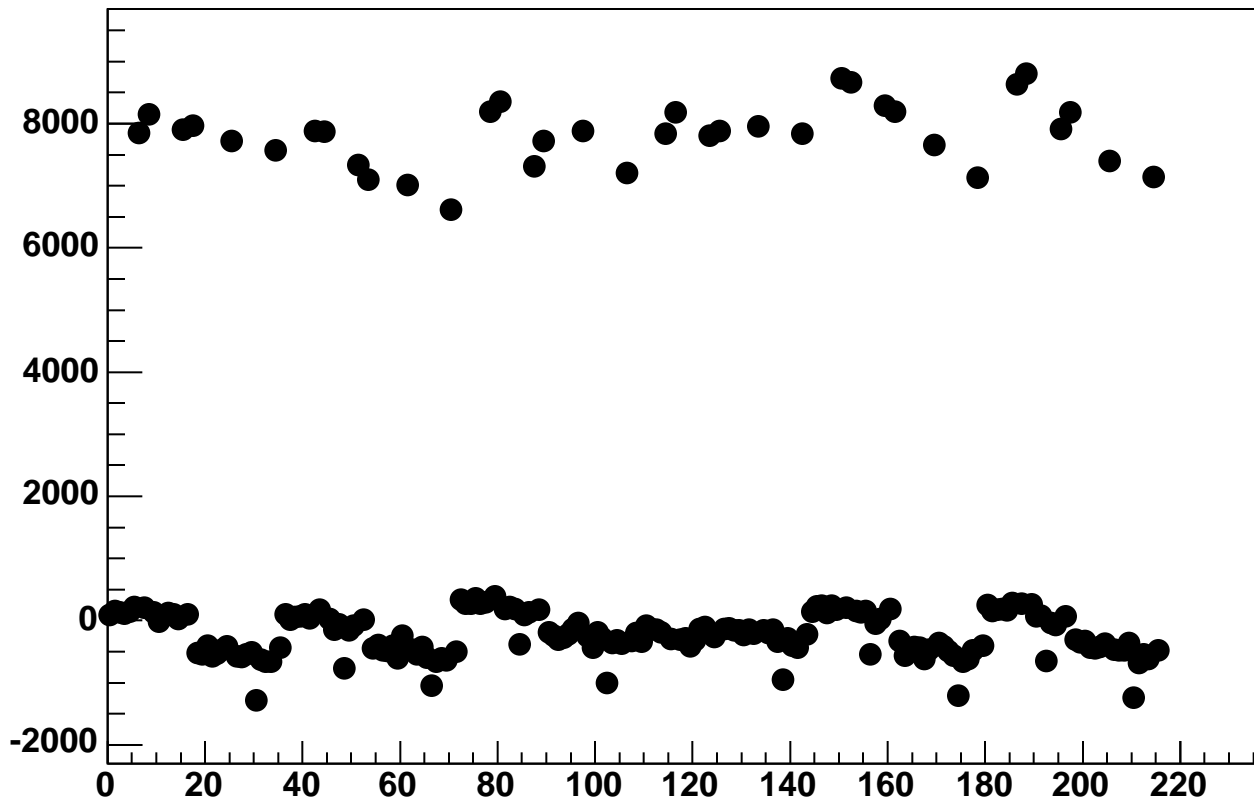
Enable 2, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



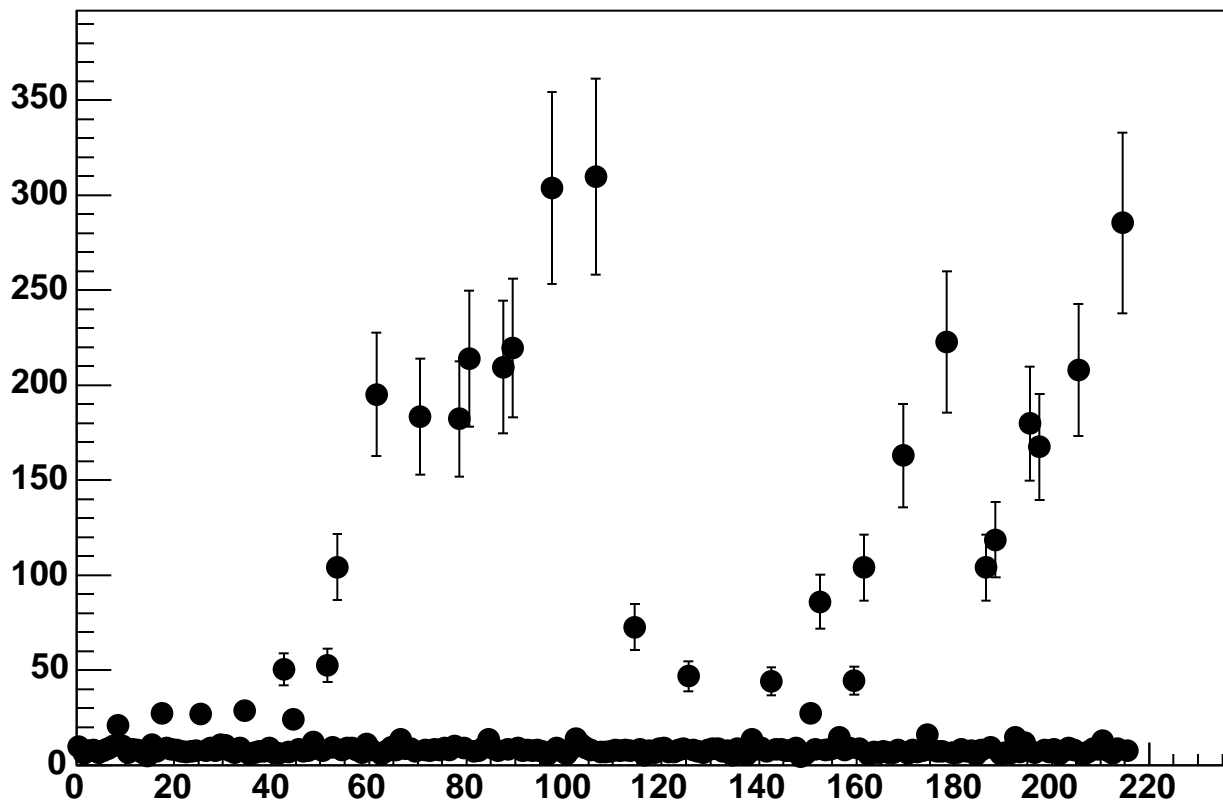
Enable 2, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



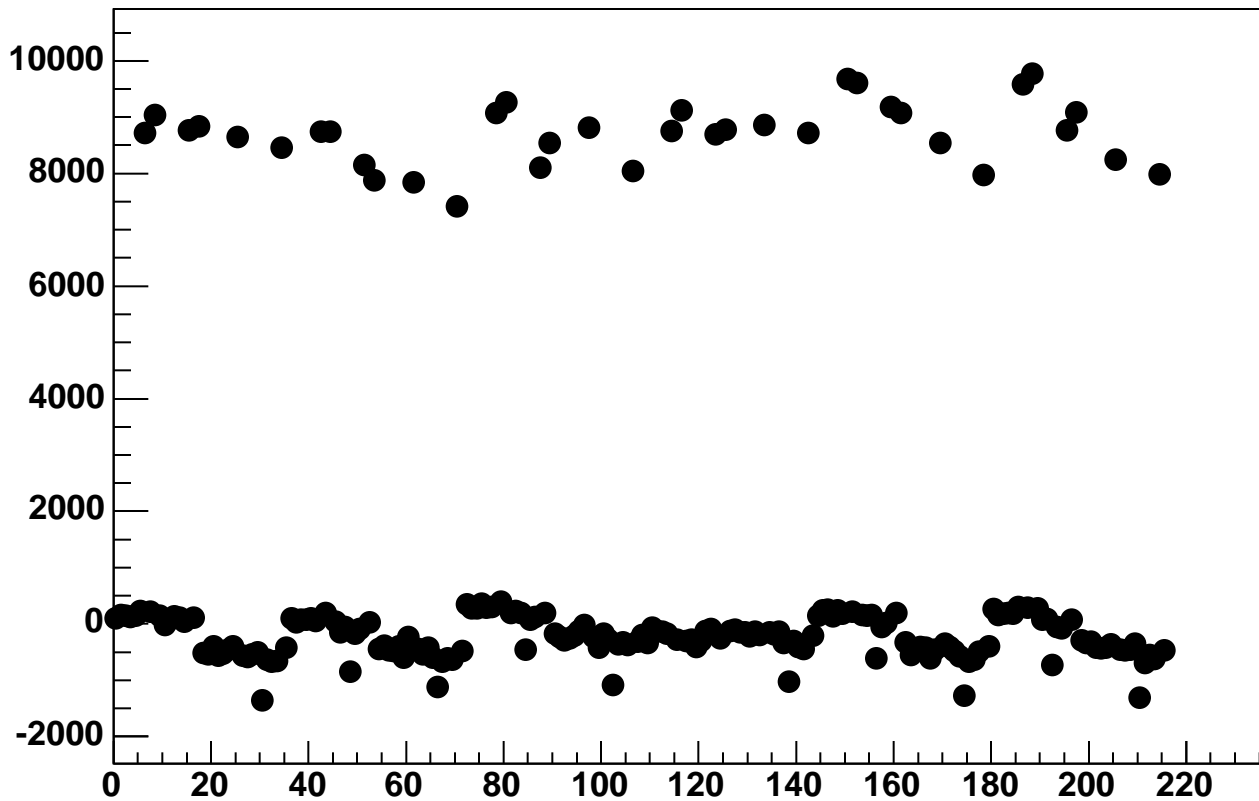
Enable 2, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



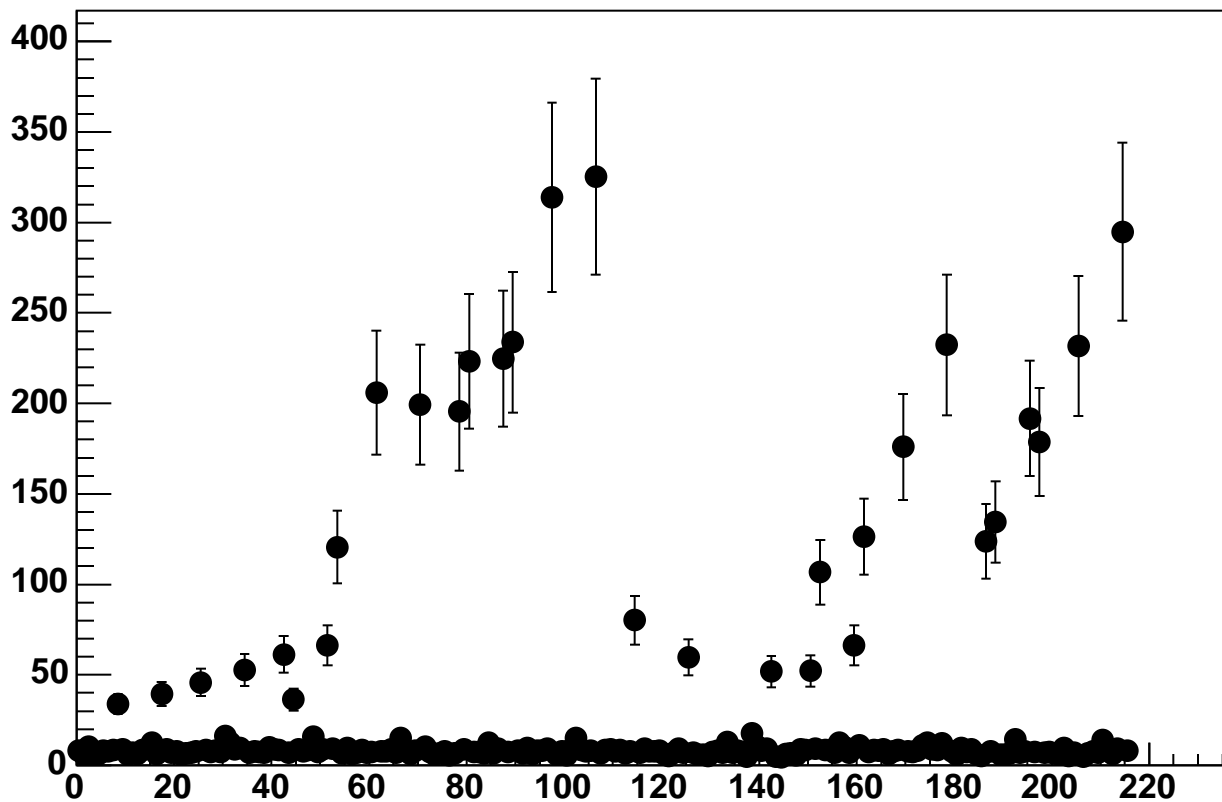
Enable 2, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



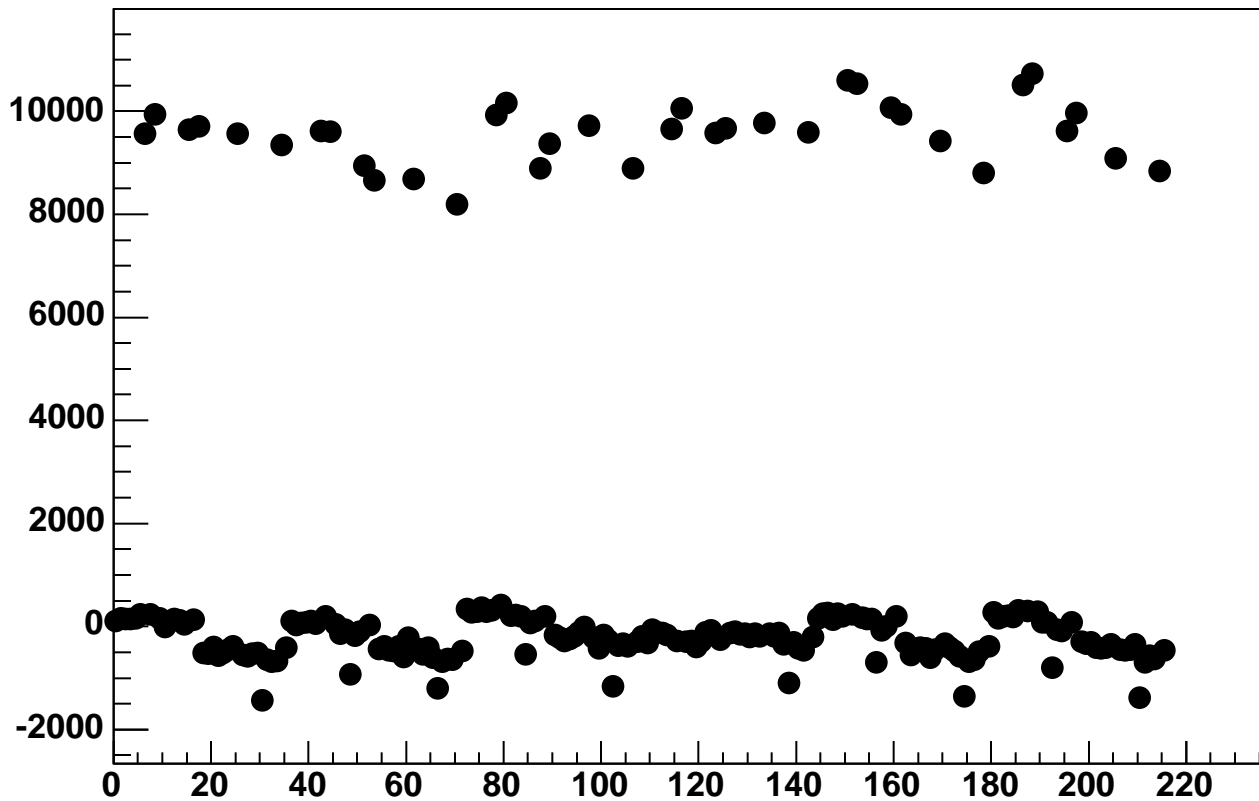
Enable 2, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



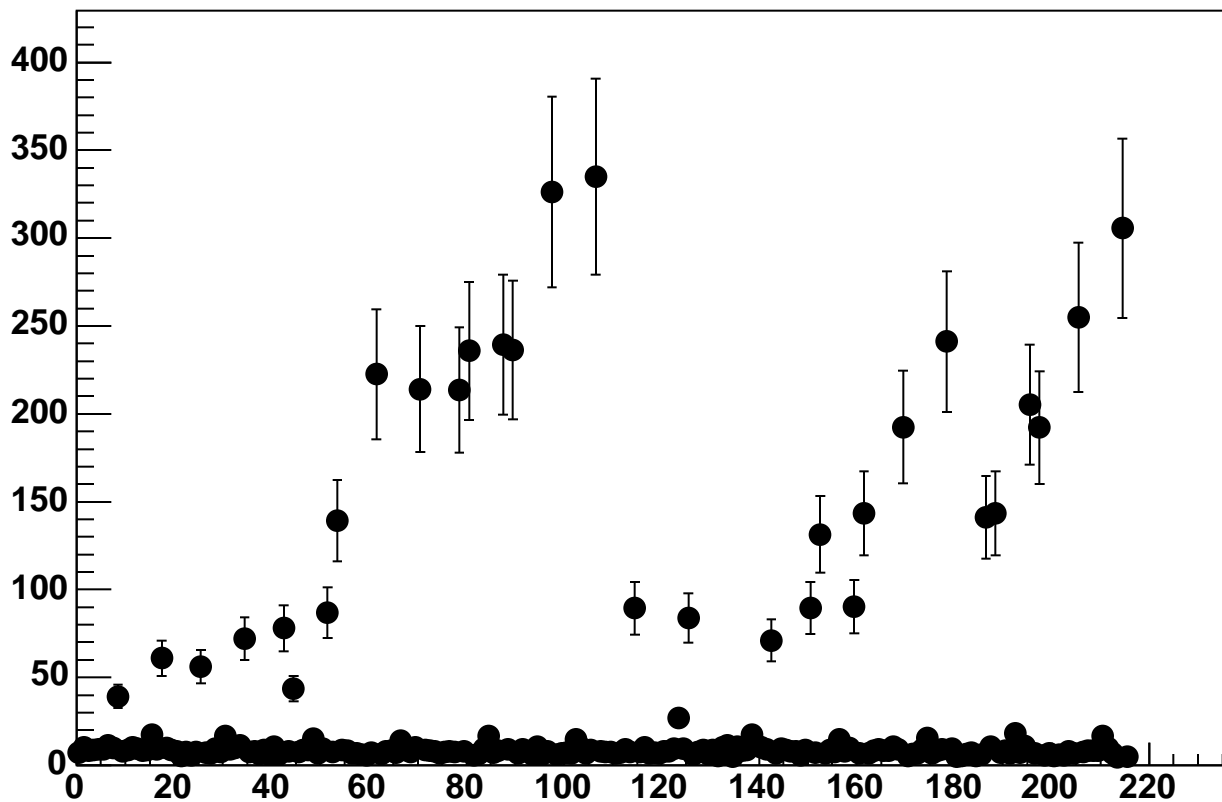
Enable 2, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



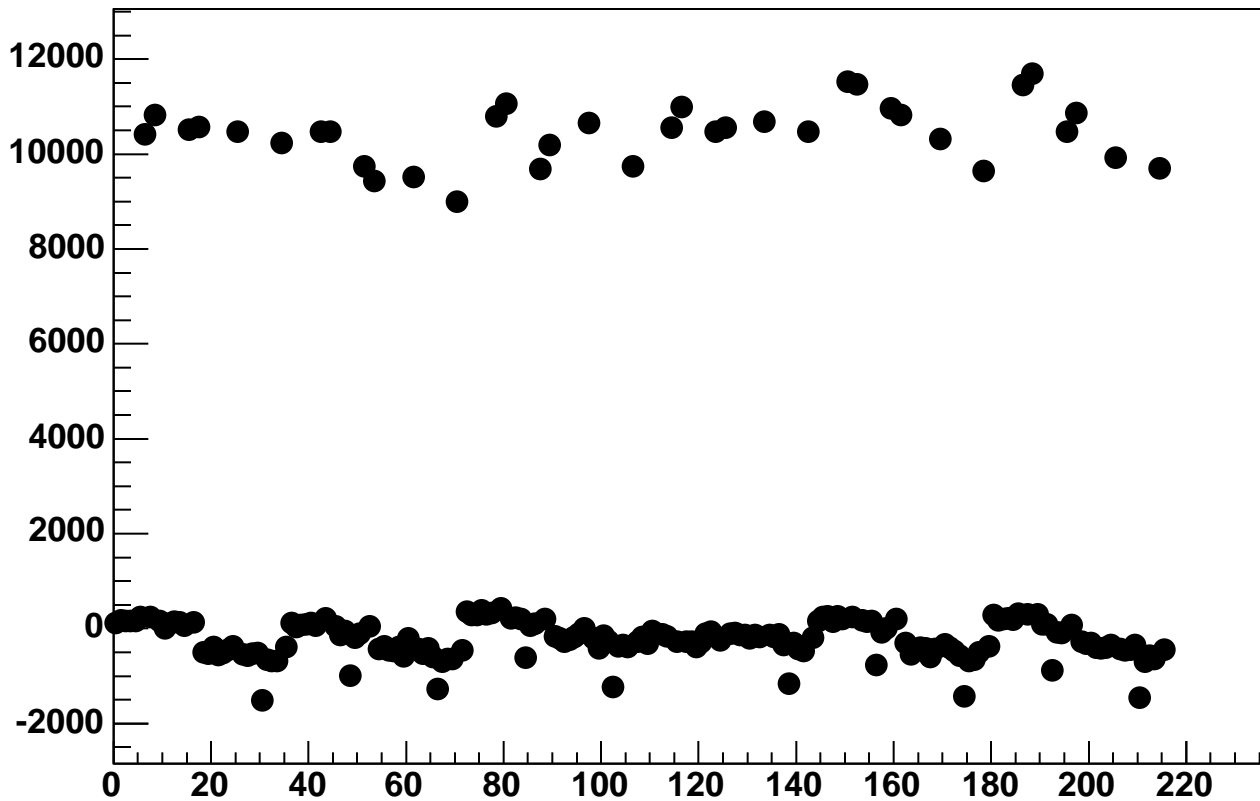
Enable 2, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



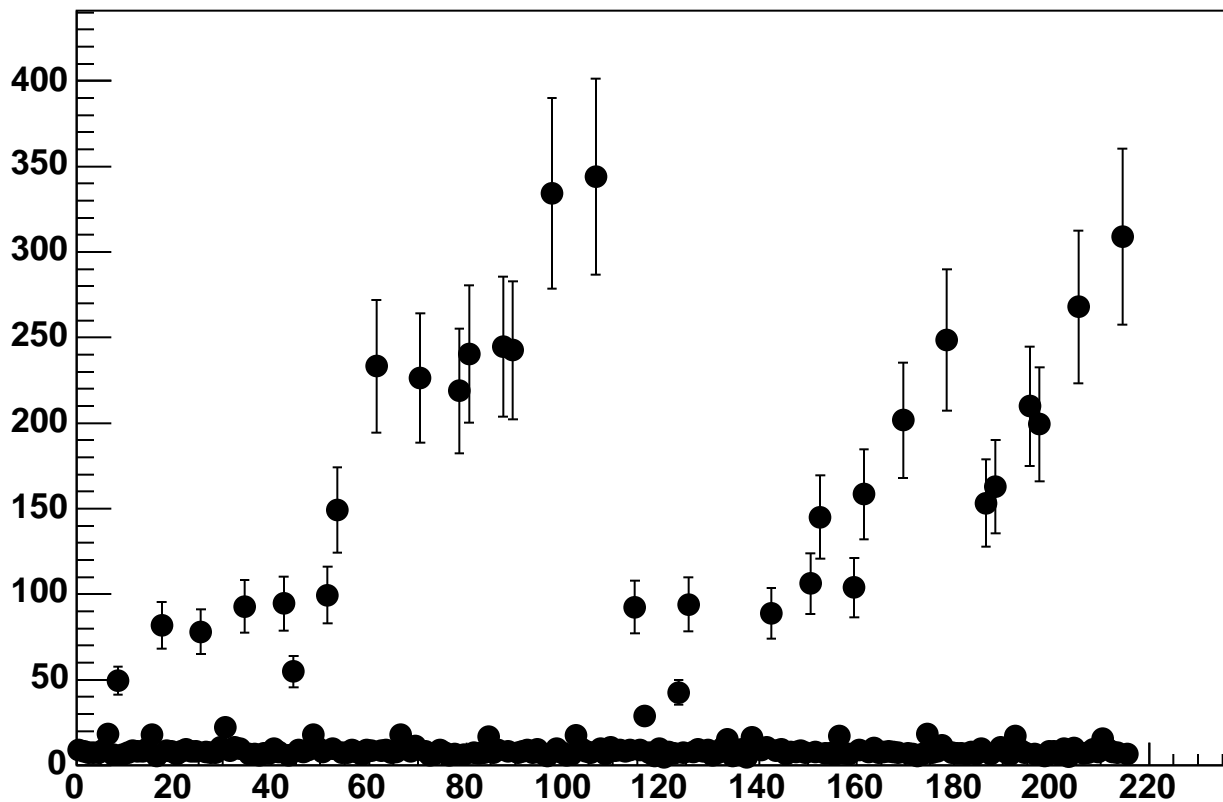
Enable 2, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



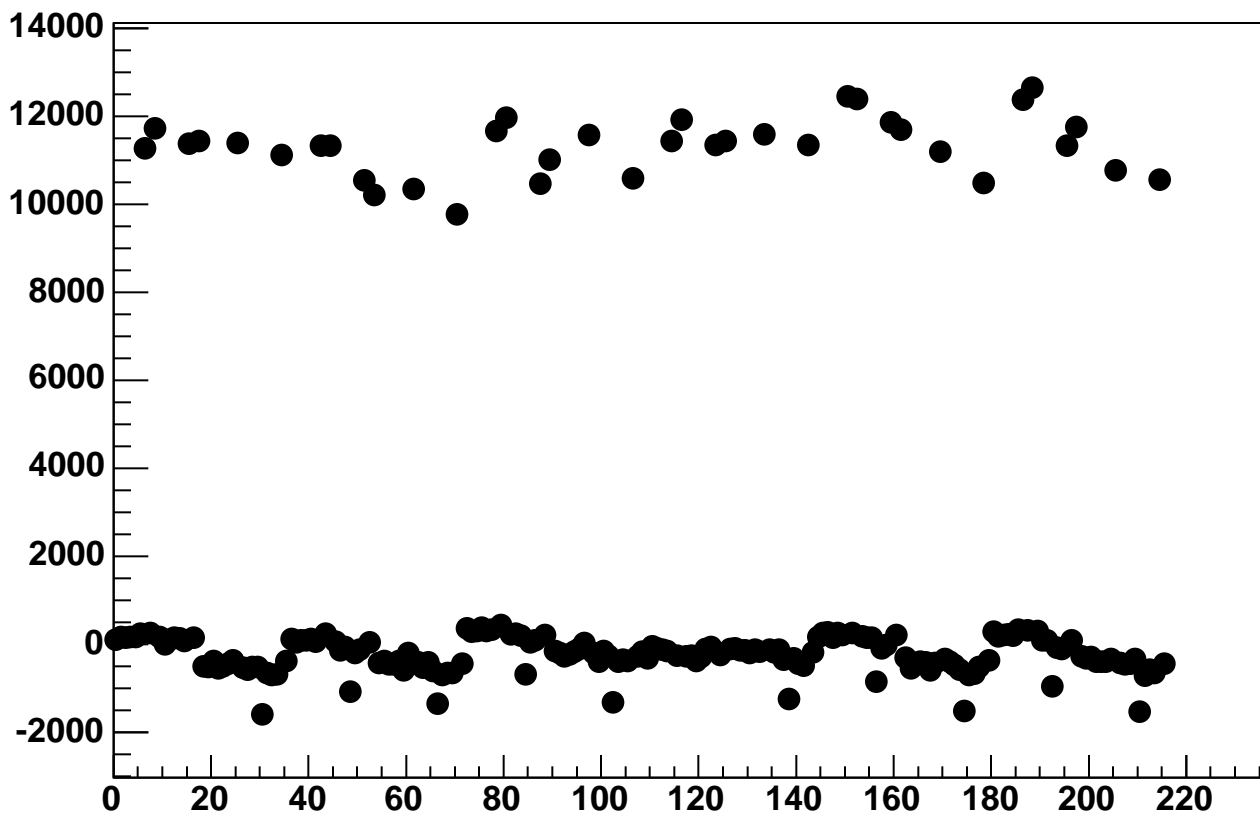
Enable 2, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



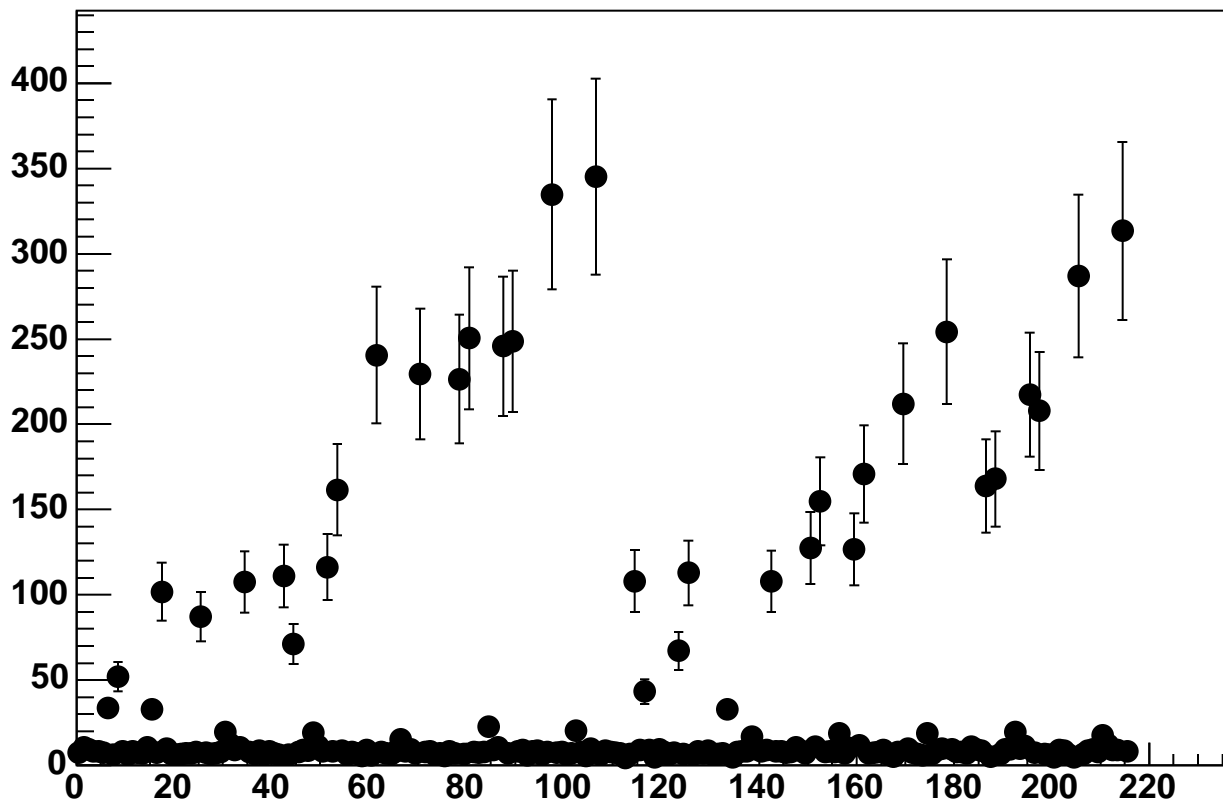
Enable 2, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



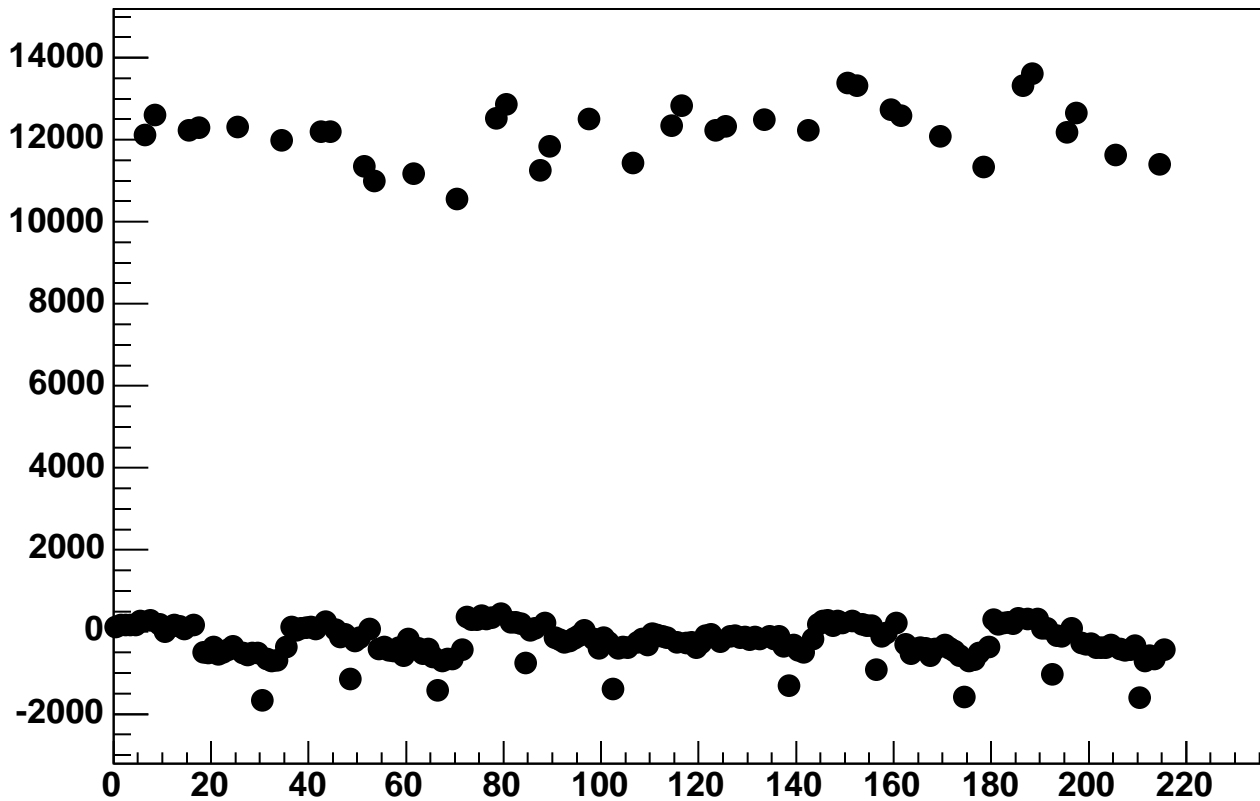
Enable 2, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



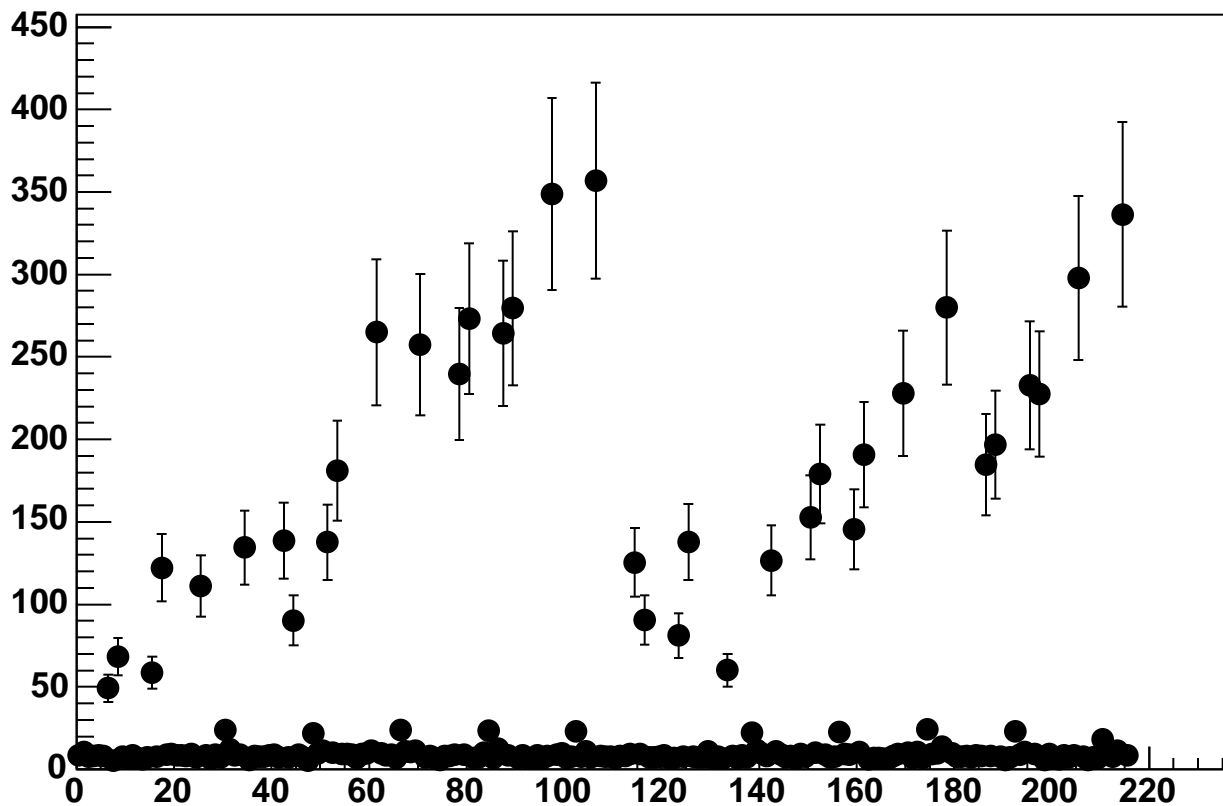
Enable 2, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



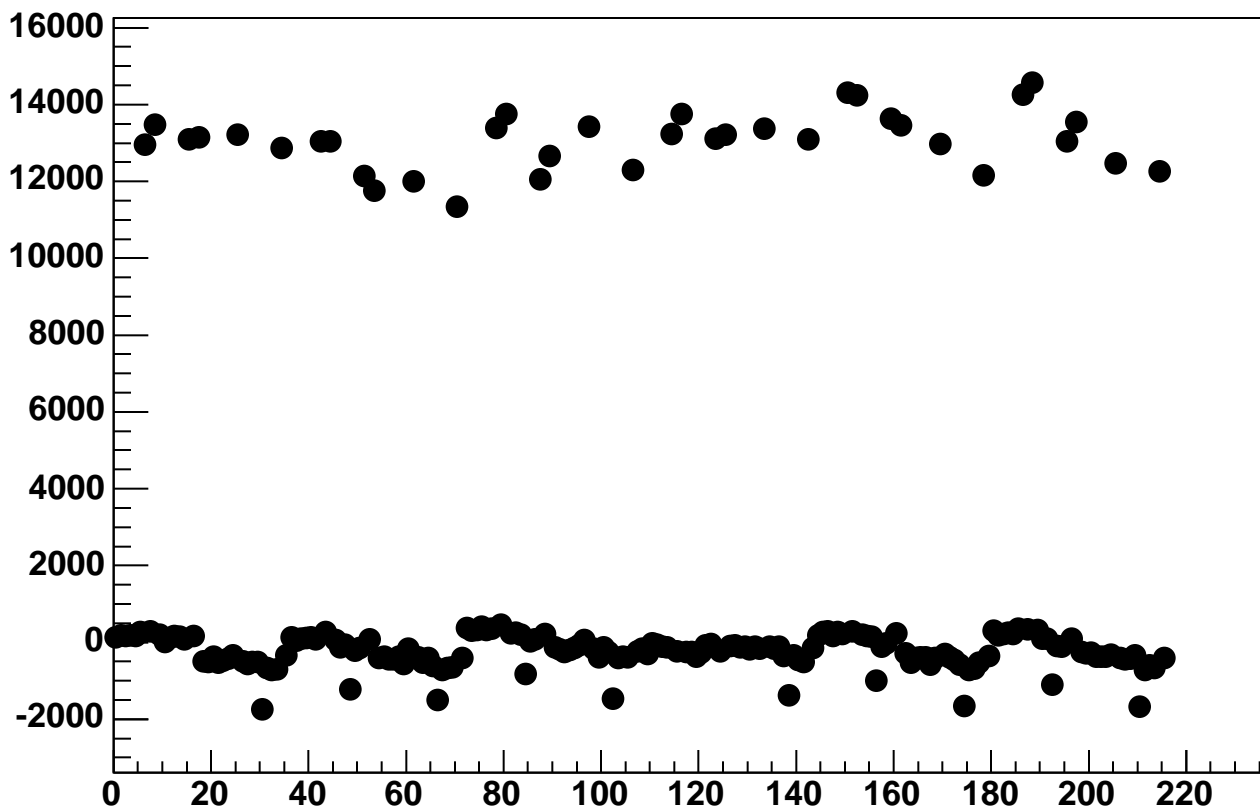
Enable 2, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



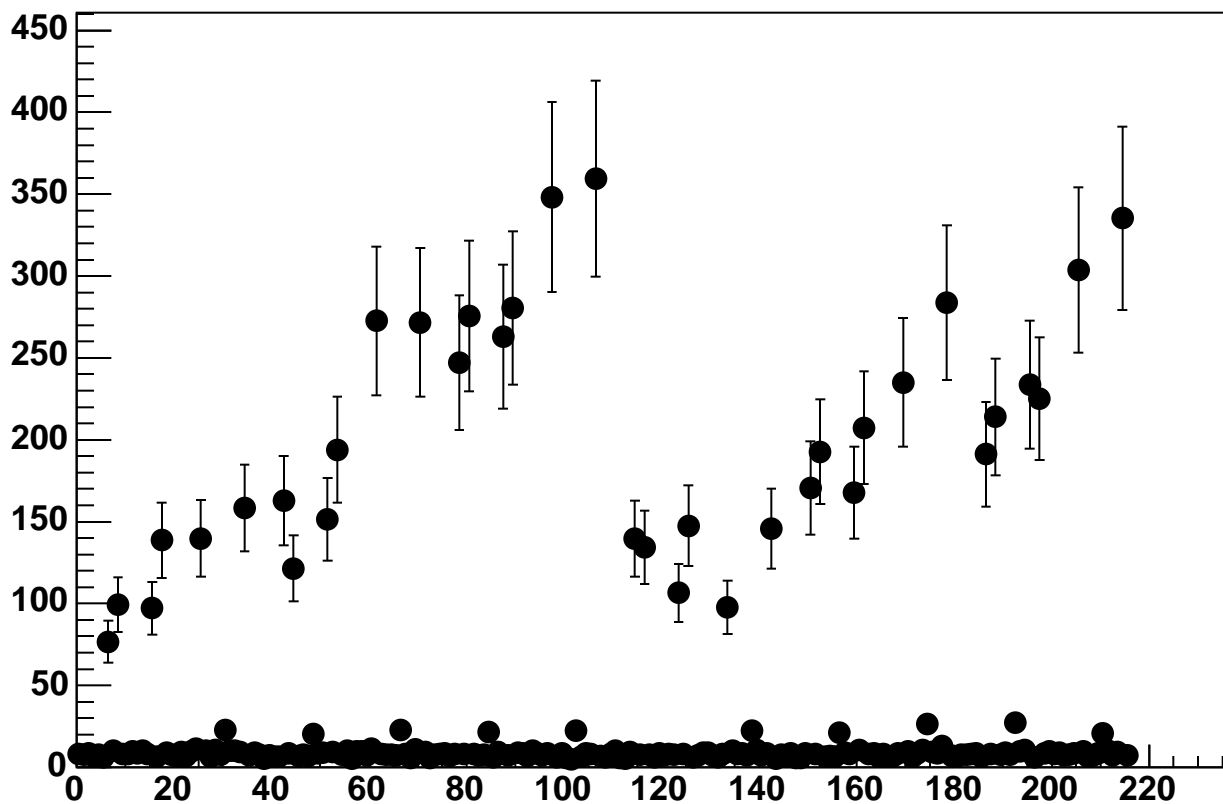
Enable 2, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



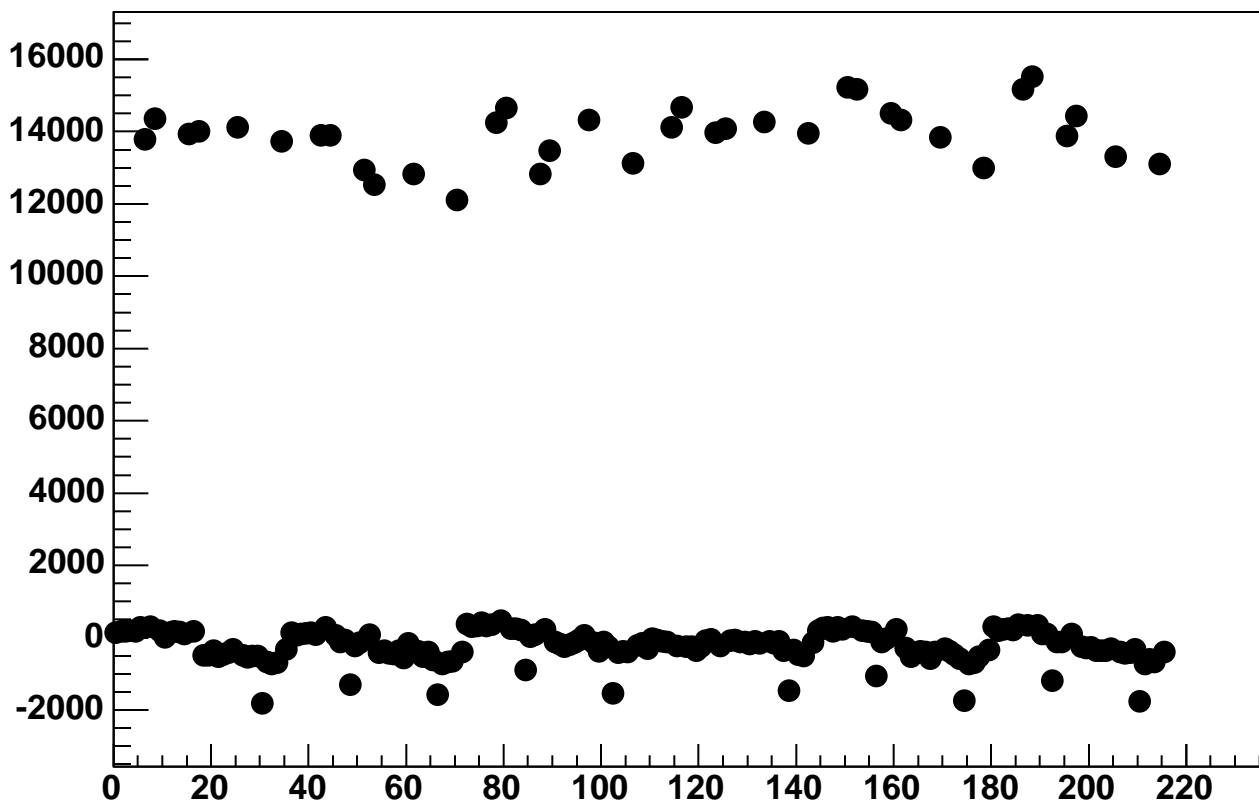
Enable 2, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



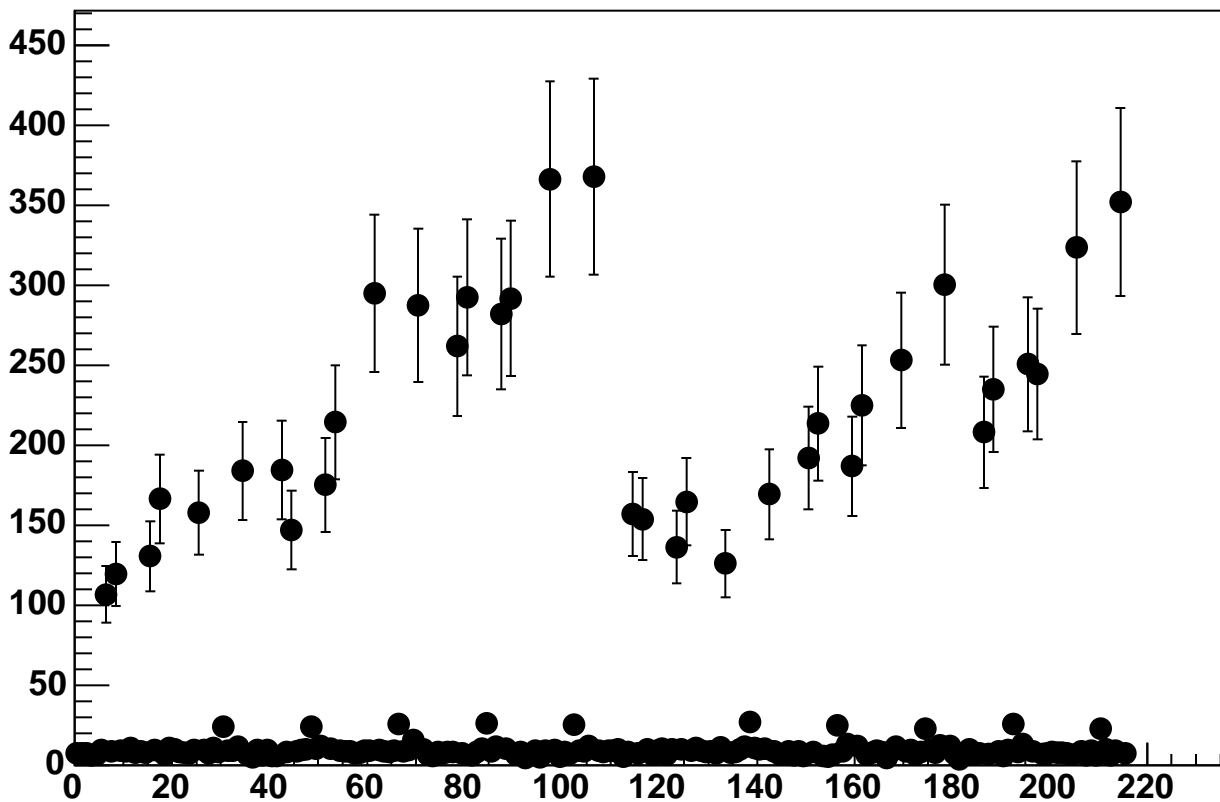
Enable 2, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



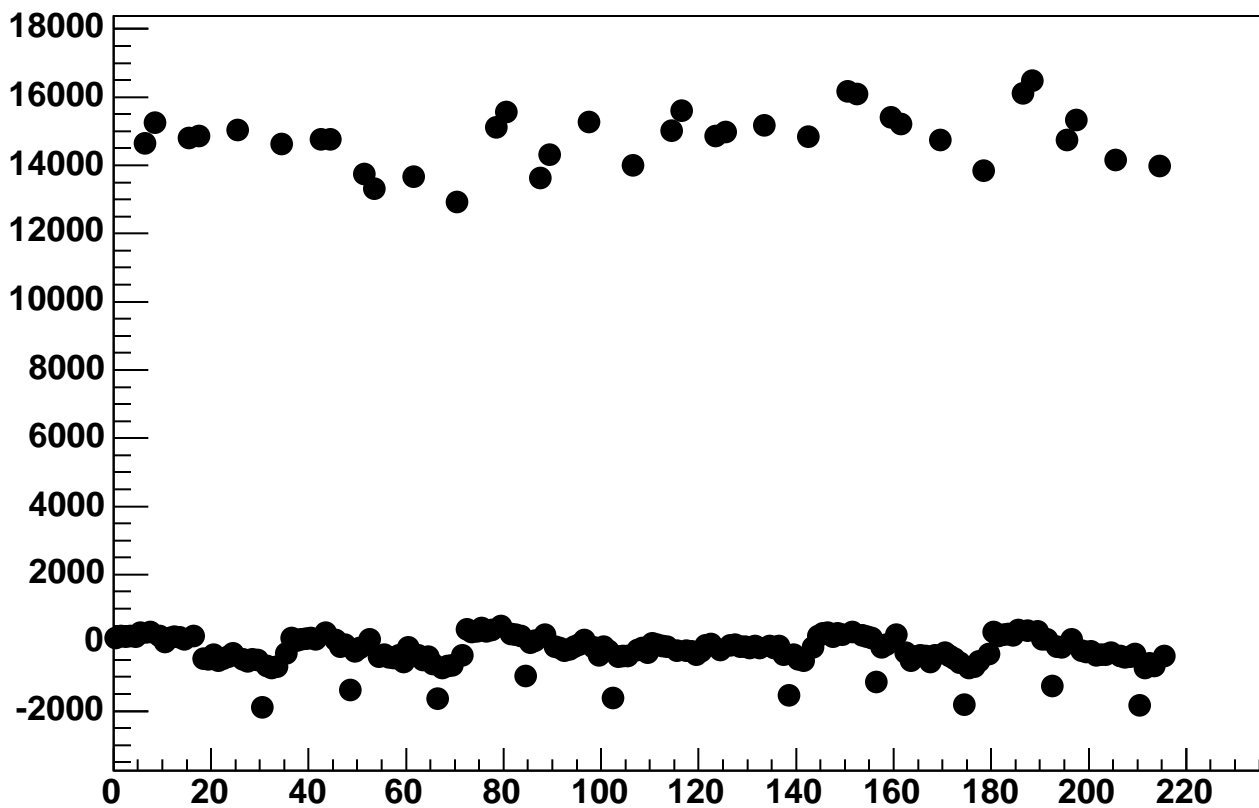
Enable 2, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



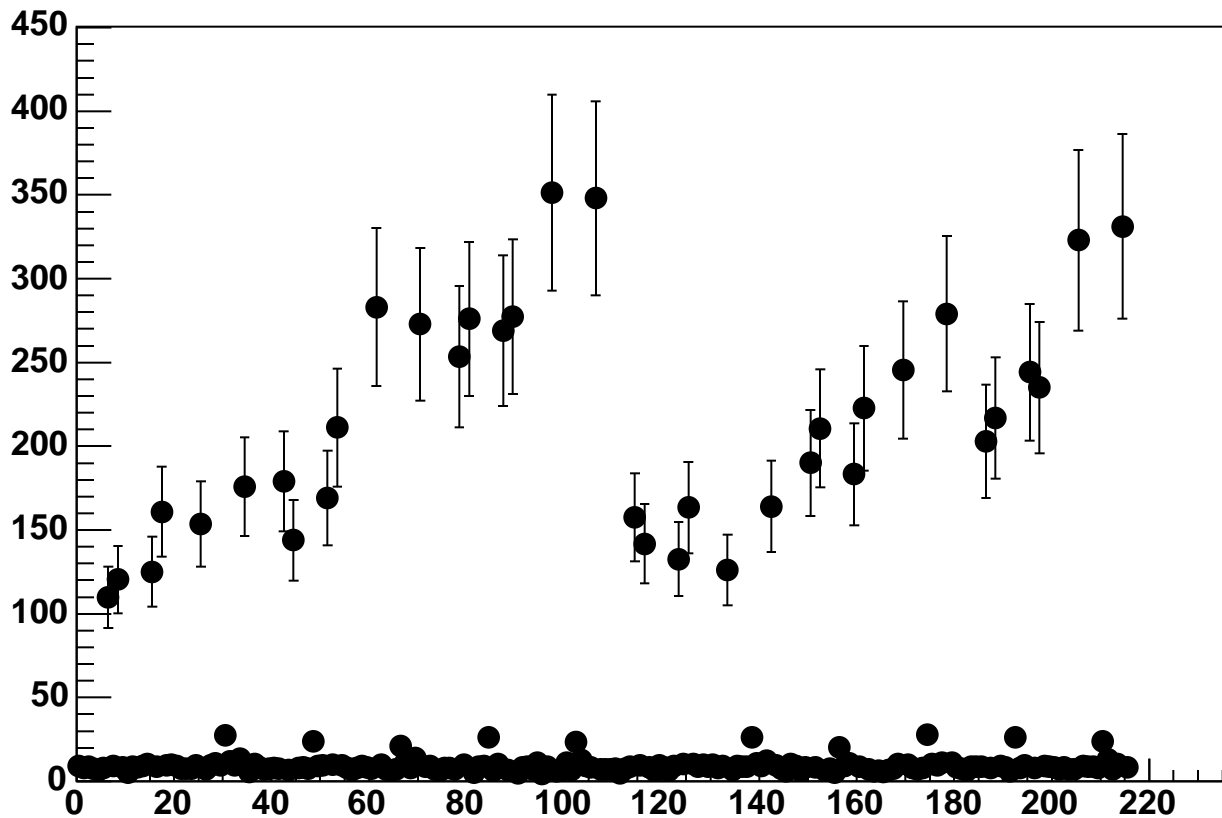
Enable 2, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



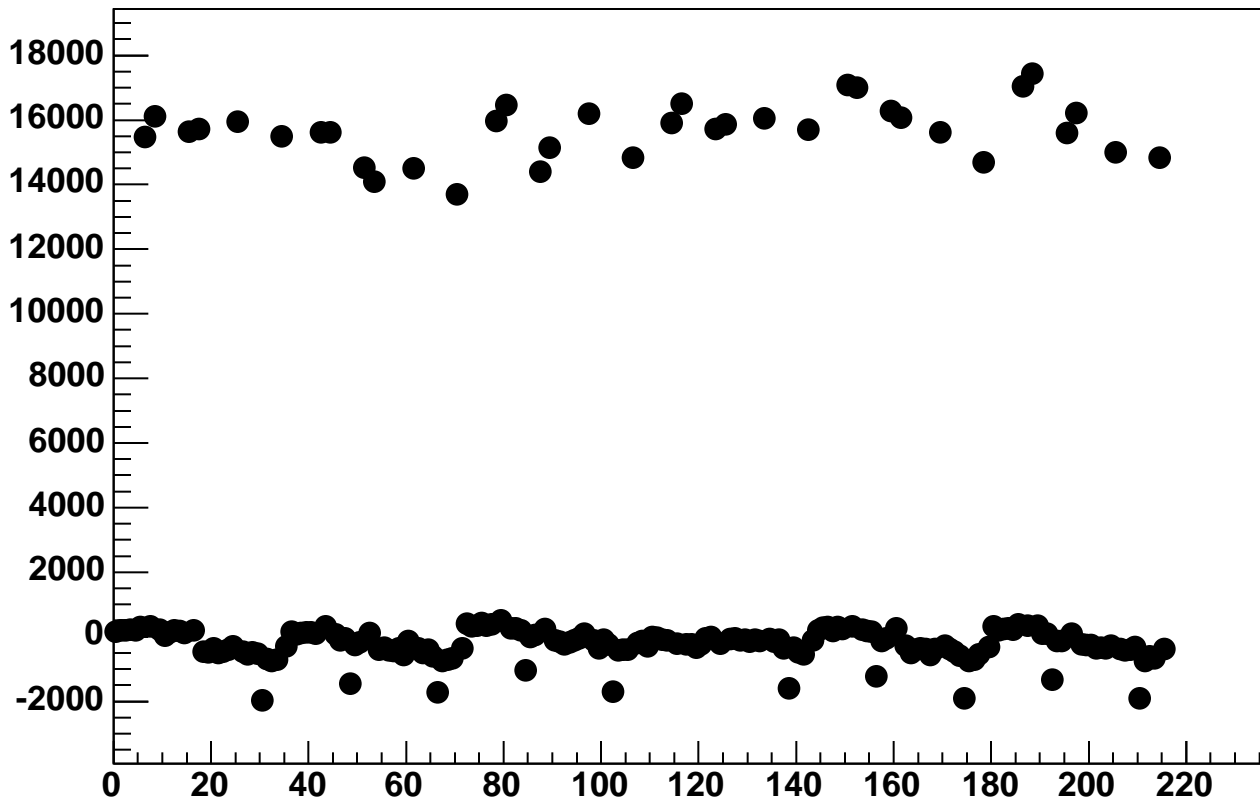
Enable 2, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



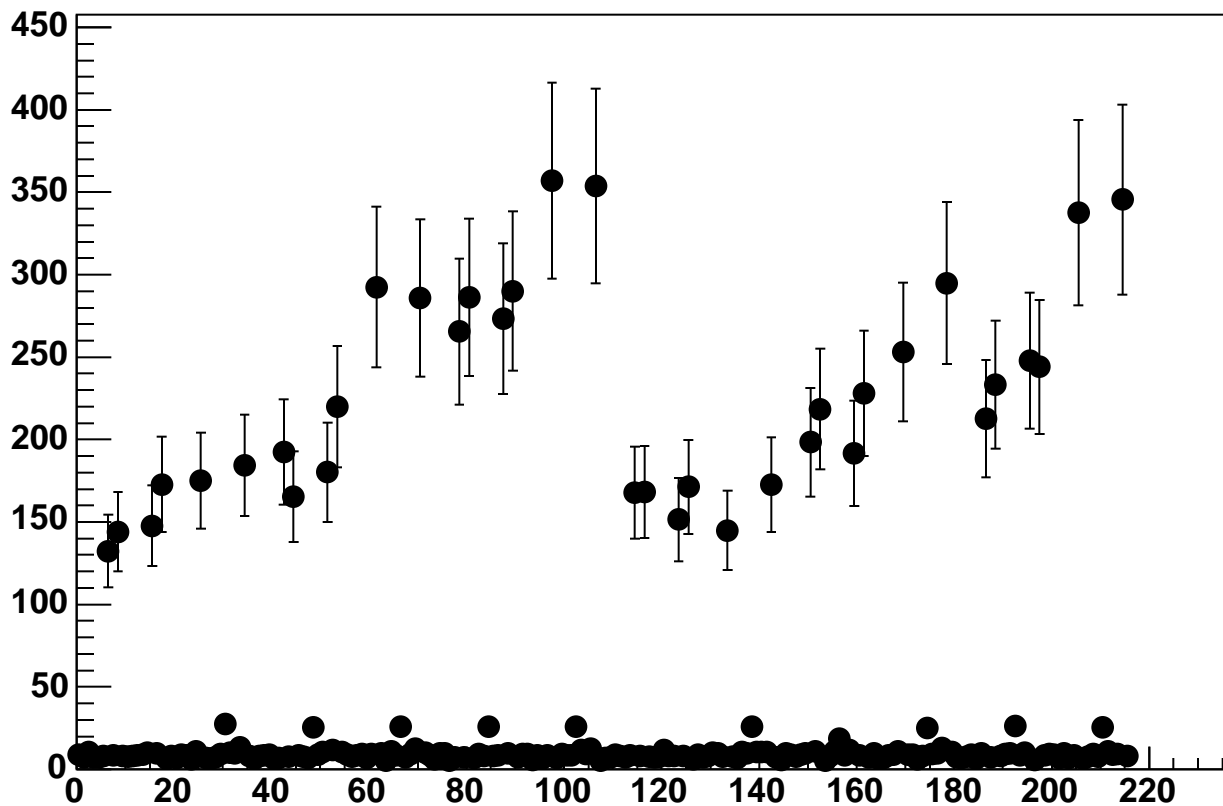
Enable 2, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



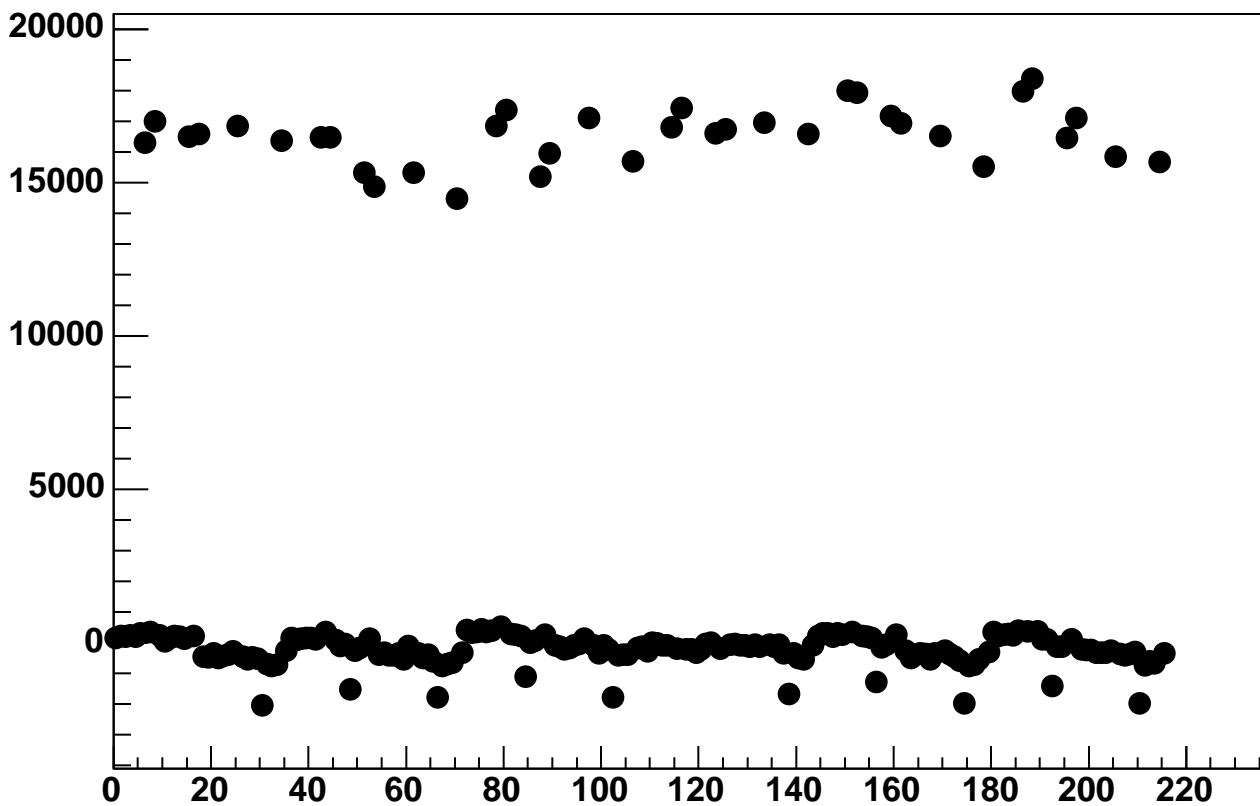
Enable 2, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



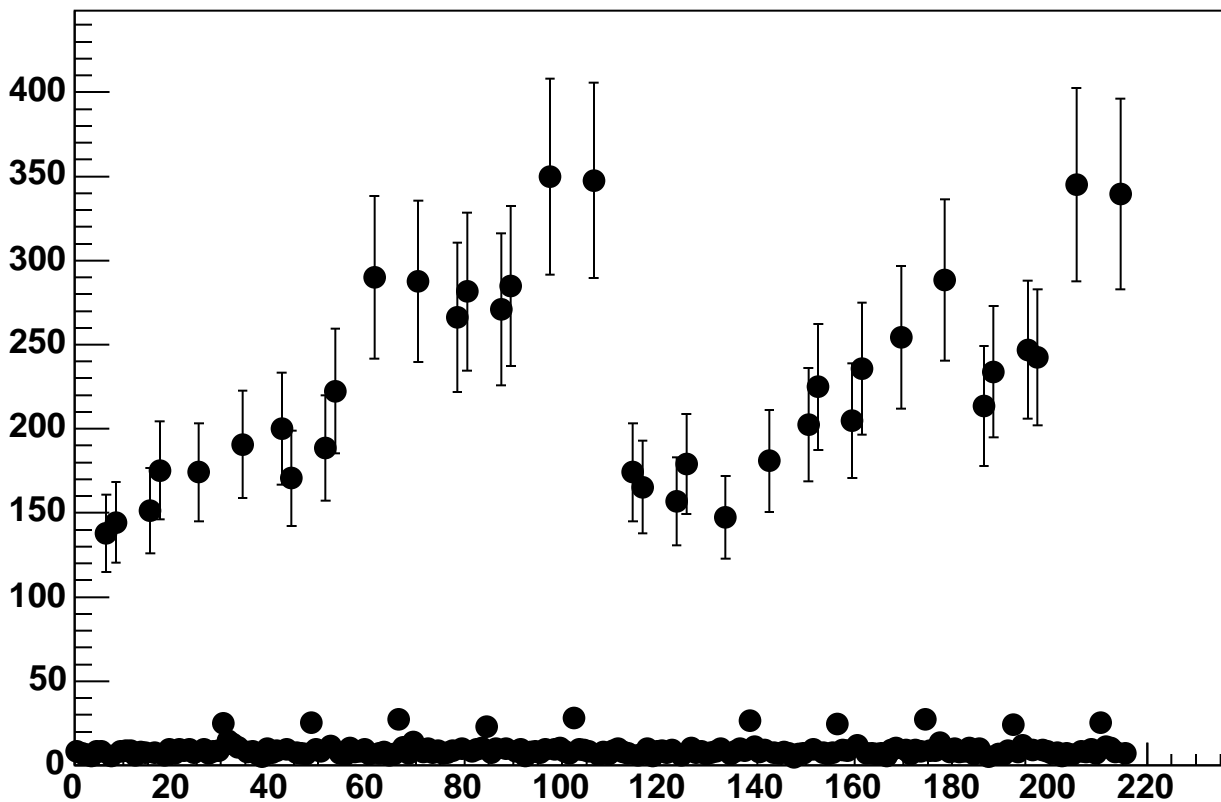
Enable 2, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



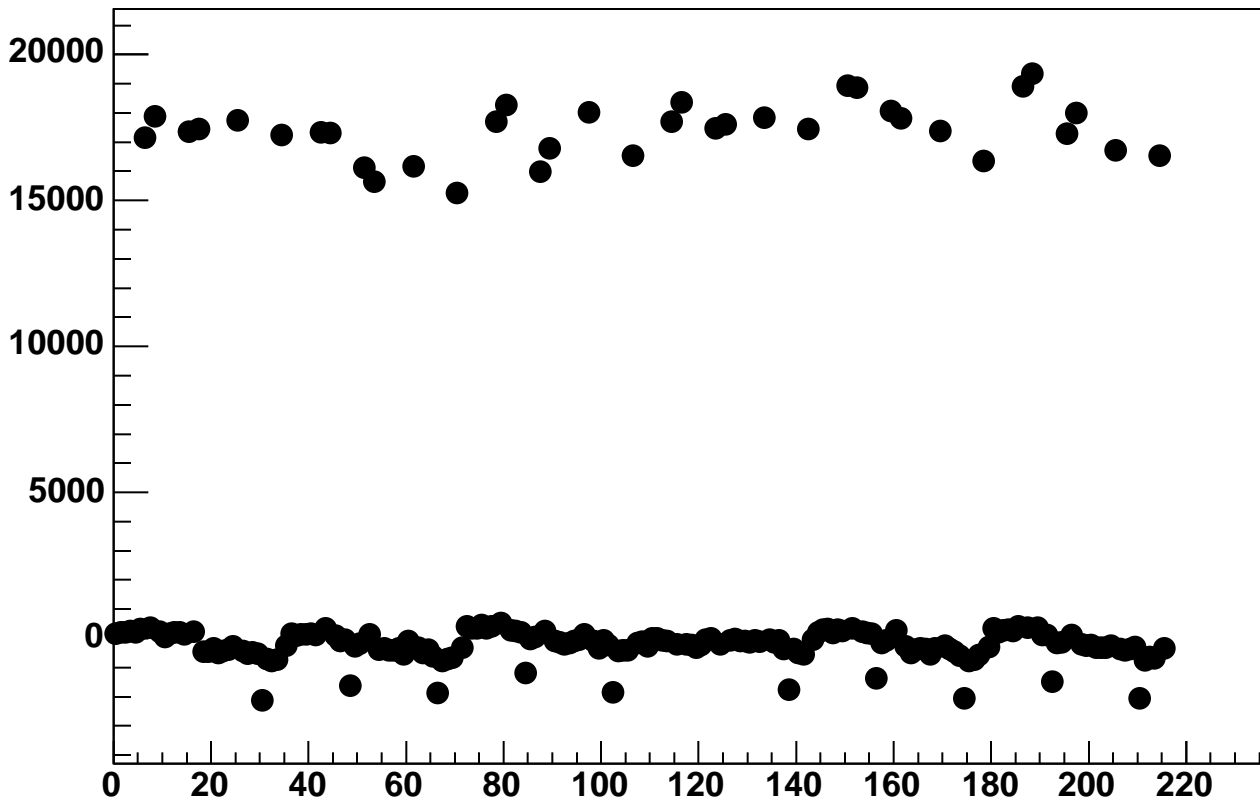
Enable 2, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



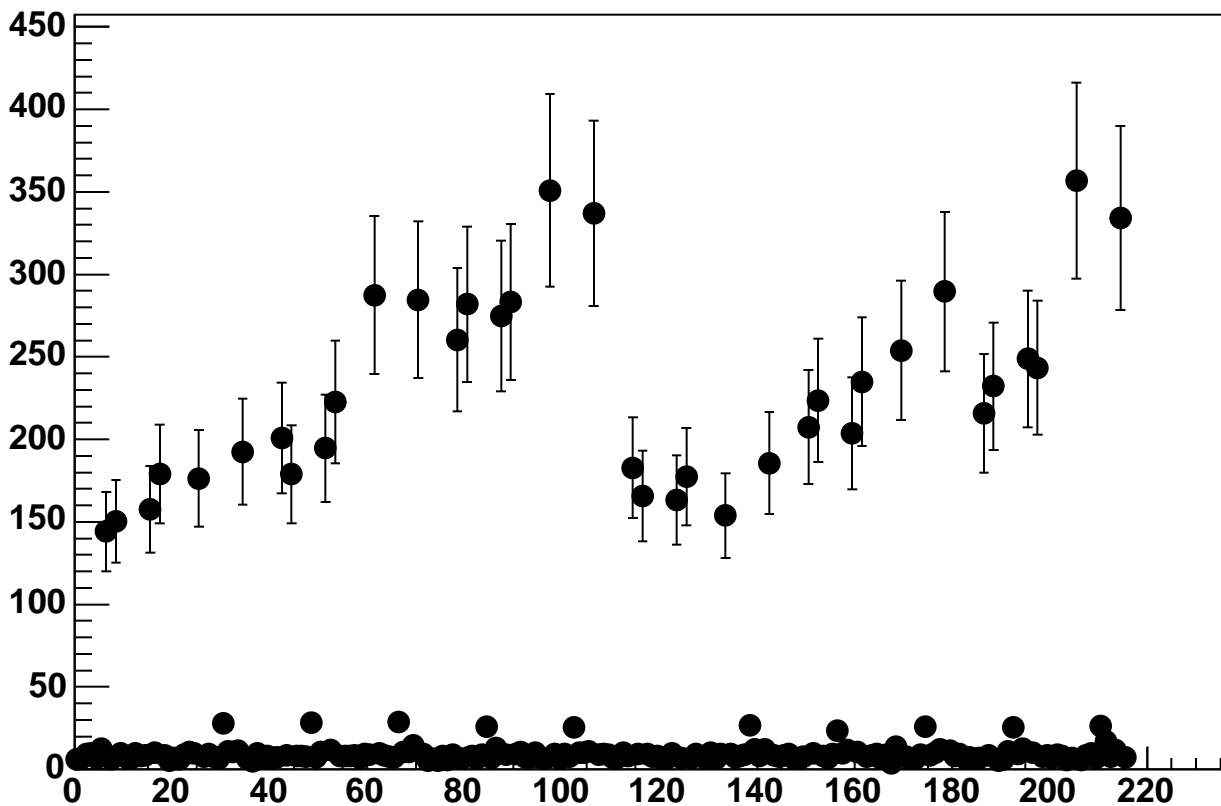
Enable 2, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



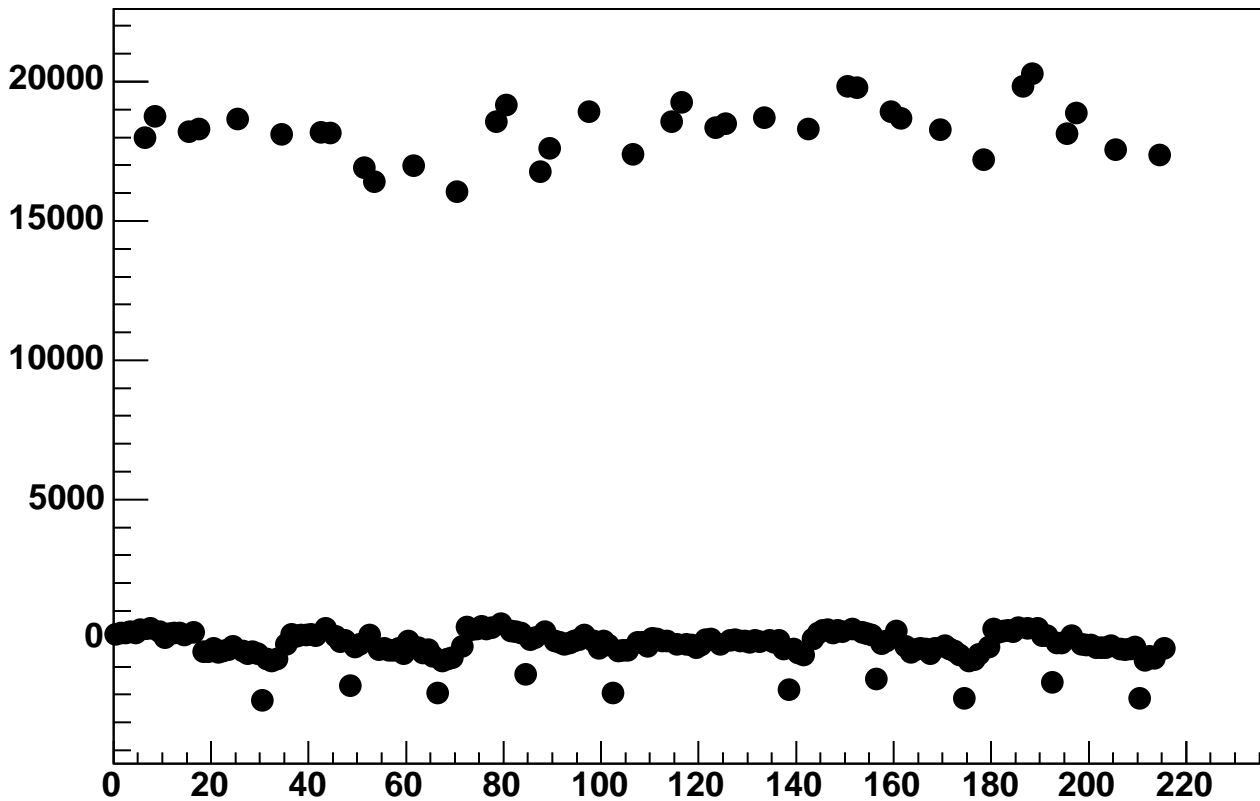
Enable 2, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



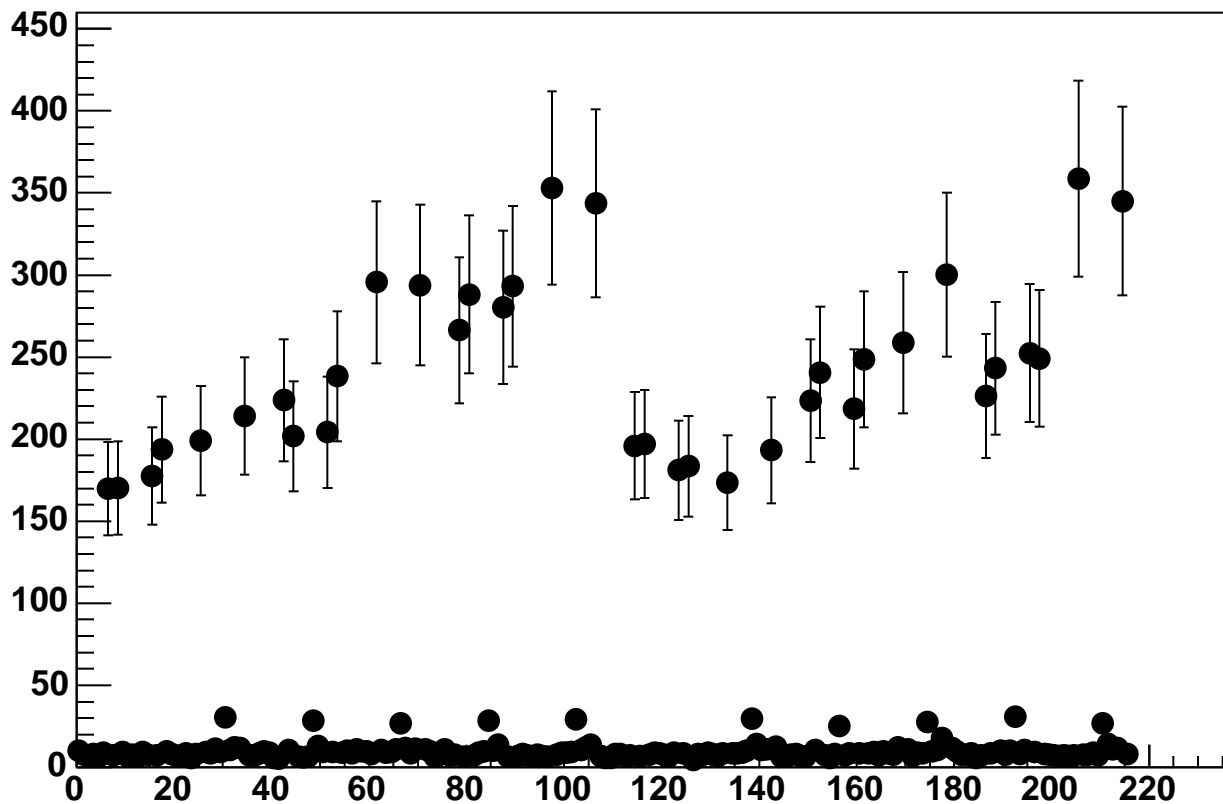
Enable 2, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



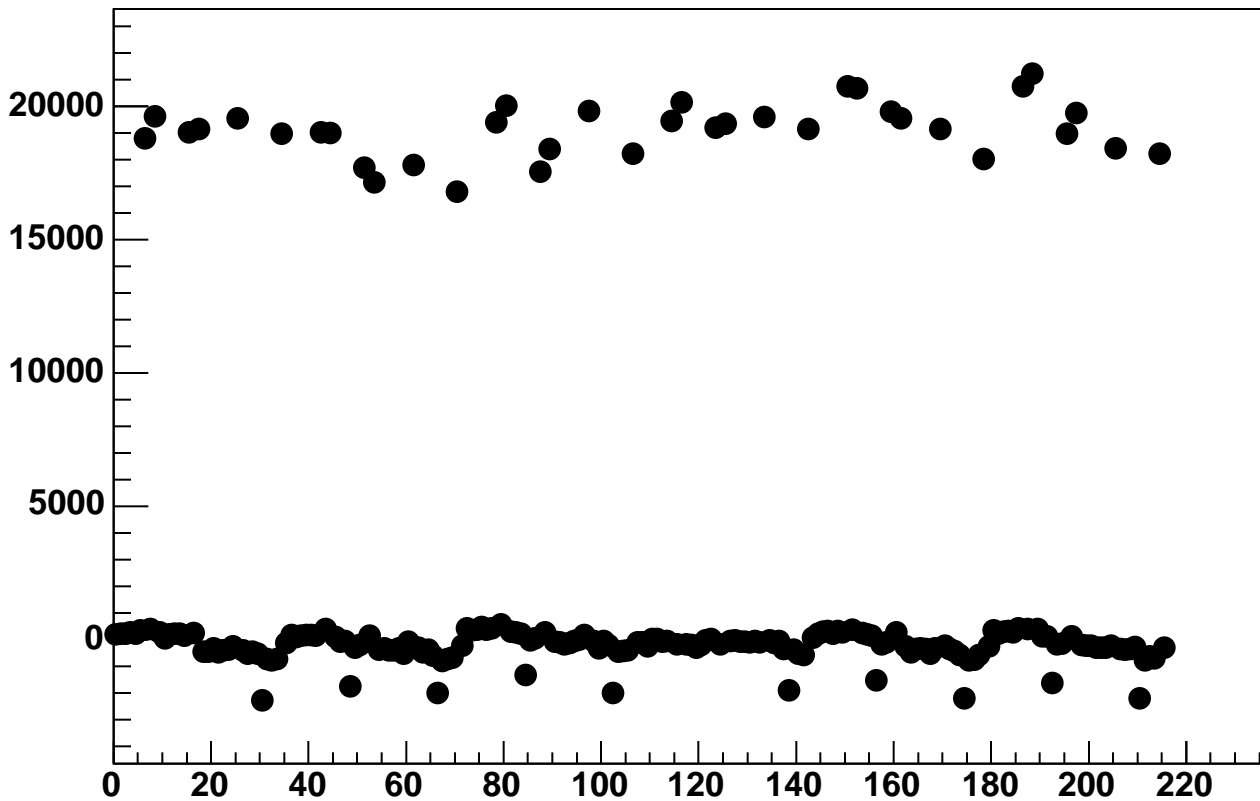
Enable 2, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



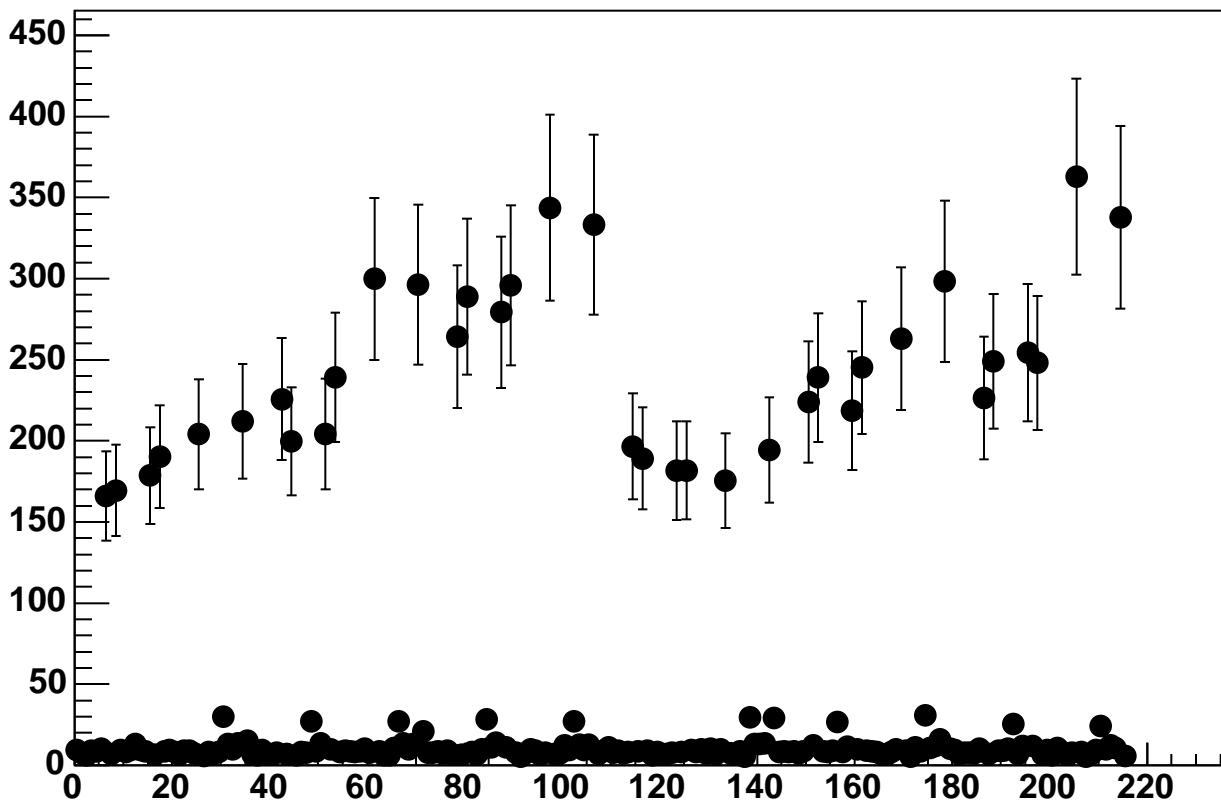
Enable 2, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



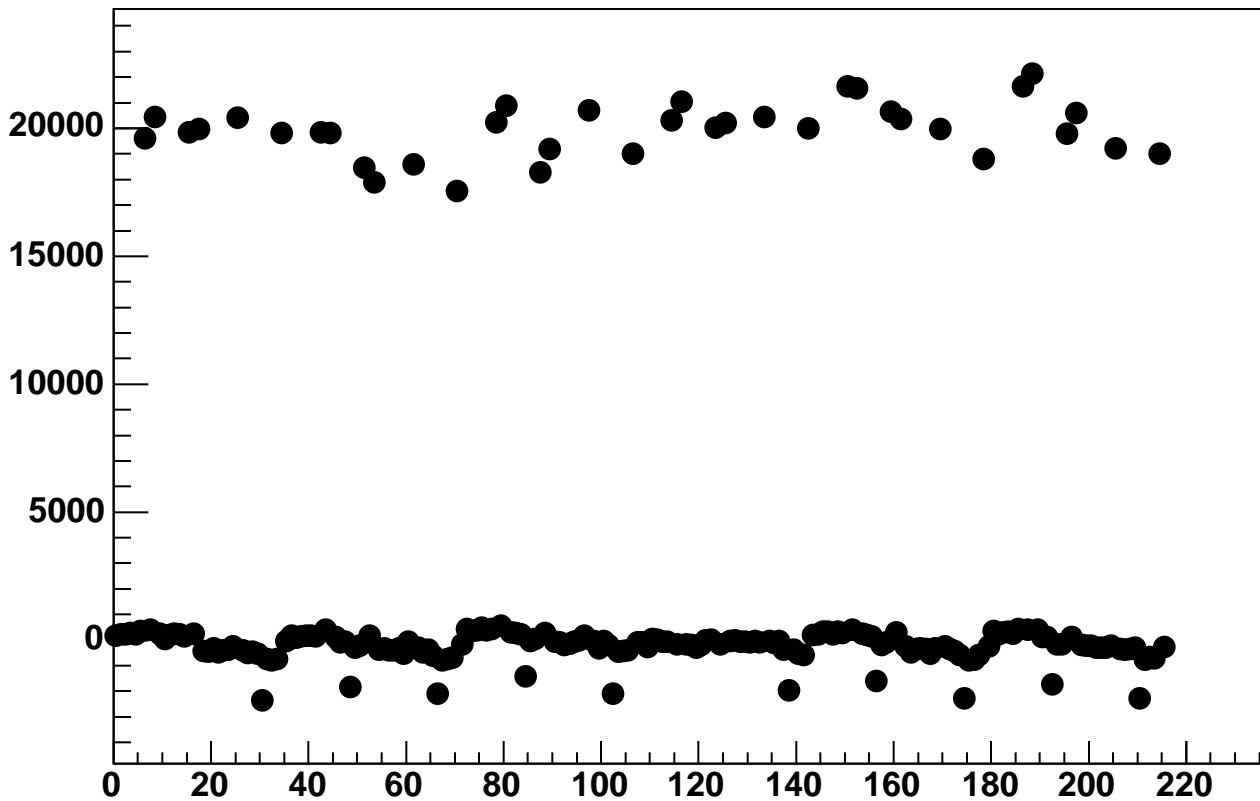
Enable 2, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



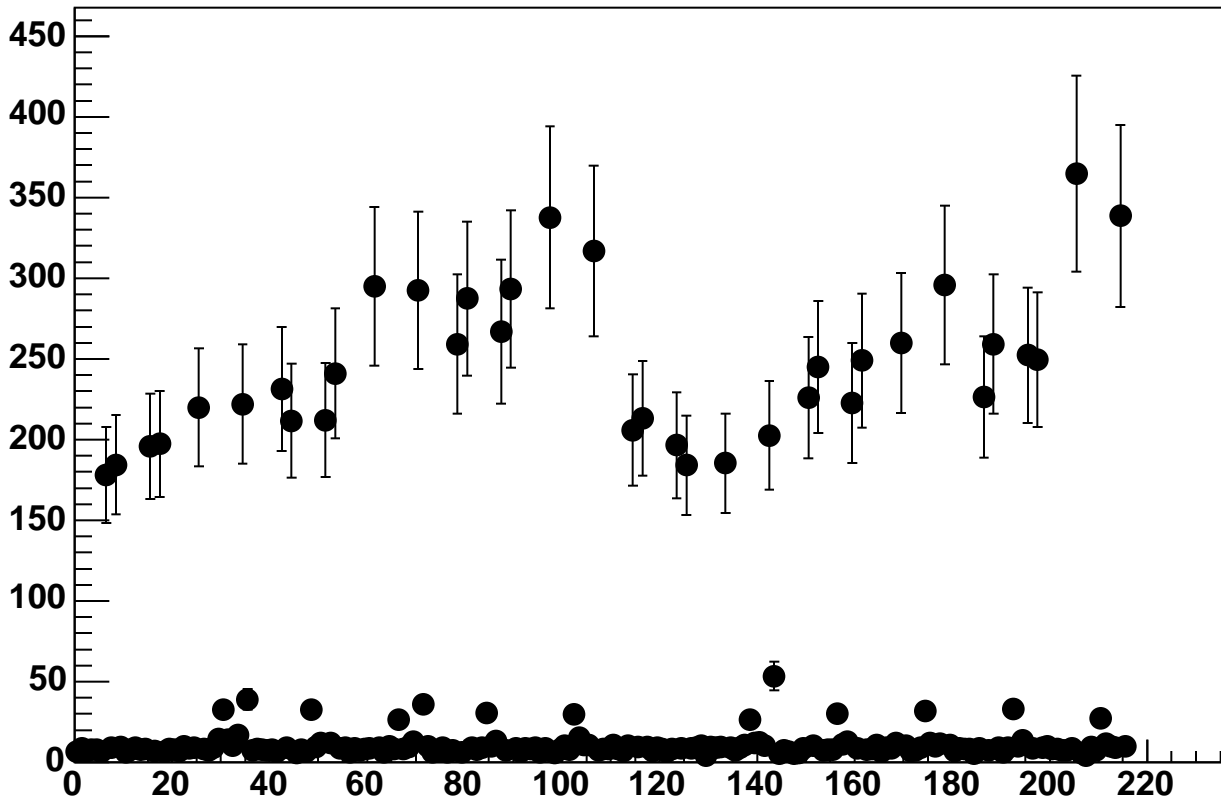
Enable 2, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



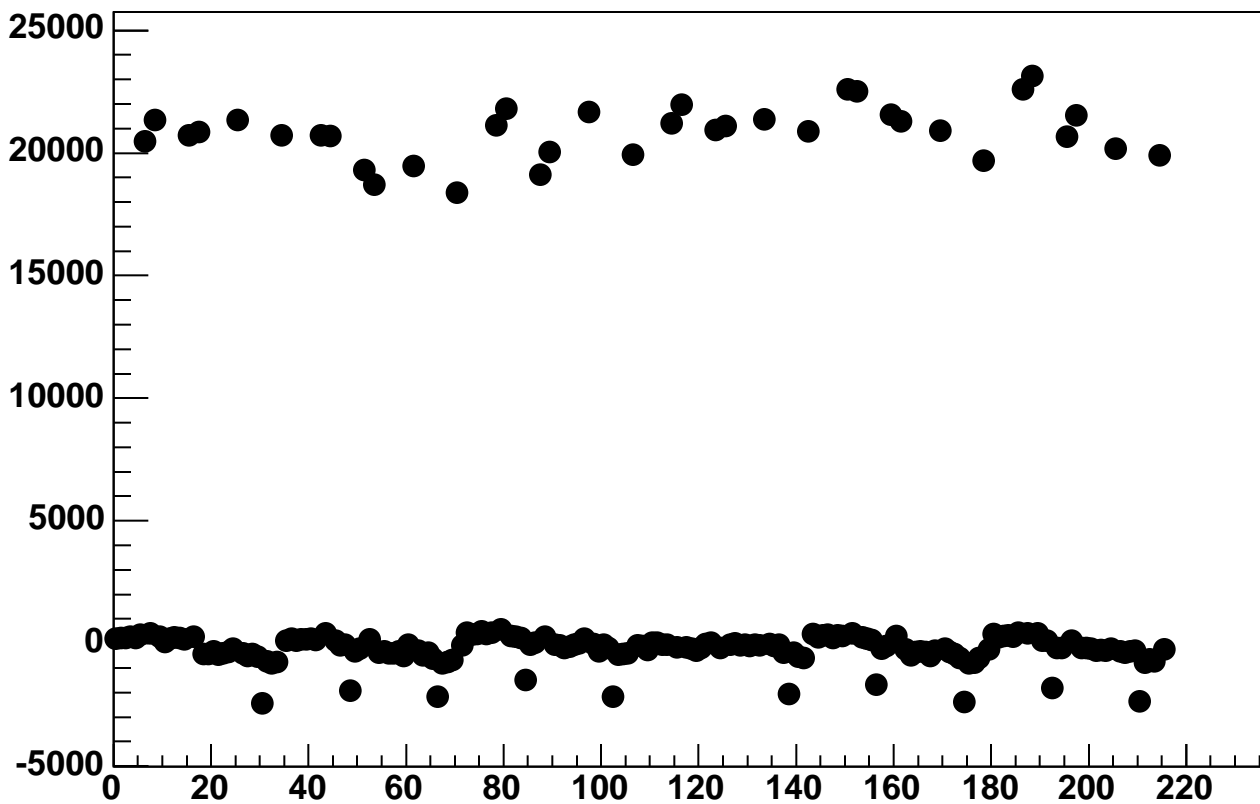
Enable 2, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



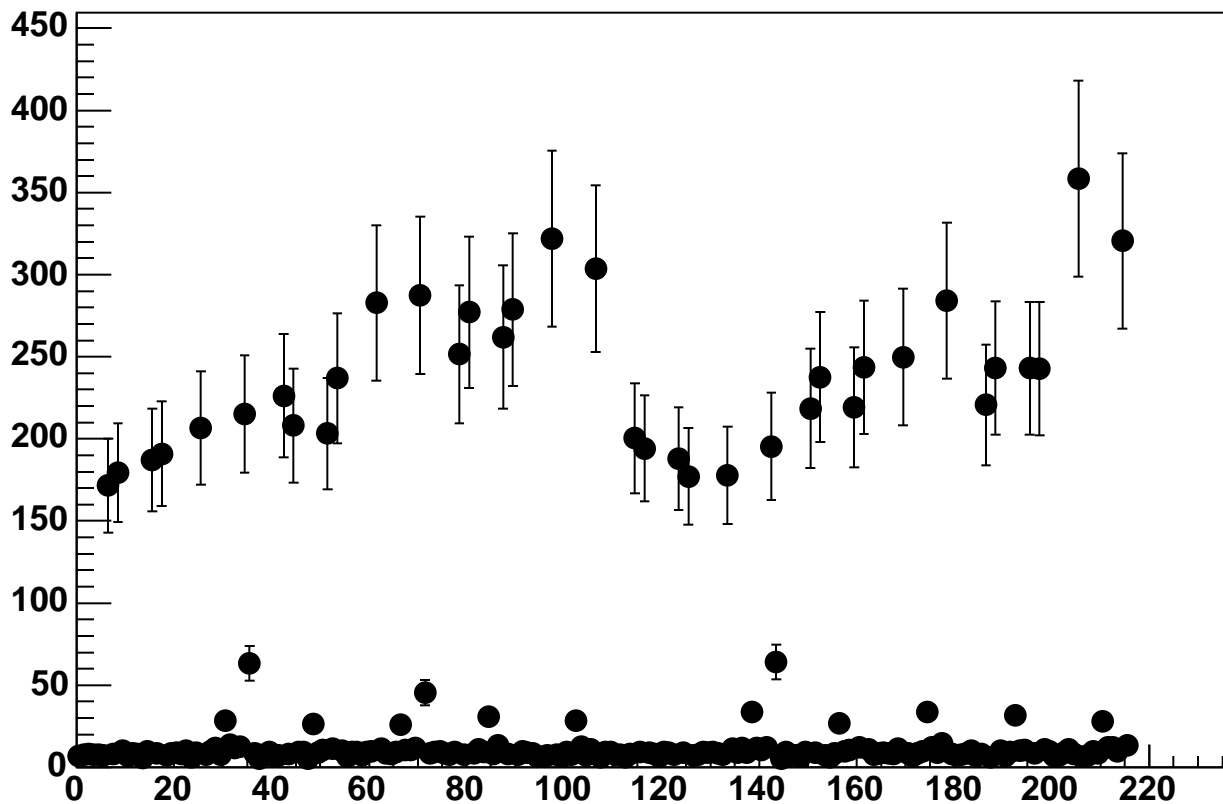
Enable 2, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



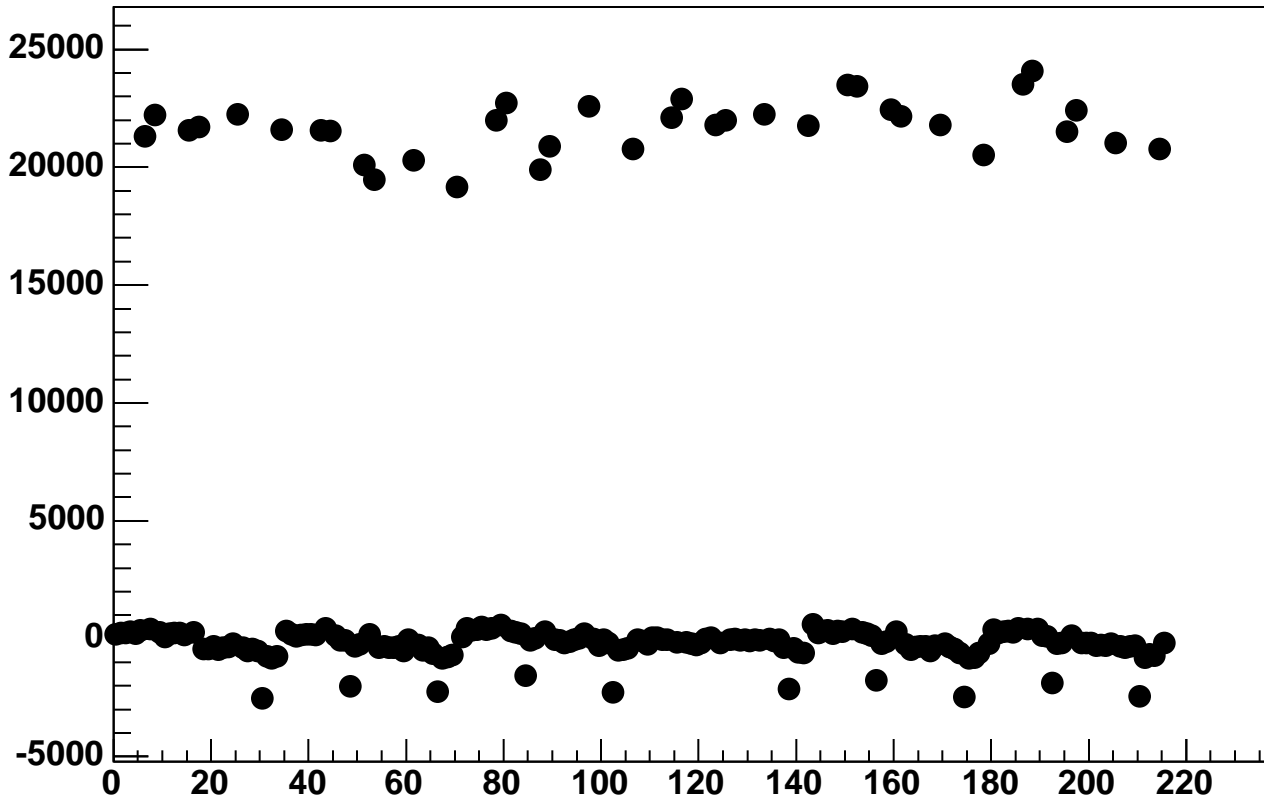
Enable 2, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



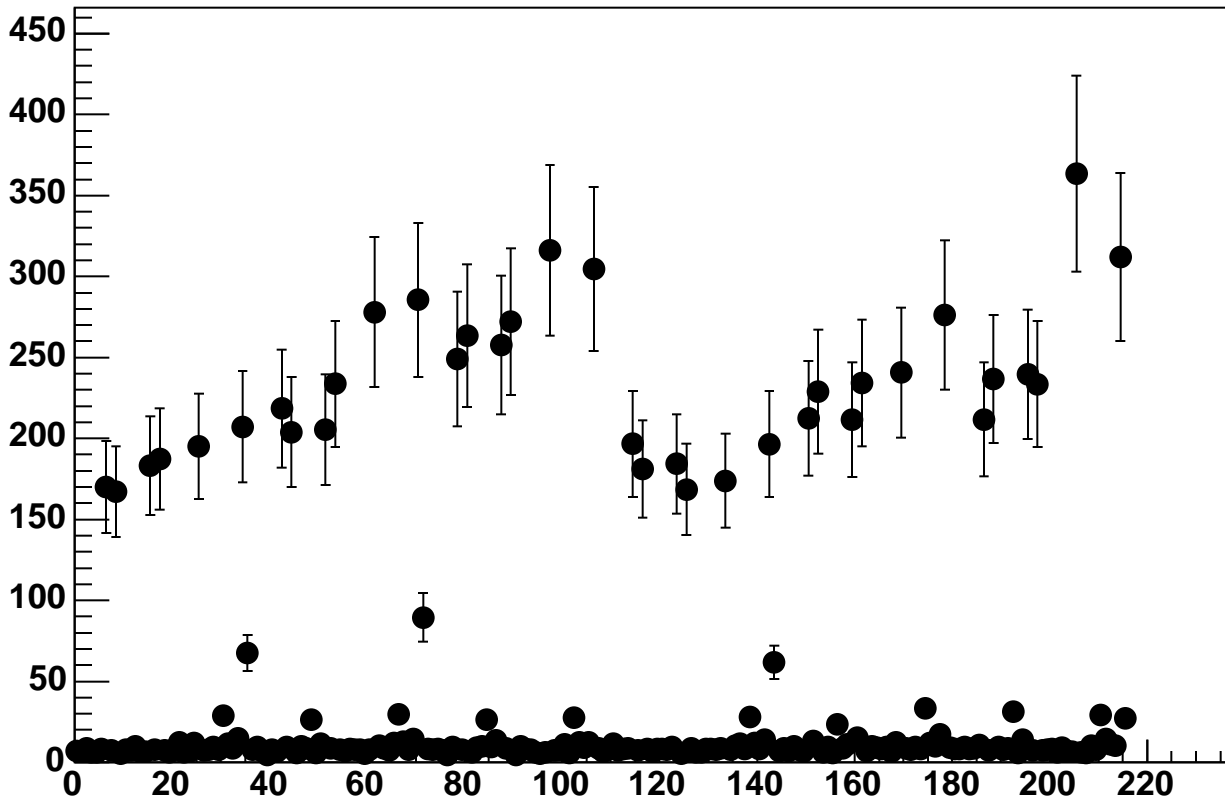
Enable 2, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



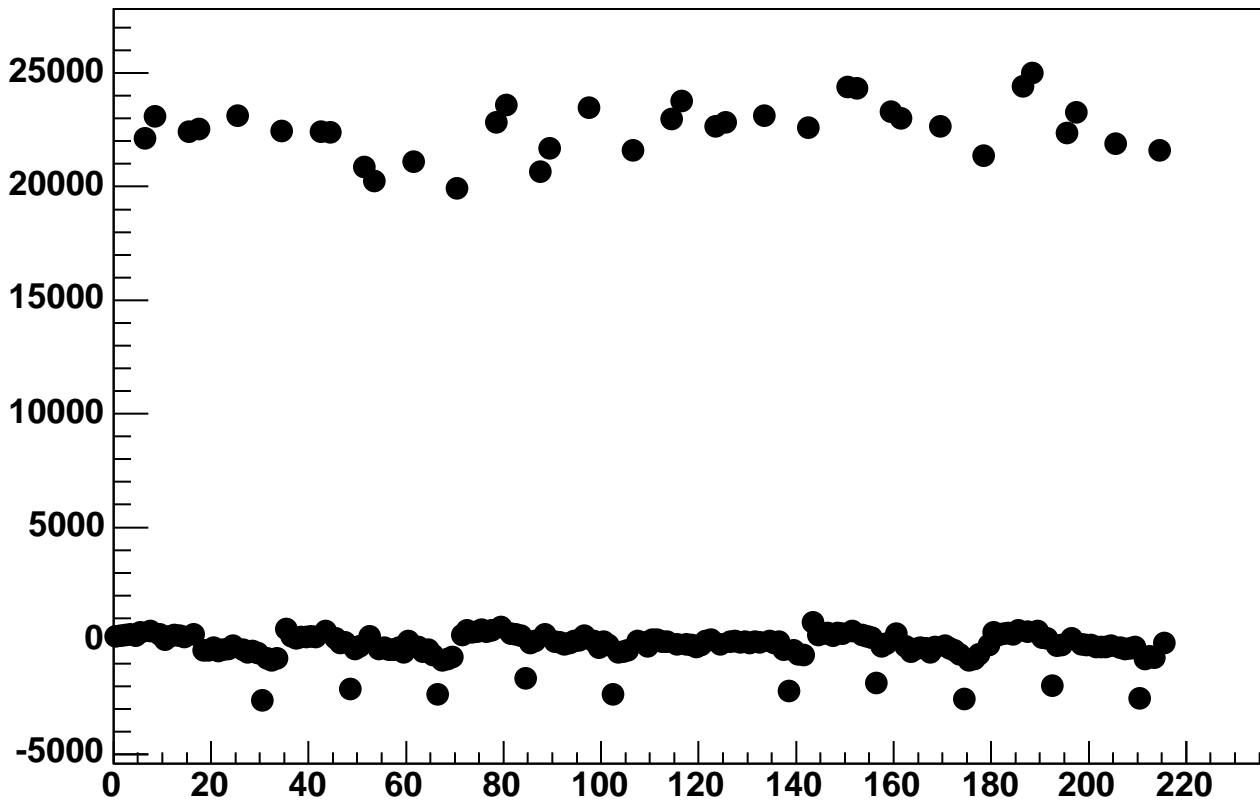
Enable 2, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



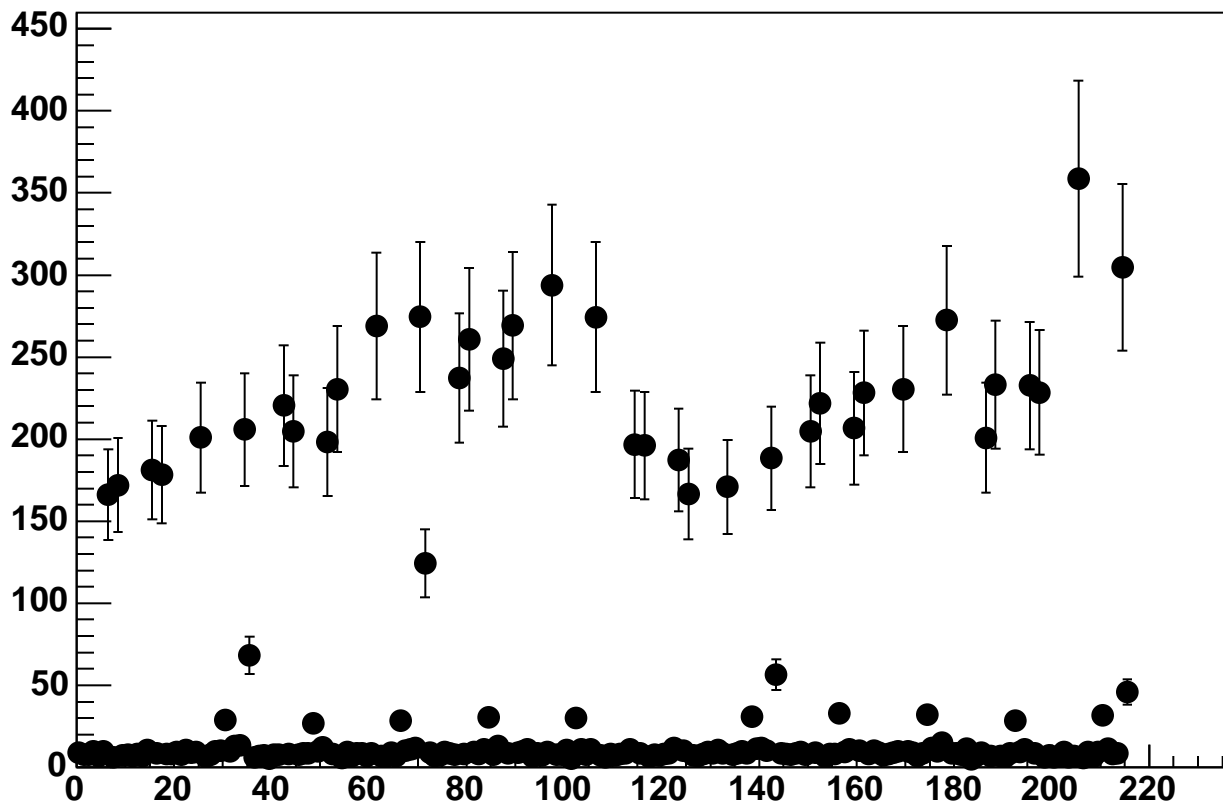
Enable 2, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



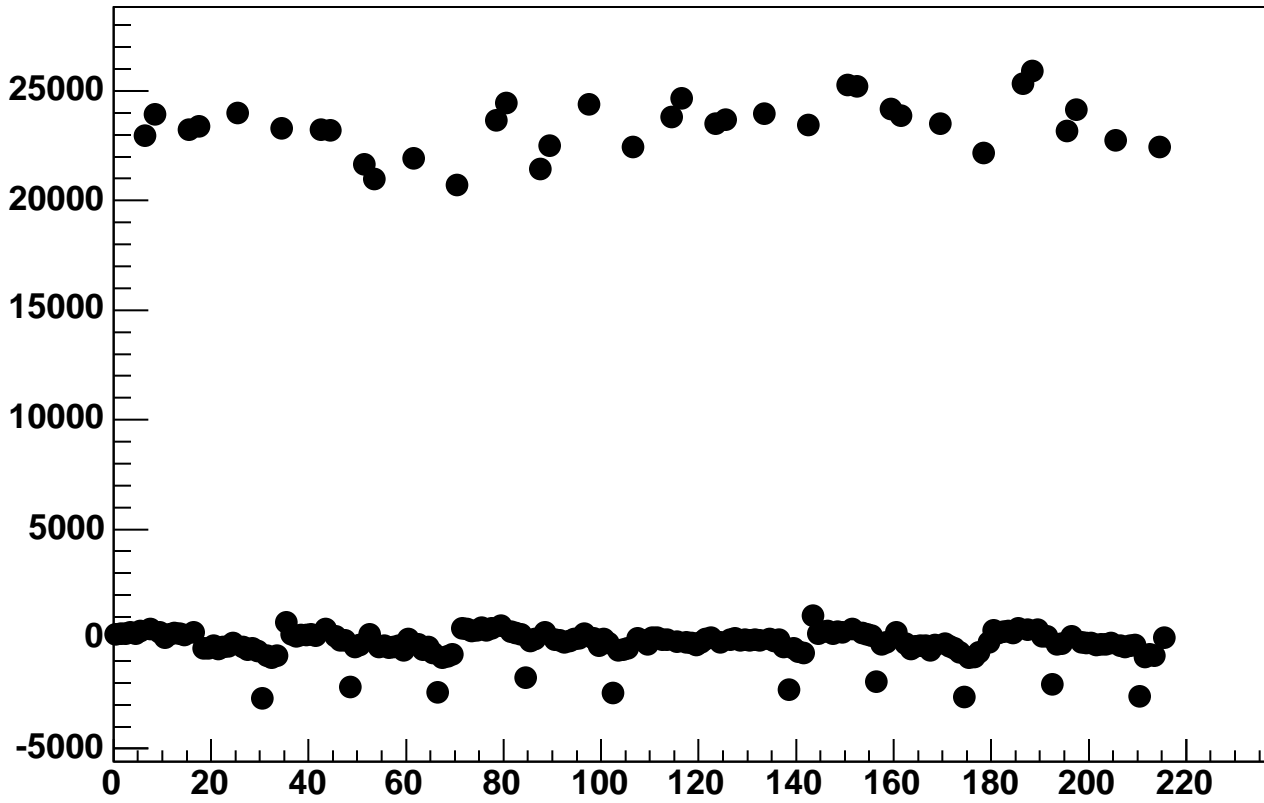
Enable 2, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



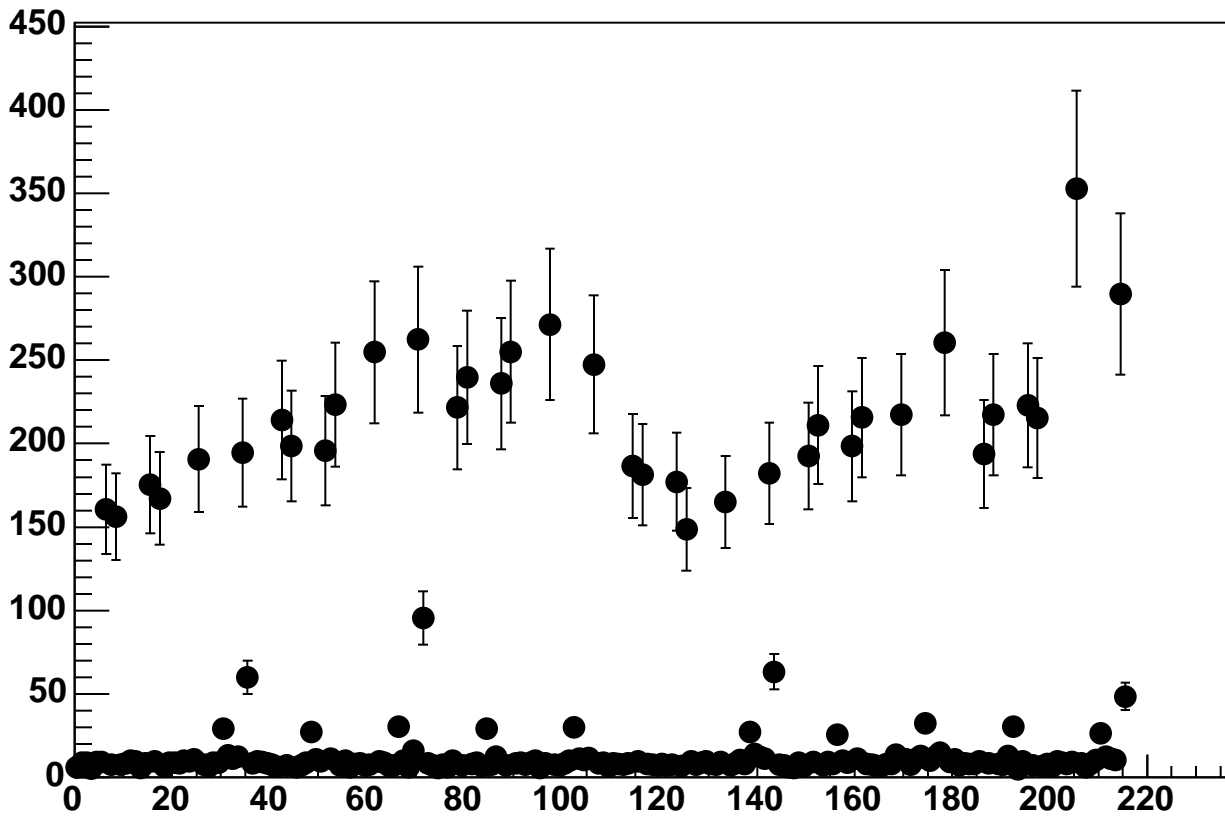
Enable 2, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



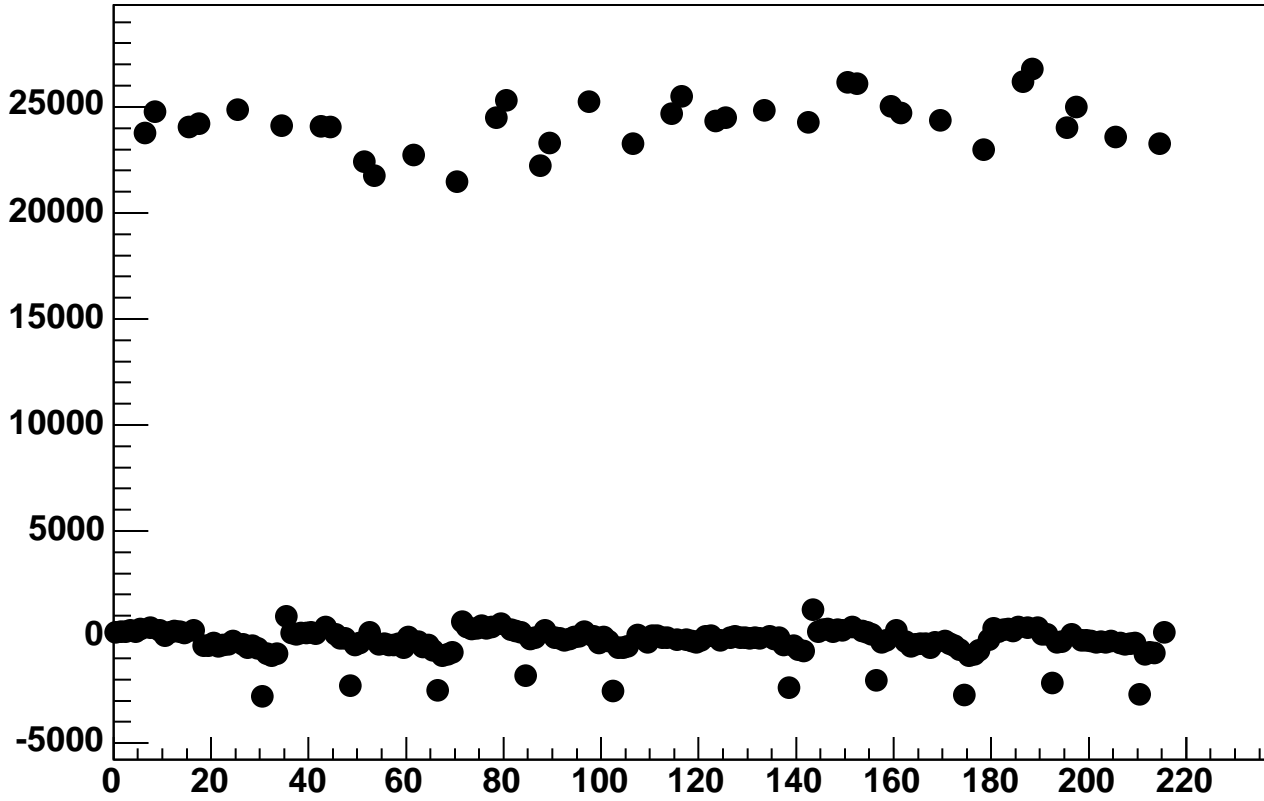
Enable 2, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



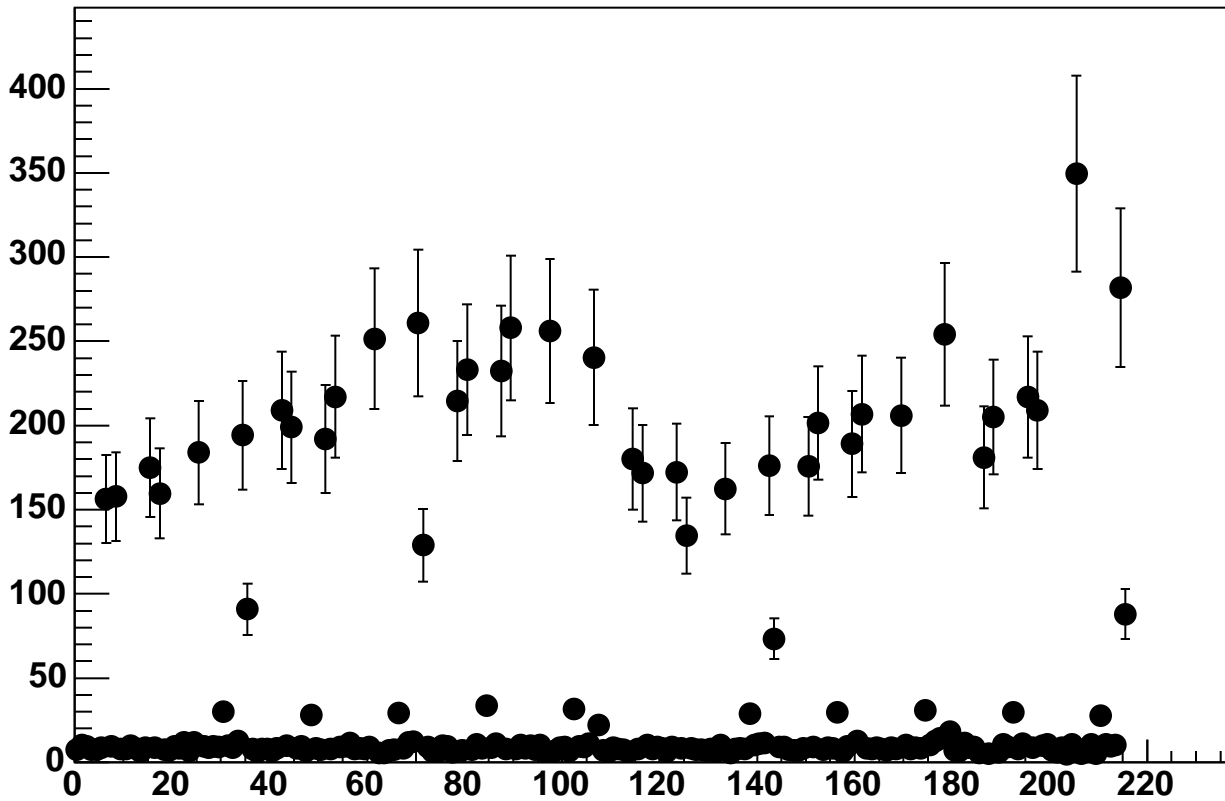
Enable 2, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



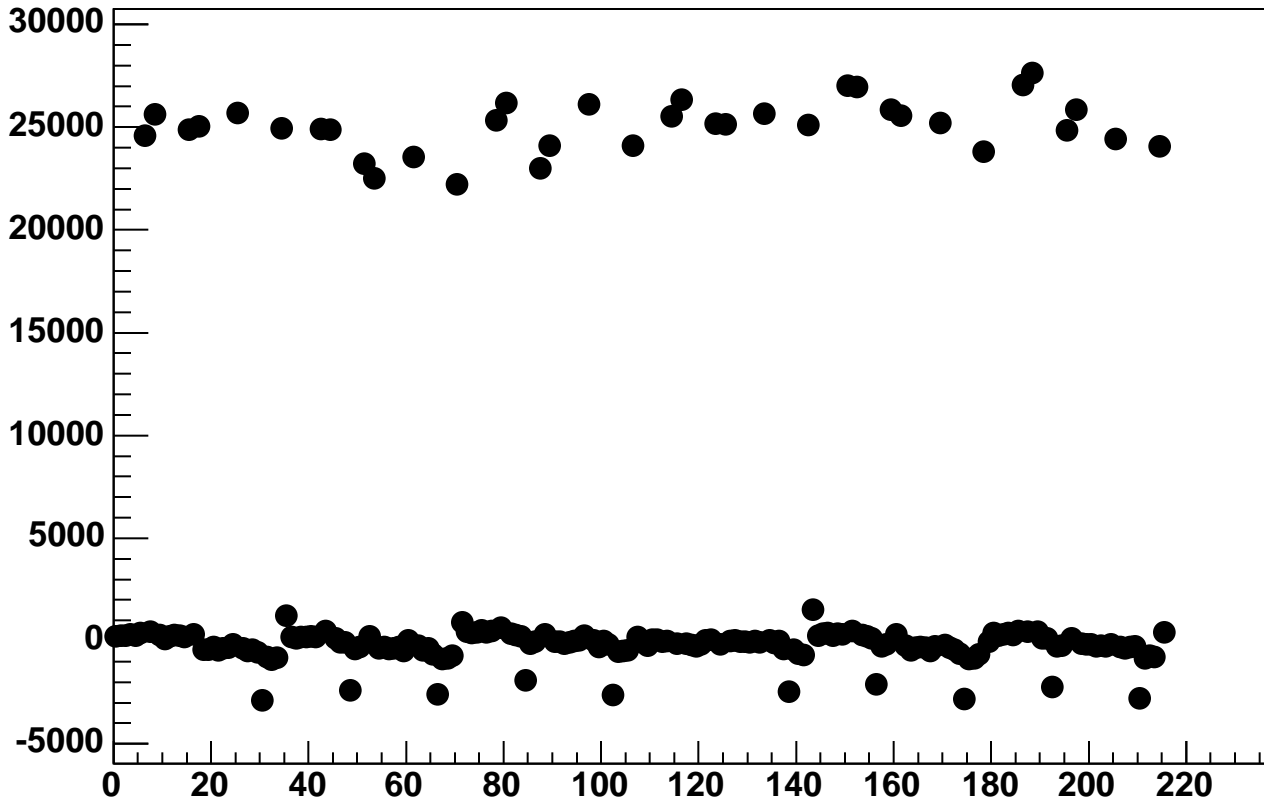
Enable 2, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



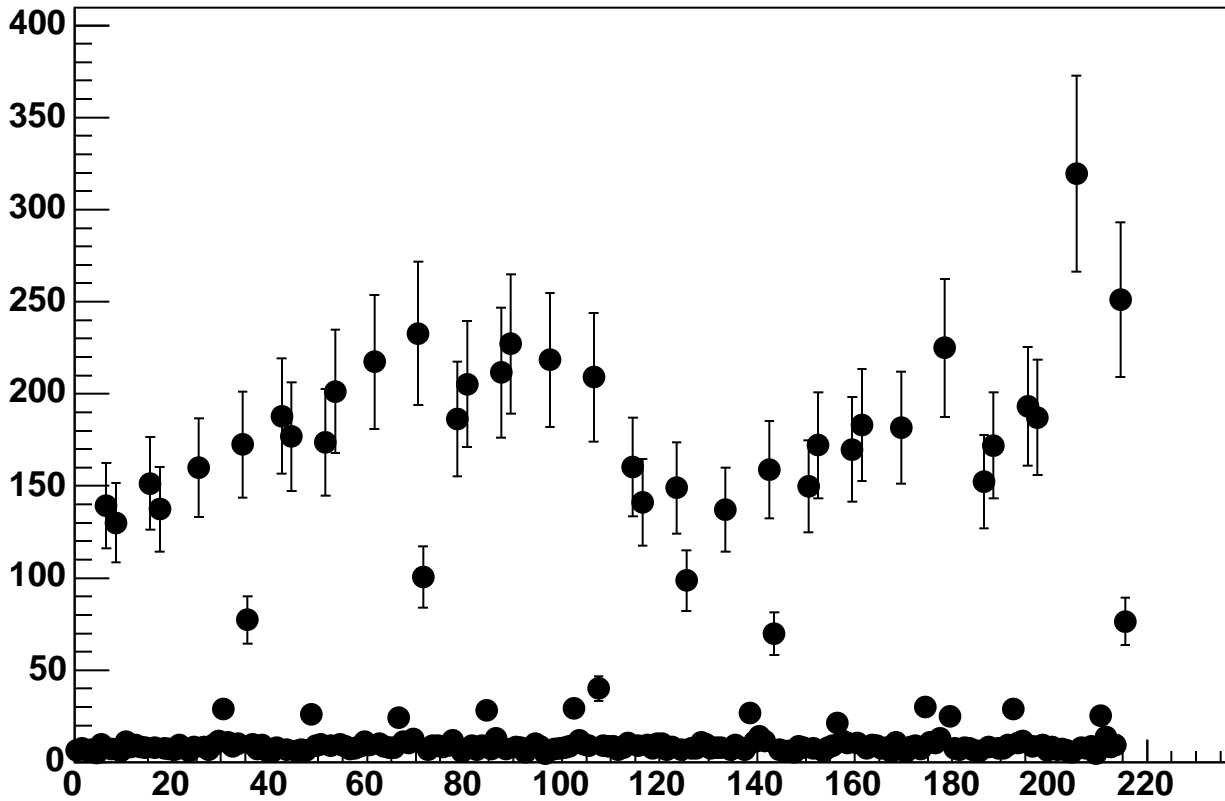
Enable 2, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



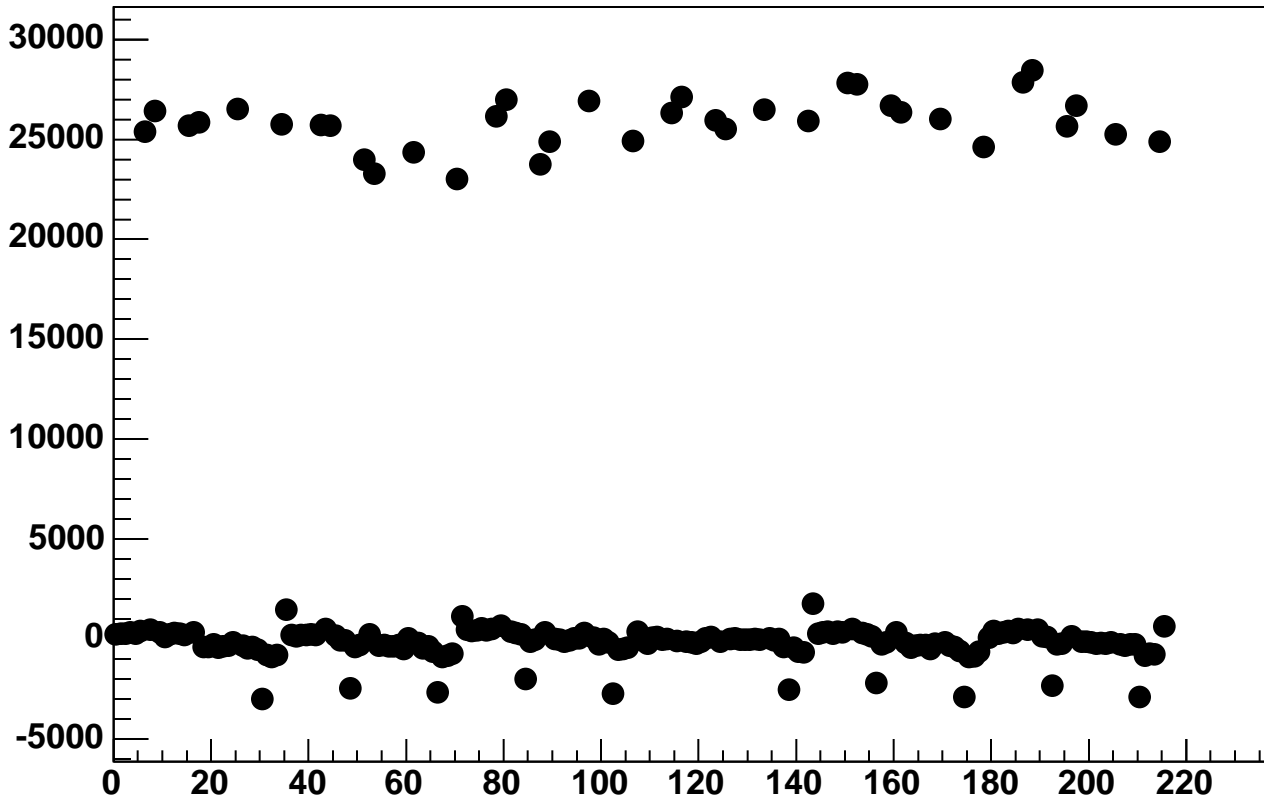
Enable 2, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



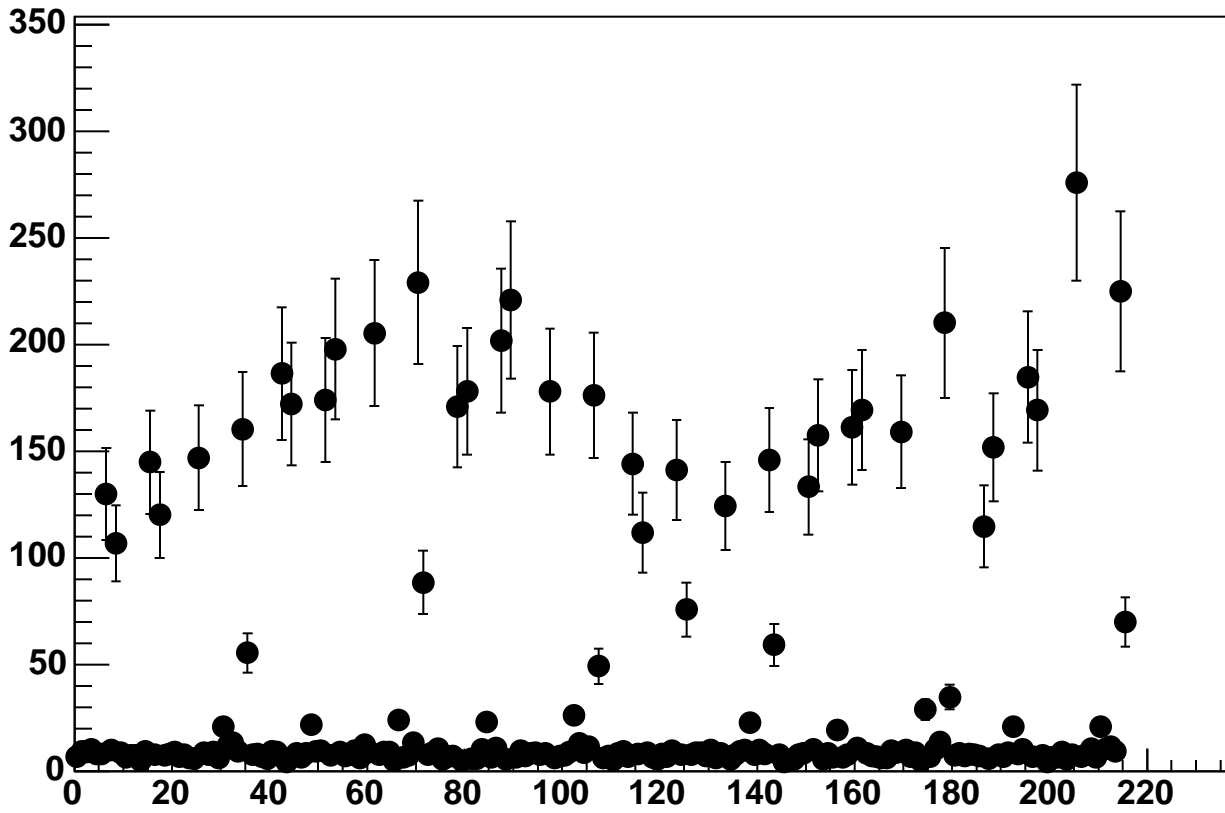
Enable 2, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



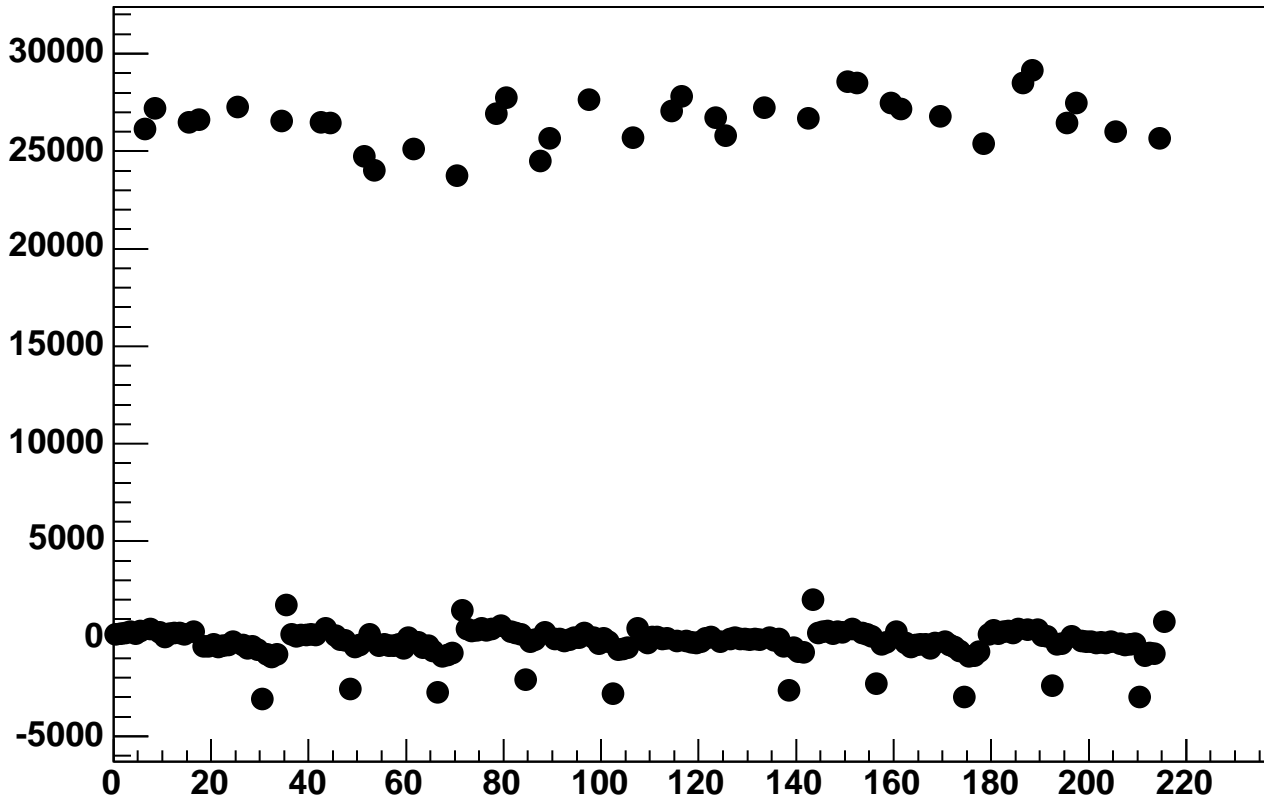
Enable 2, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



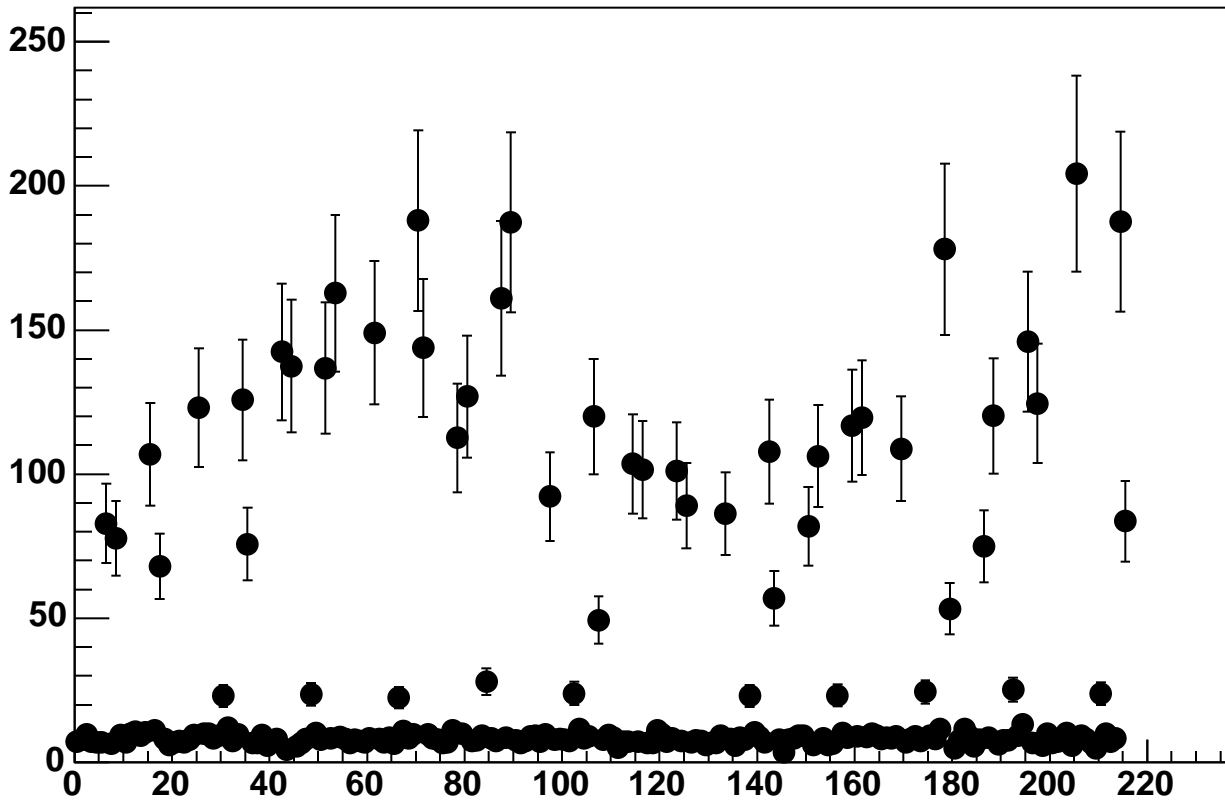
Enable 2, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



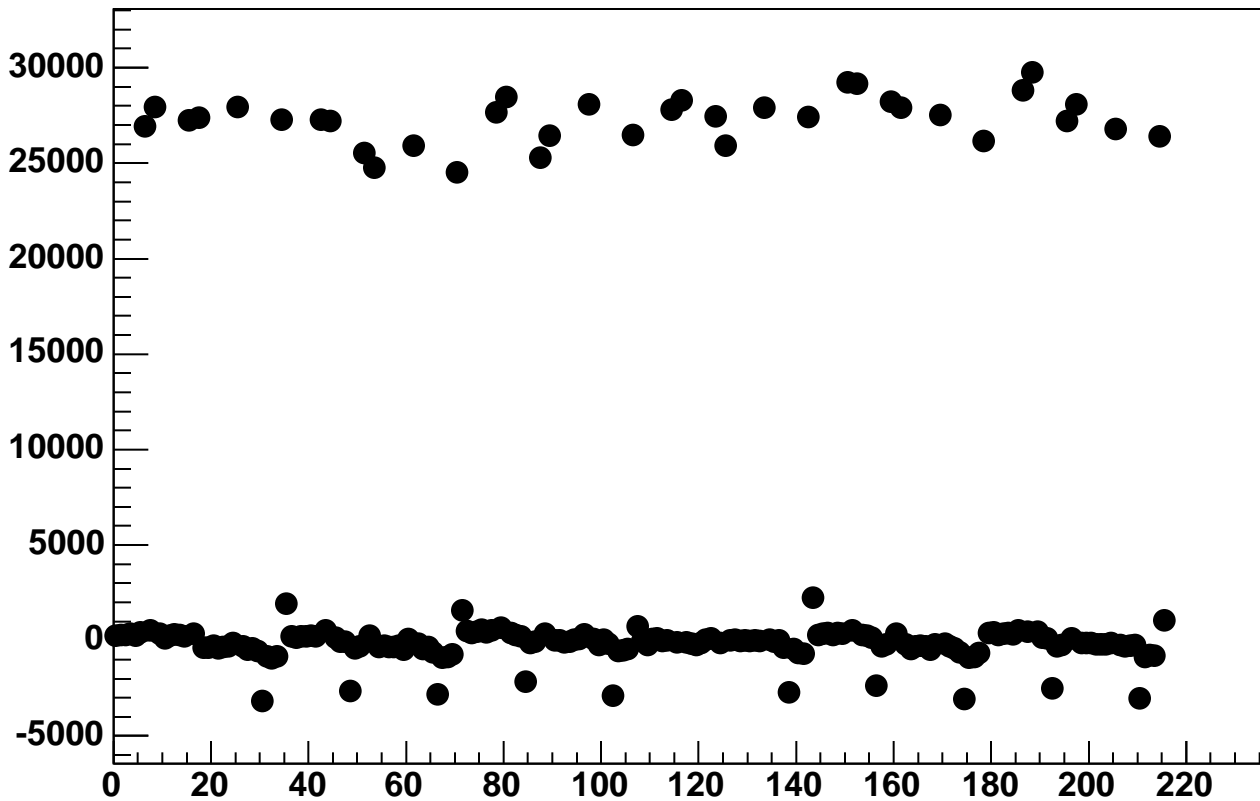
Enable 2, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



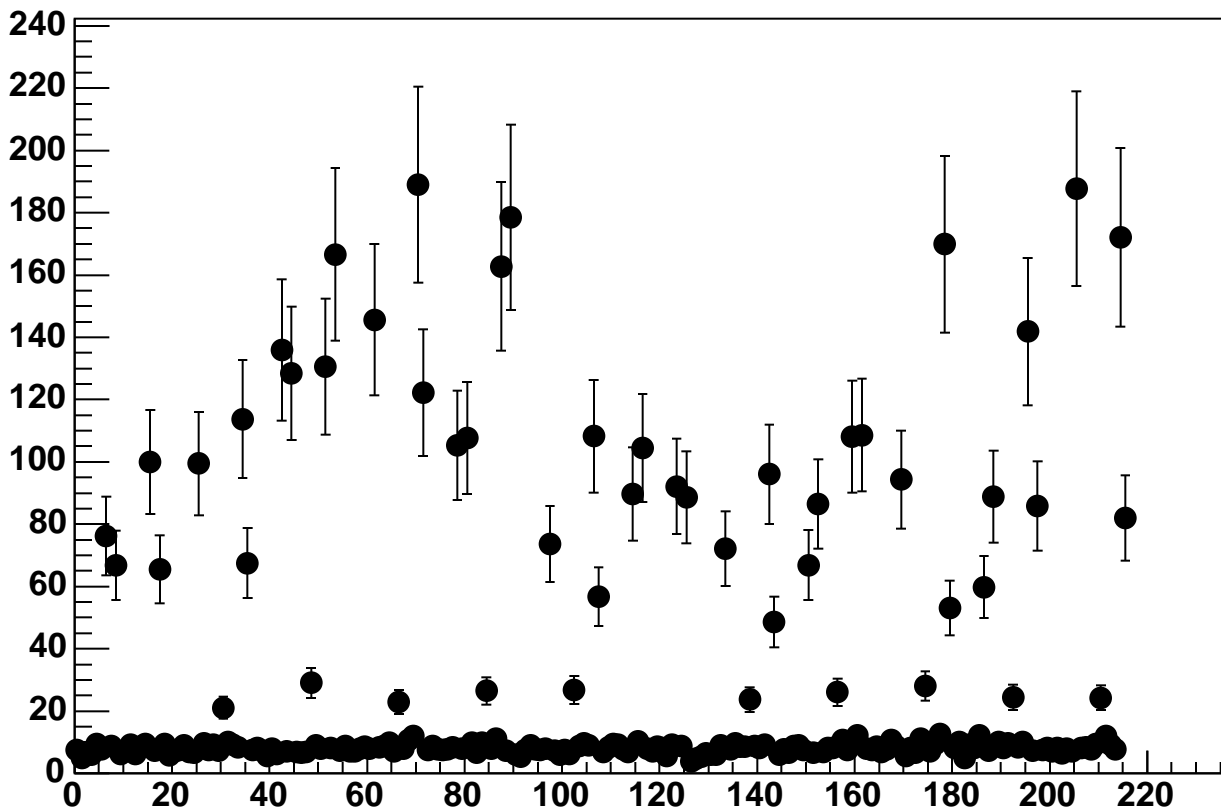
Enable 2, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



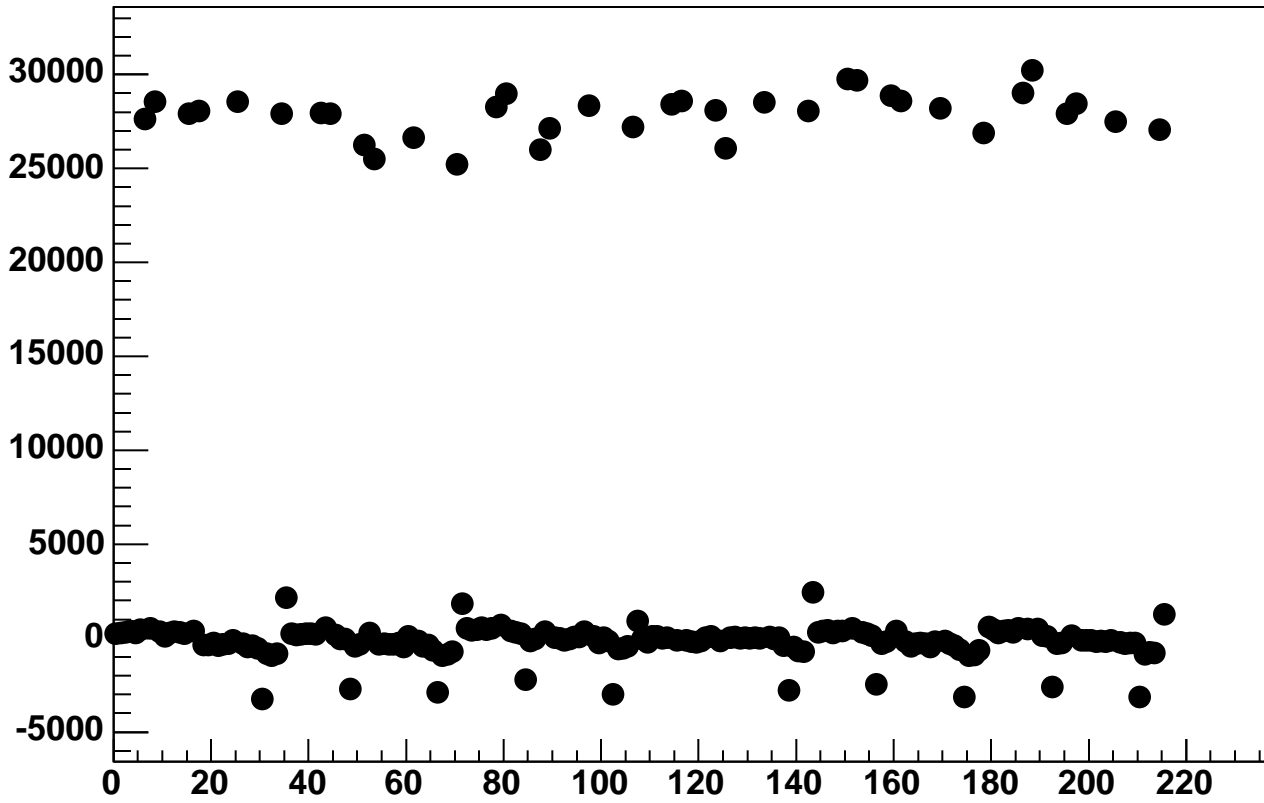
Enable 2, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



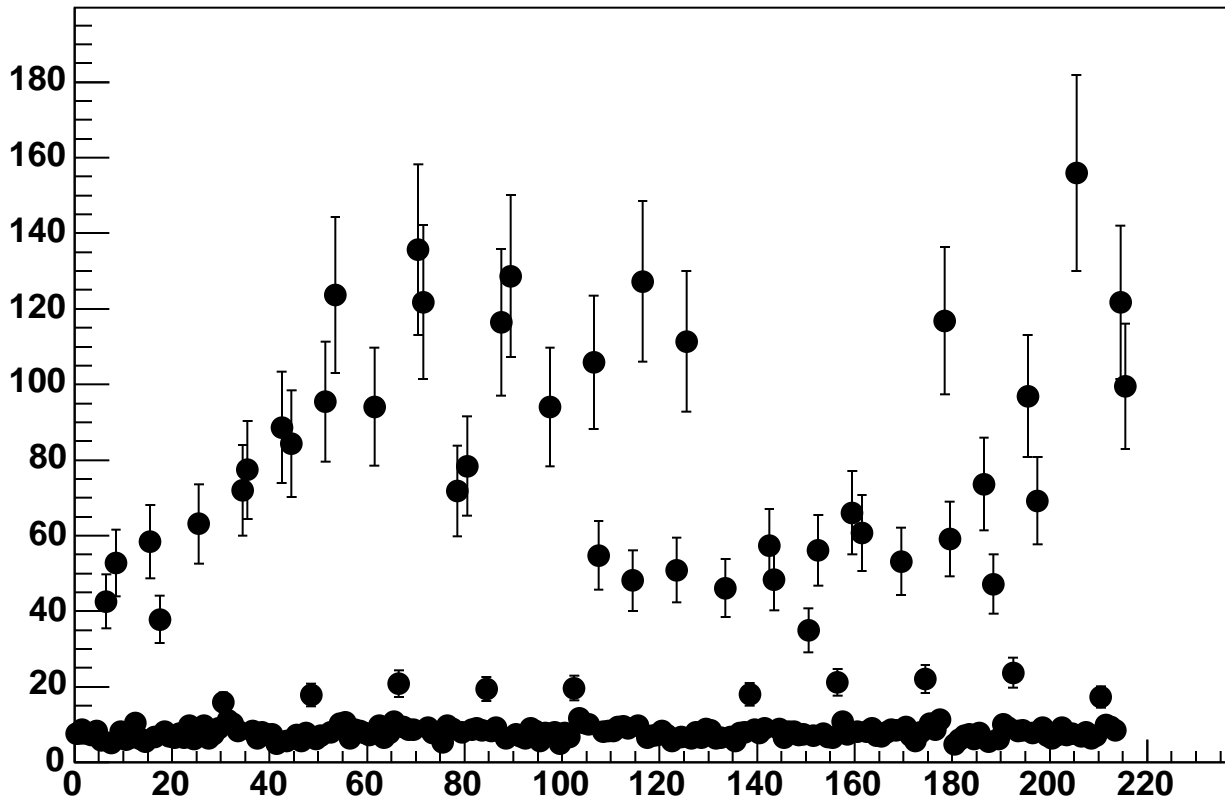
Enable 2, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



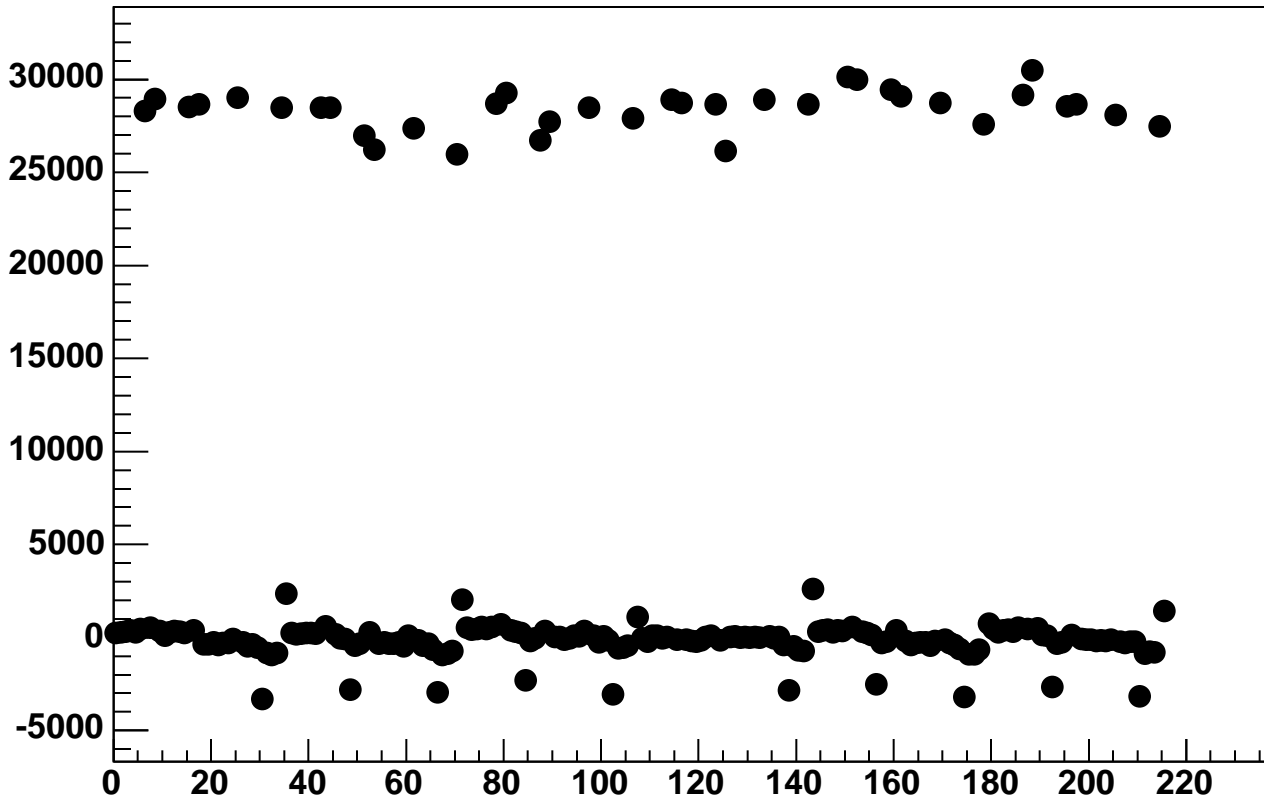
Enable 2, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



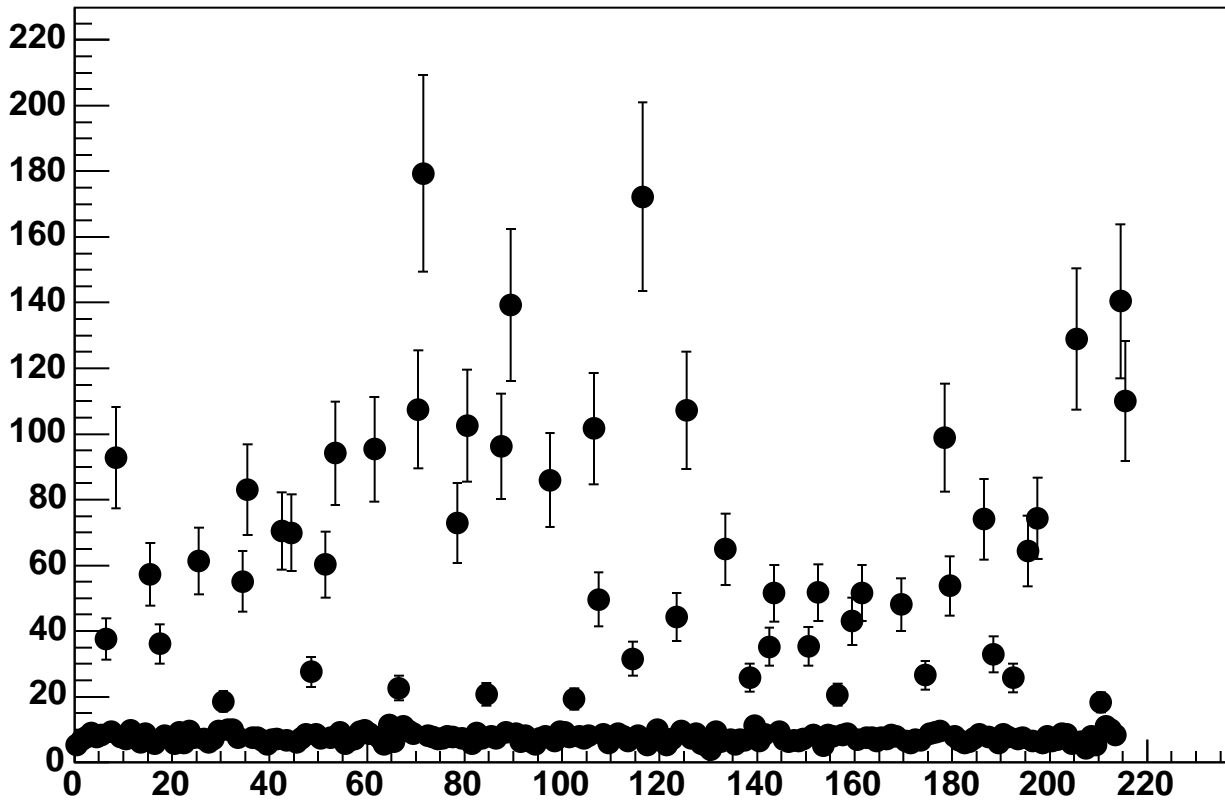
Enable 2, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



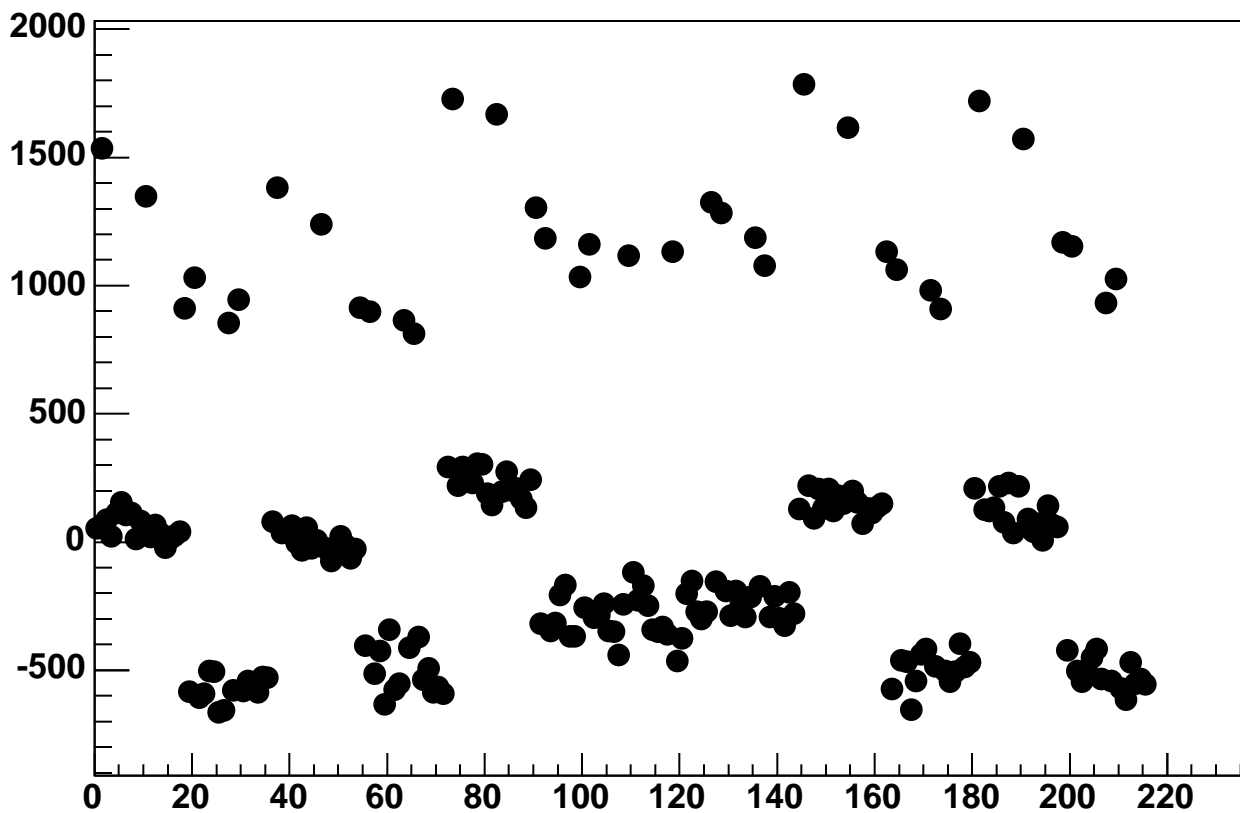
Enable 2, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



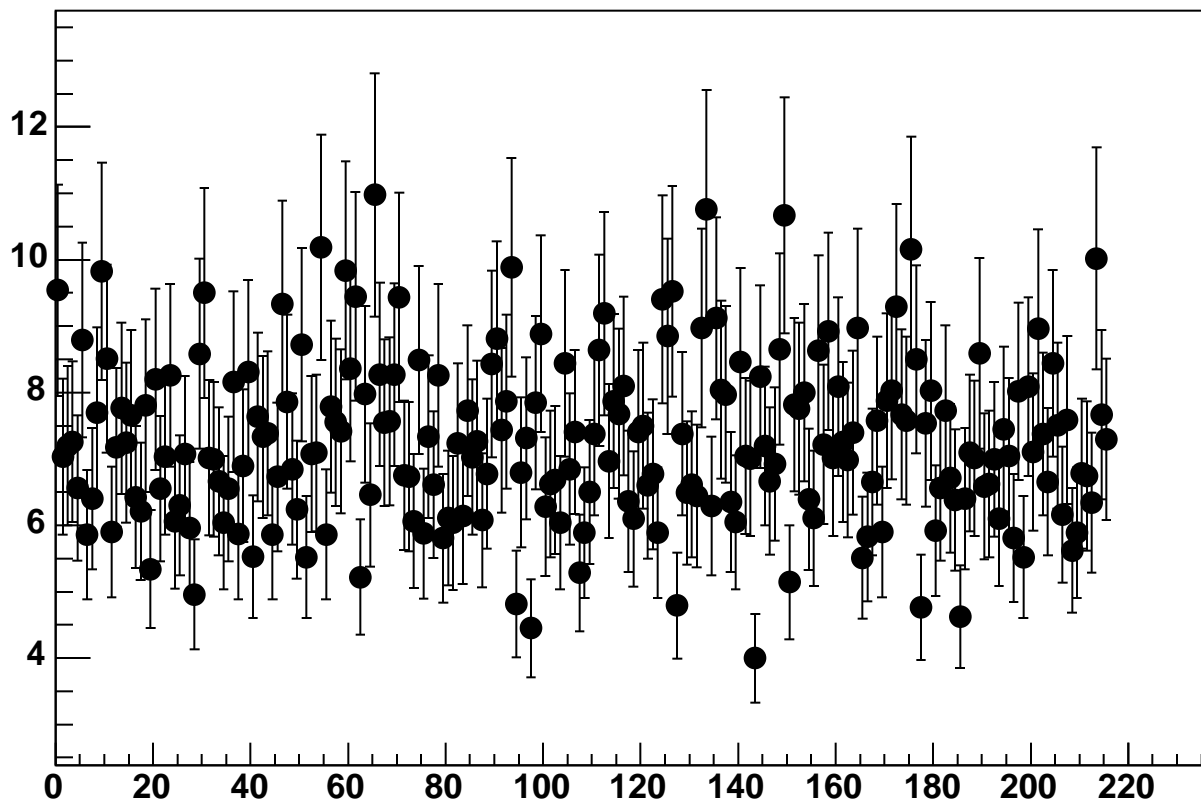
Enable 2, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



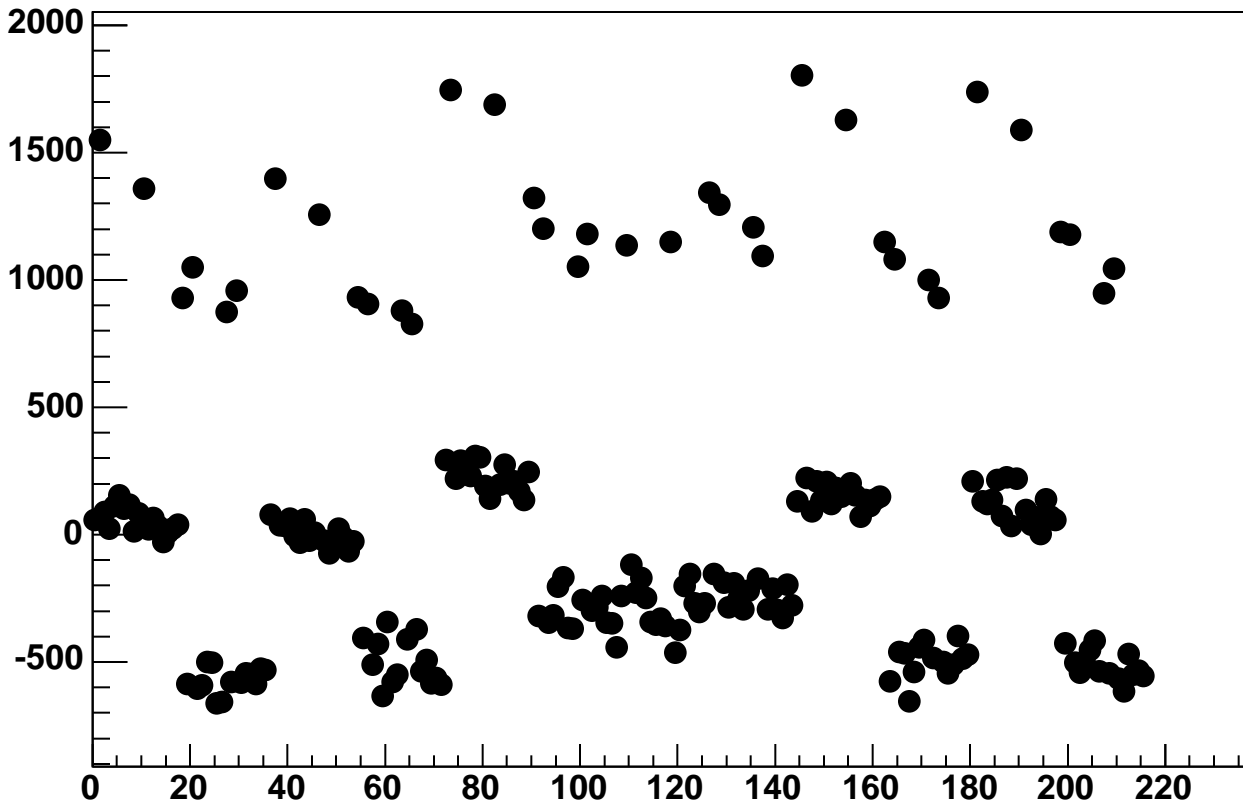
Enable 3, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



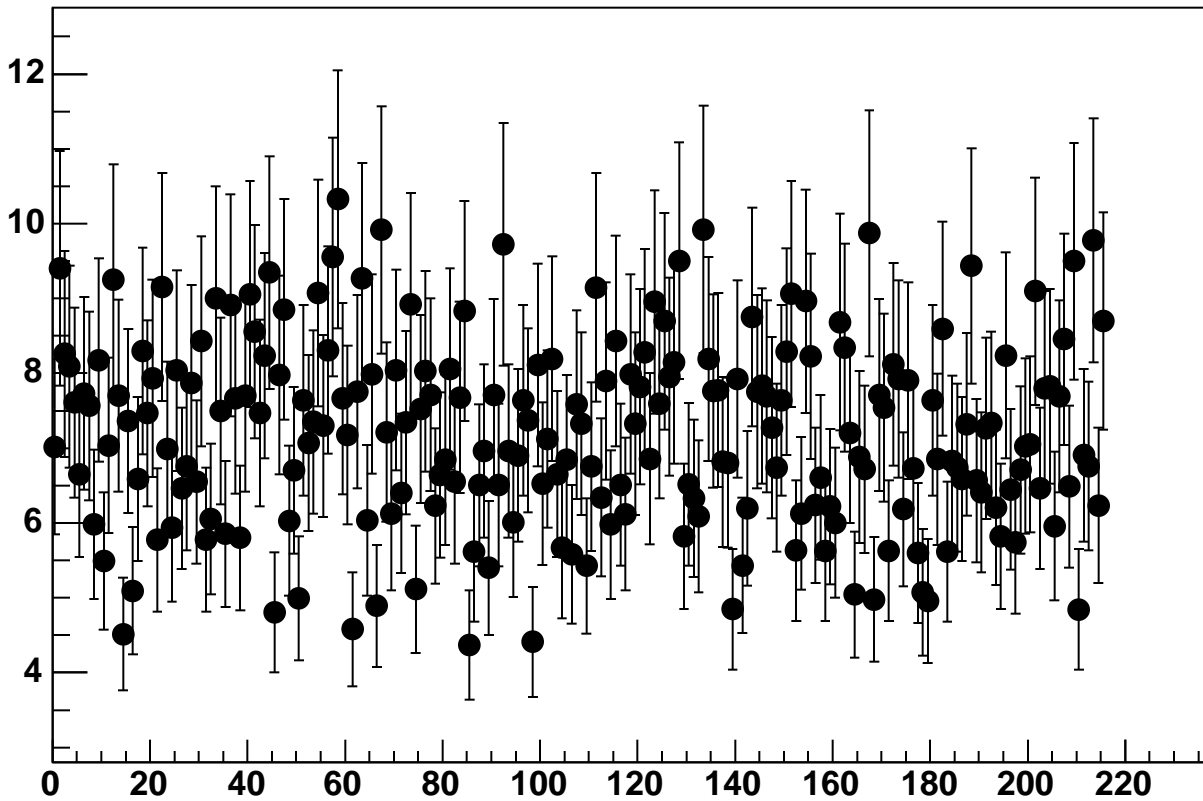
Enable 3, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



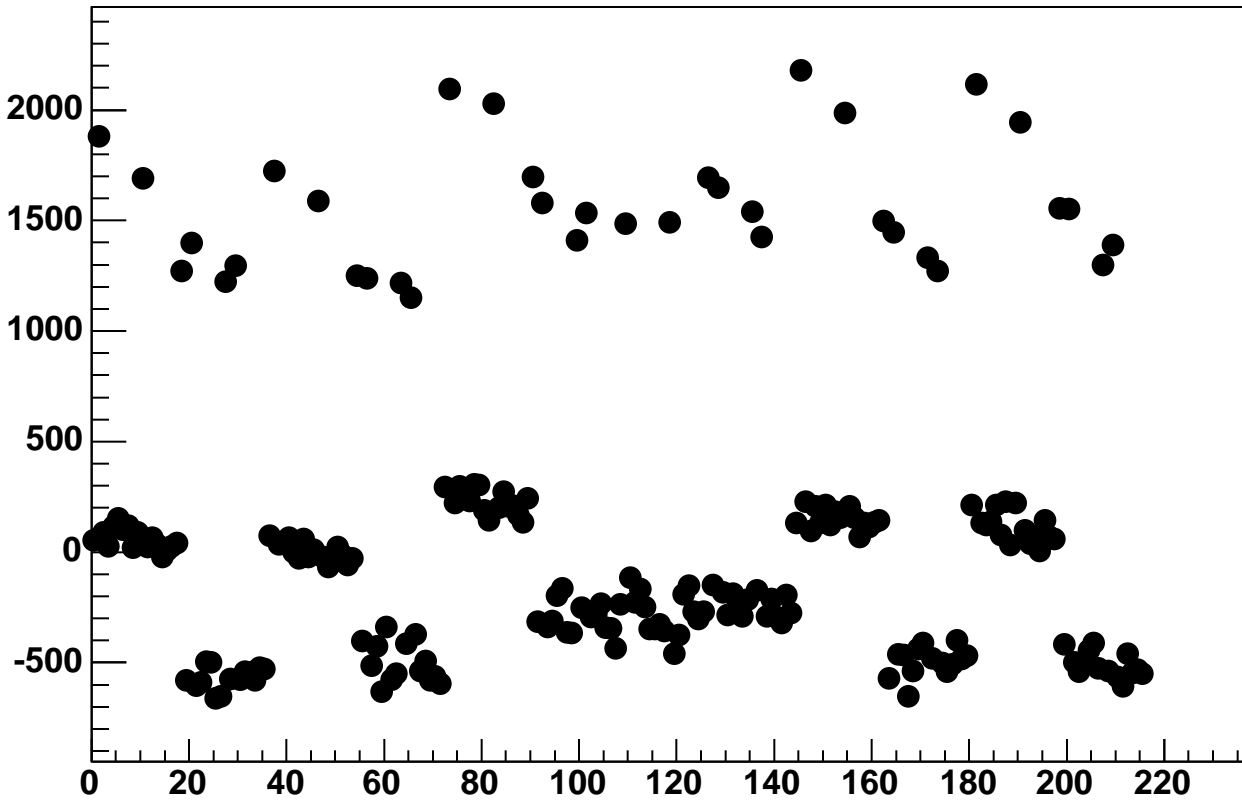
Enable 3, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



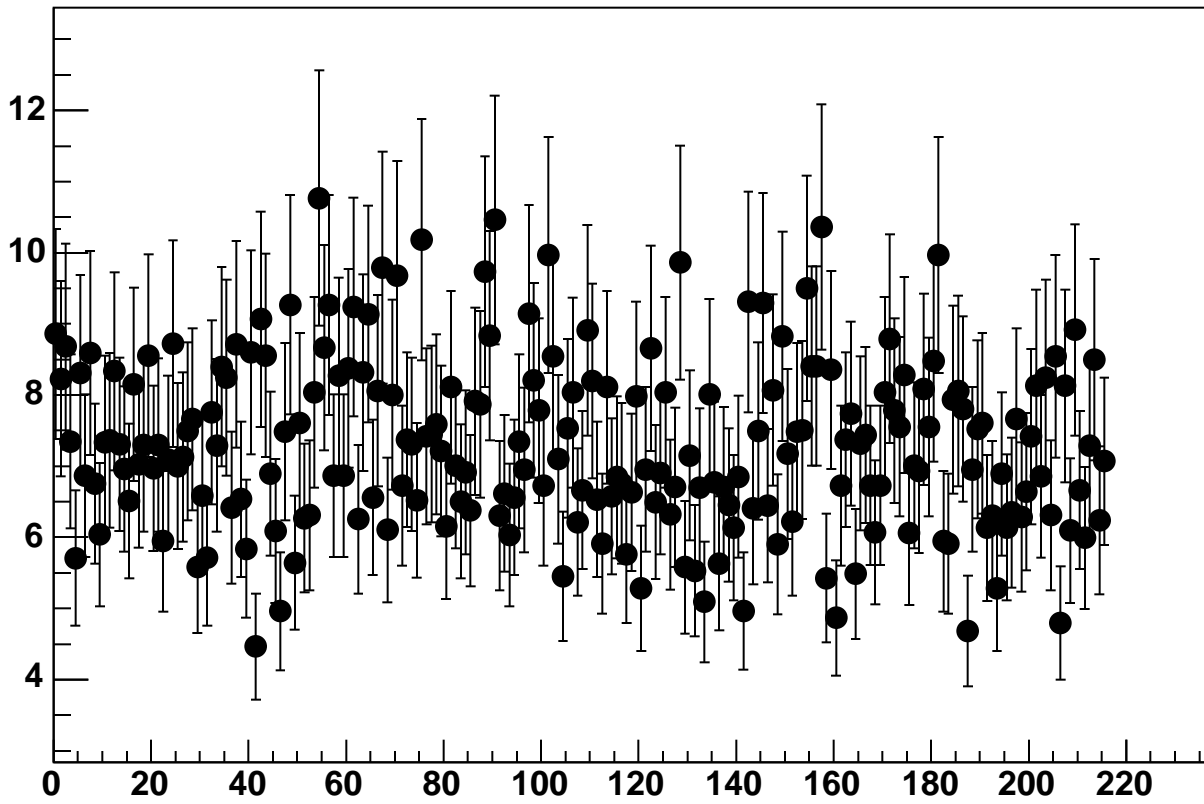
Enable 3, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



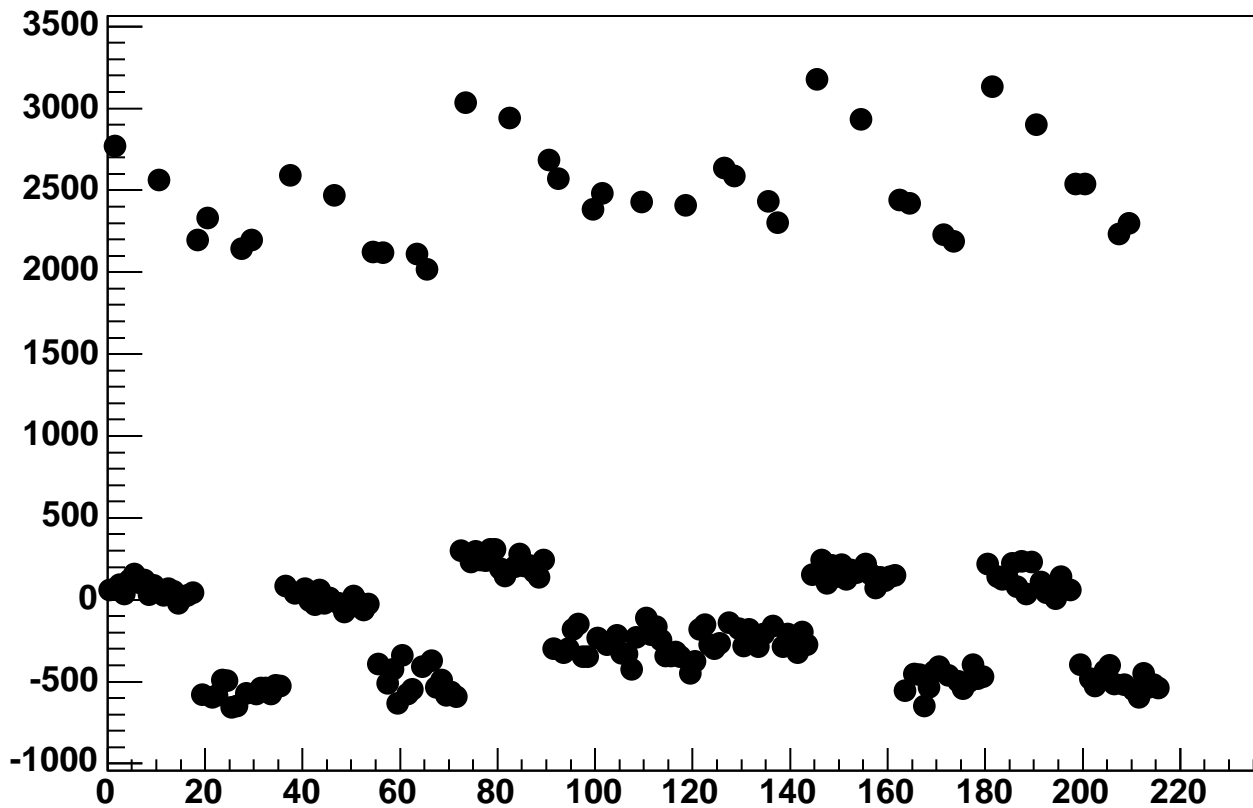
Enable 3, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



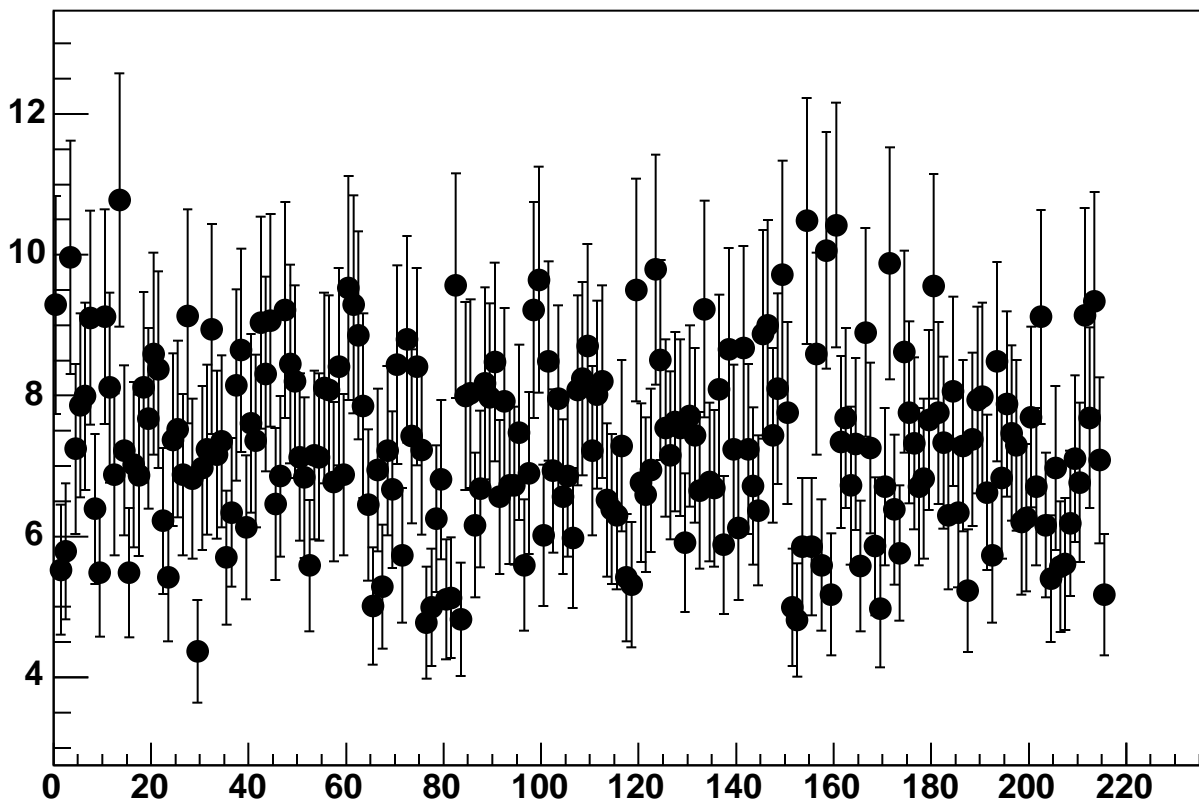
Enable 3, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



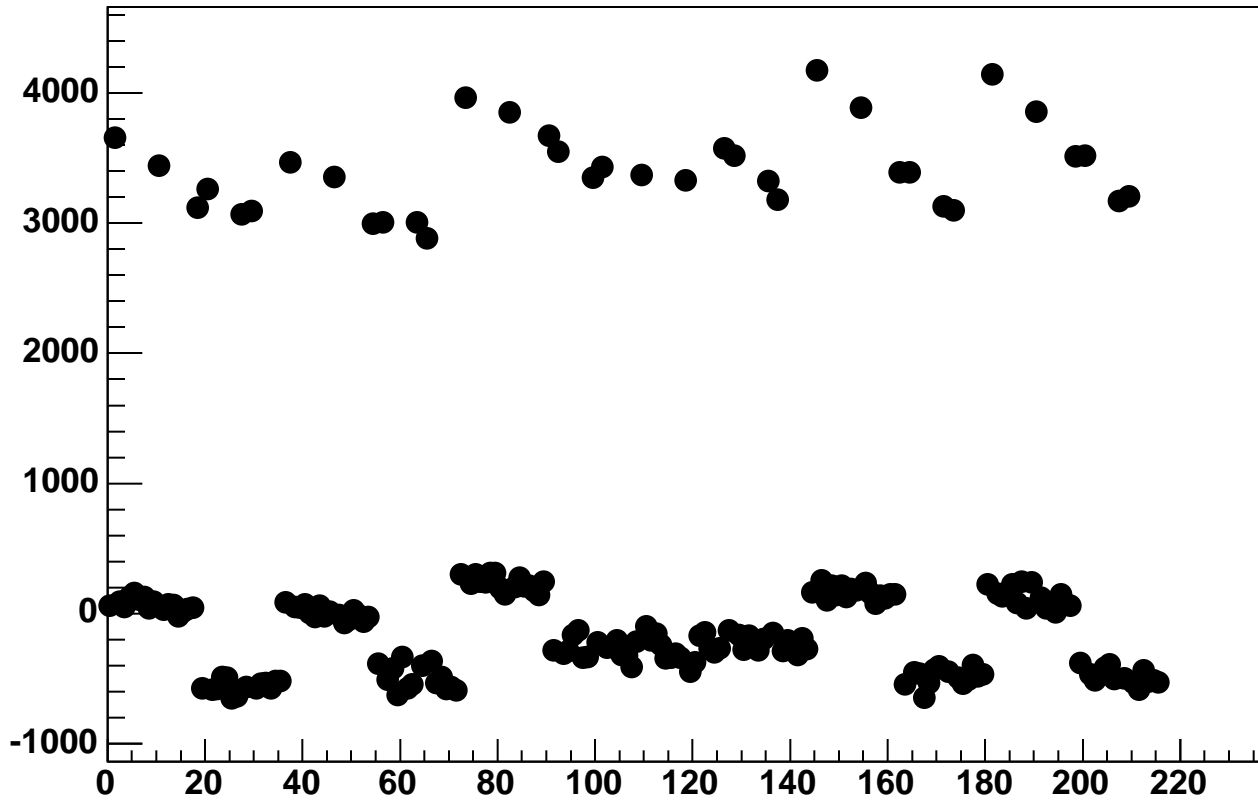
Enable 3, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



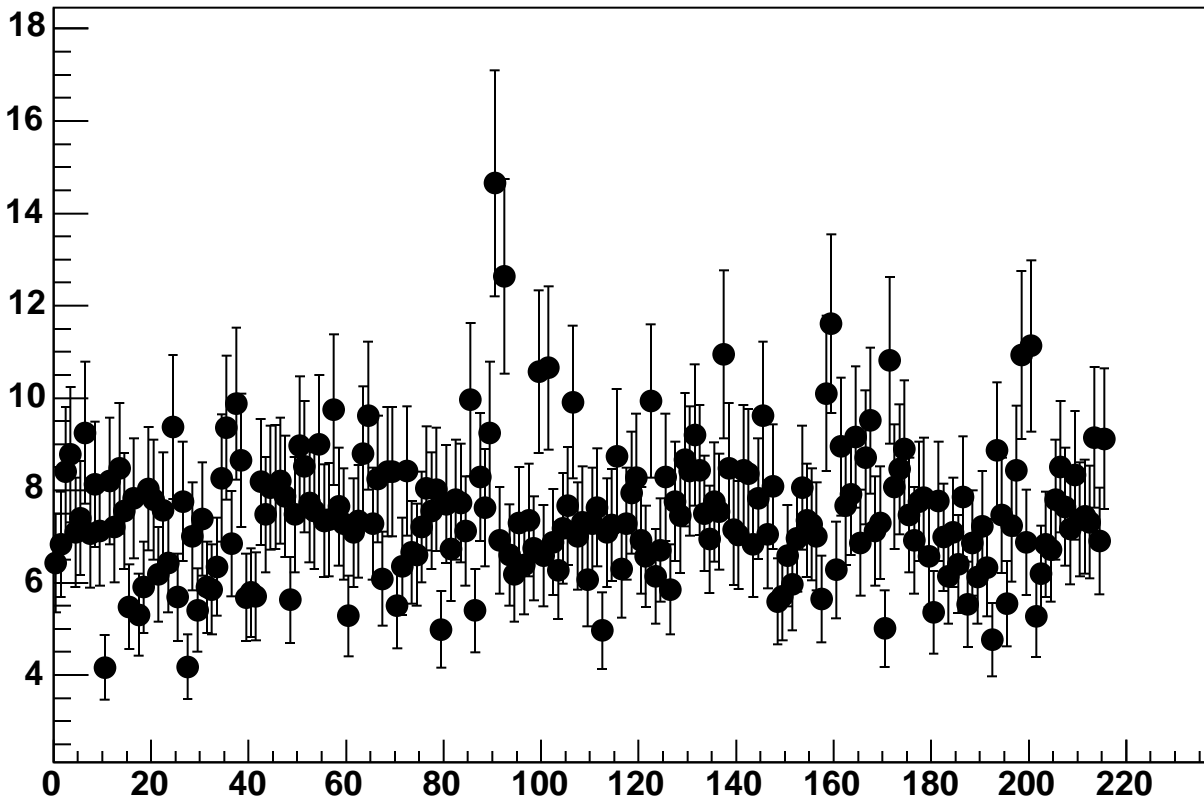
Enable 3, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



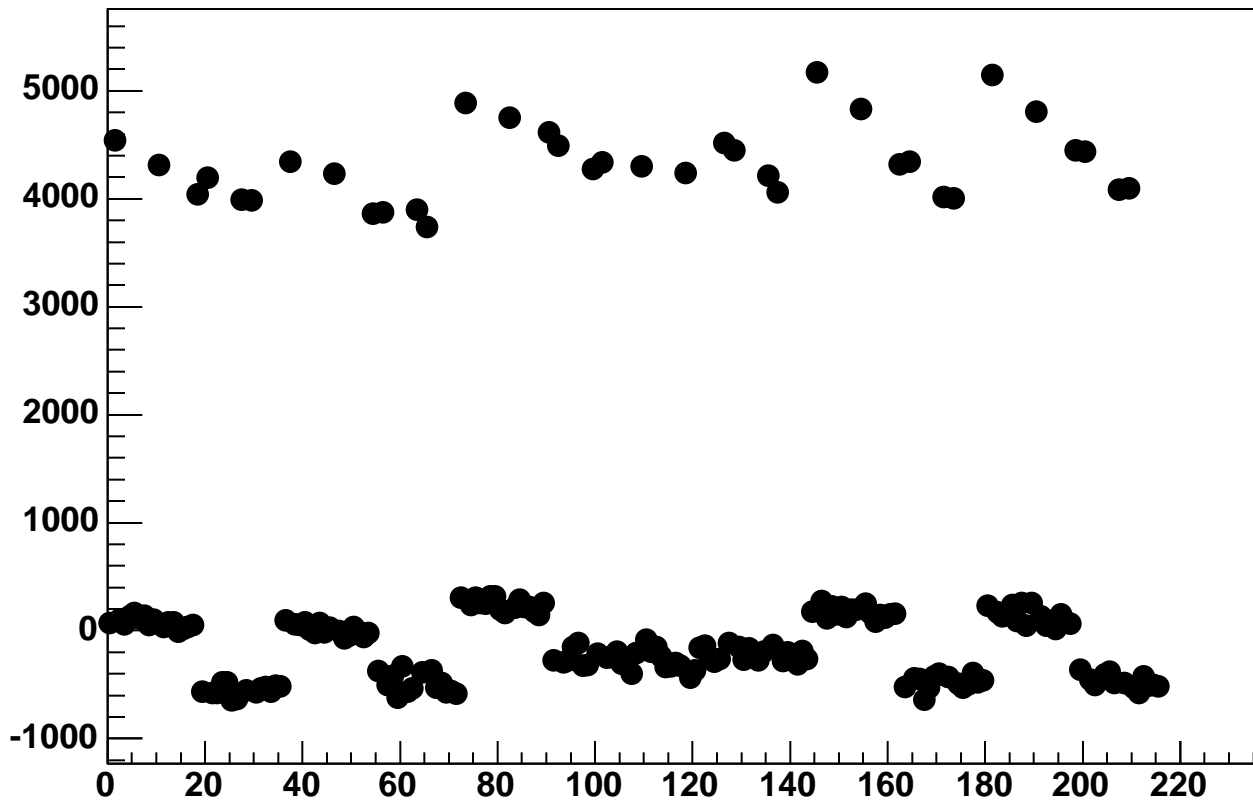
Enable 3, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



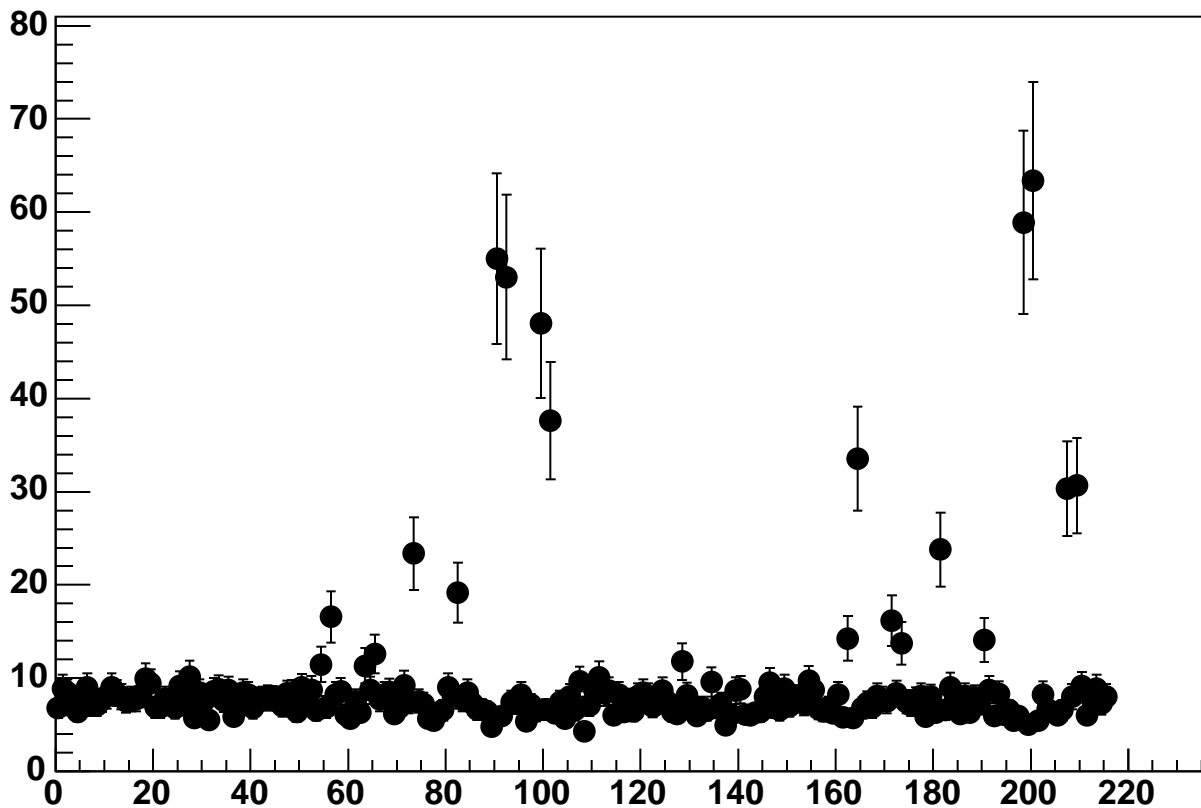
Enable 3, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



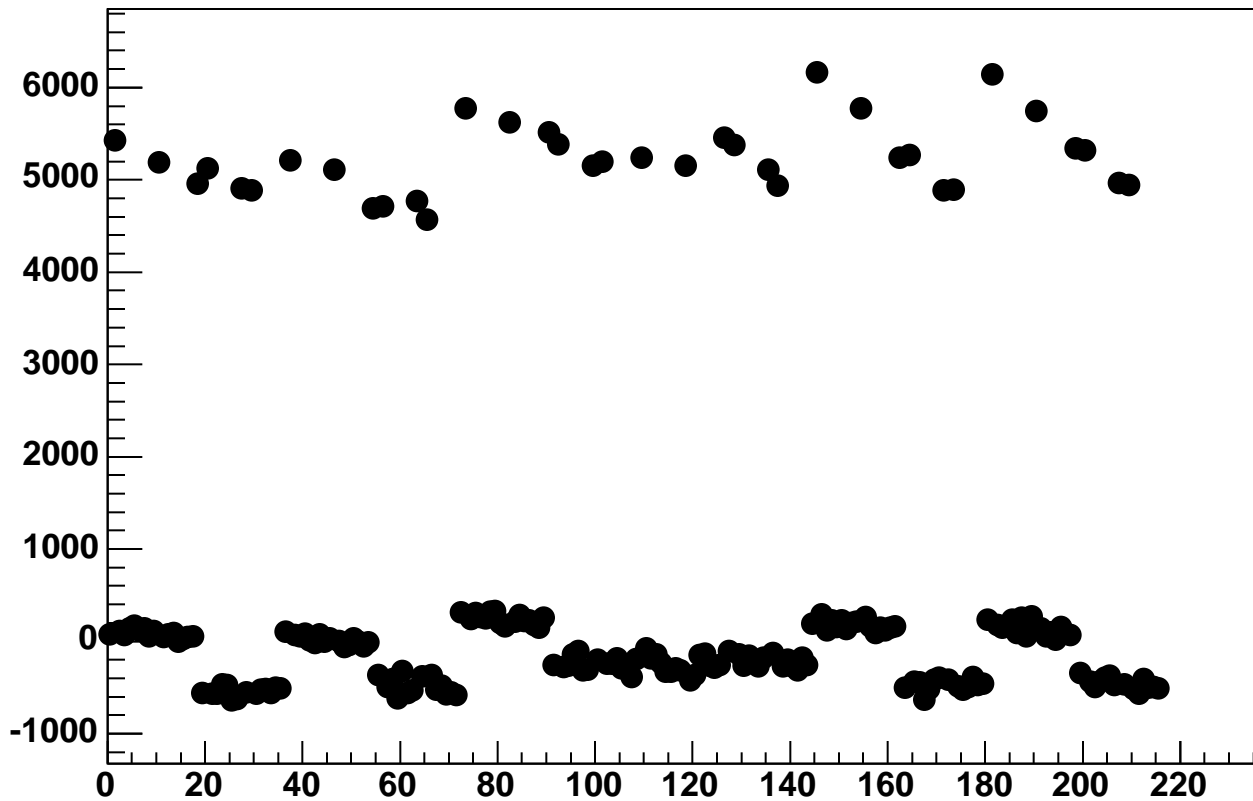
Enable 3, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



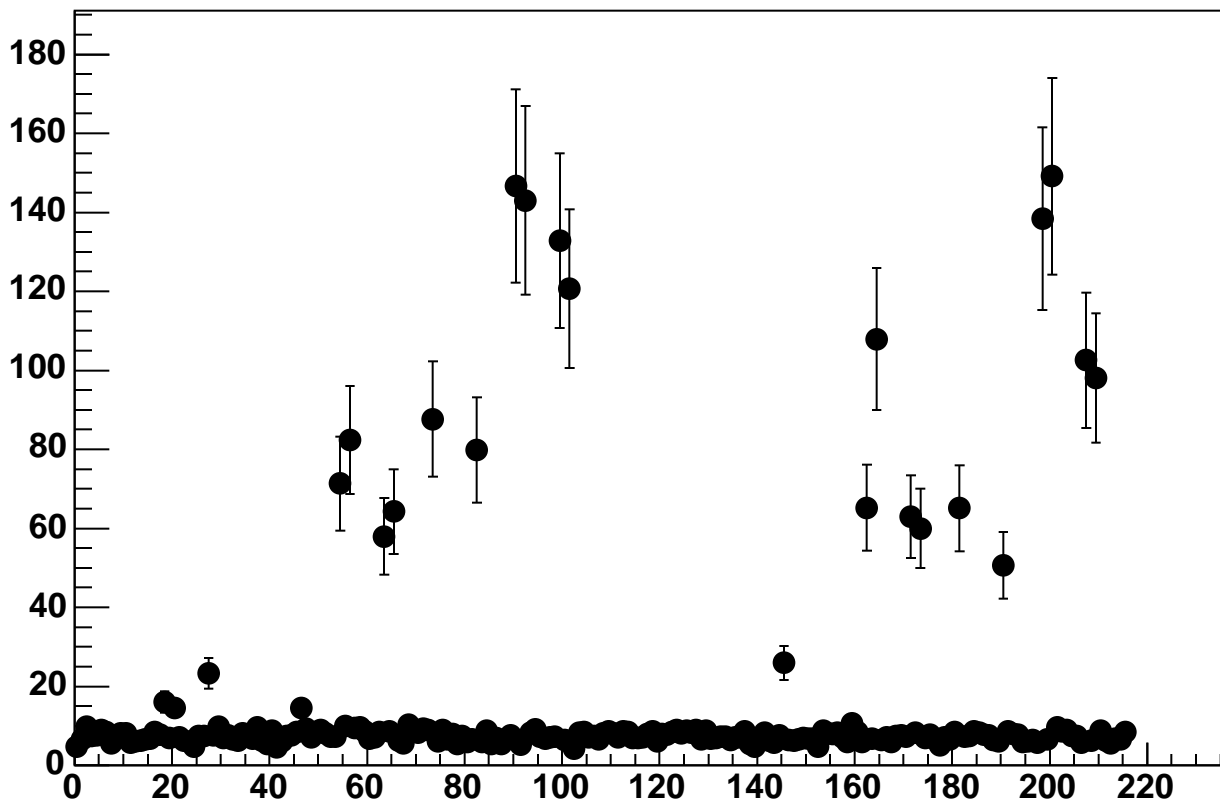
Enable 3, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



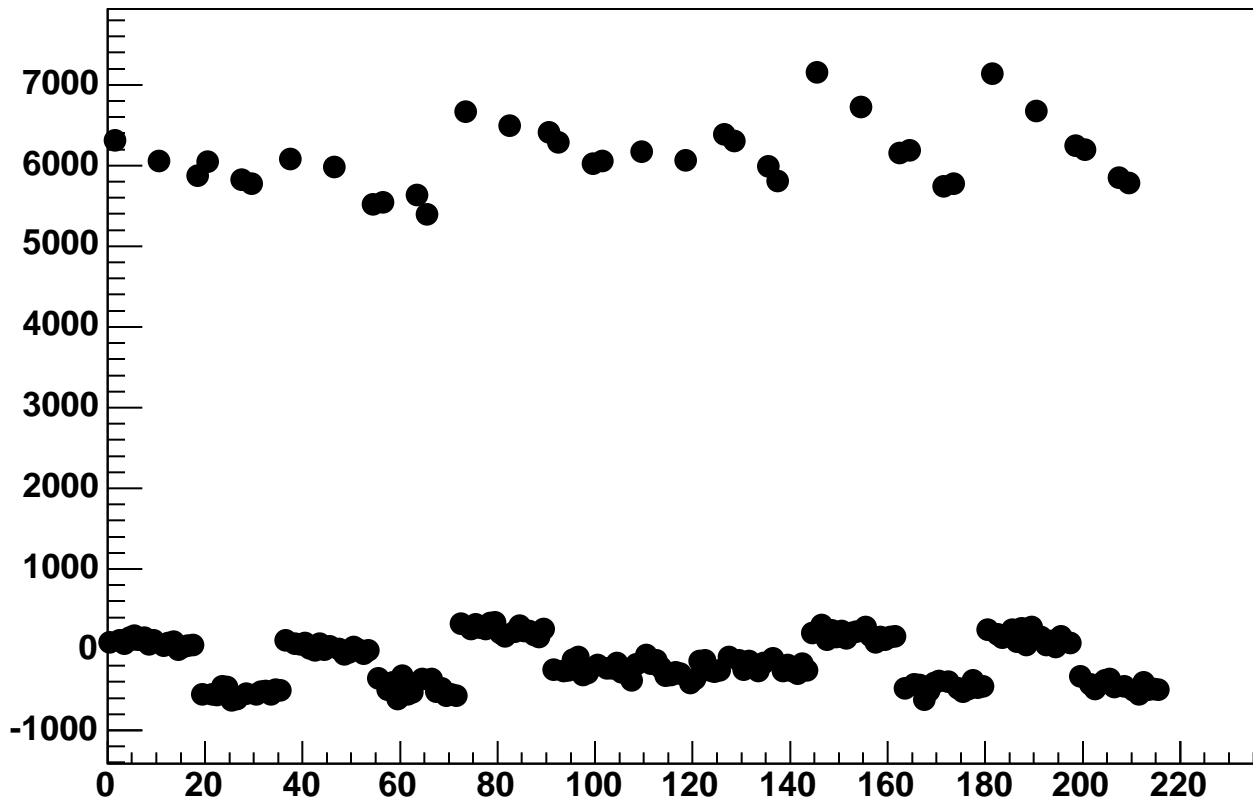
Enable 3, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



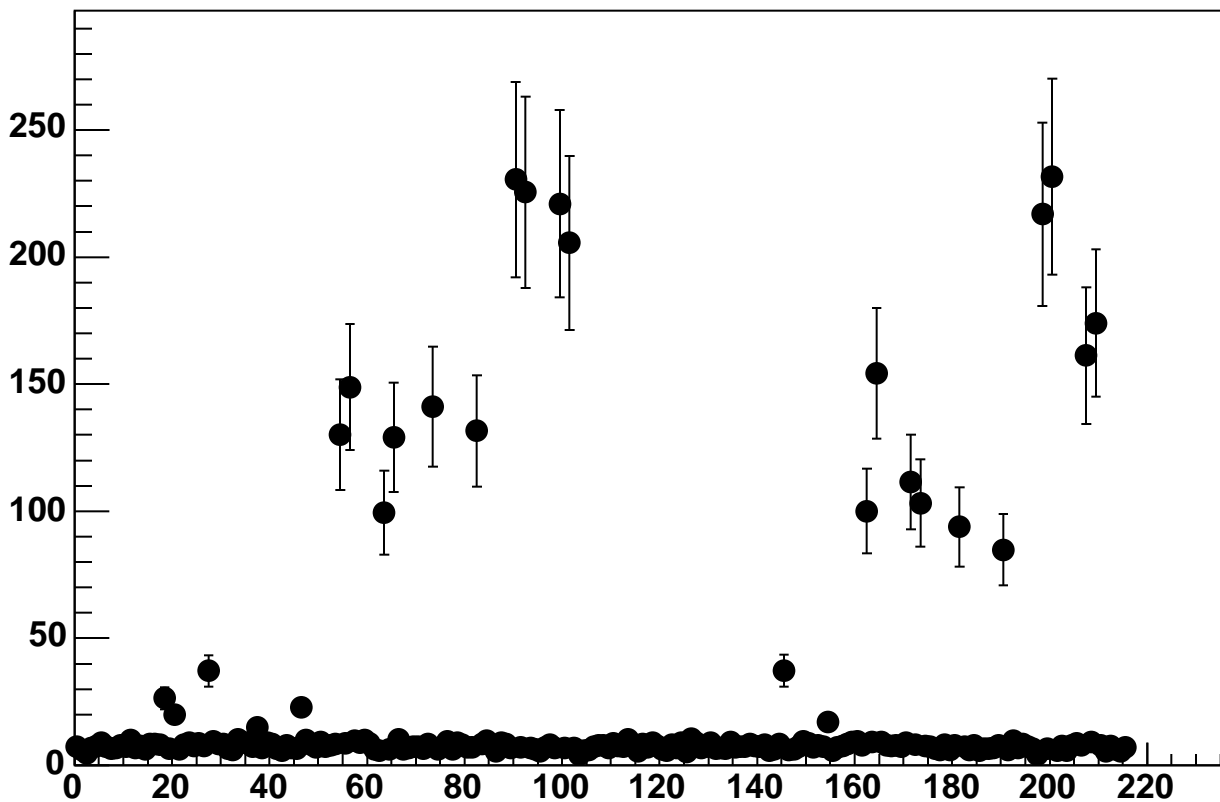
Enable 3, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



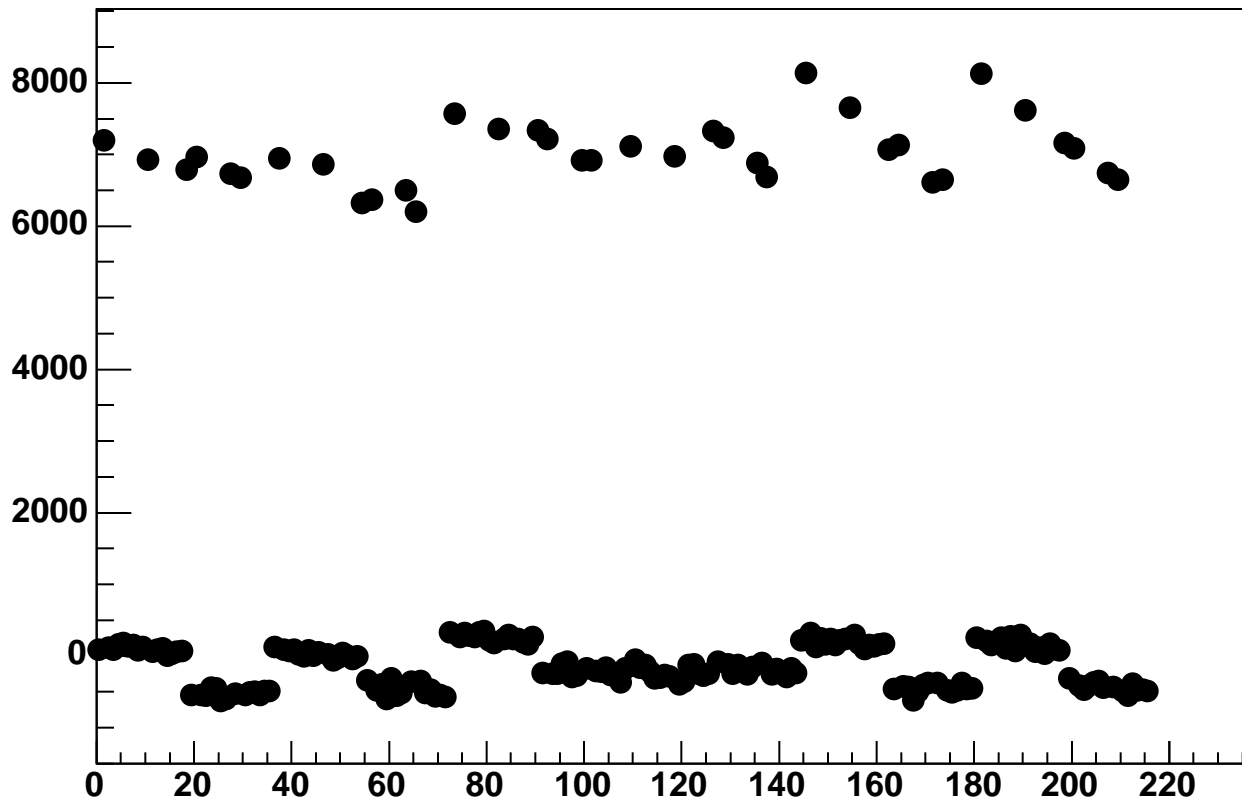
Enable 3, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



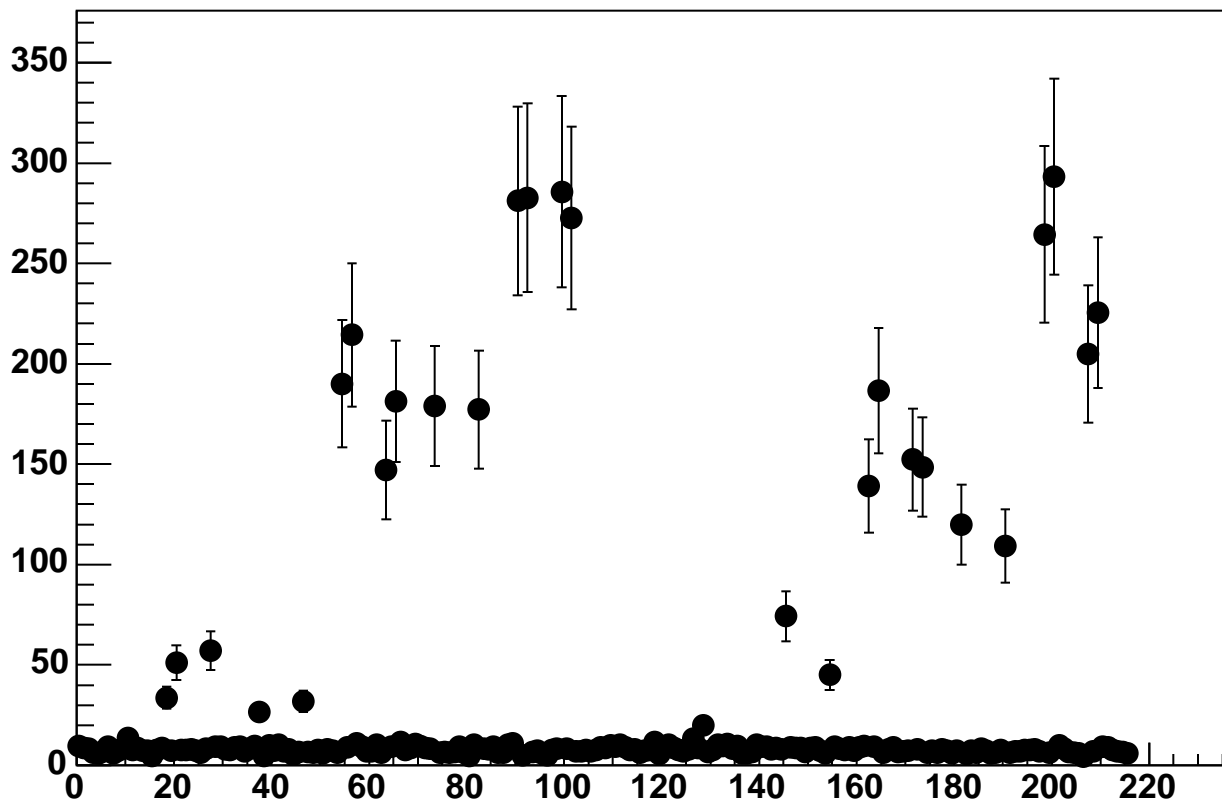
Enable 3, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



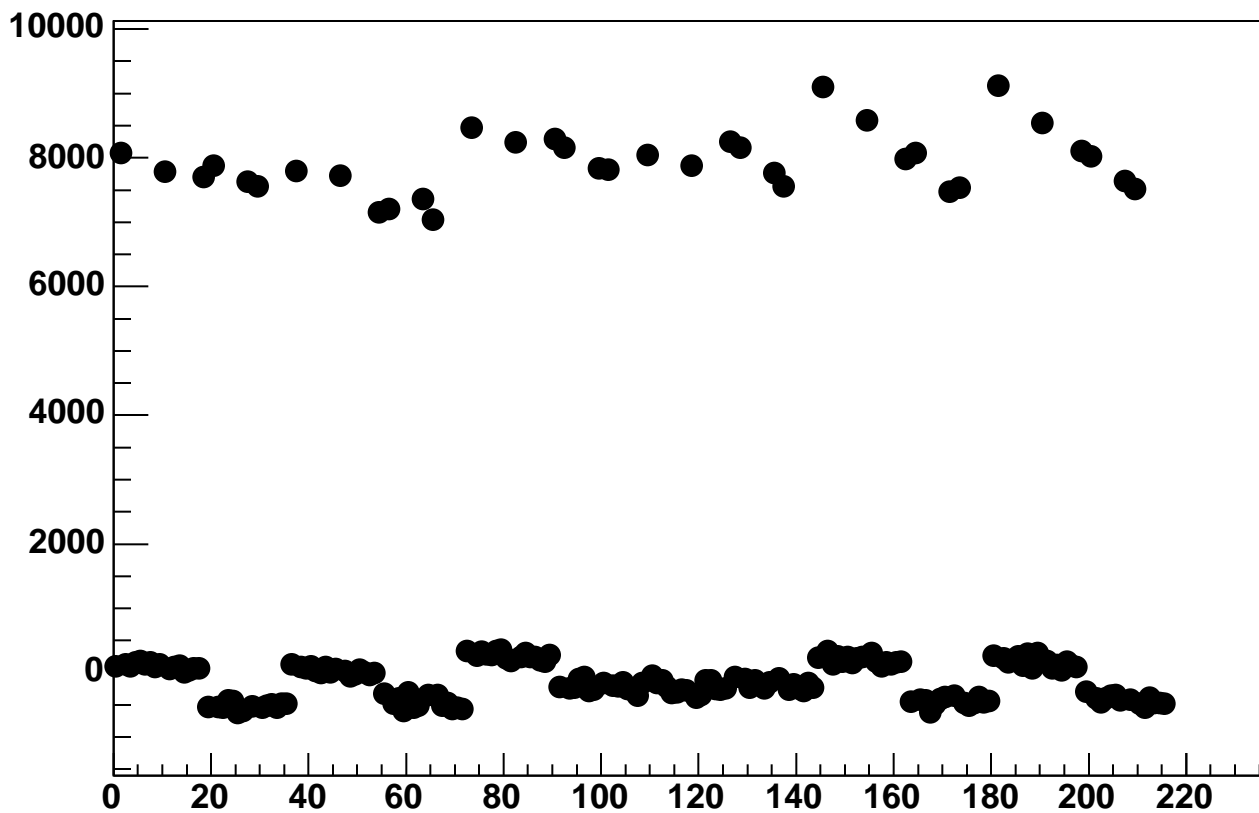
Enable 3, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



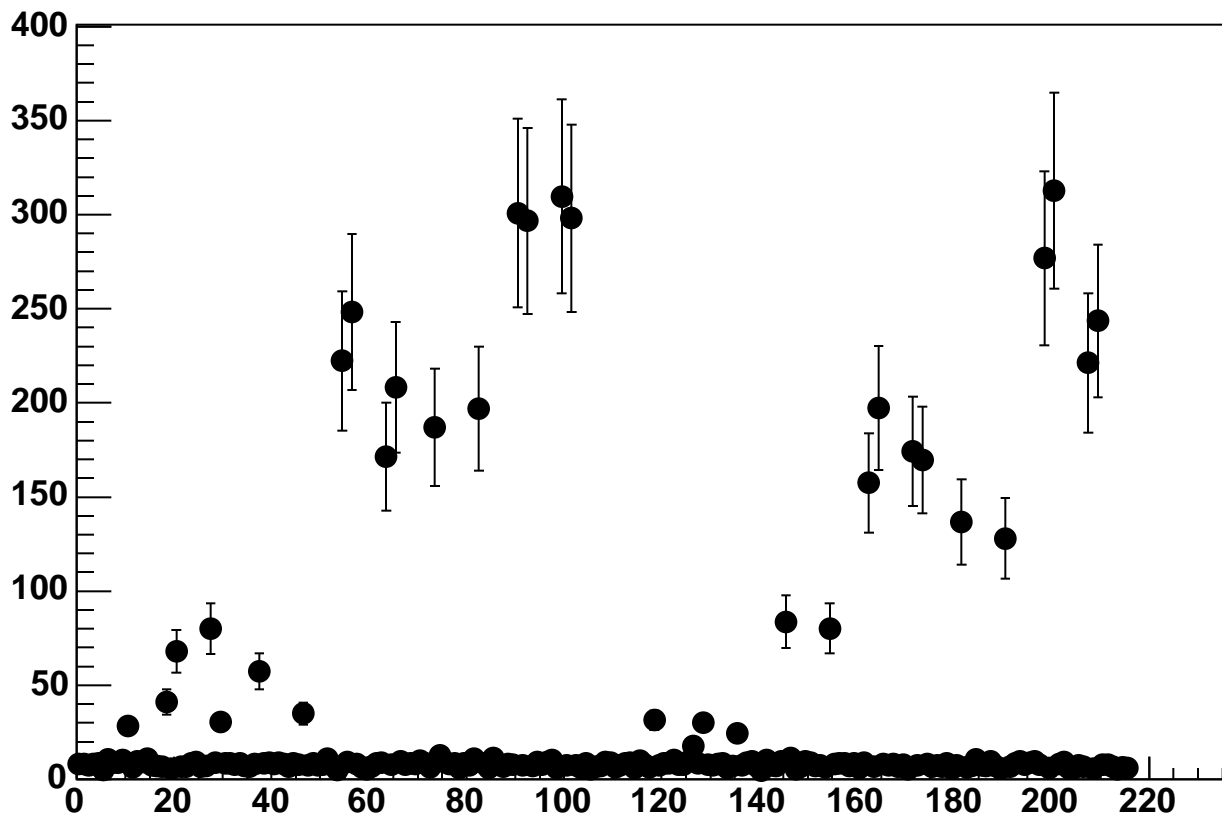
Enable 3, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



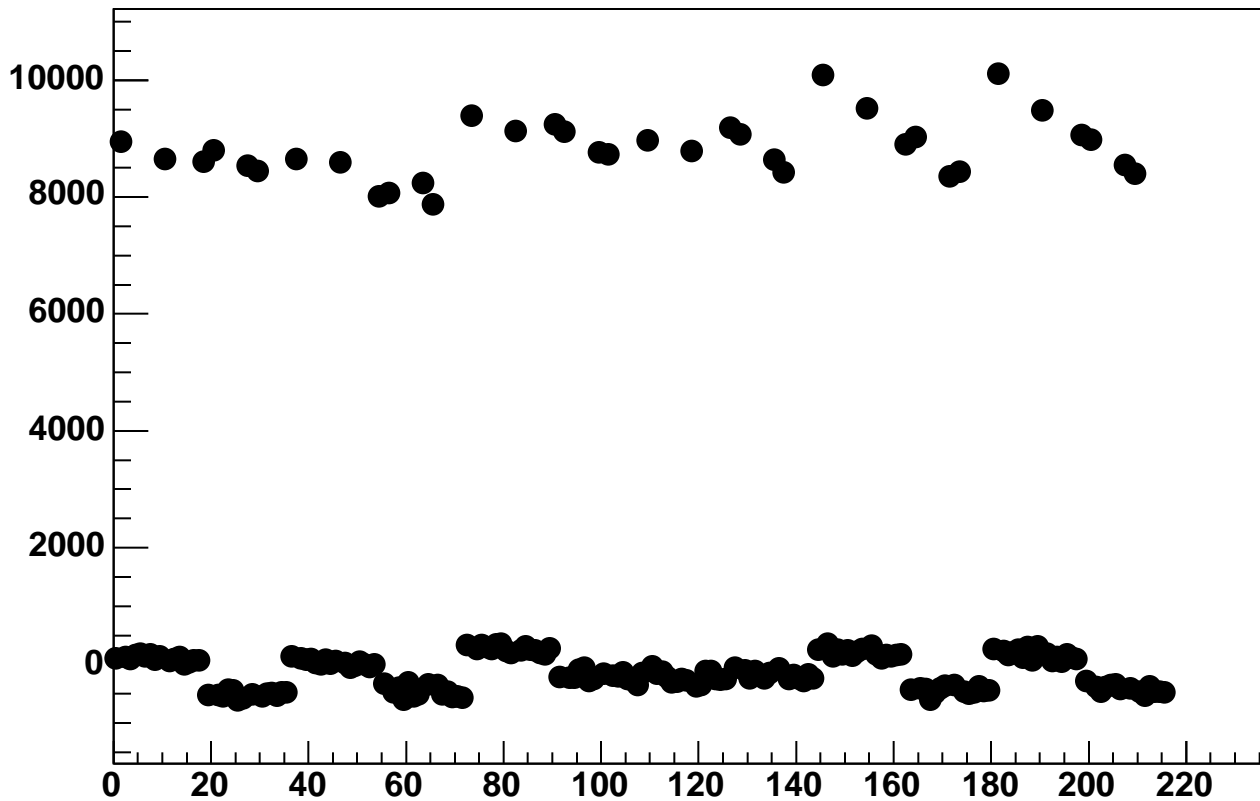
Enable 3, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



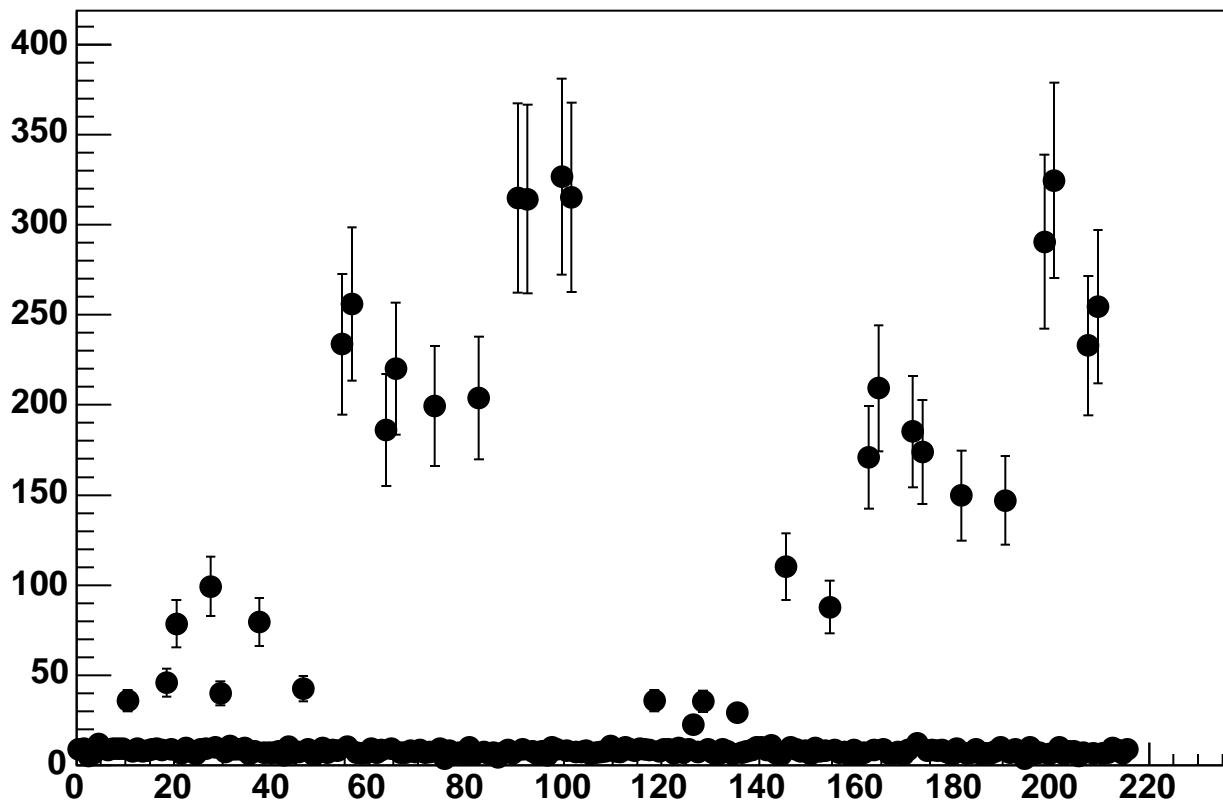
Enable 3, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



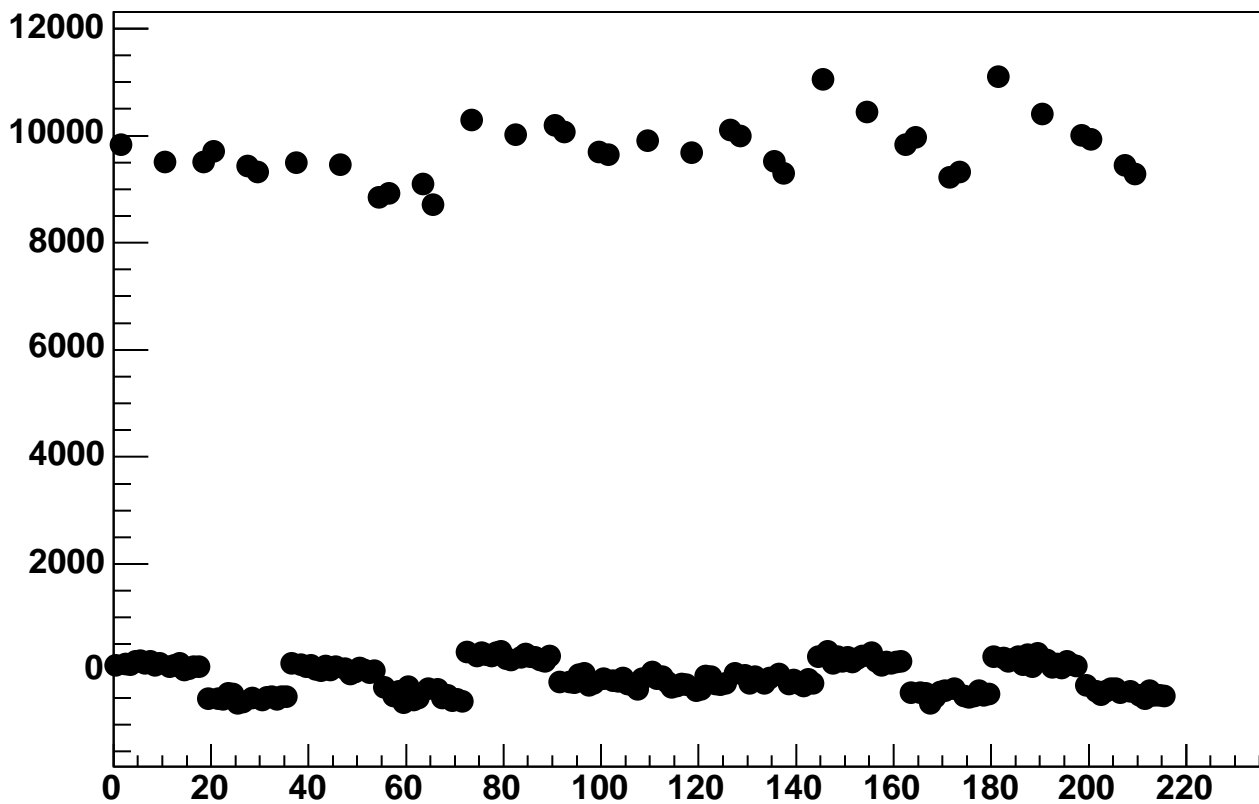
Enable 3, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



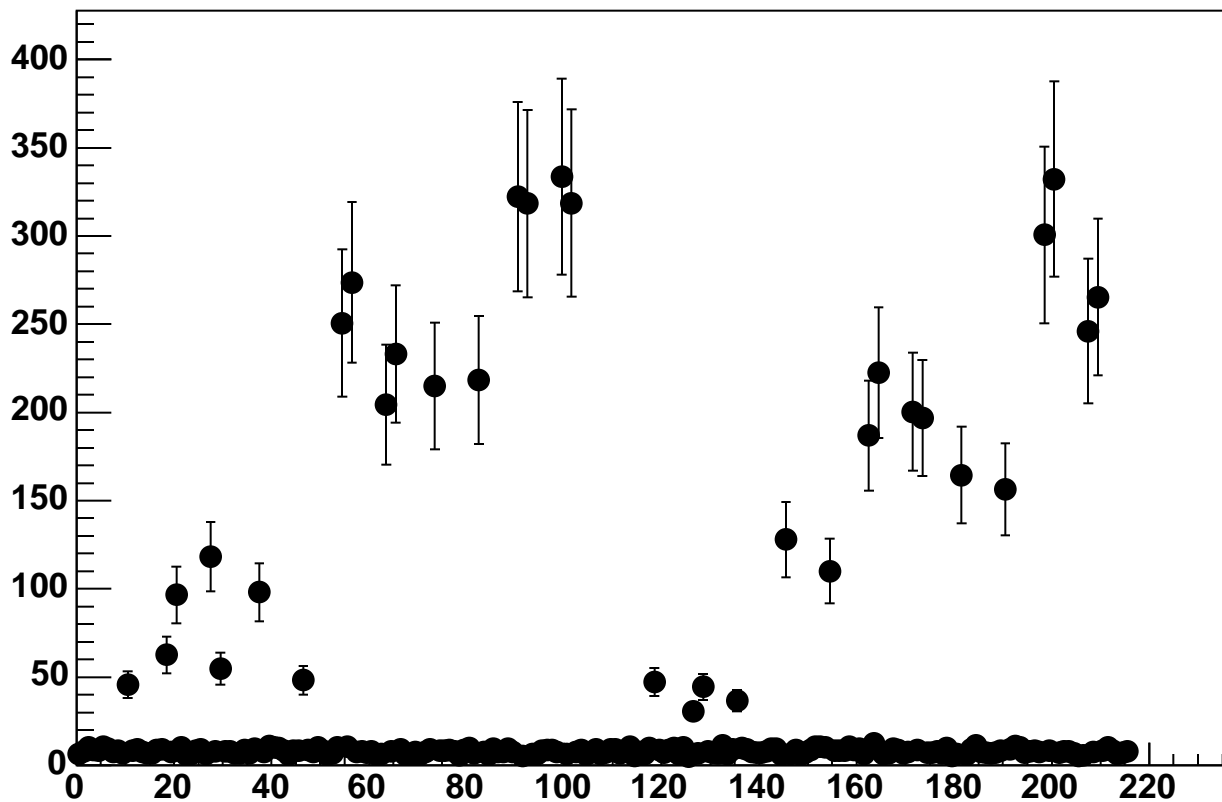
Enable 3, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



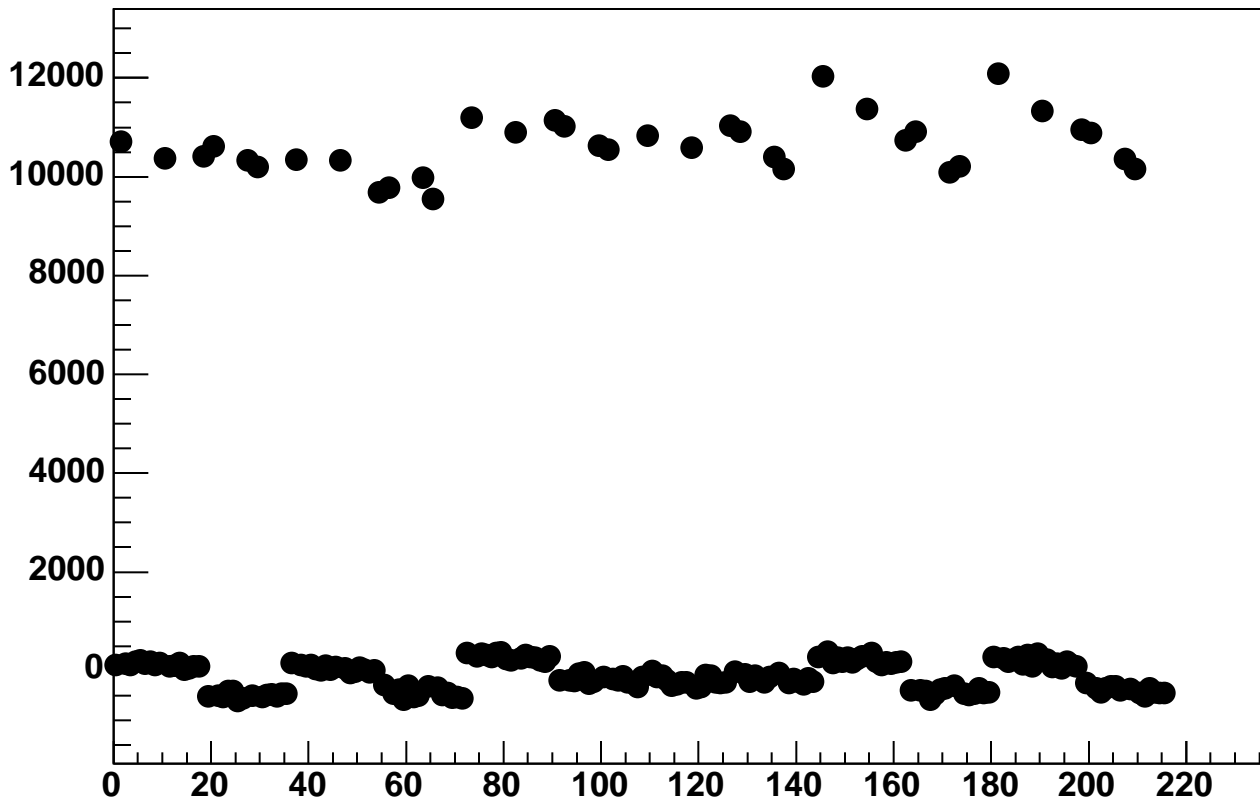
Enable 3, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



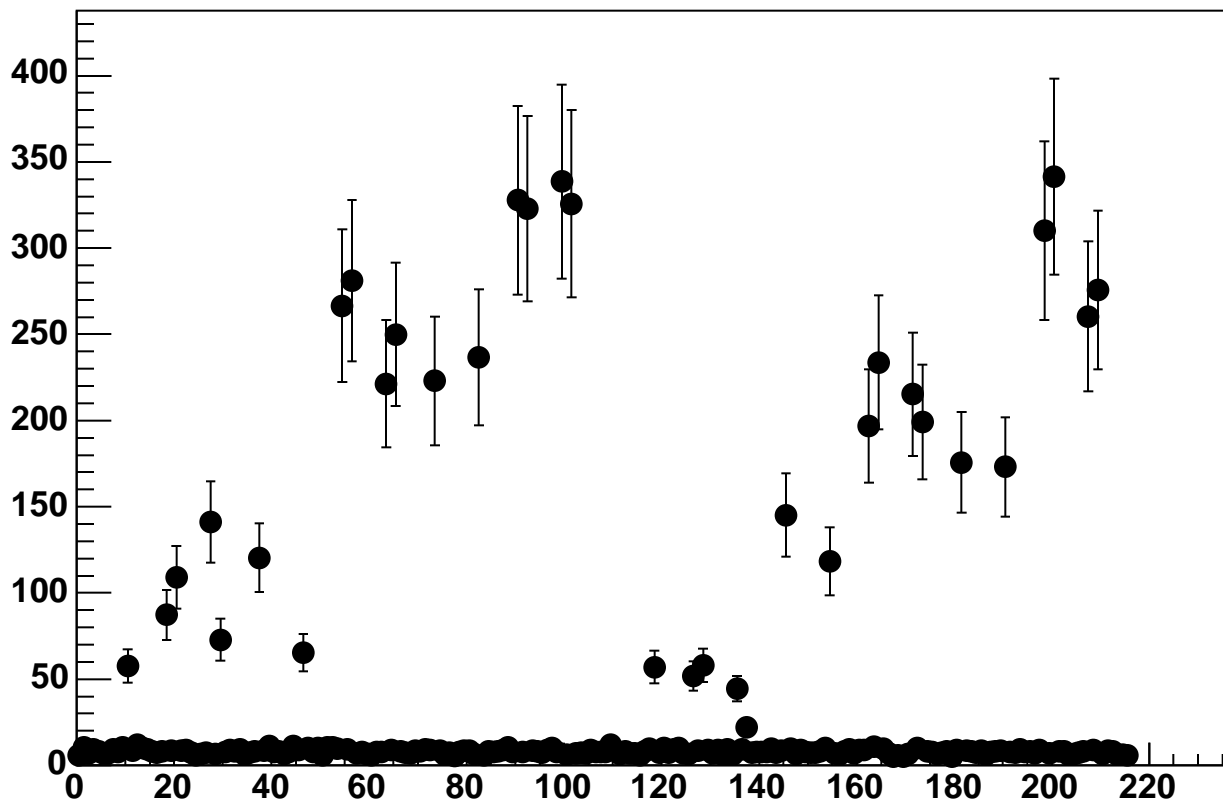
Enable 3, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



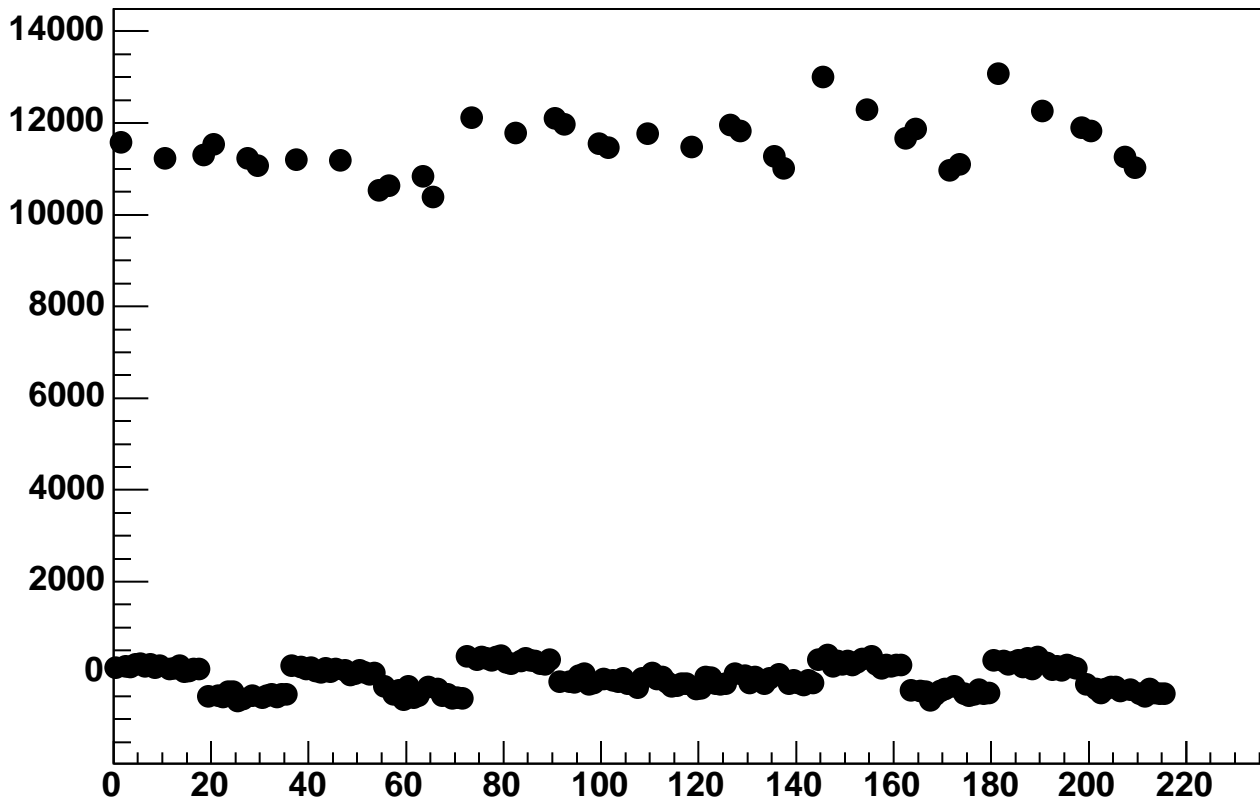
Enable 3, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



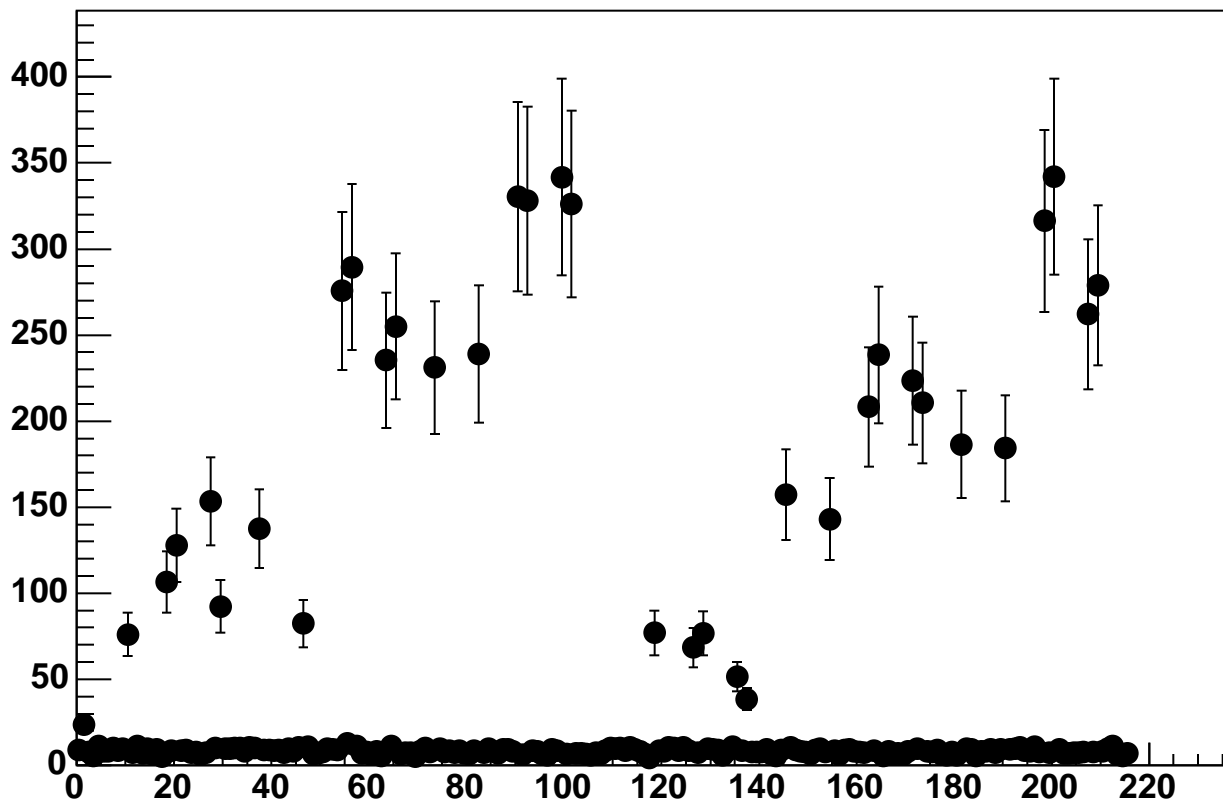
Enable 3, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



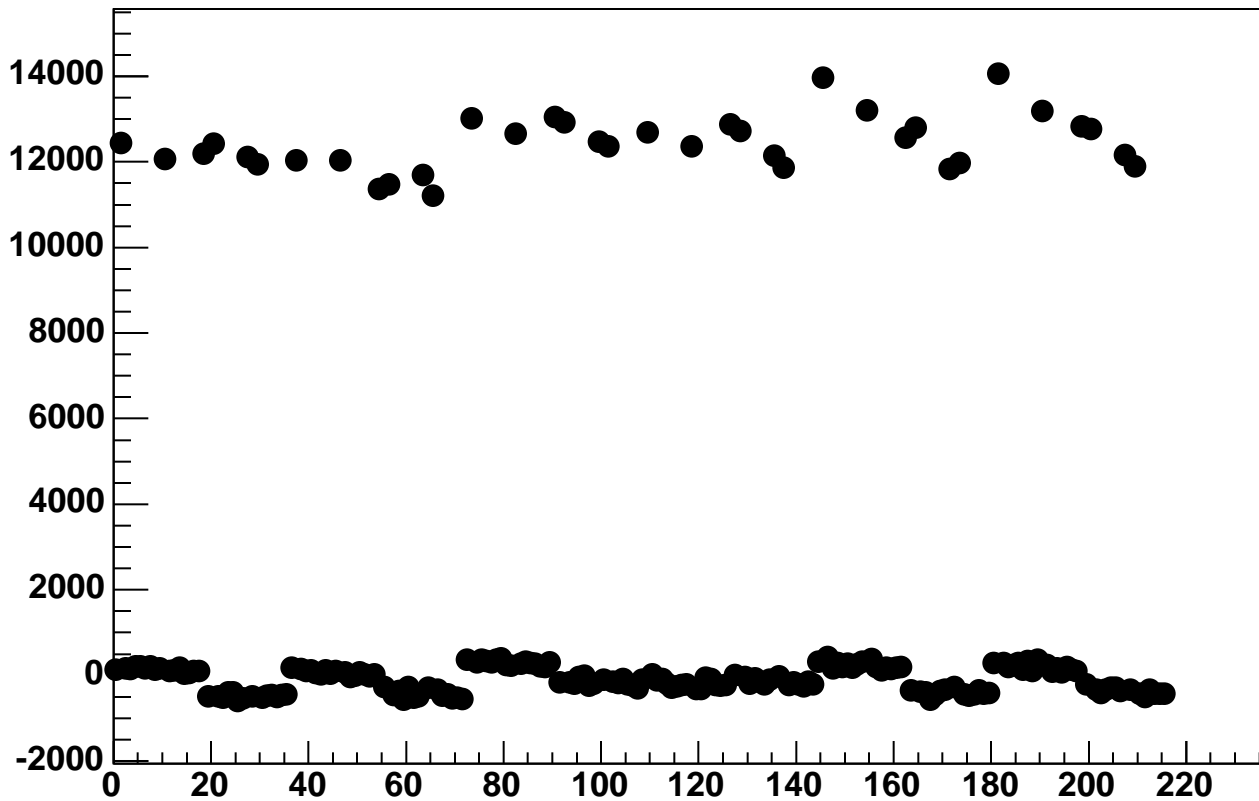
Enable 3, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



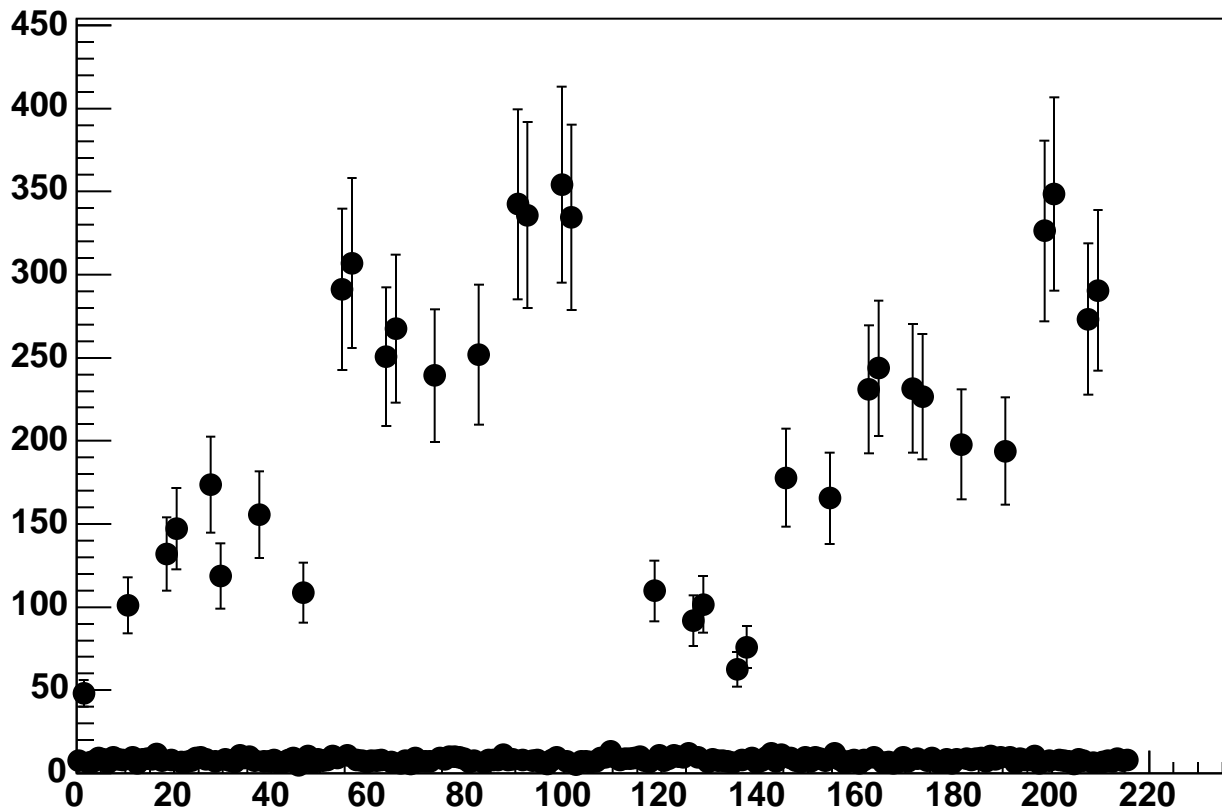
Enable 3, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



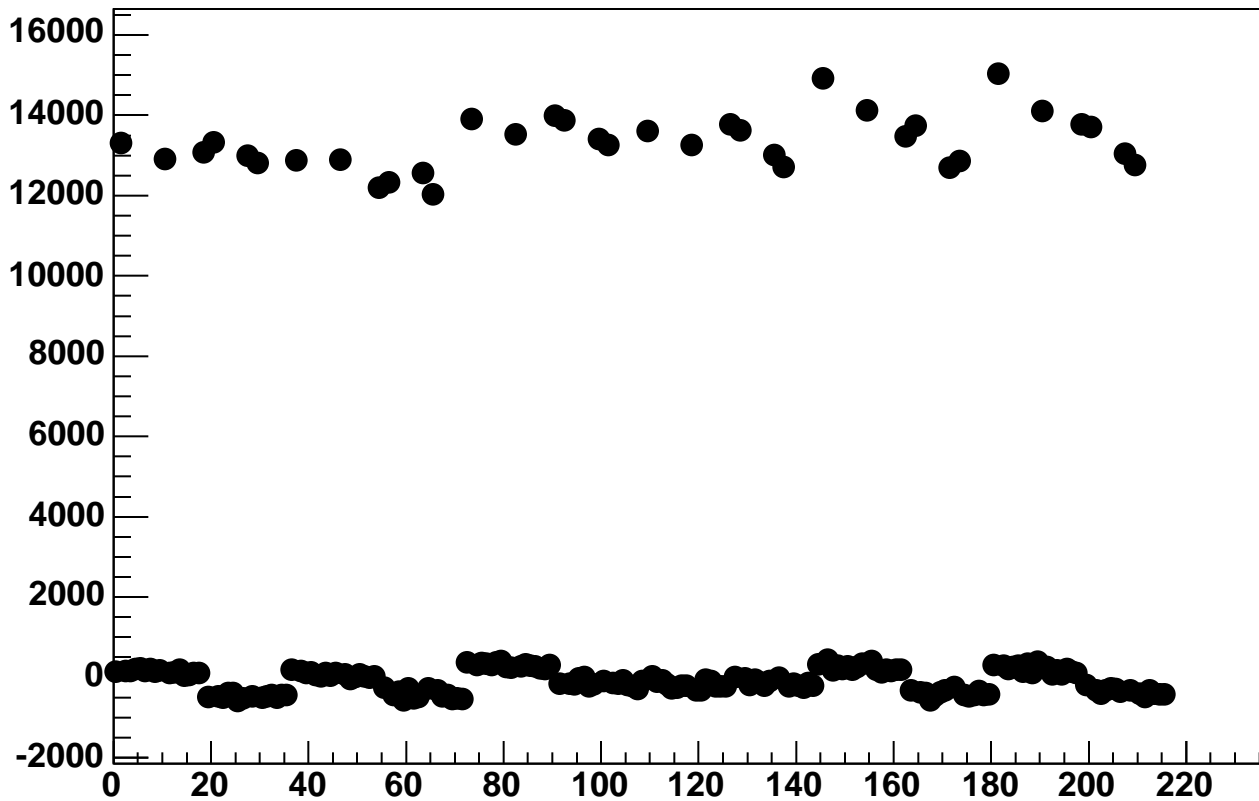
Enable 3, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



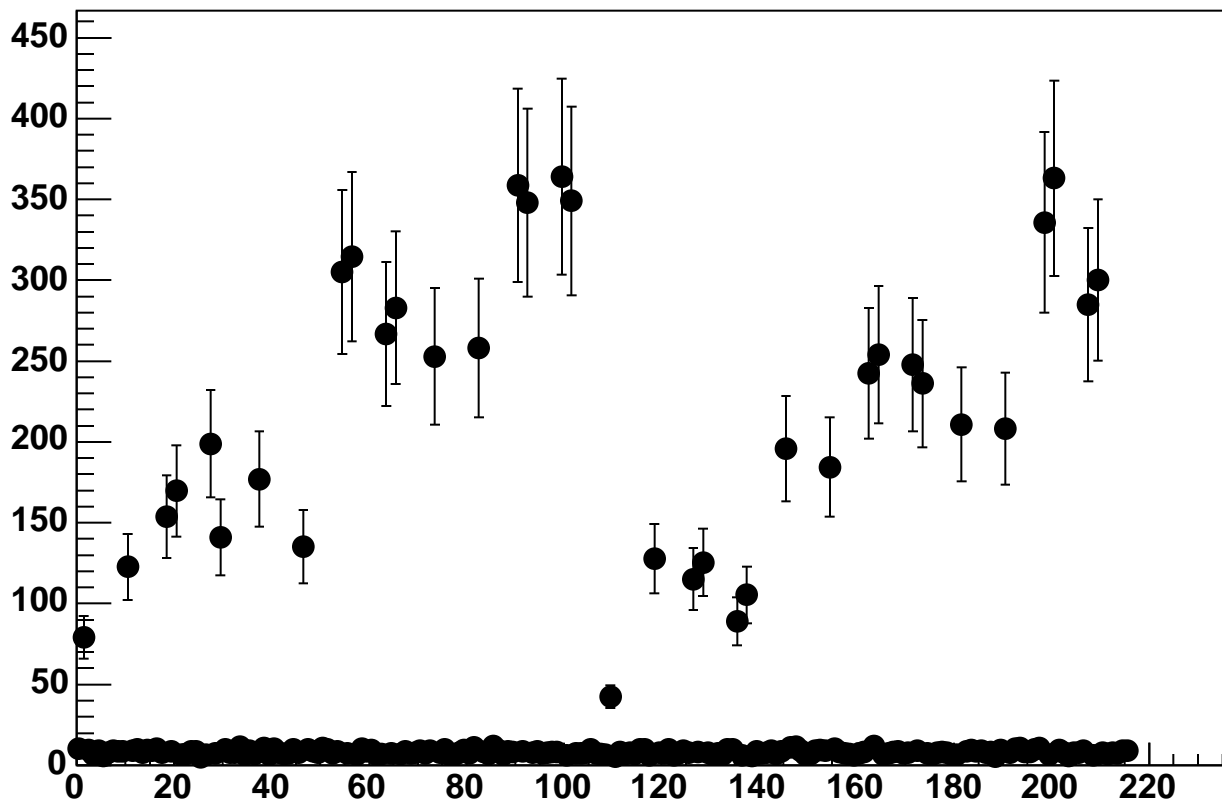
Enable 3, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



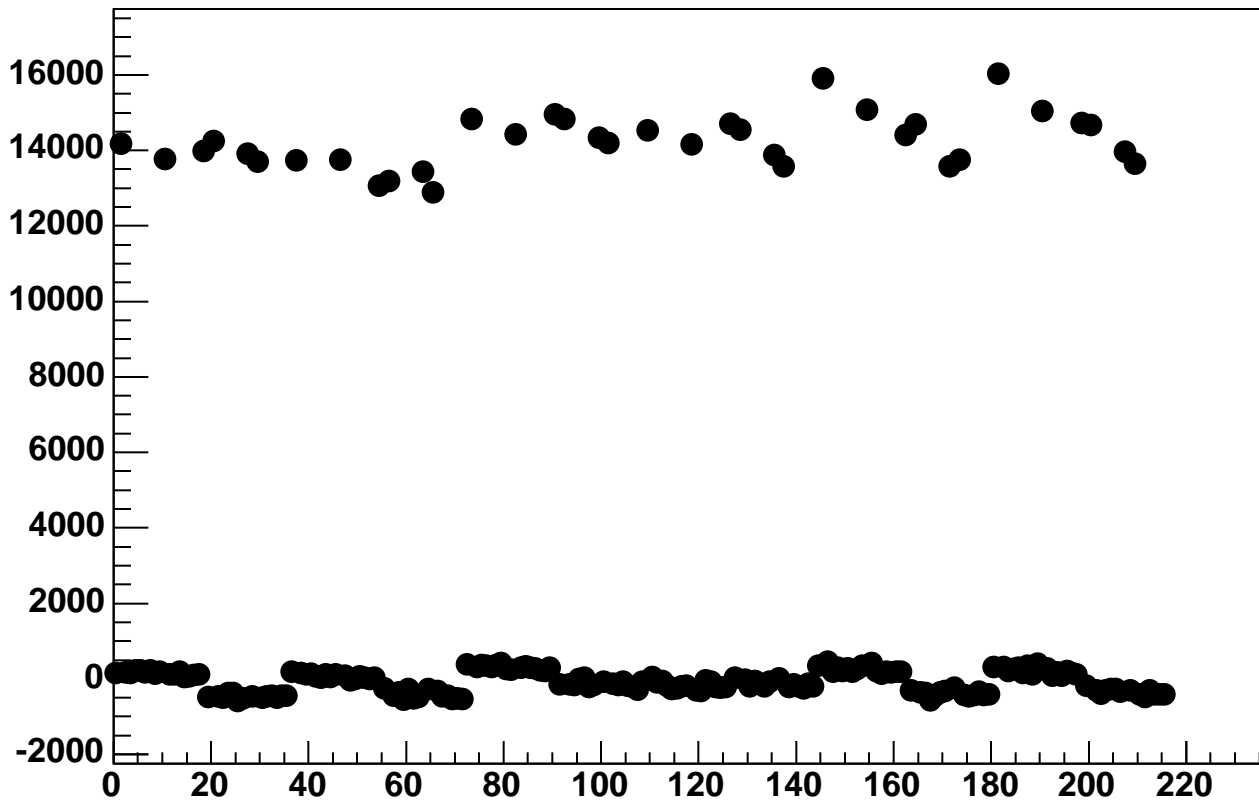
Enable 3, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



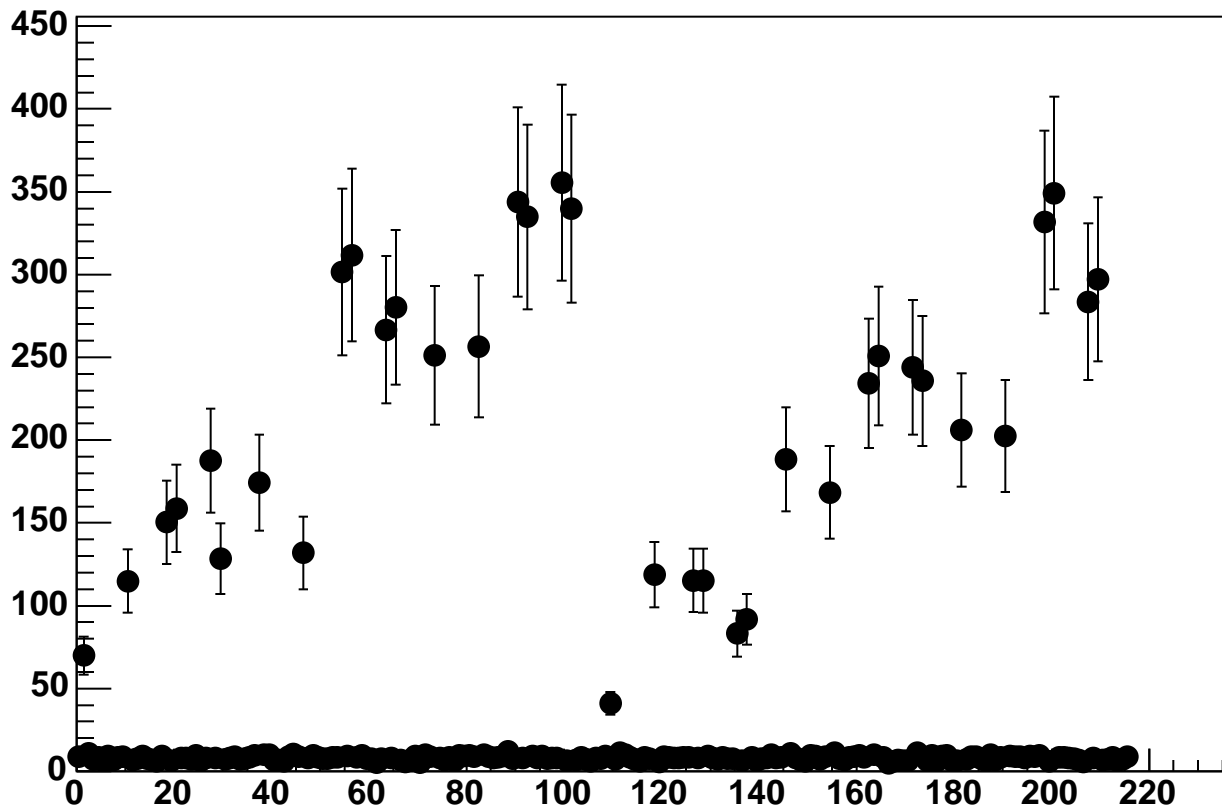
Enable 3, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



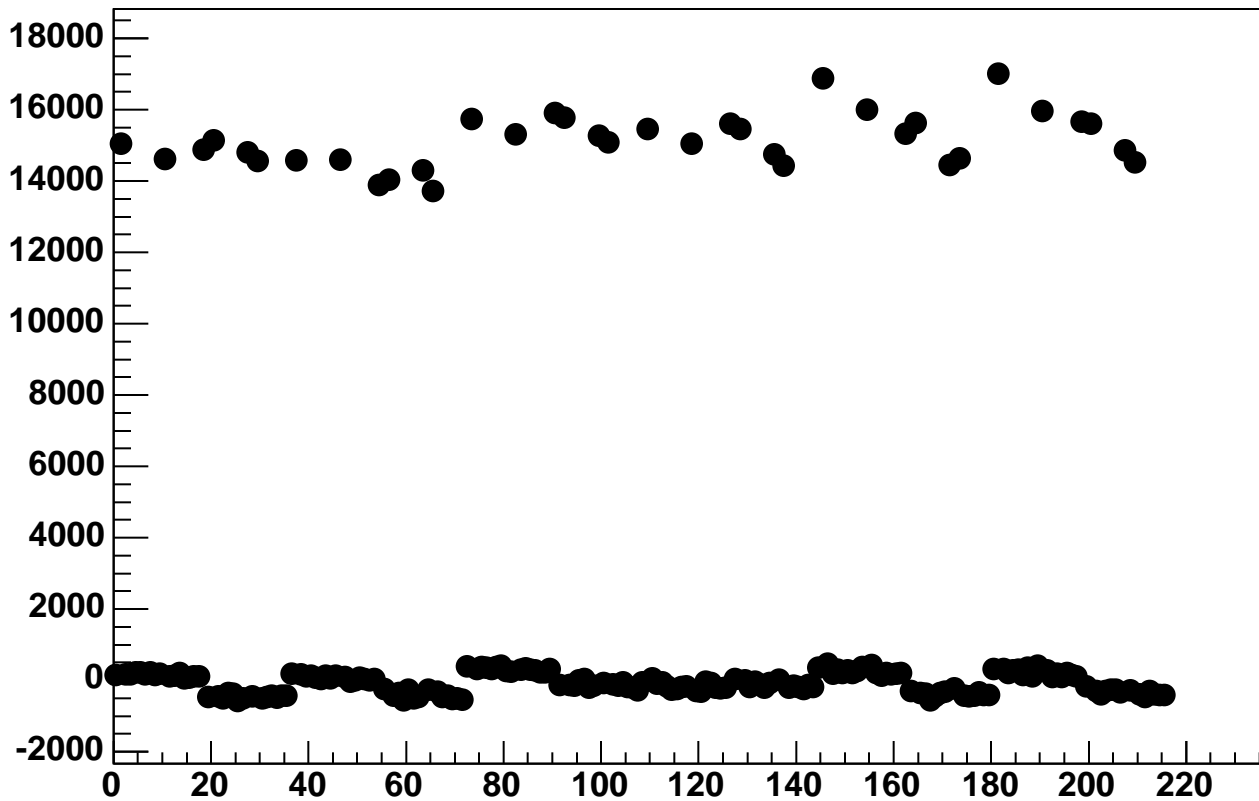
Enable 3, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



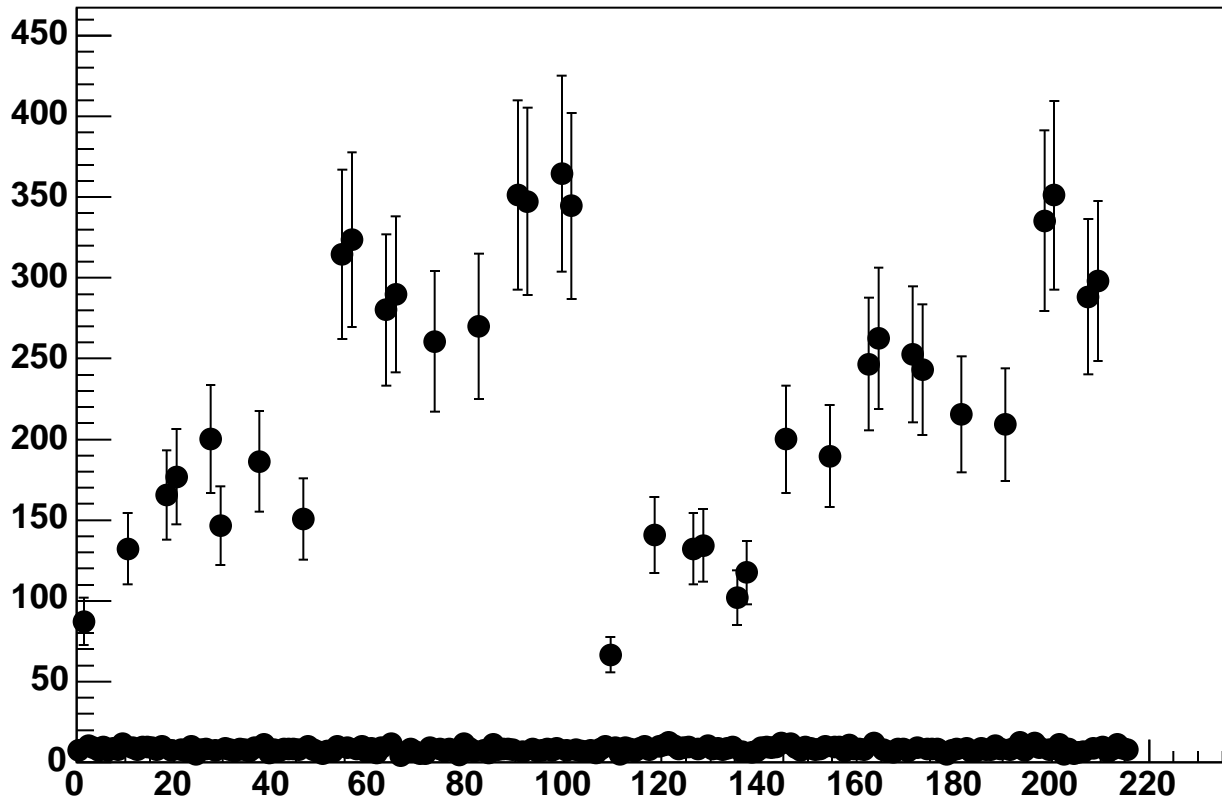
Enable 3, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



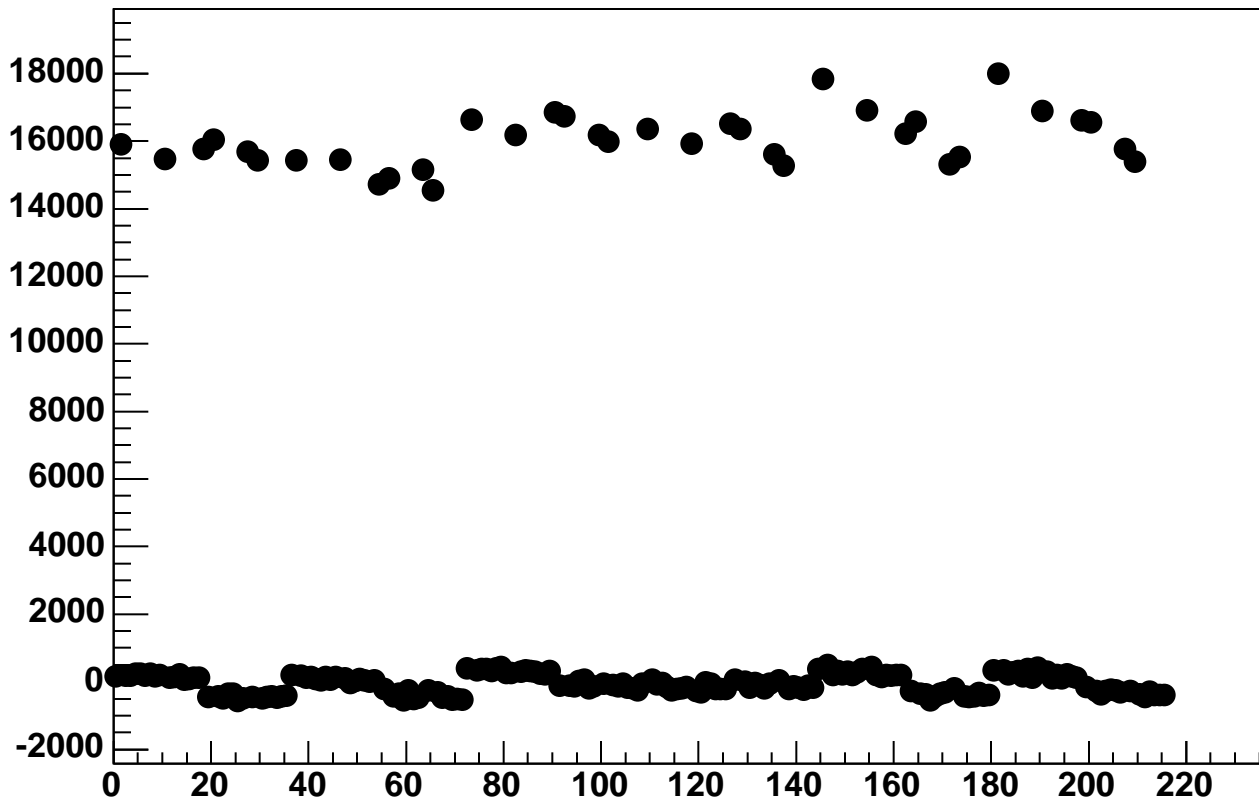
Enable 3, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



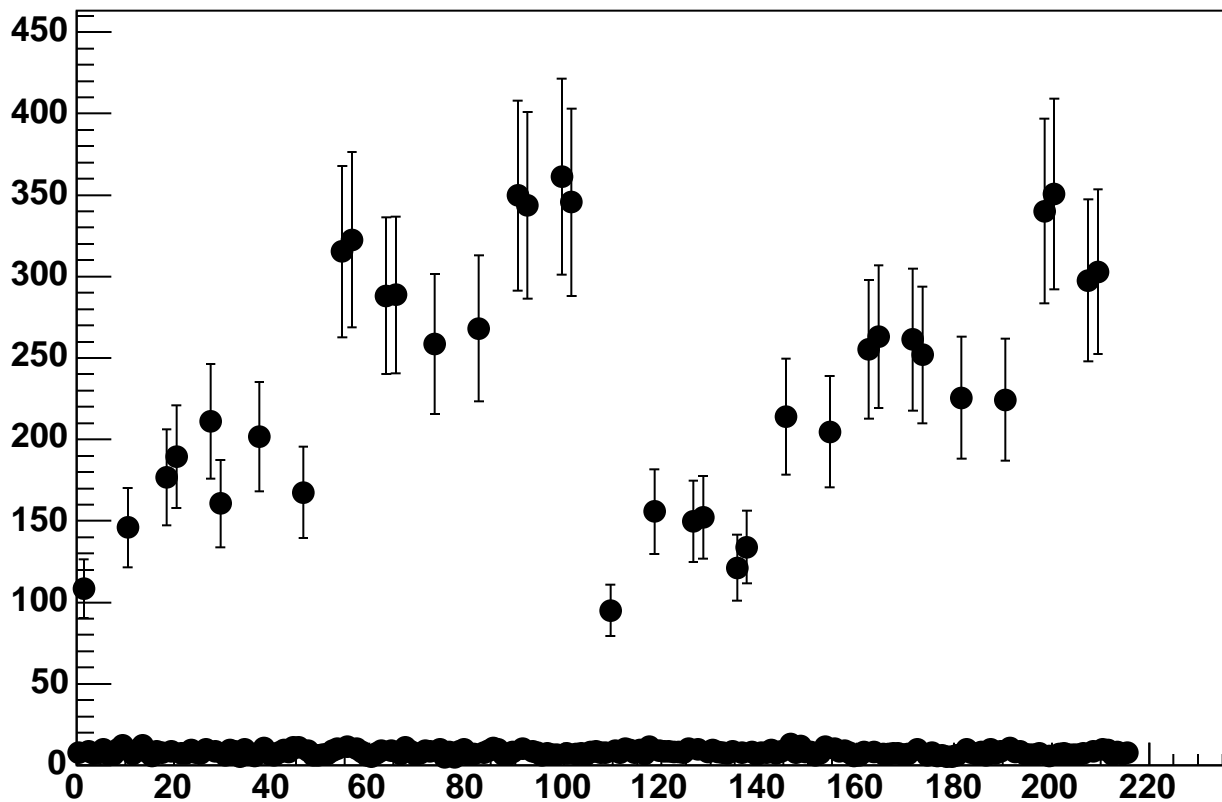
Enable 3, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



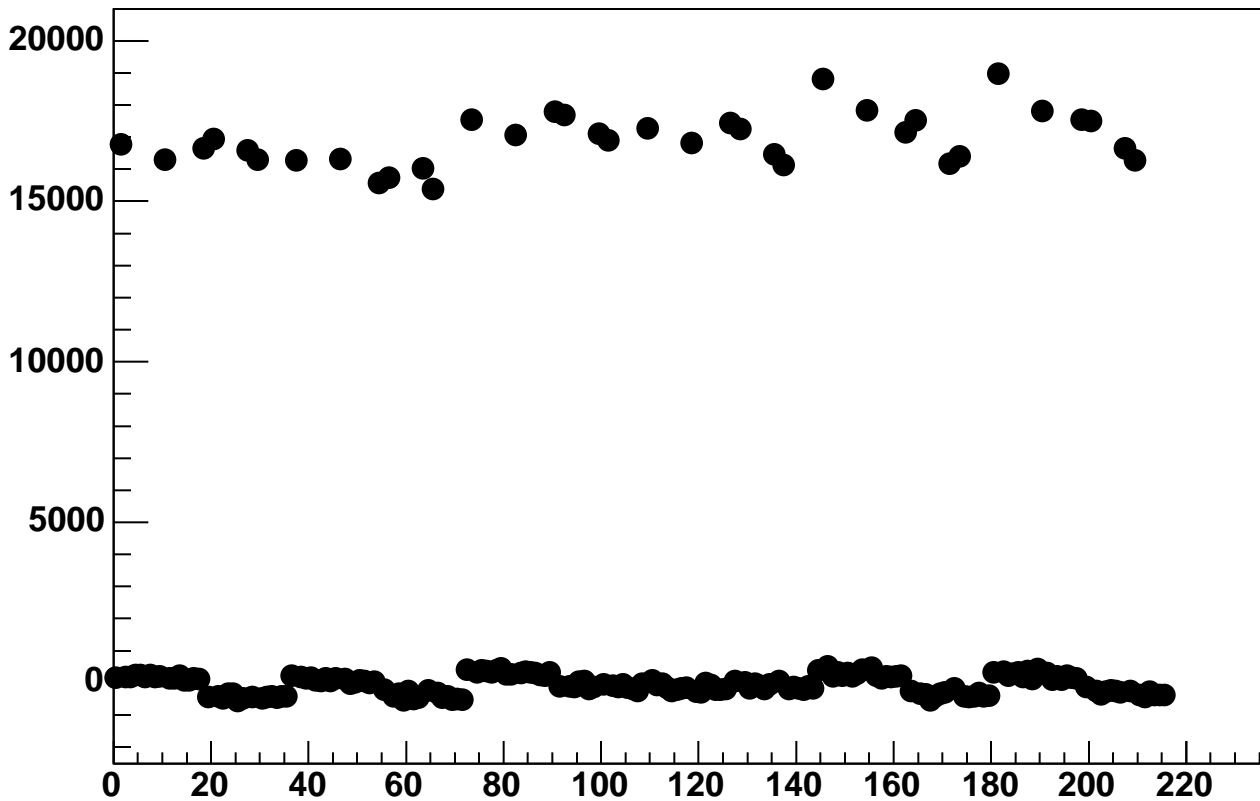
Enable 3, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



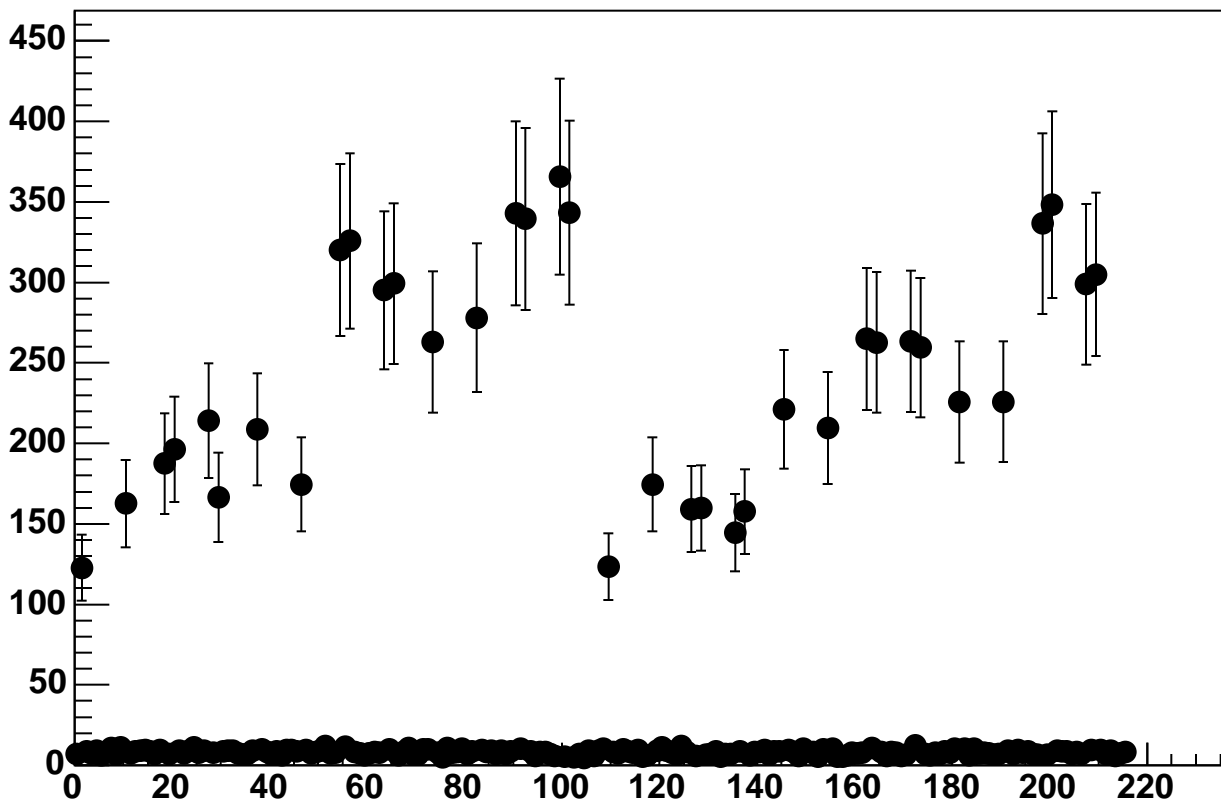
Enable 3, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



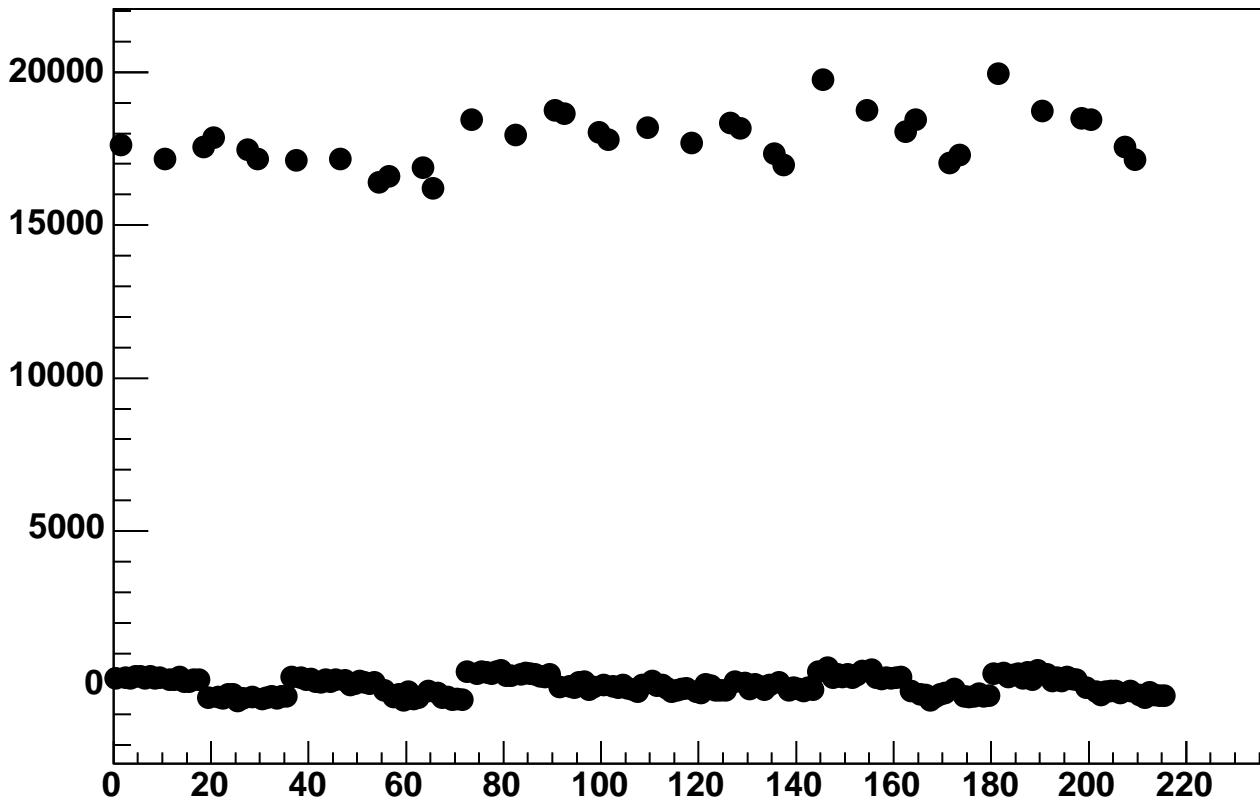
Enable 3, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



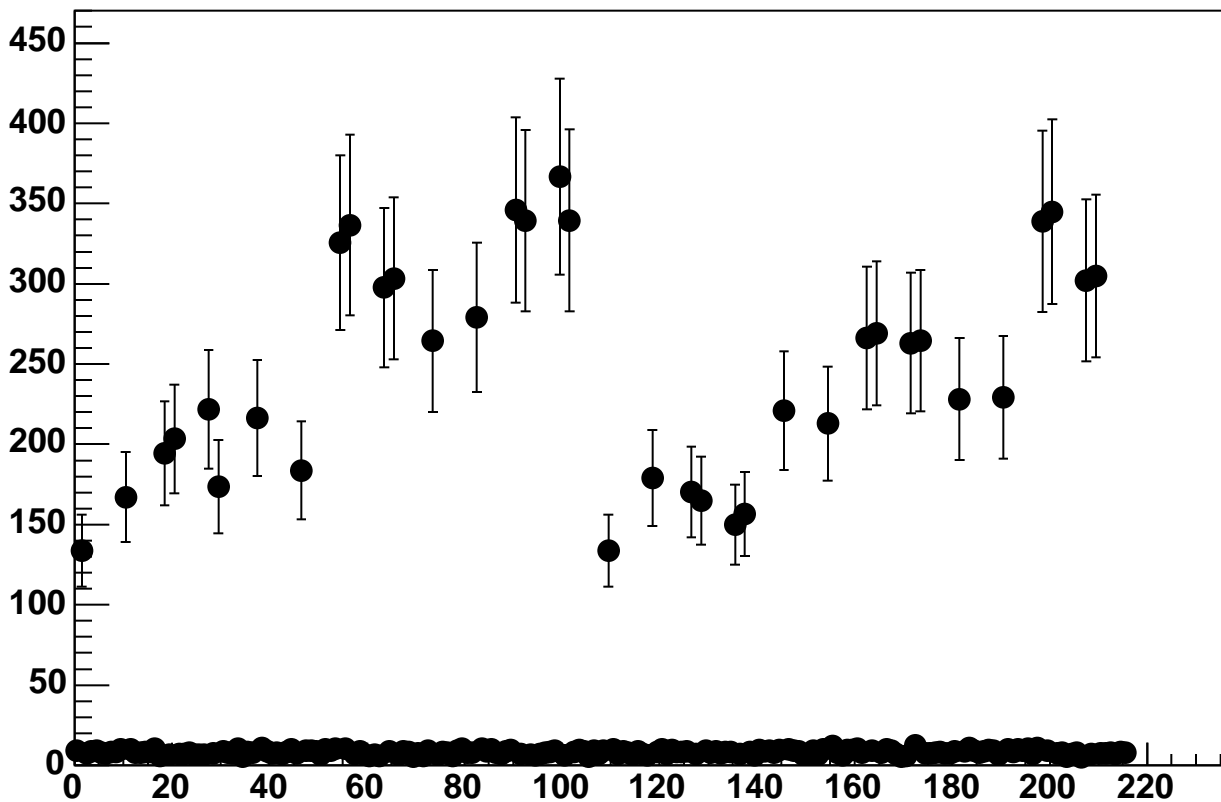
Enable 3, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



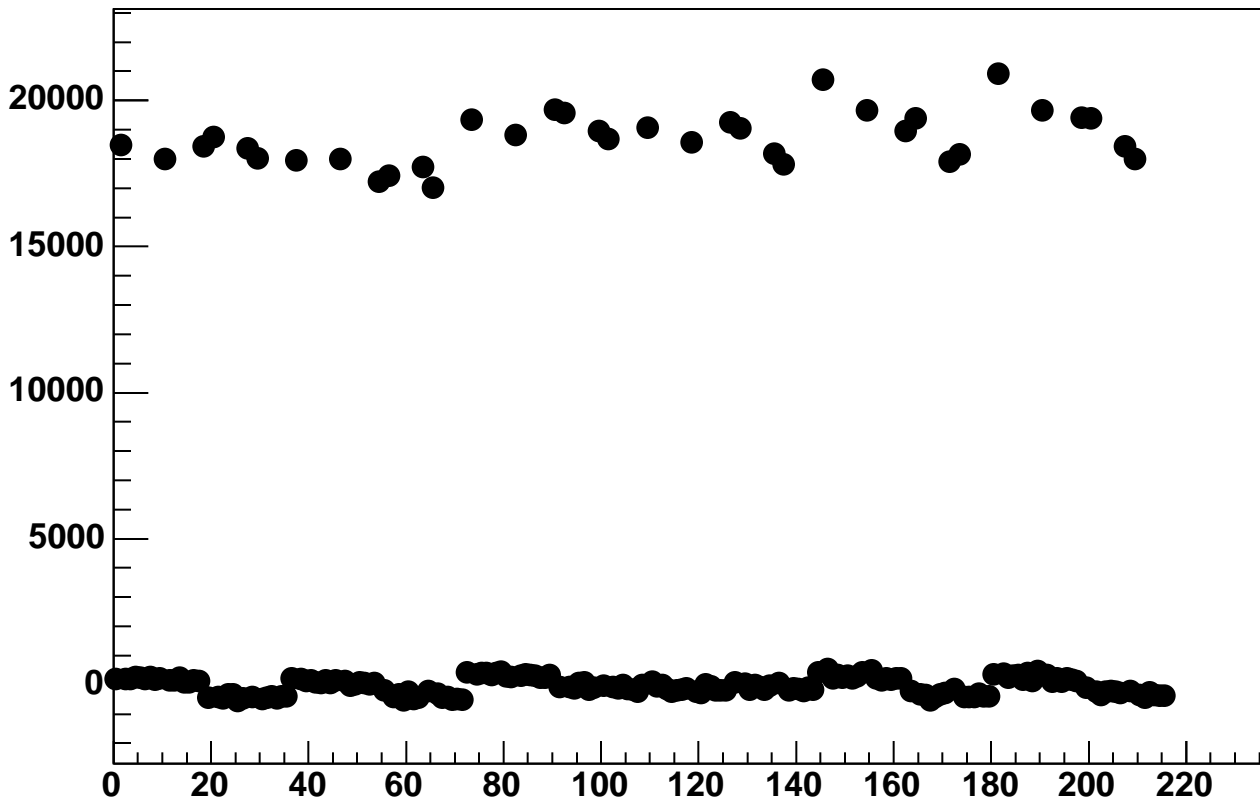
Enable 3, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



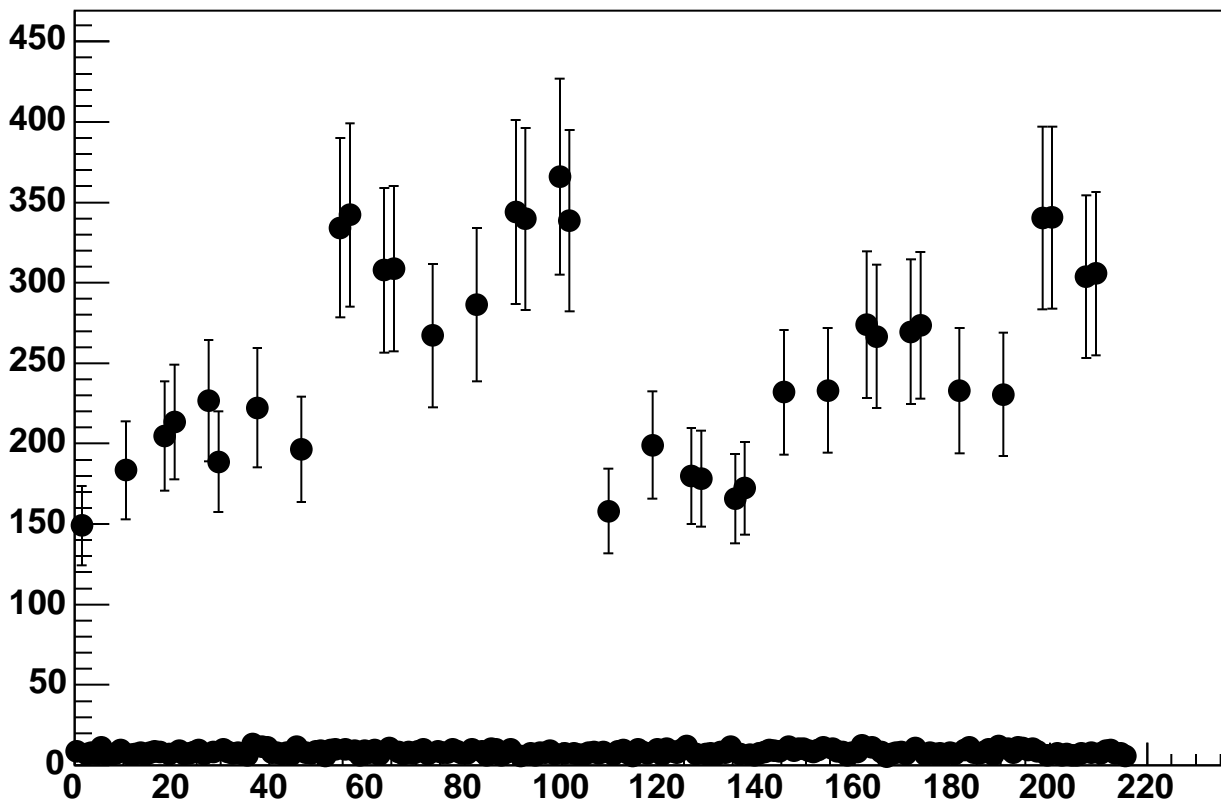
Enable 3, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



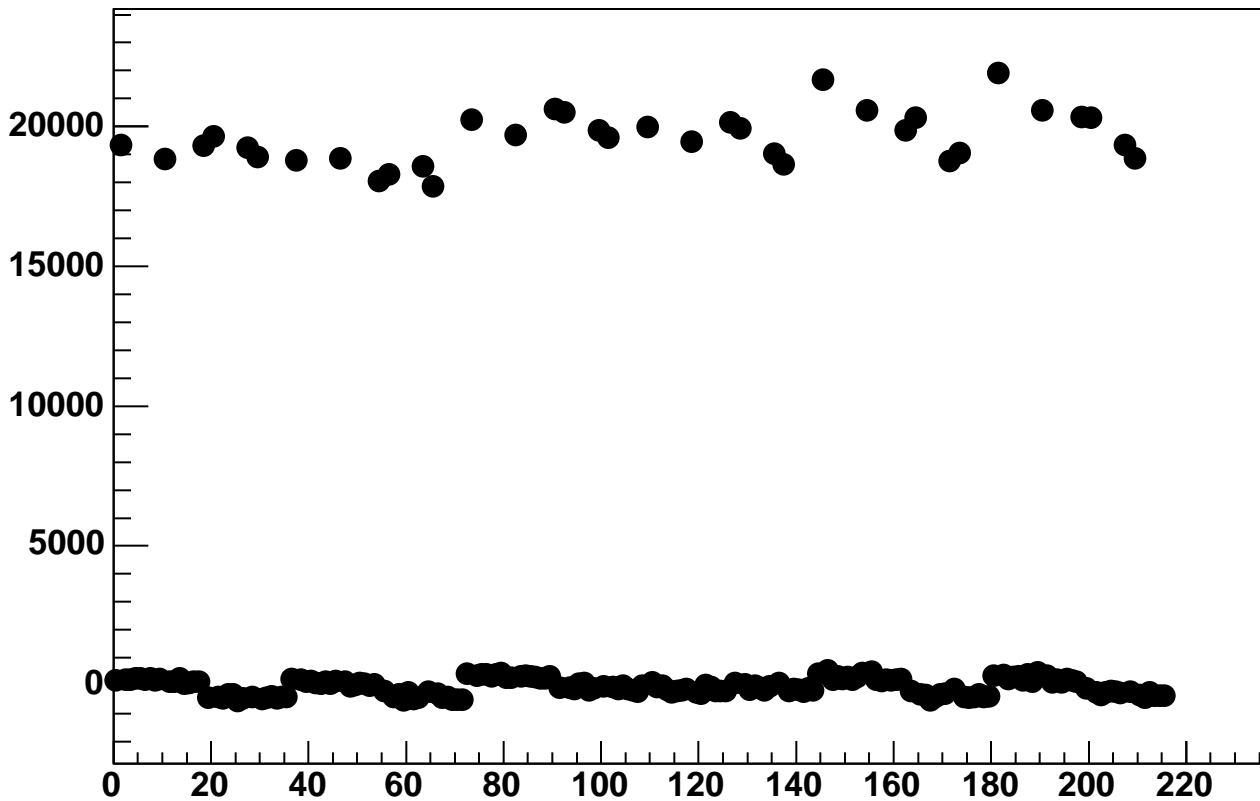
Enable 3, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



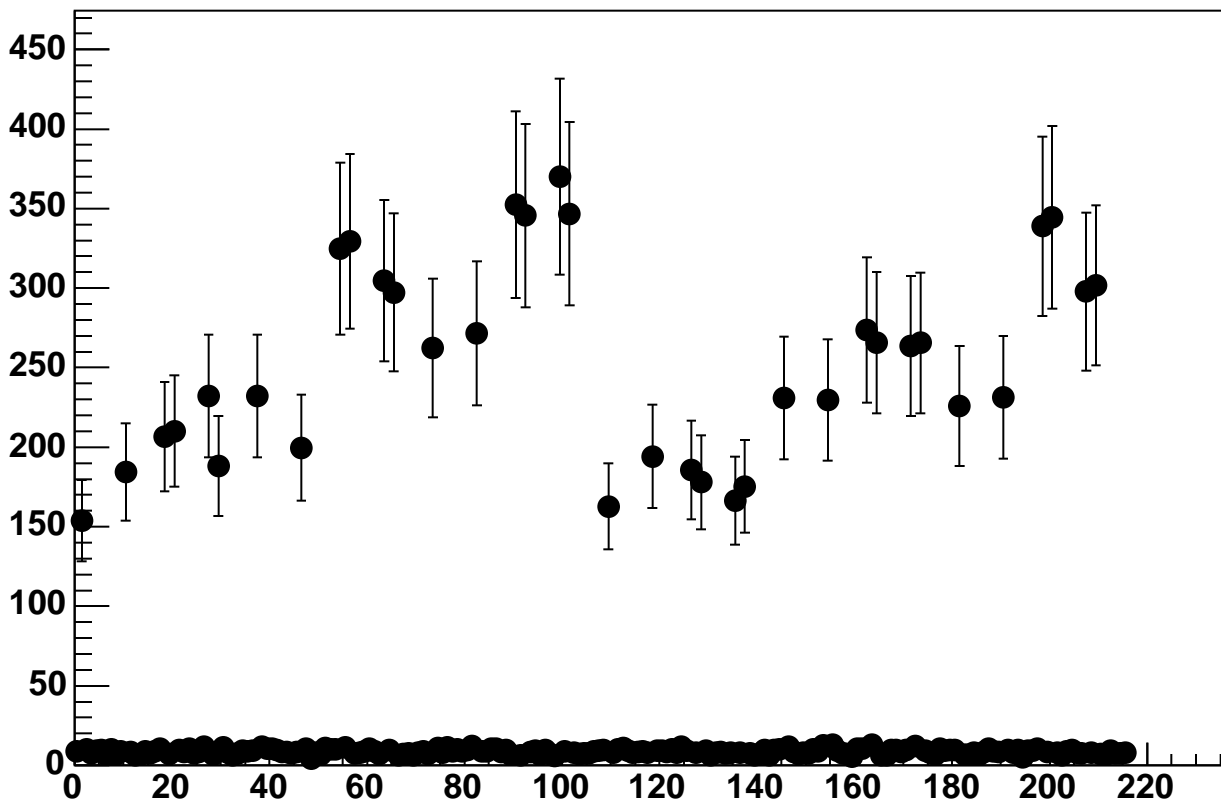
Enable 3, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



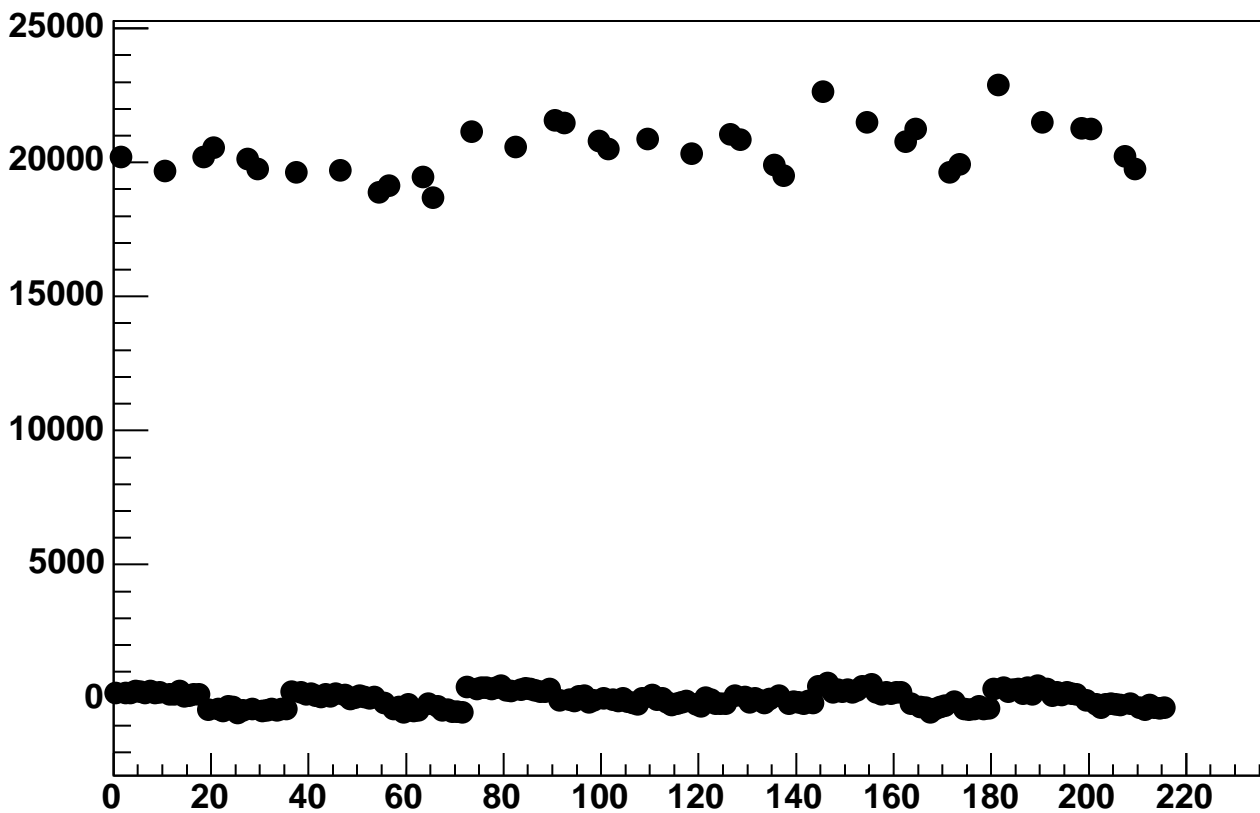
Enable 3, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



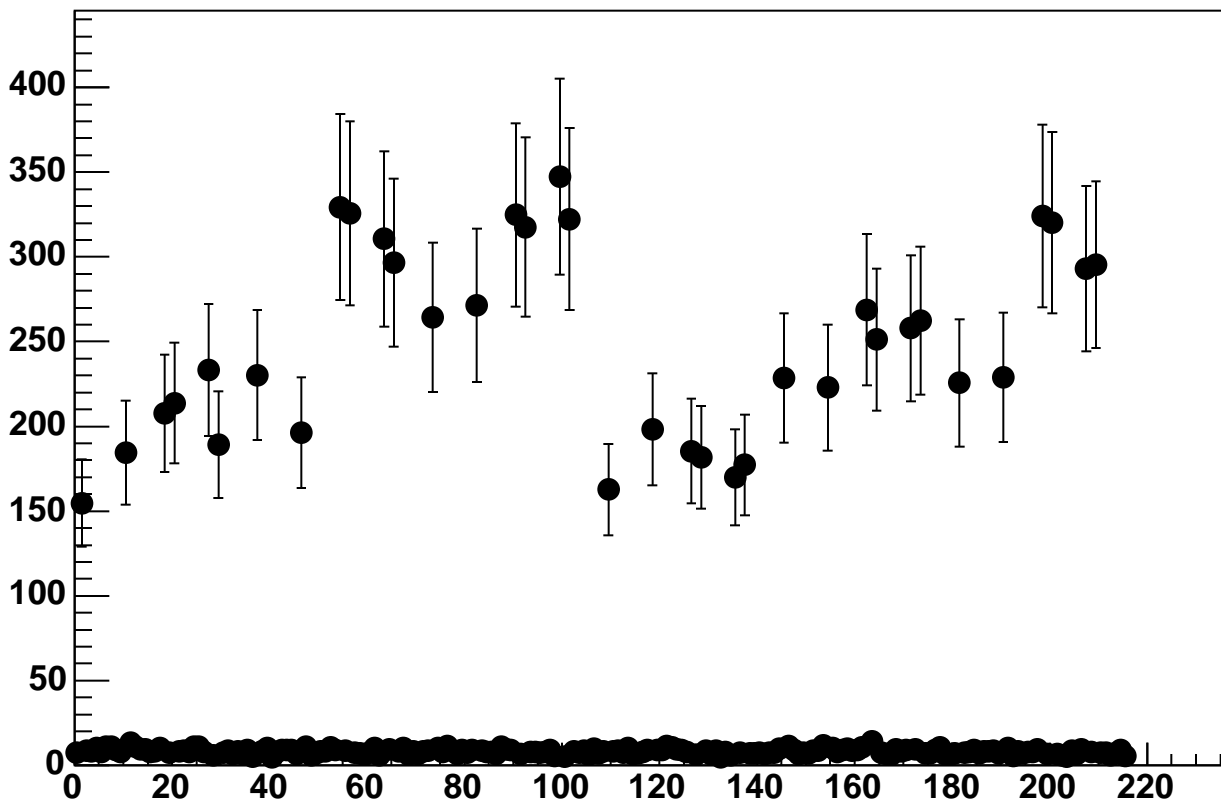
Enable 3, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



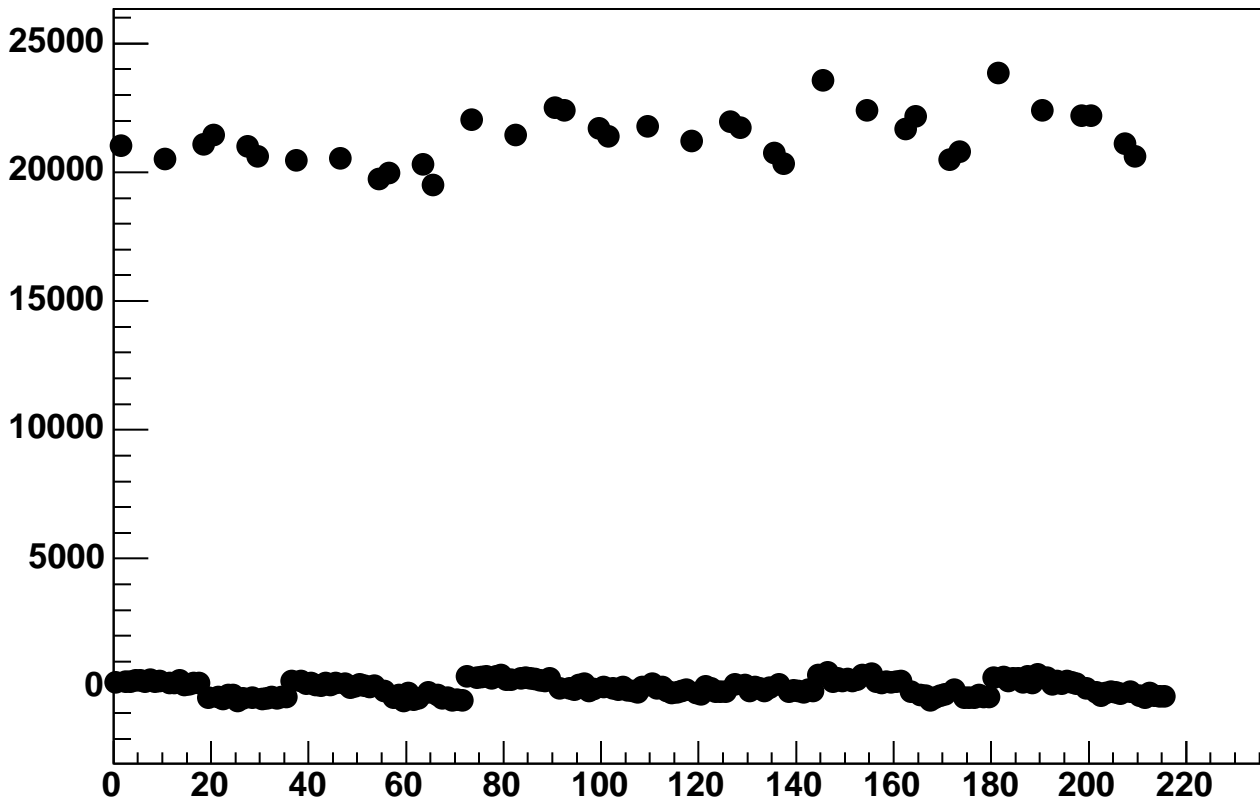
Enable 3, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



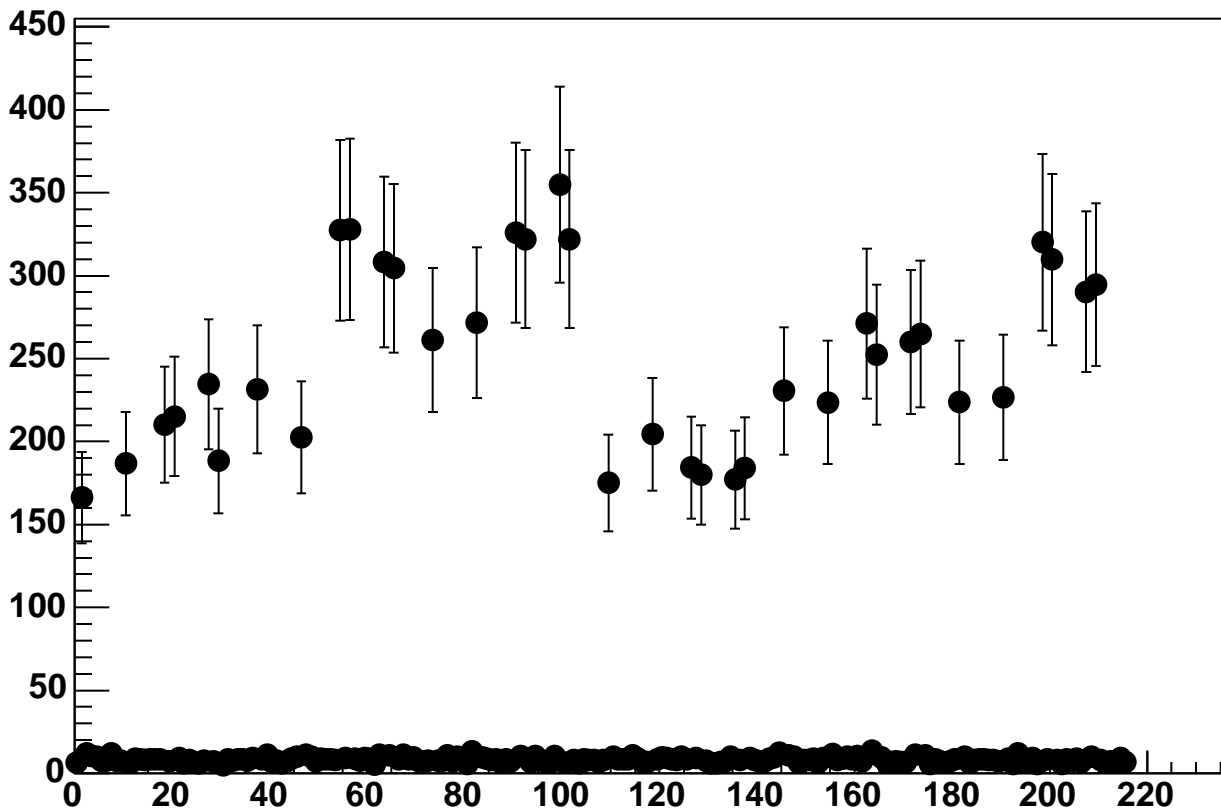
Enable 3, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



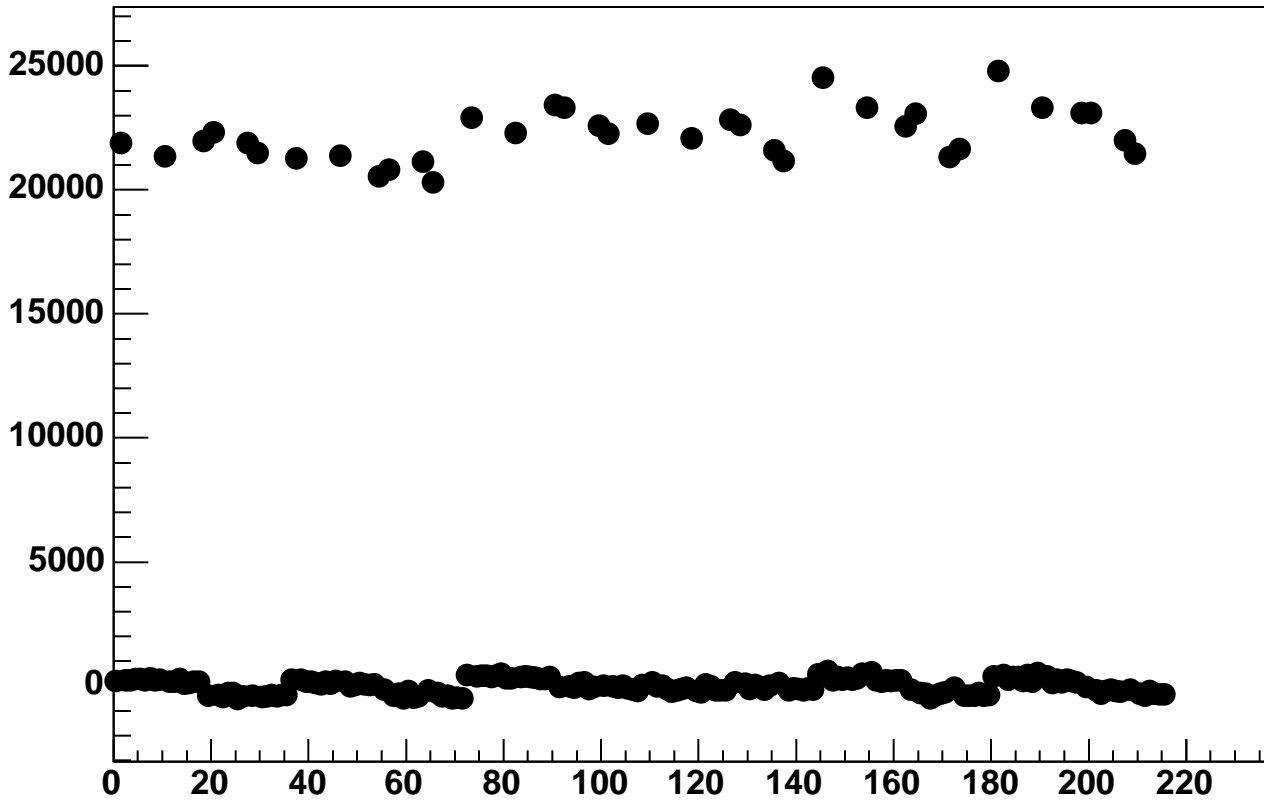
Enable 3, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



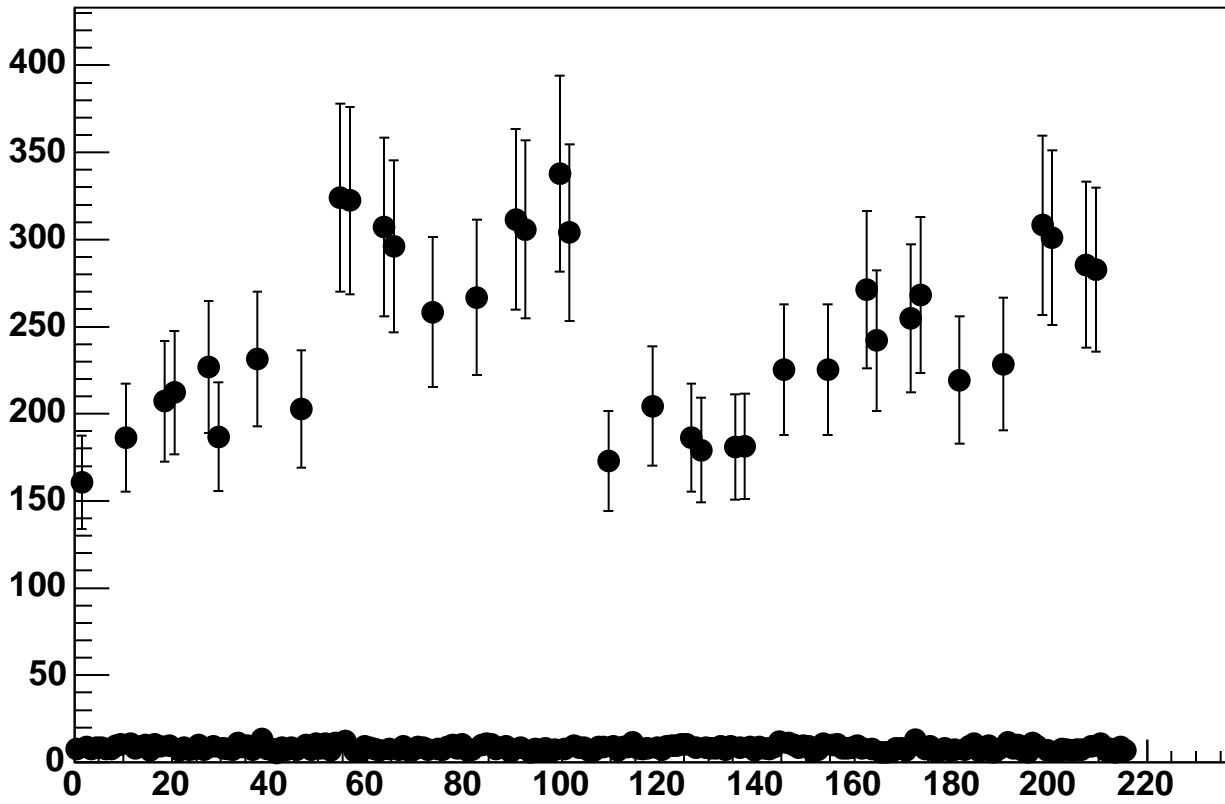
Enable 3, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



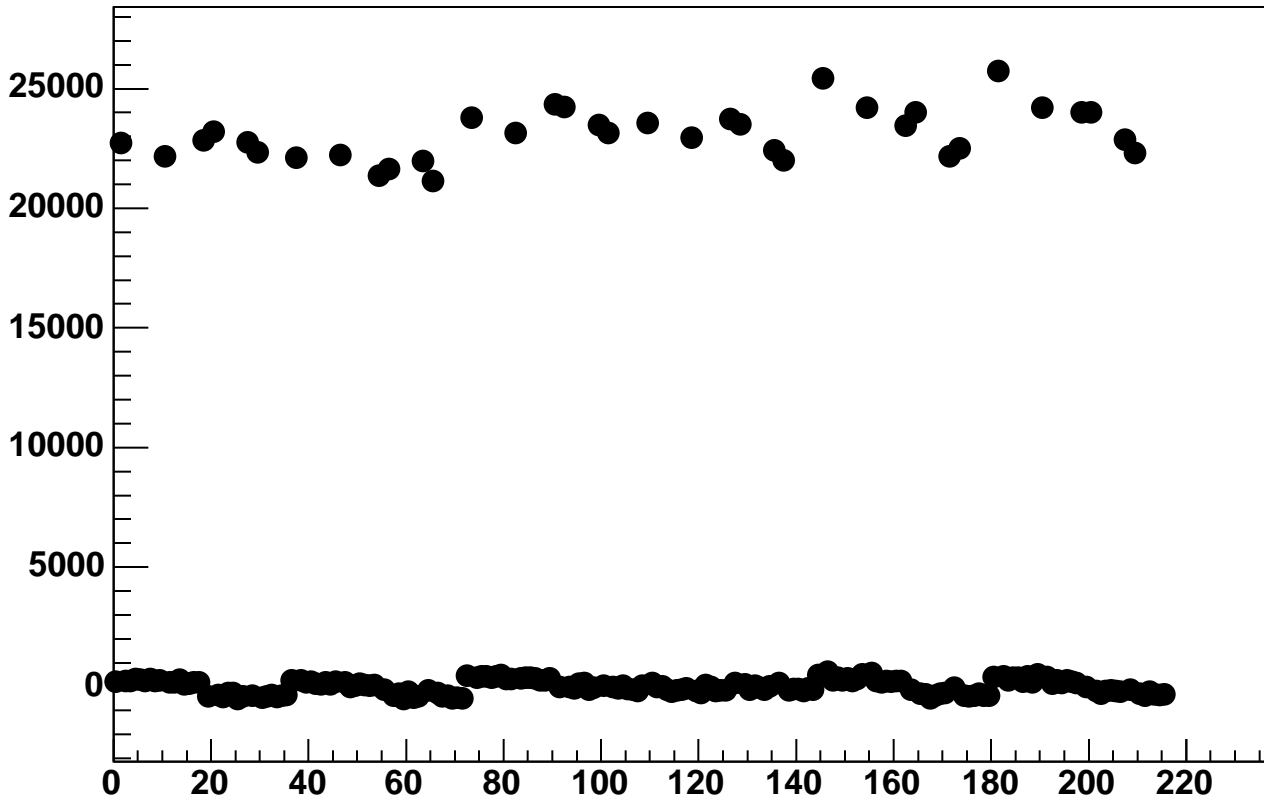
Enable 3, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



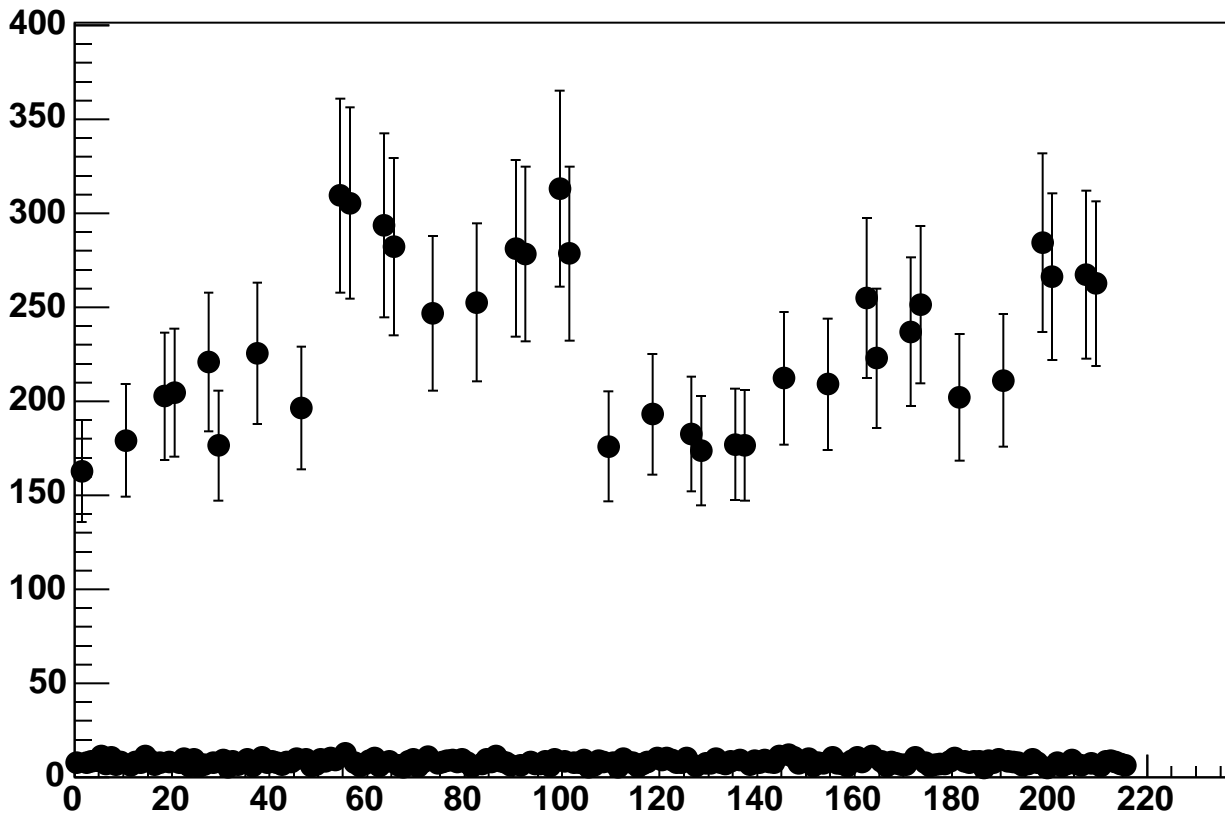
Enable 3, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



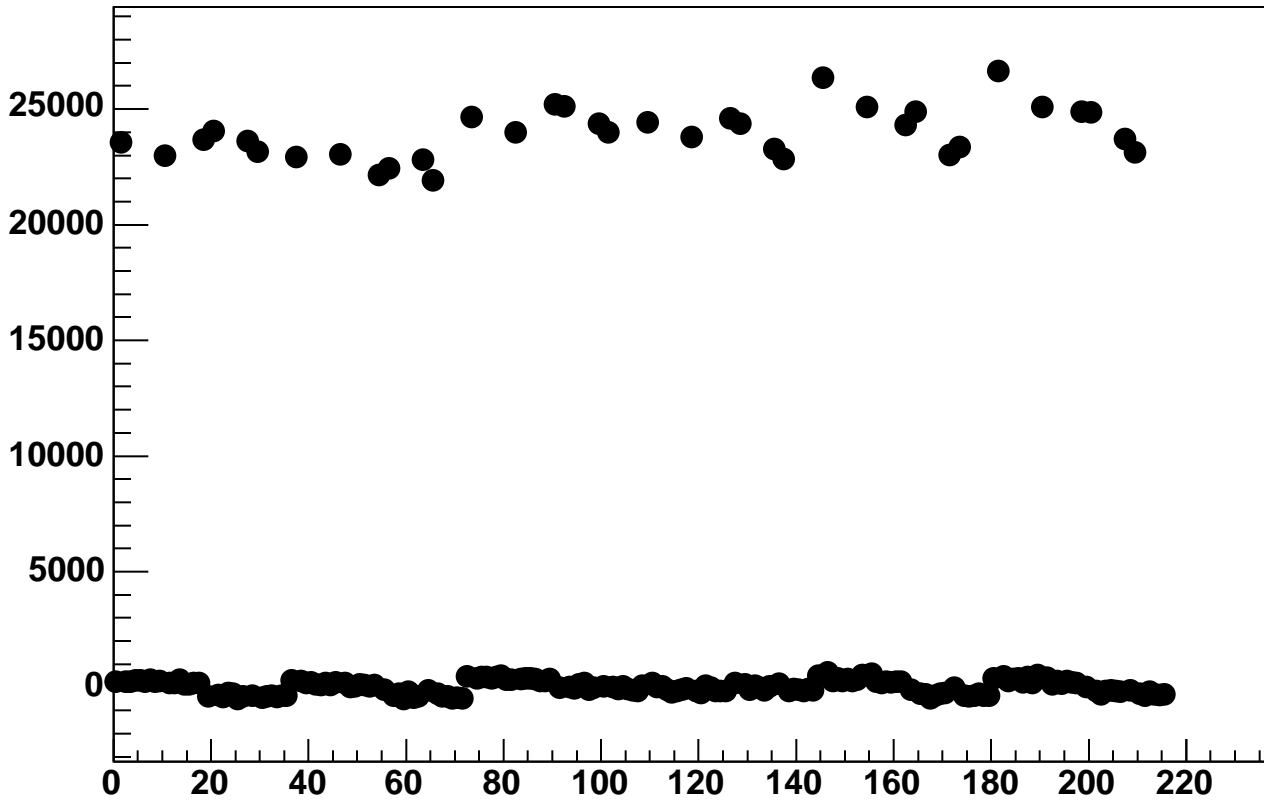
Enable 3, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



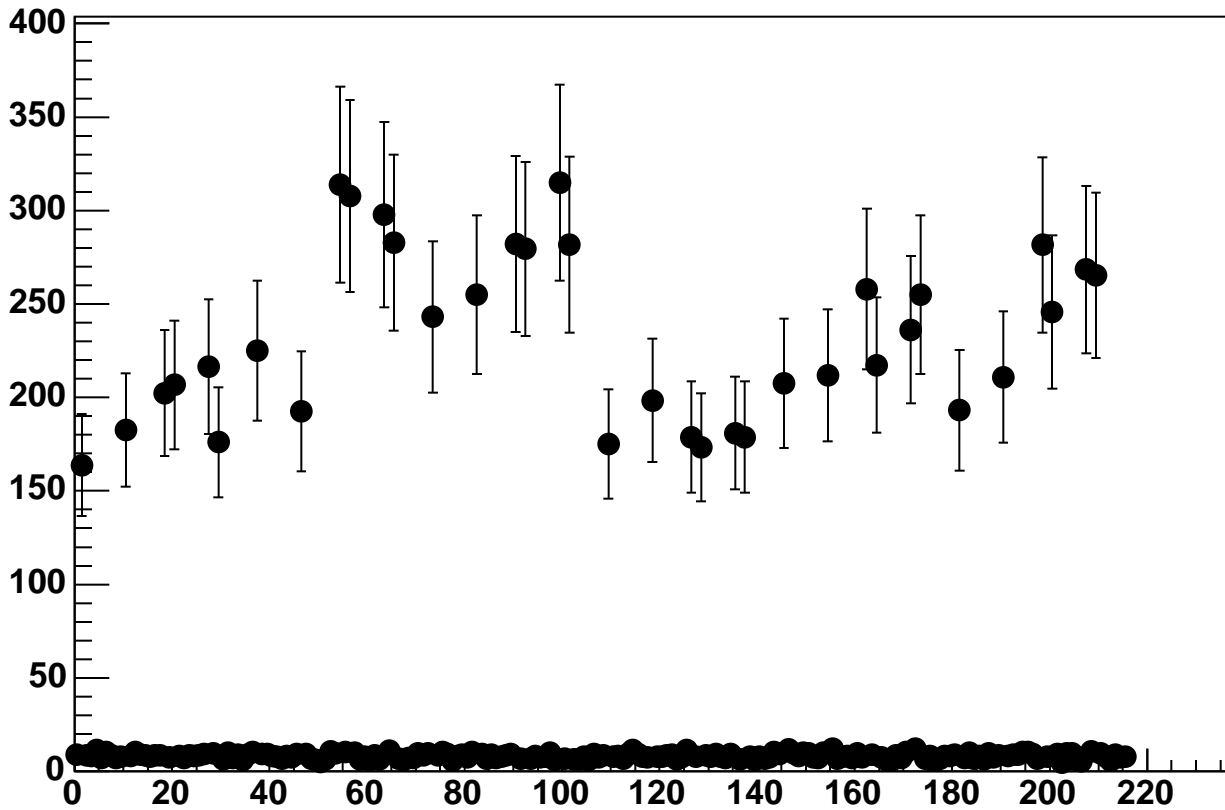
Enable 3, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



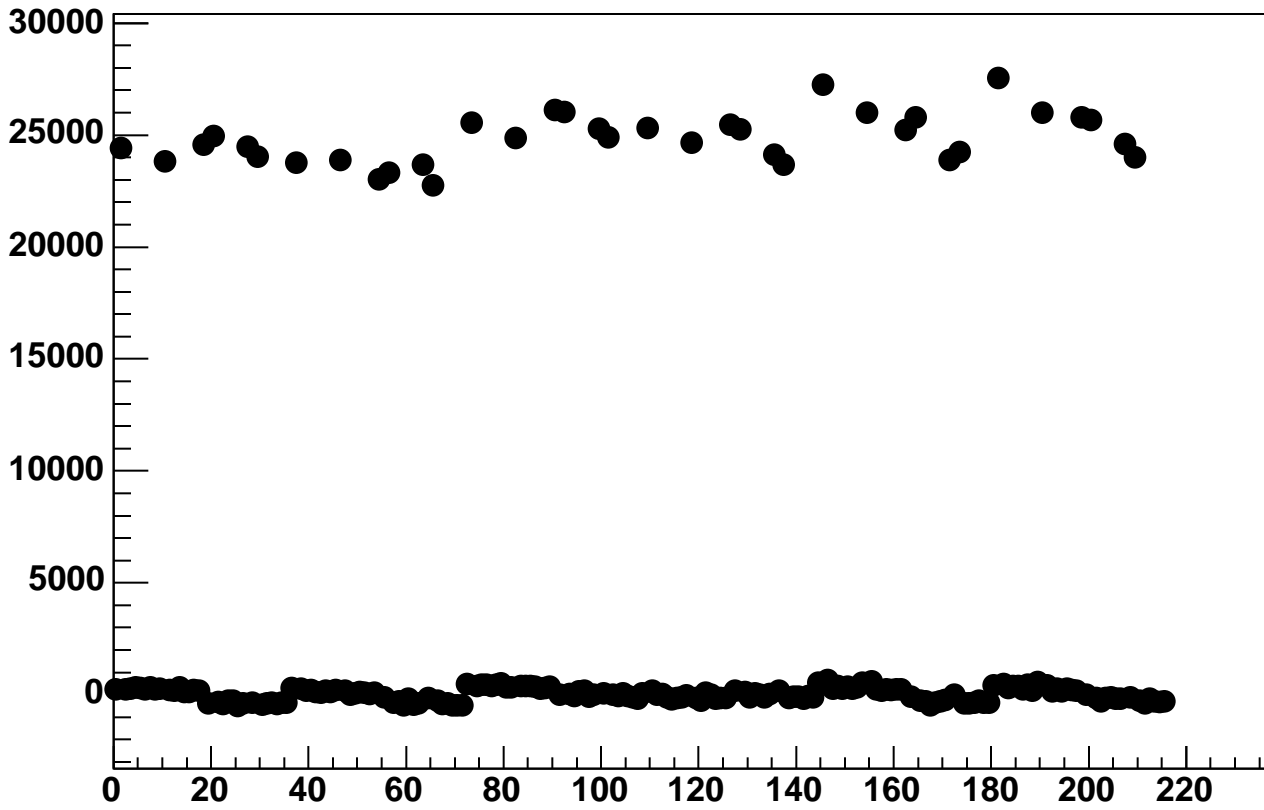
Enable 3, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



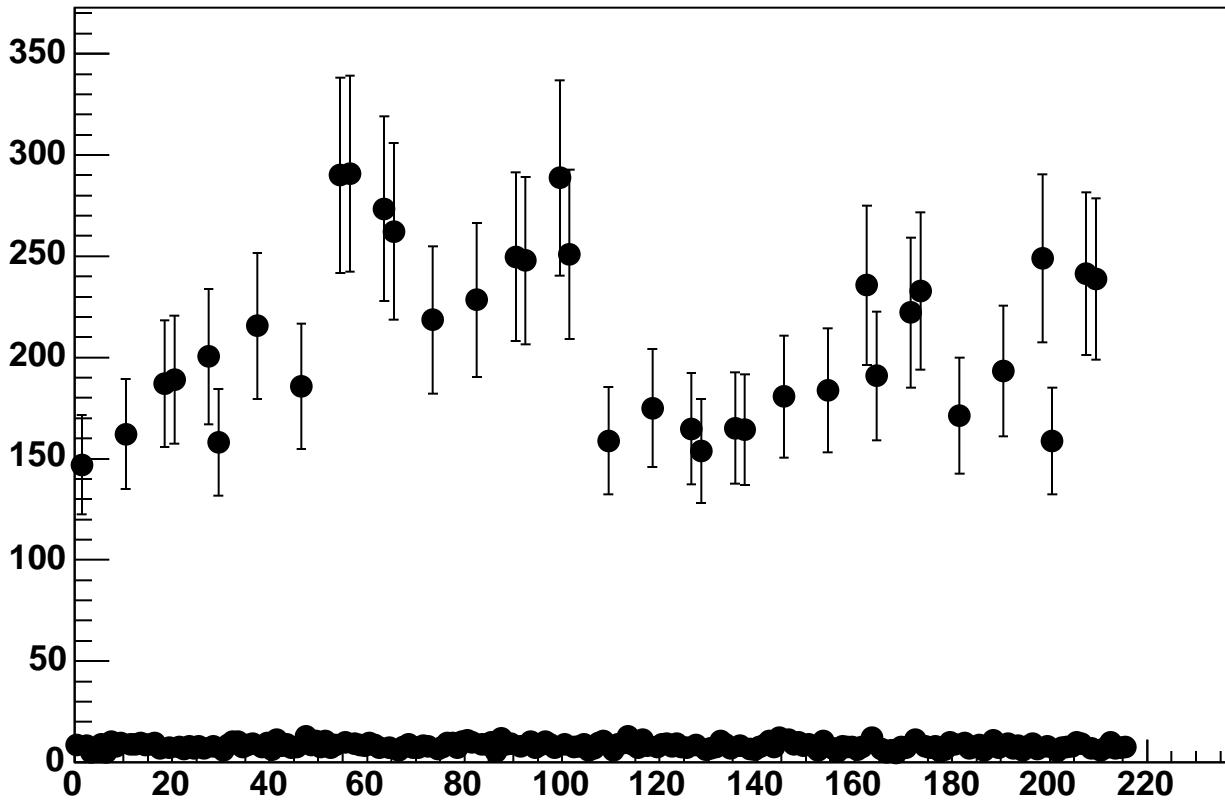
Enable 3, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



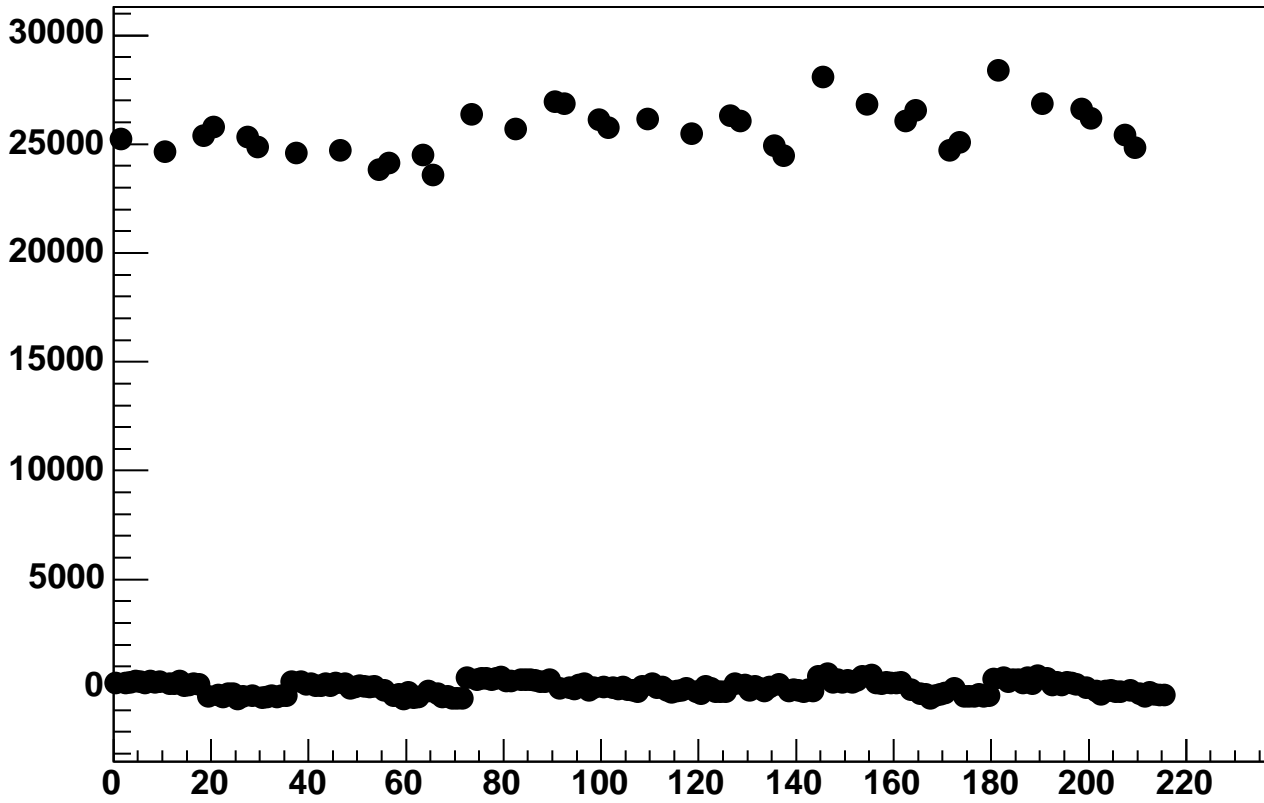
Enable 3, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



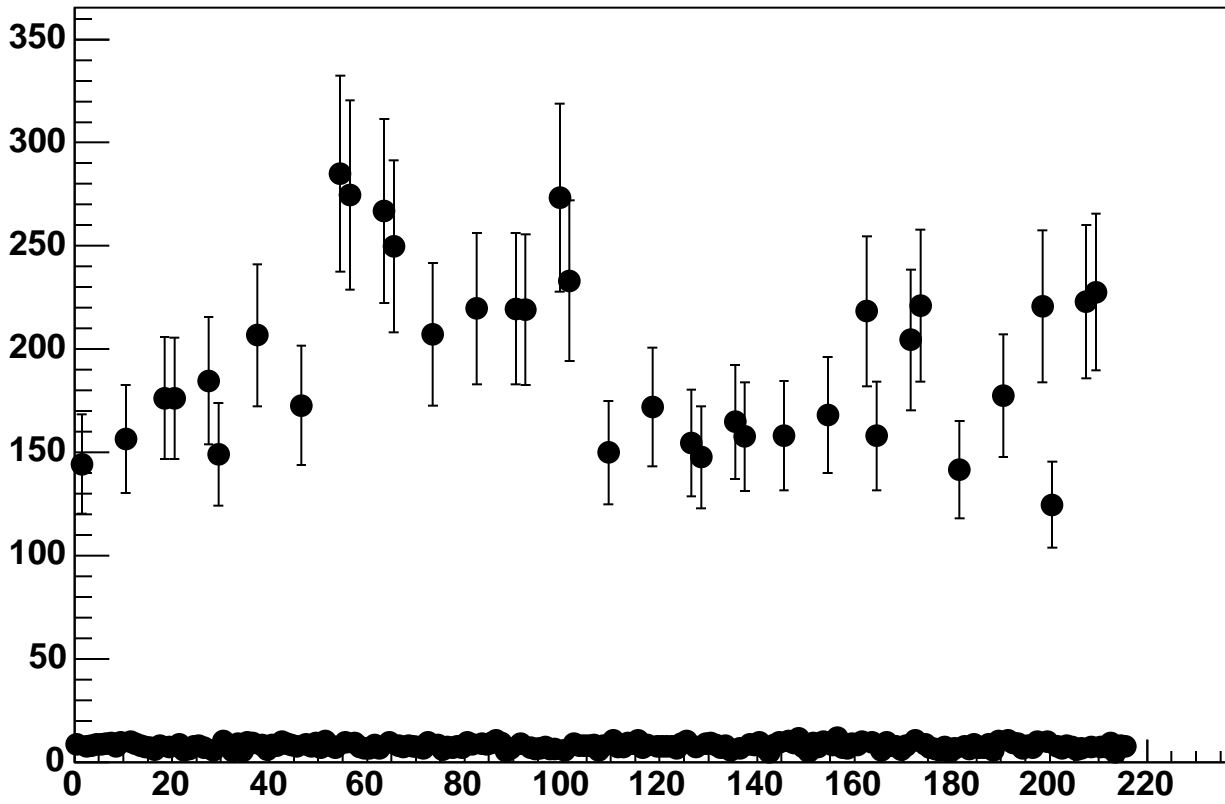
Enable 3, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



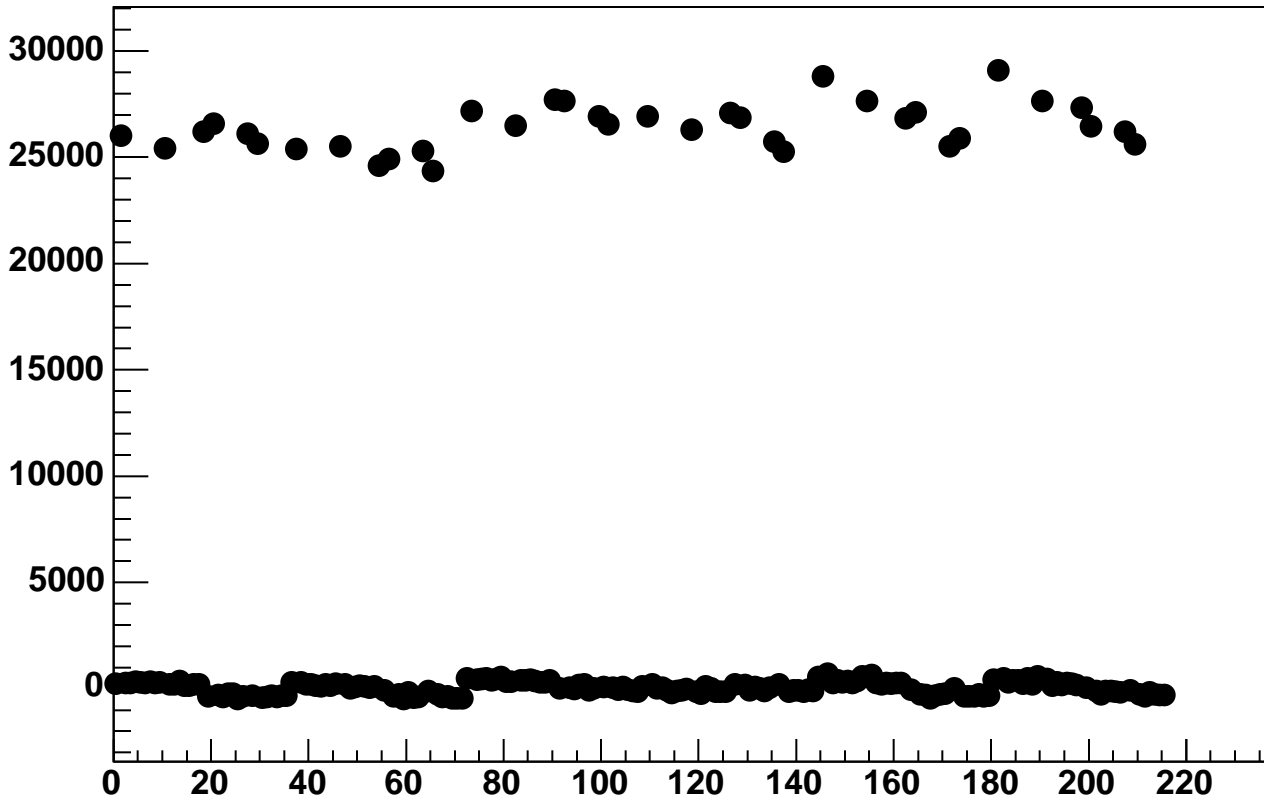
Enable 3, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



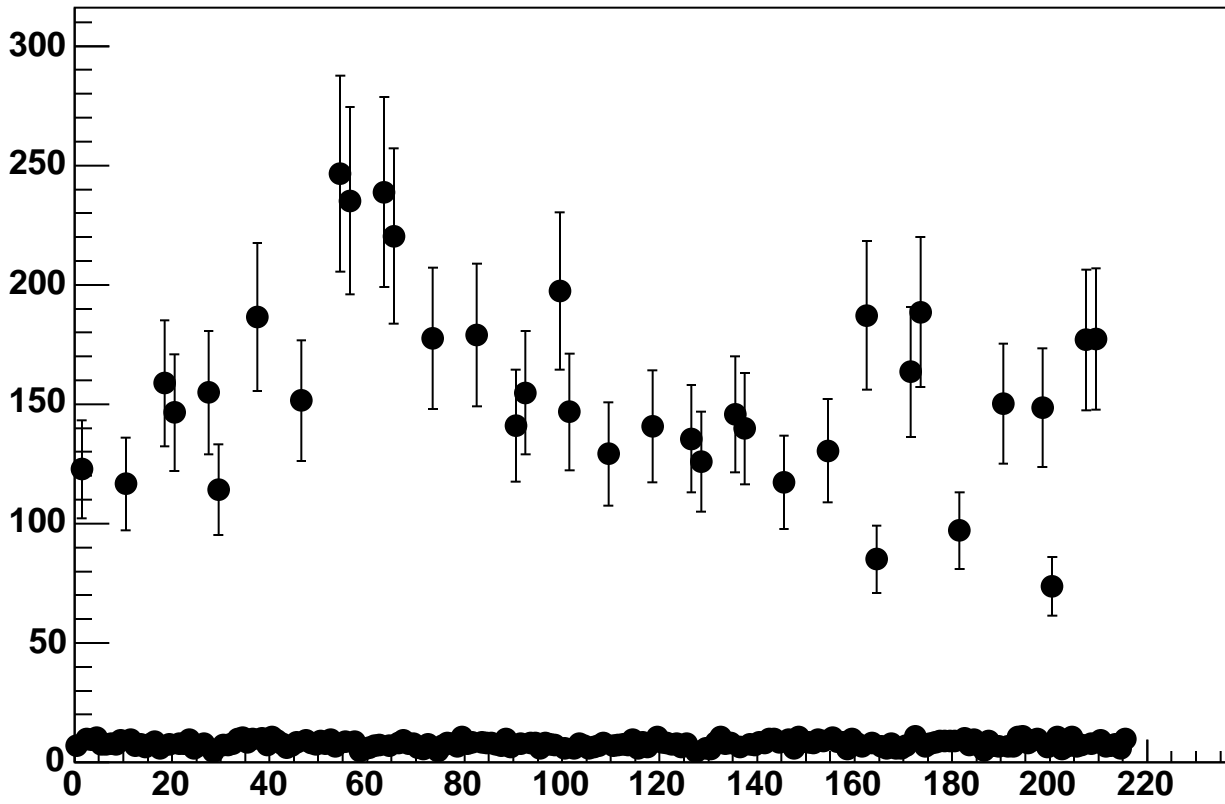
Enable 3, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



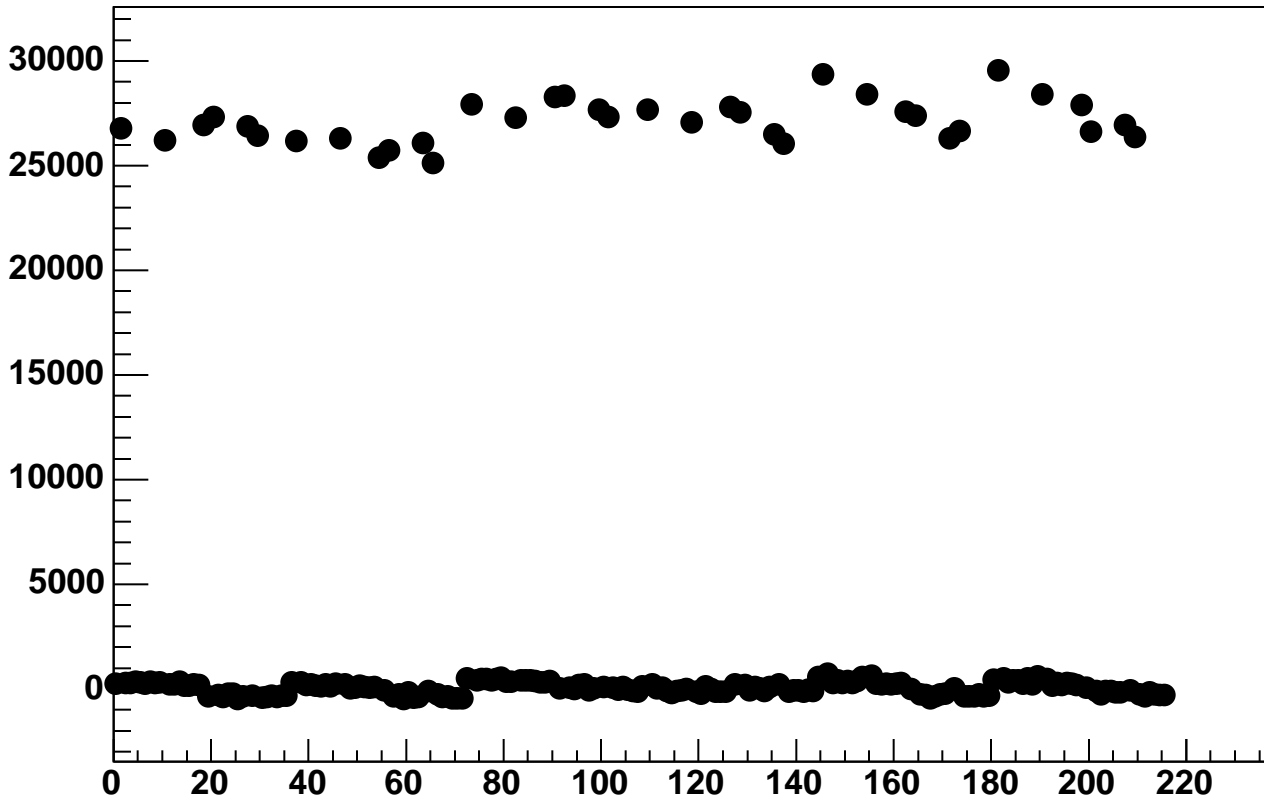
Enable 3, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



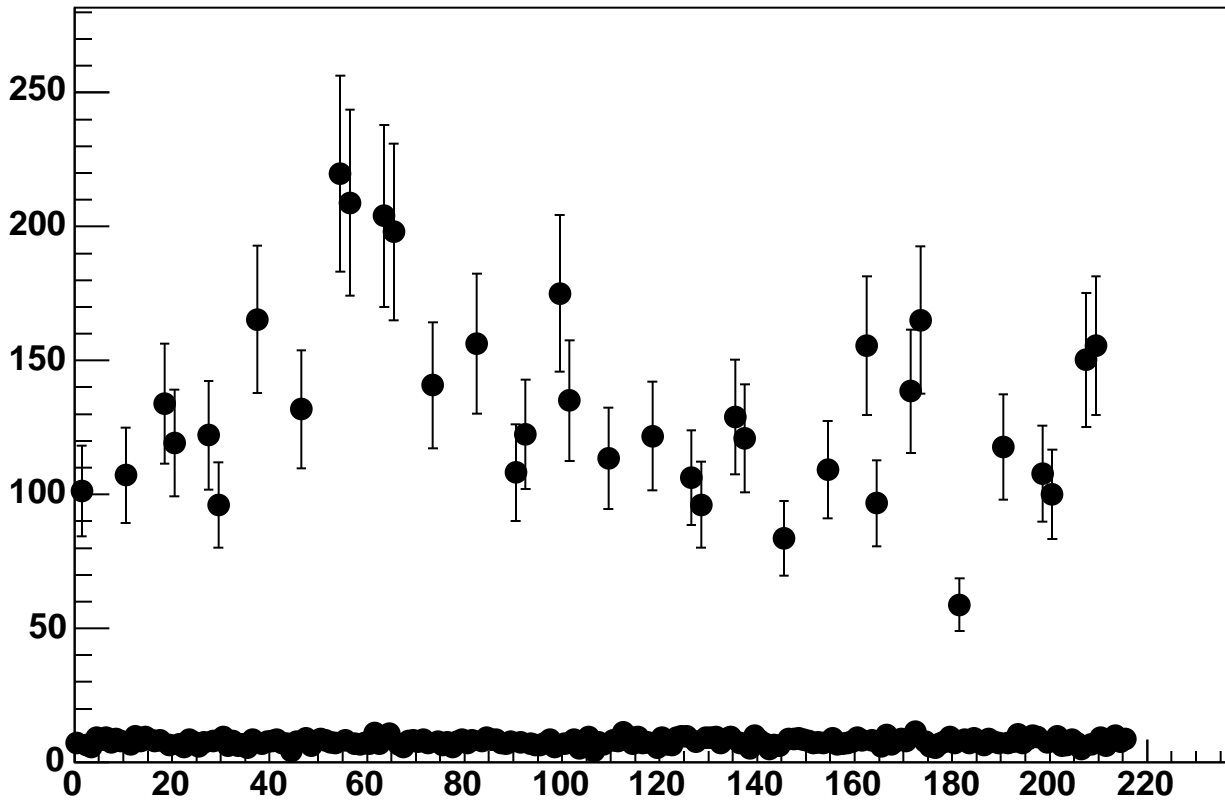
Enable 3, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



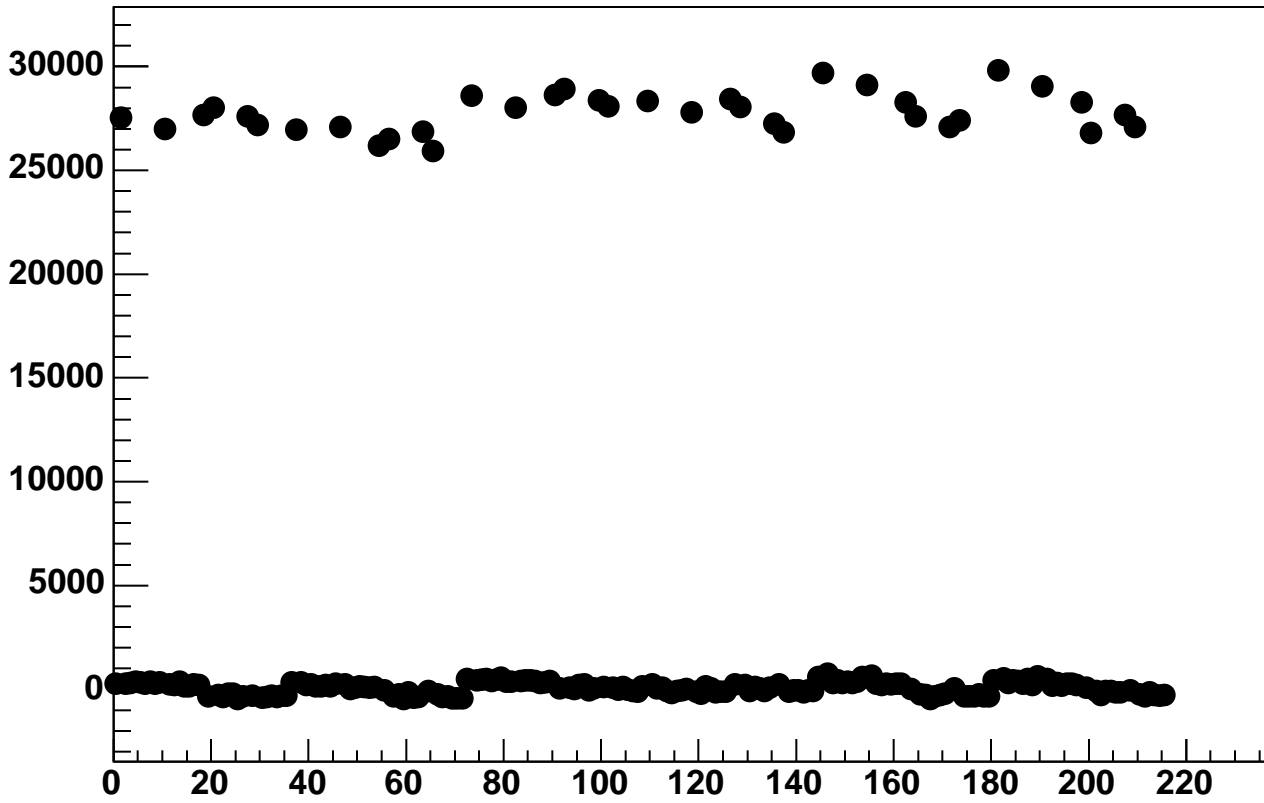
Enable 3, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



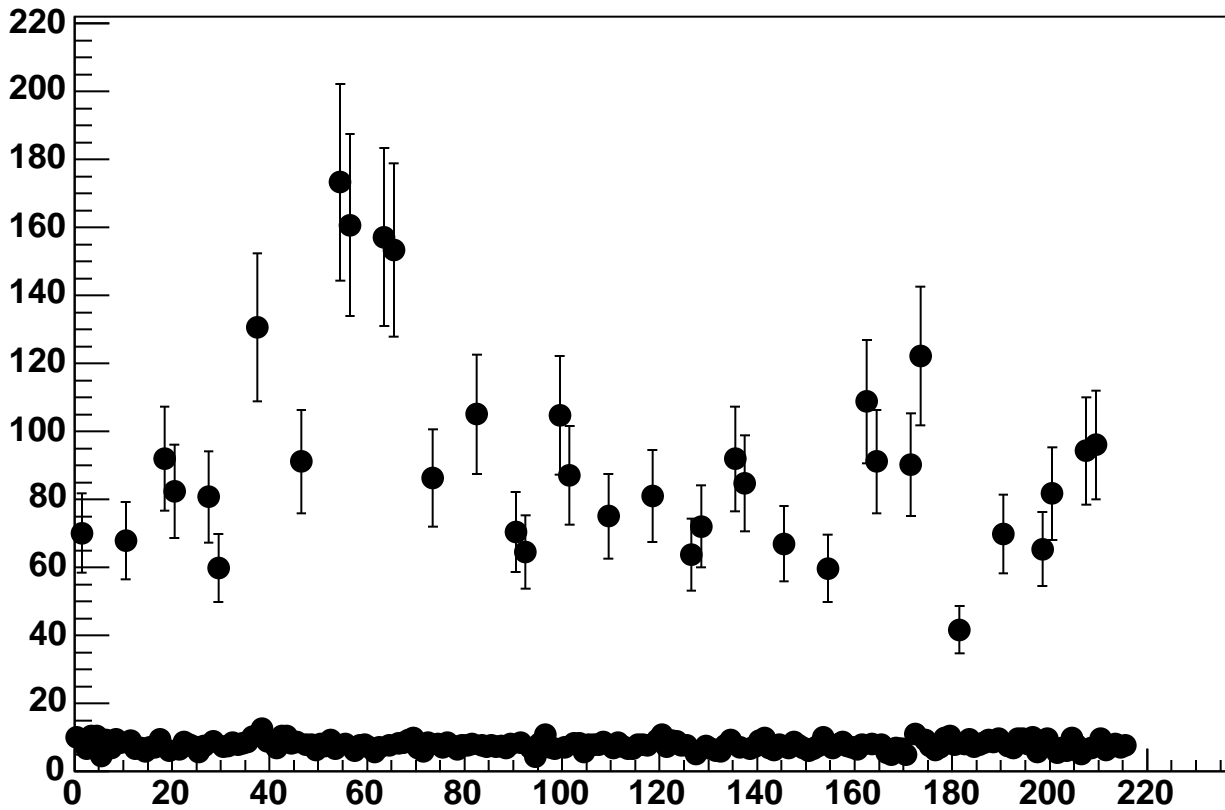
Enable 3, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



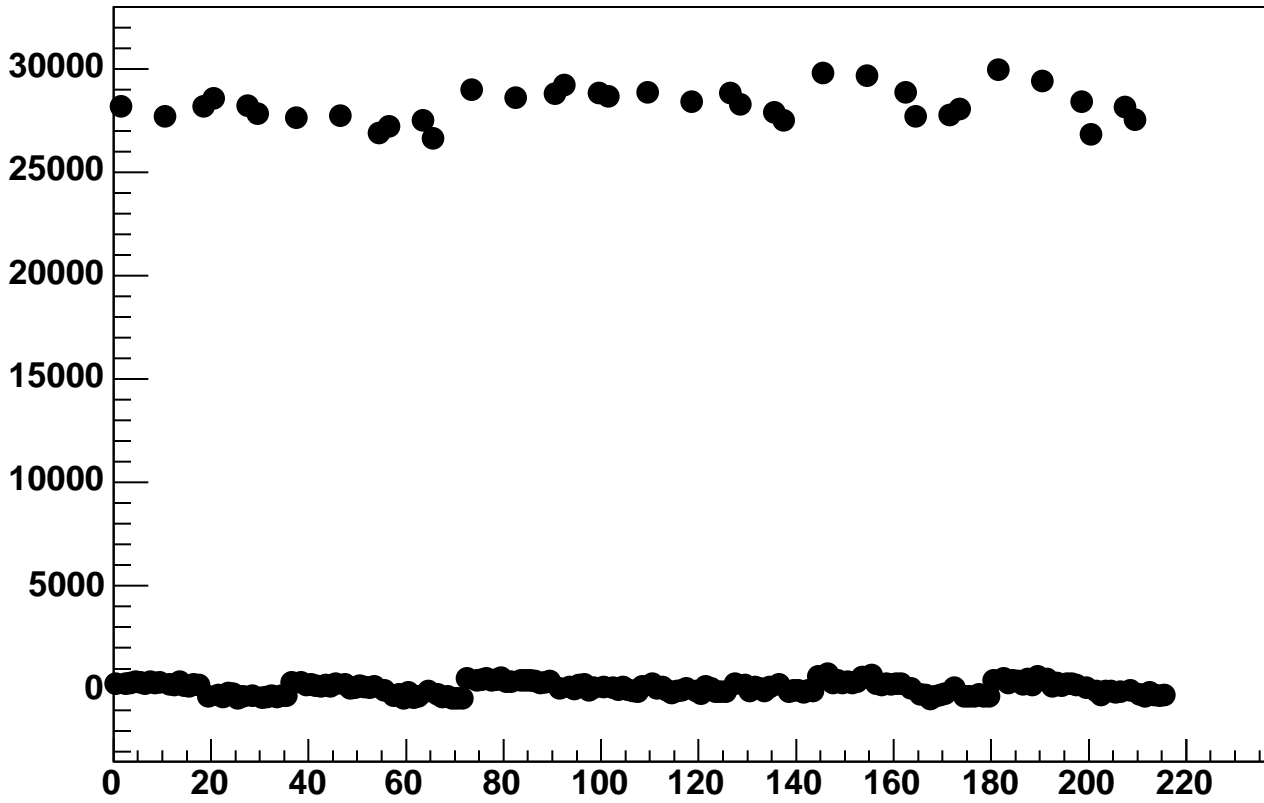
Enable 3, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



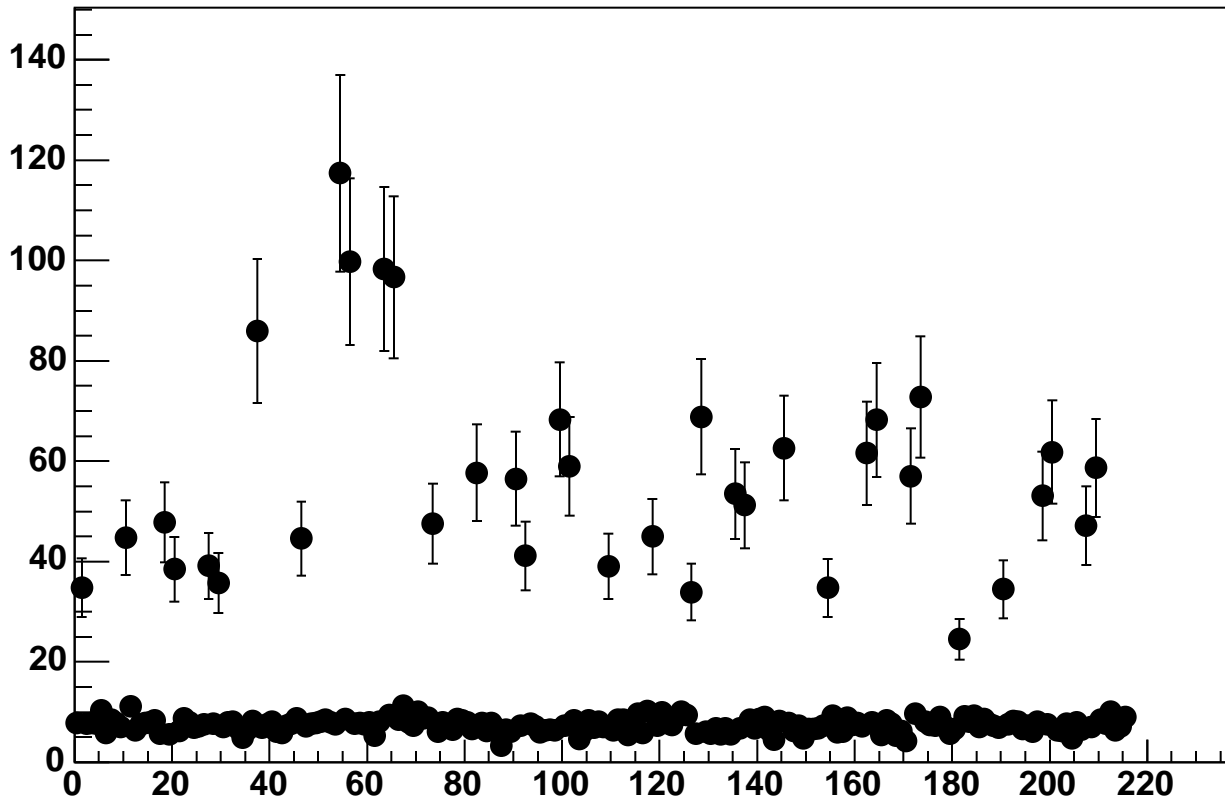
Enable 3, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



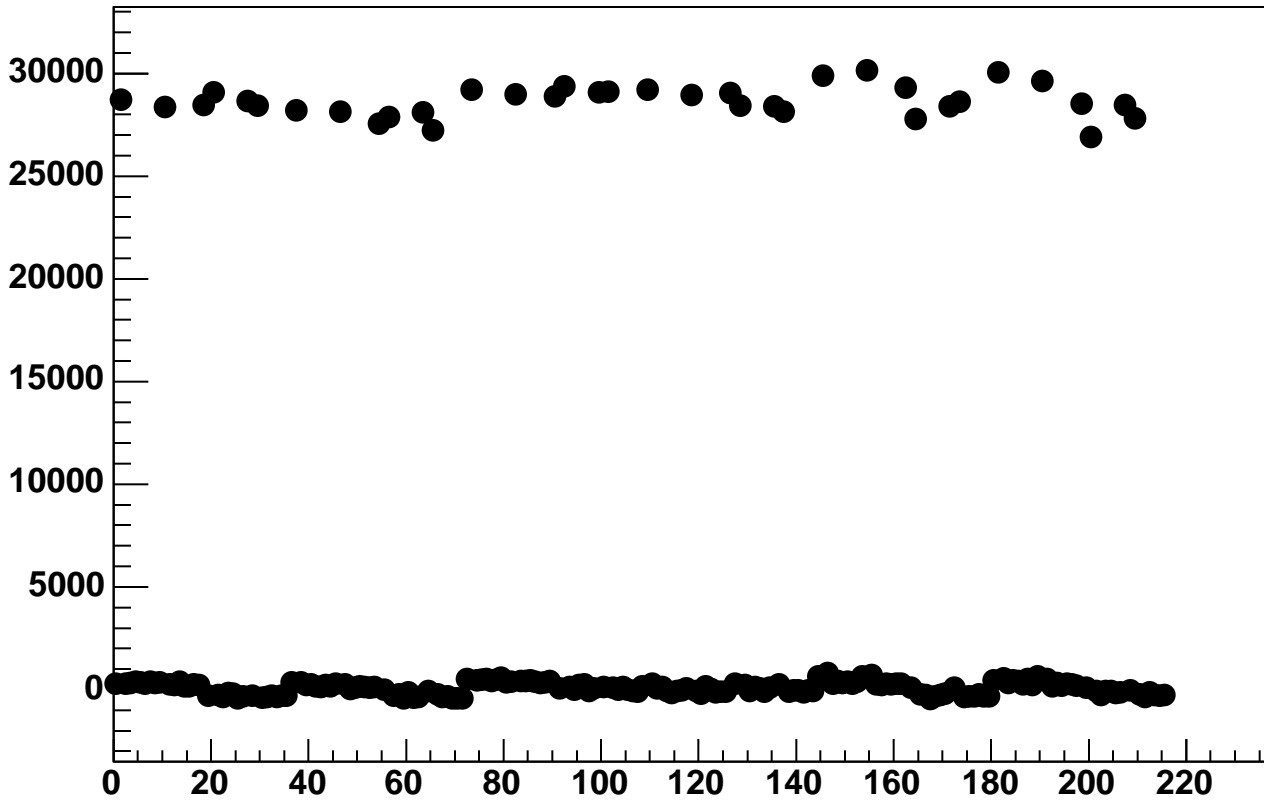
Enable 3, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



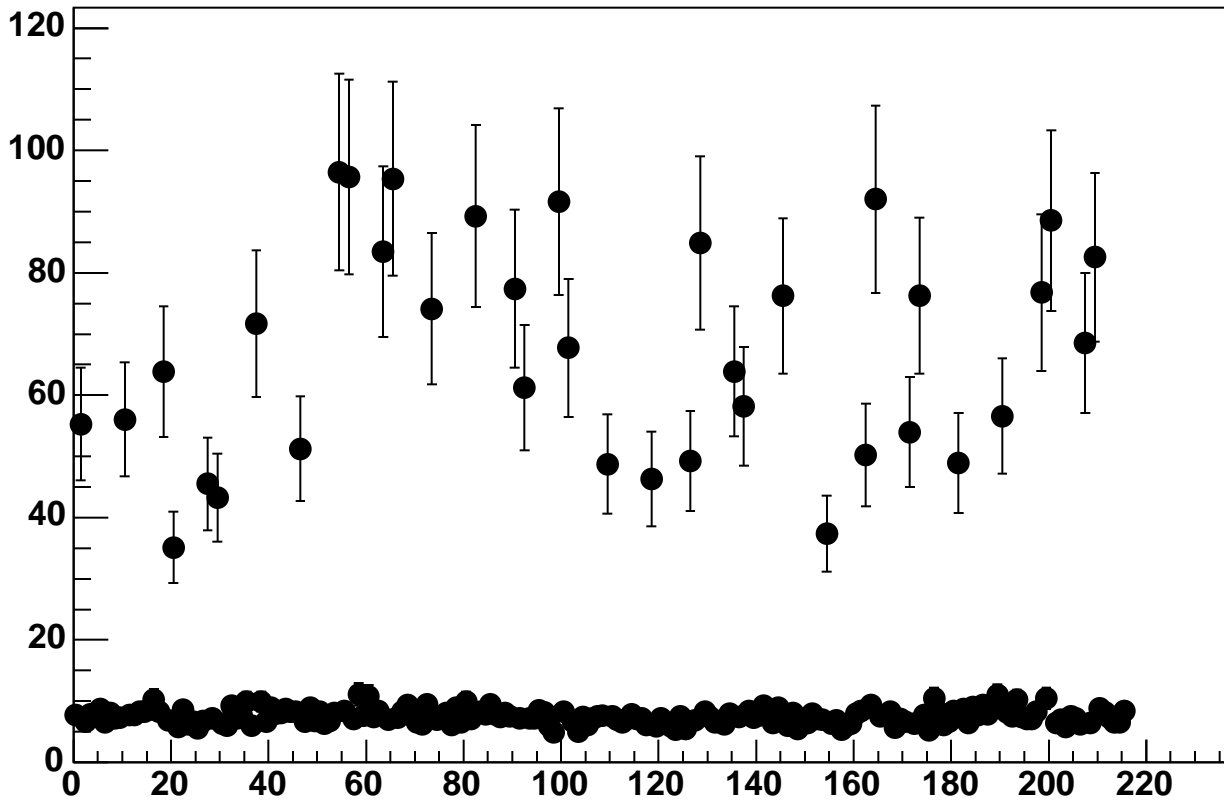
Enable 3, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



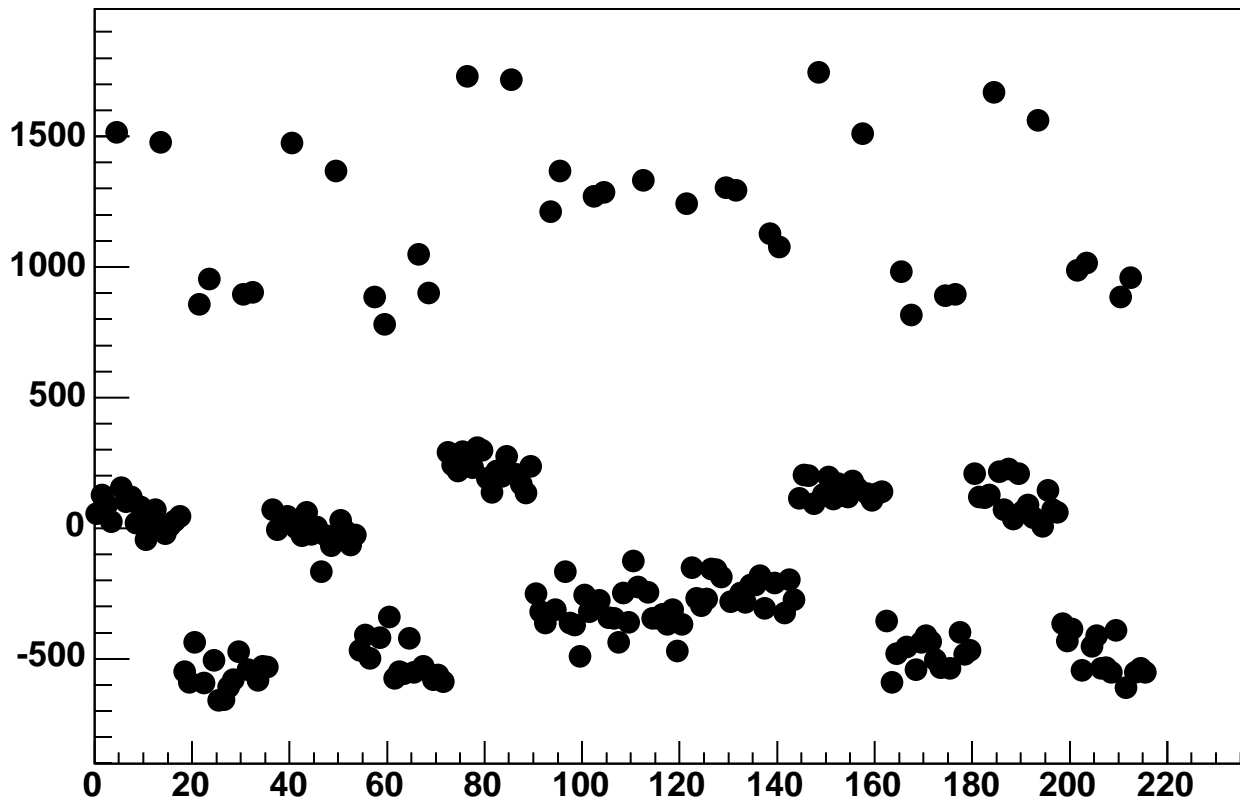
Enable 3, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



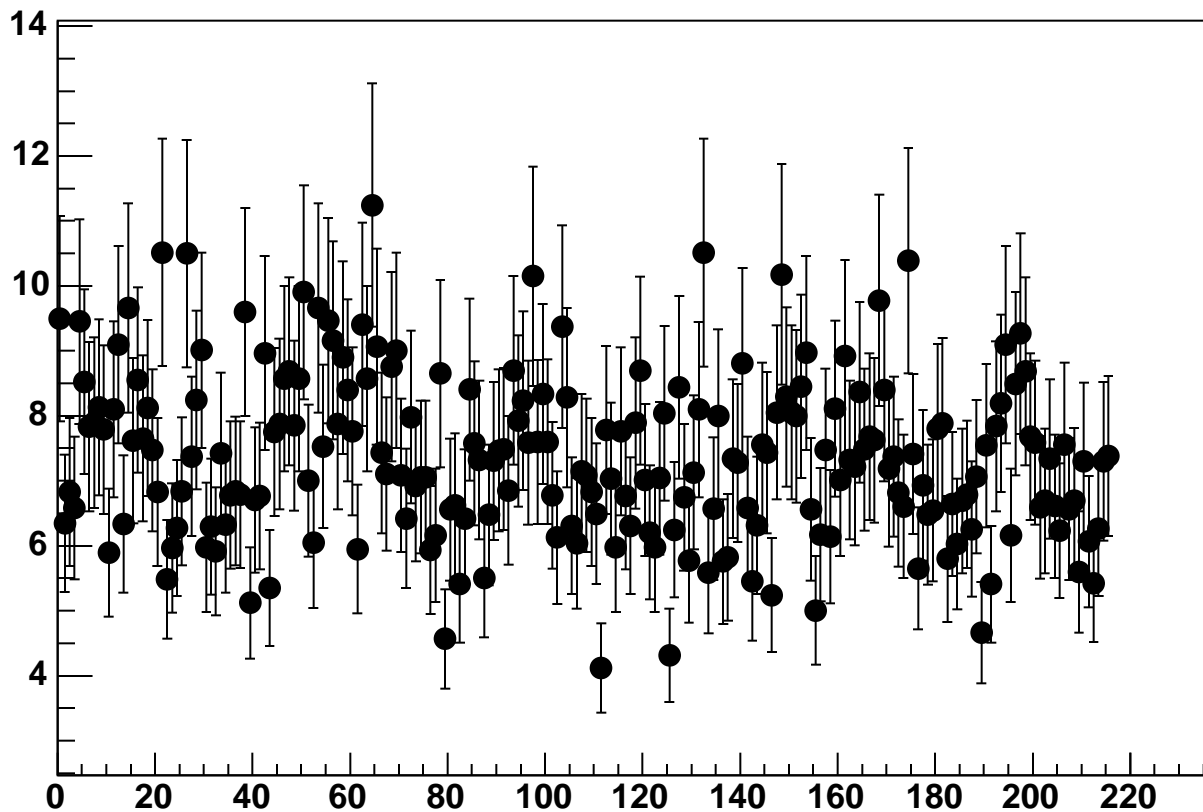
Enable 3, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



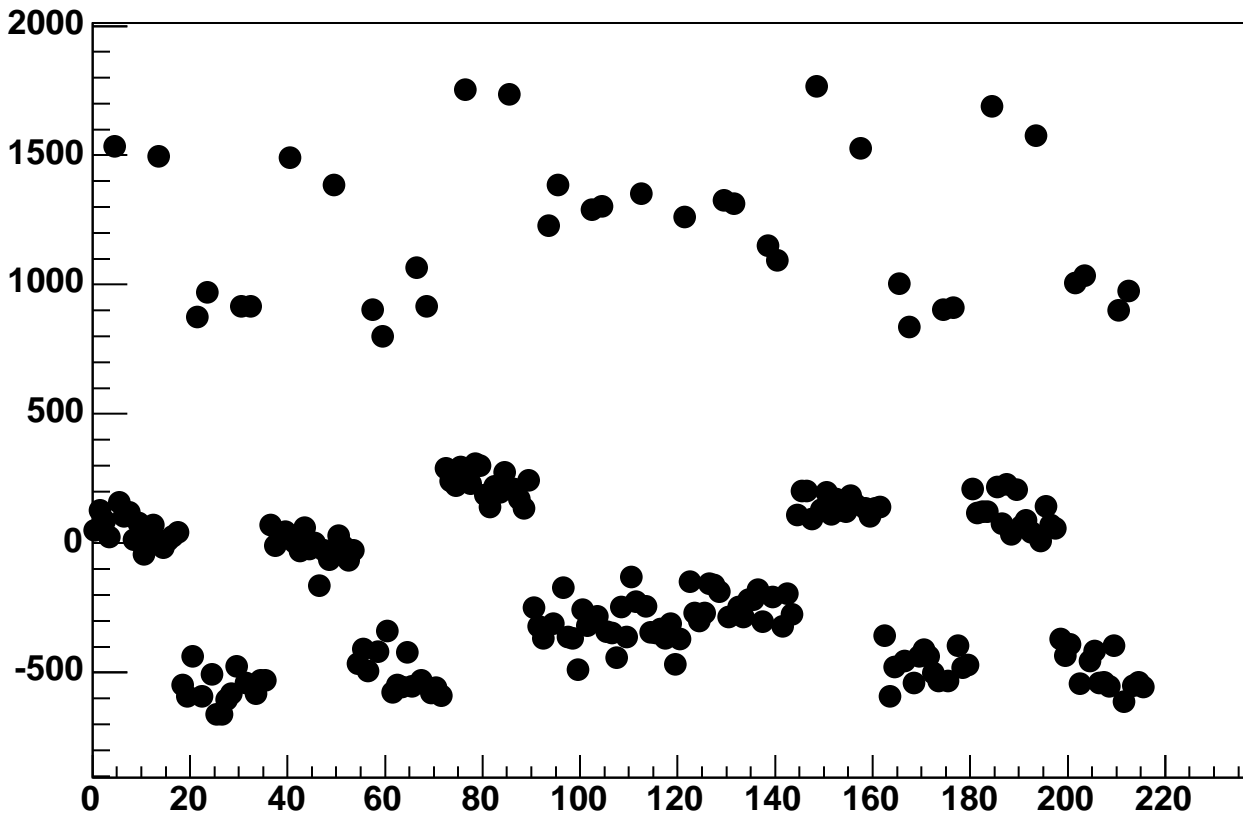
Enable 4, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



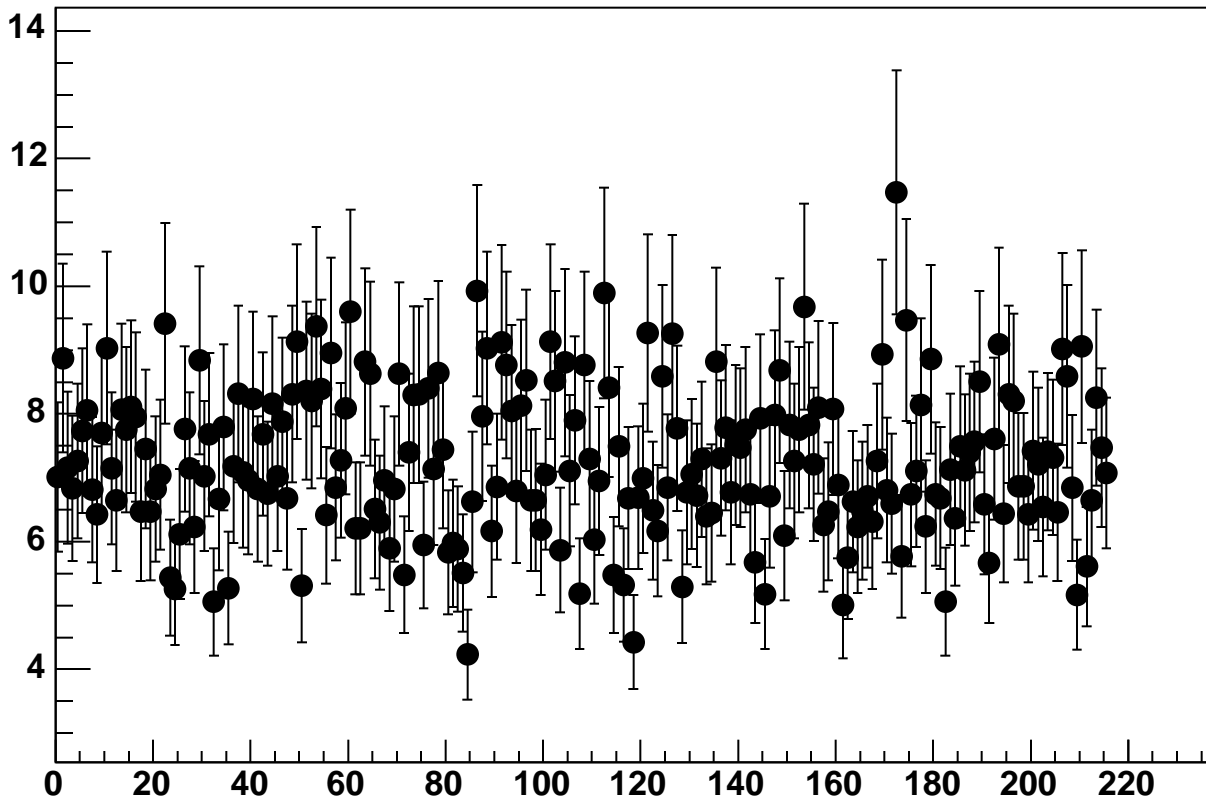
Enable 4, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



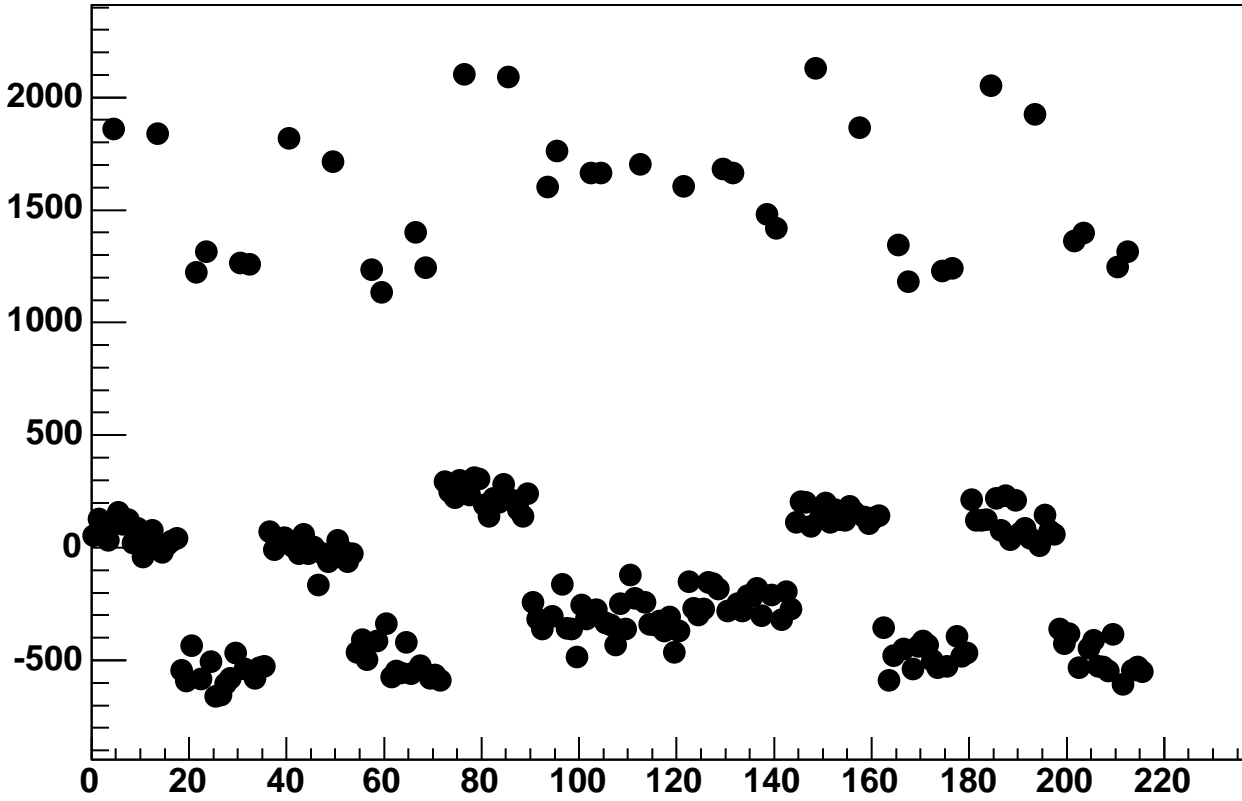
Enable 4, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



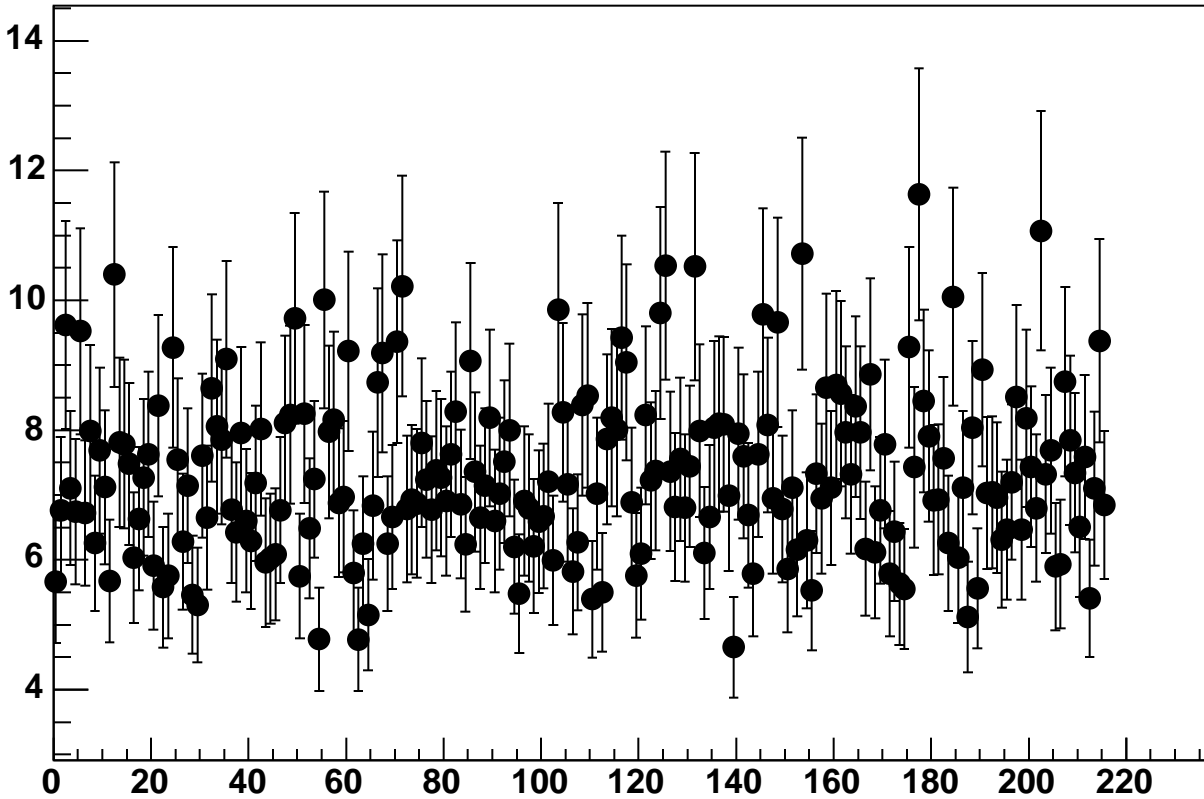
Enable 4, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



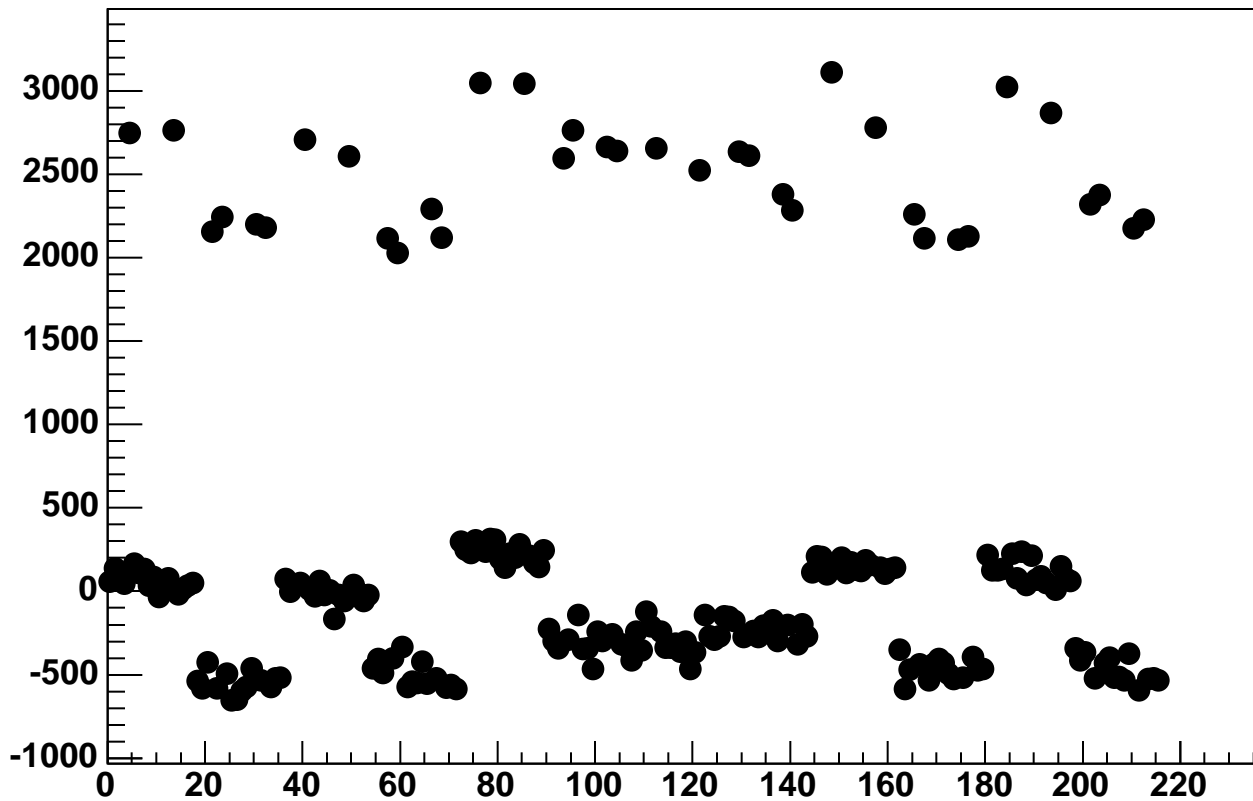
Enable 4, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



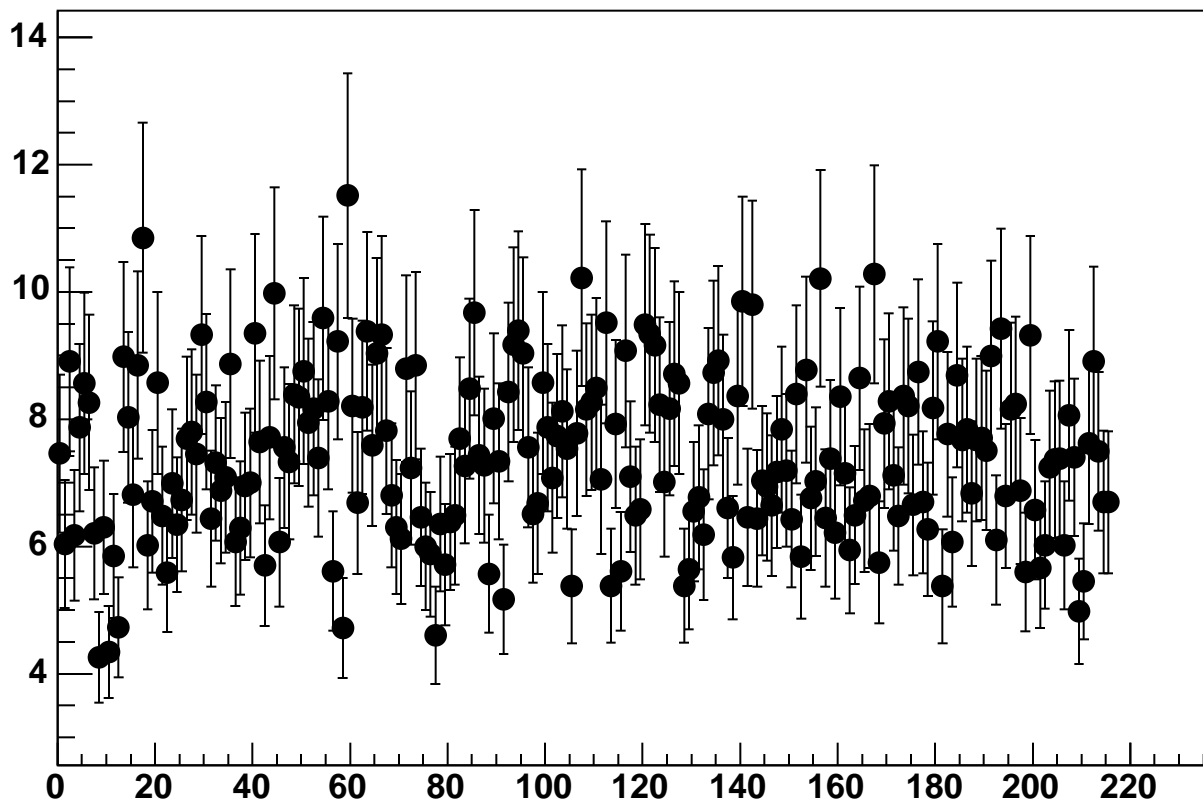
Enable 4, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



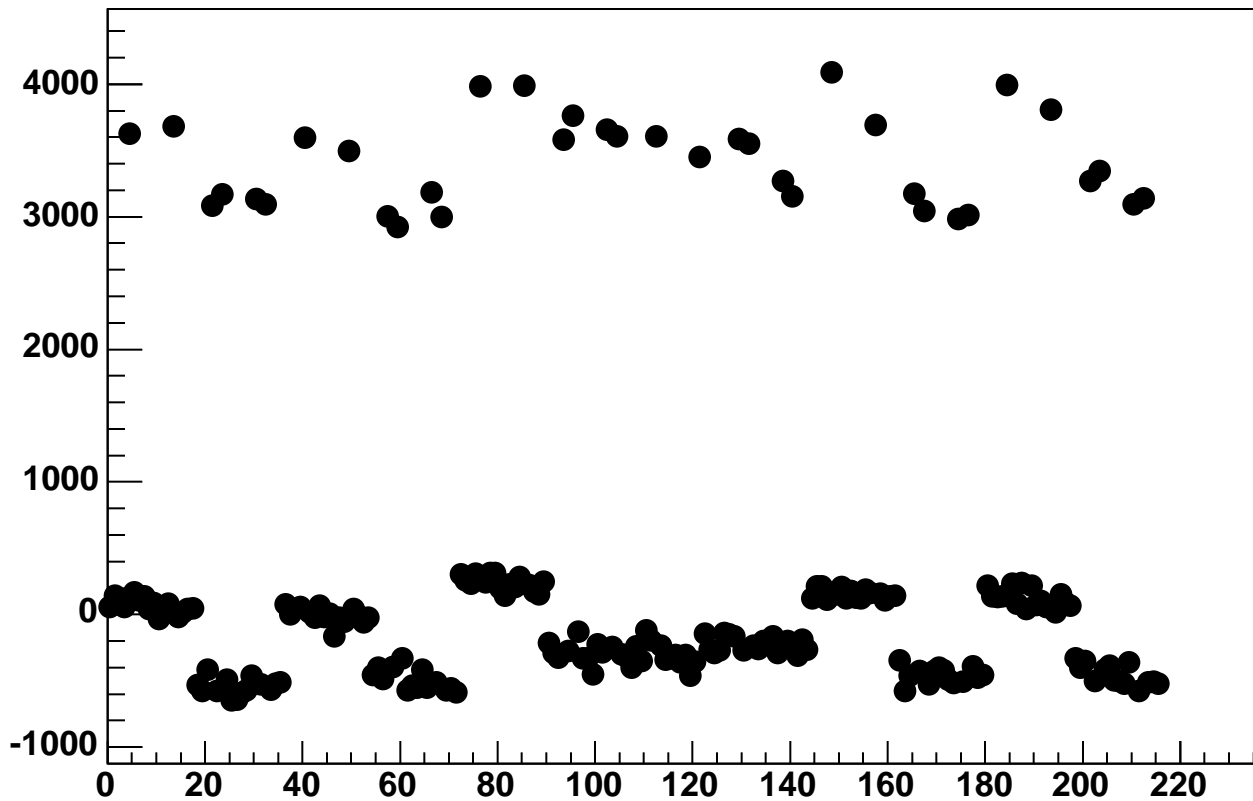
Enable 4, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



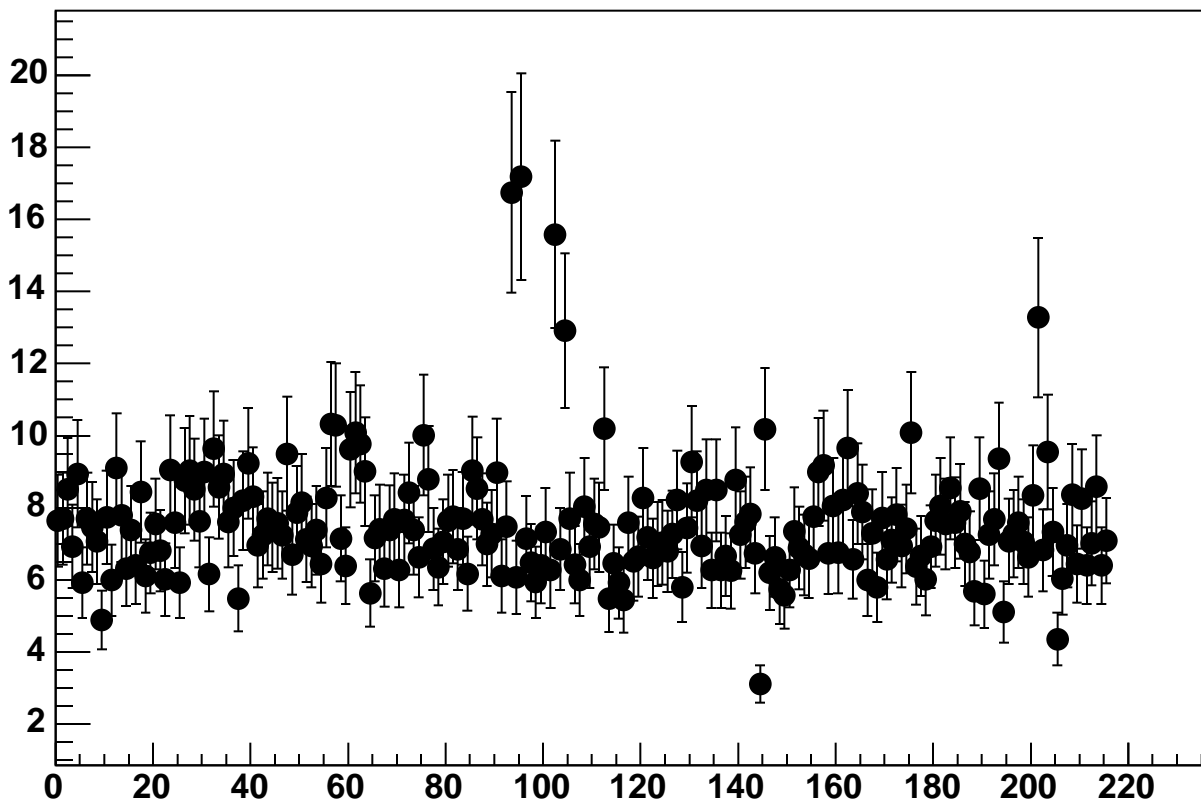
Enable 4, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



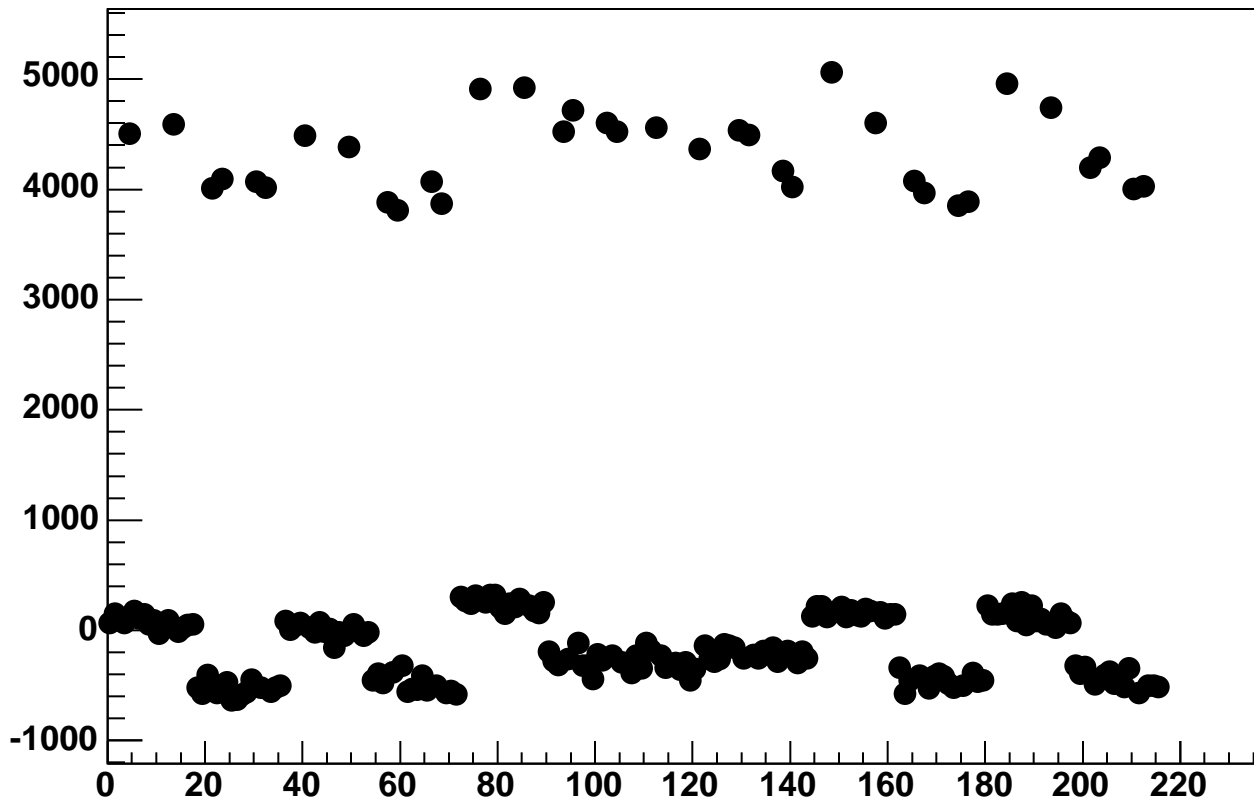
Enable 4, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



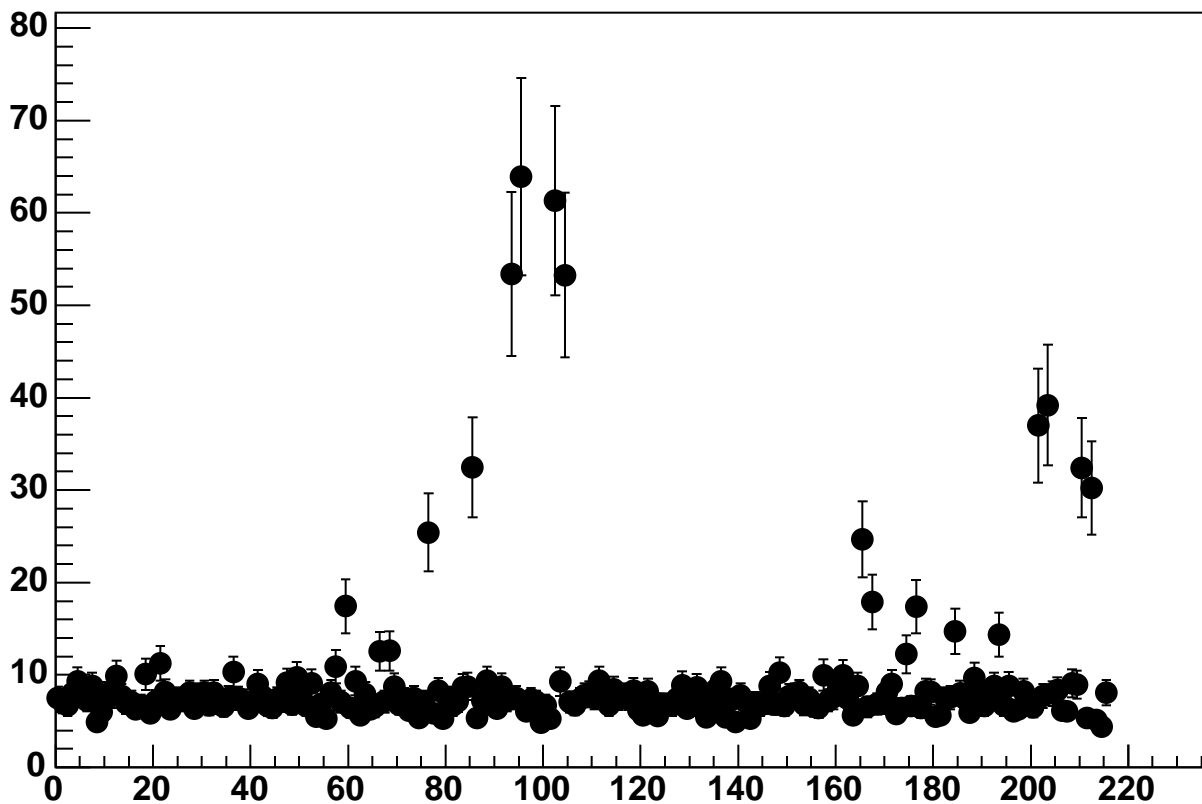
Enable 4, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



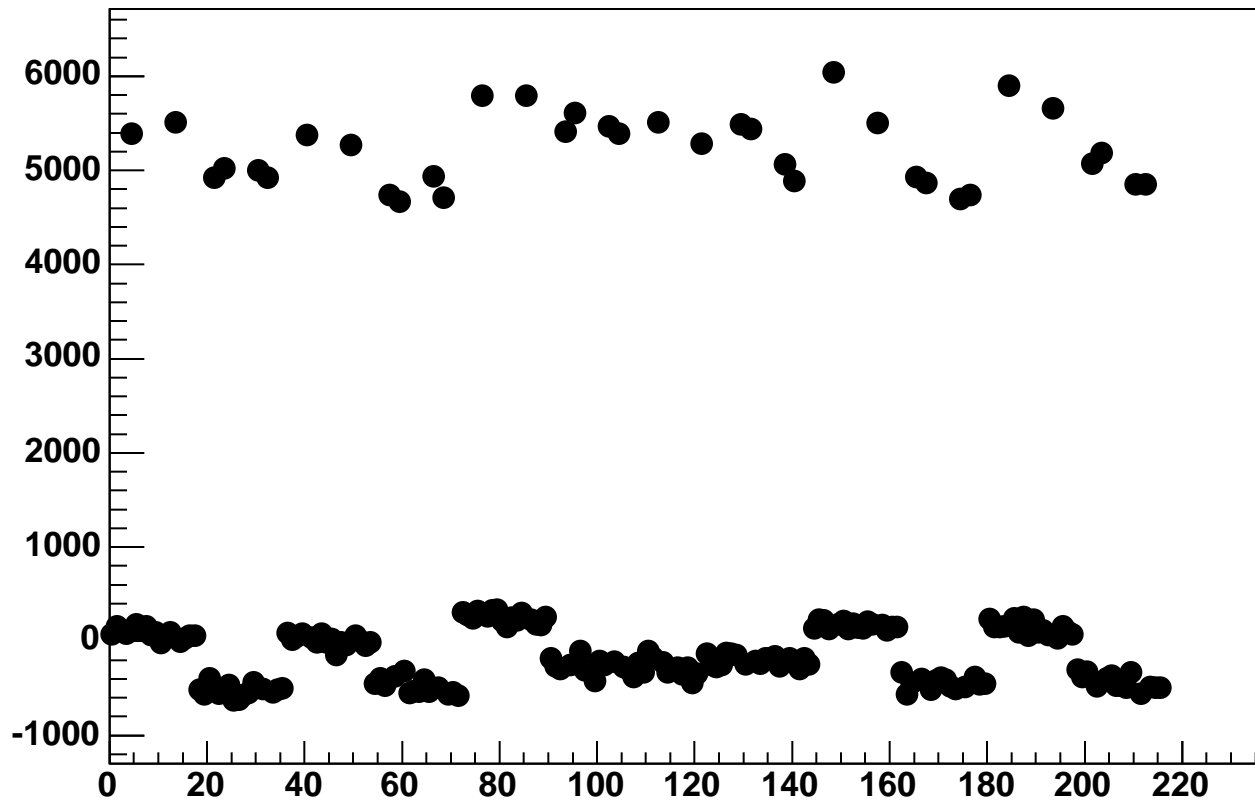
Enable 4, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



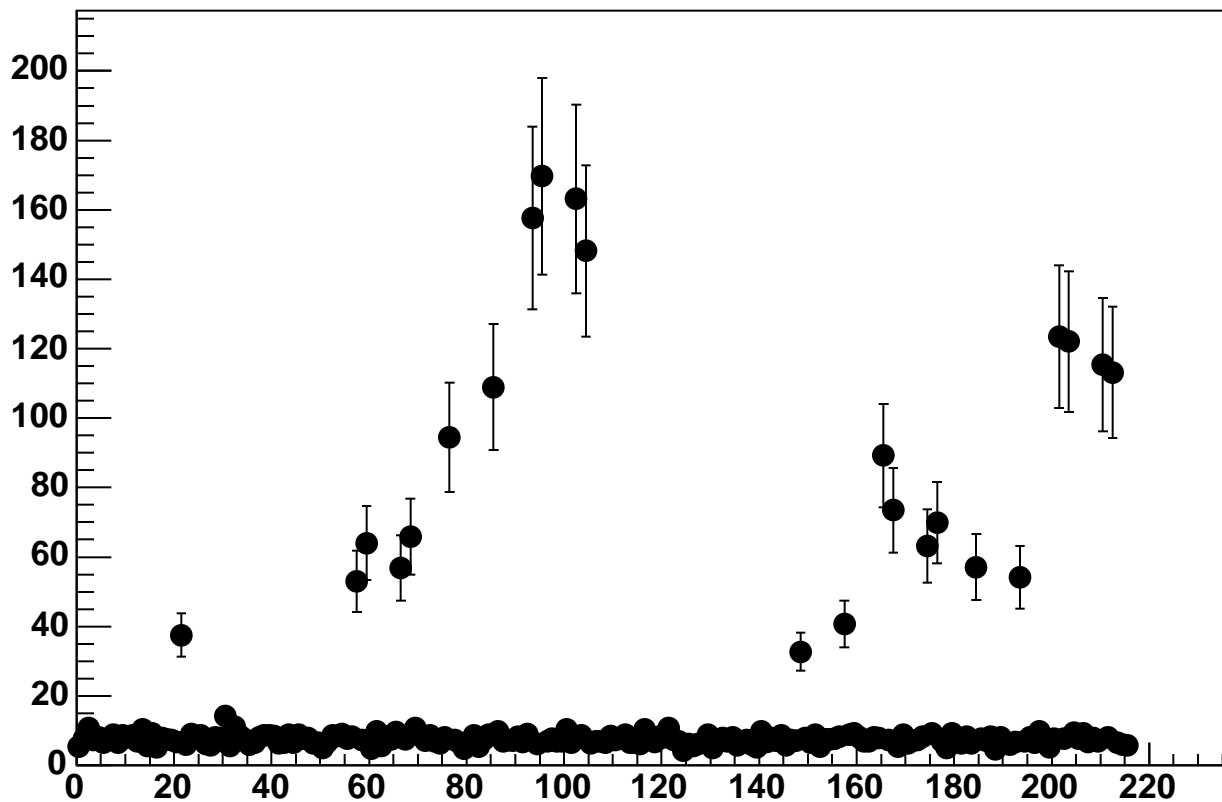
Enable 4, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



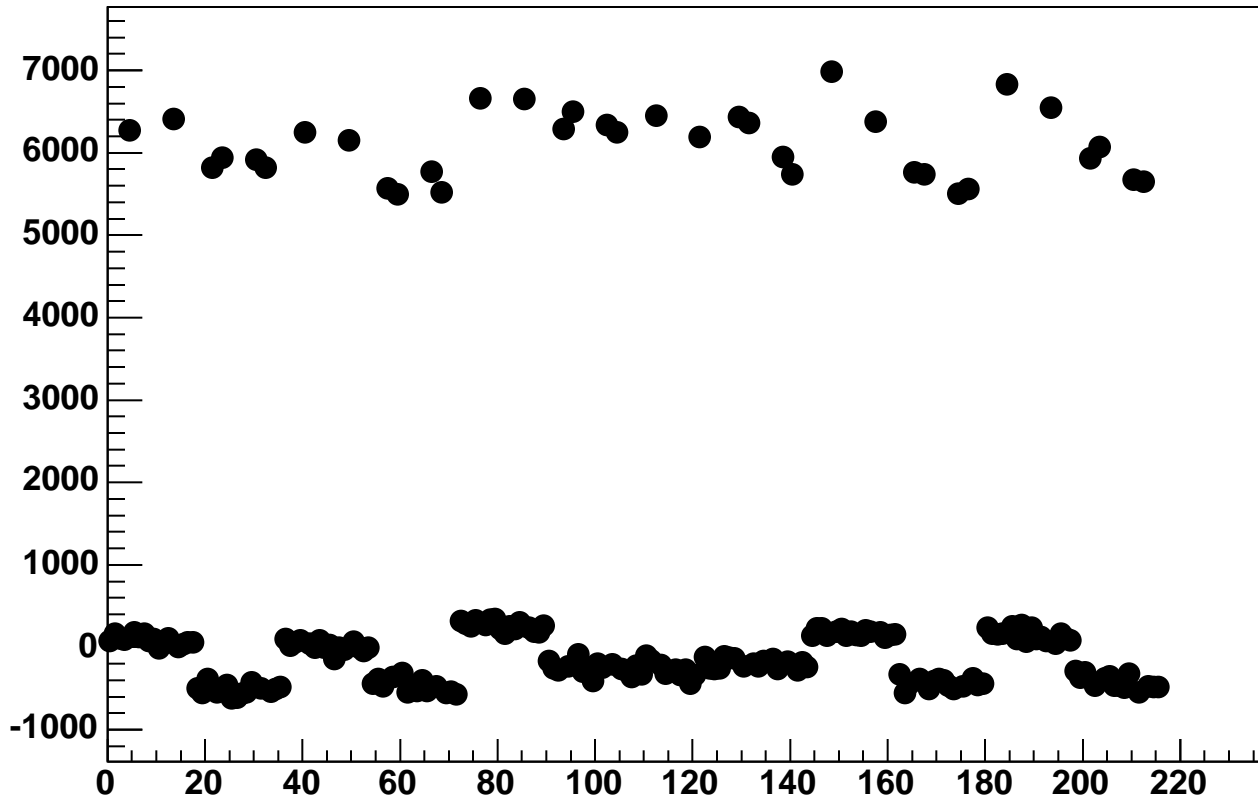
Enable 4, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



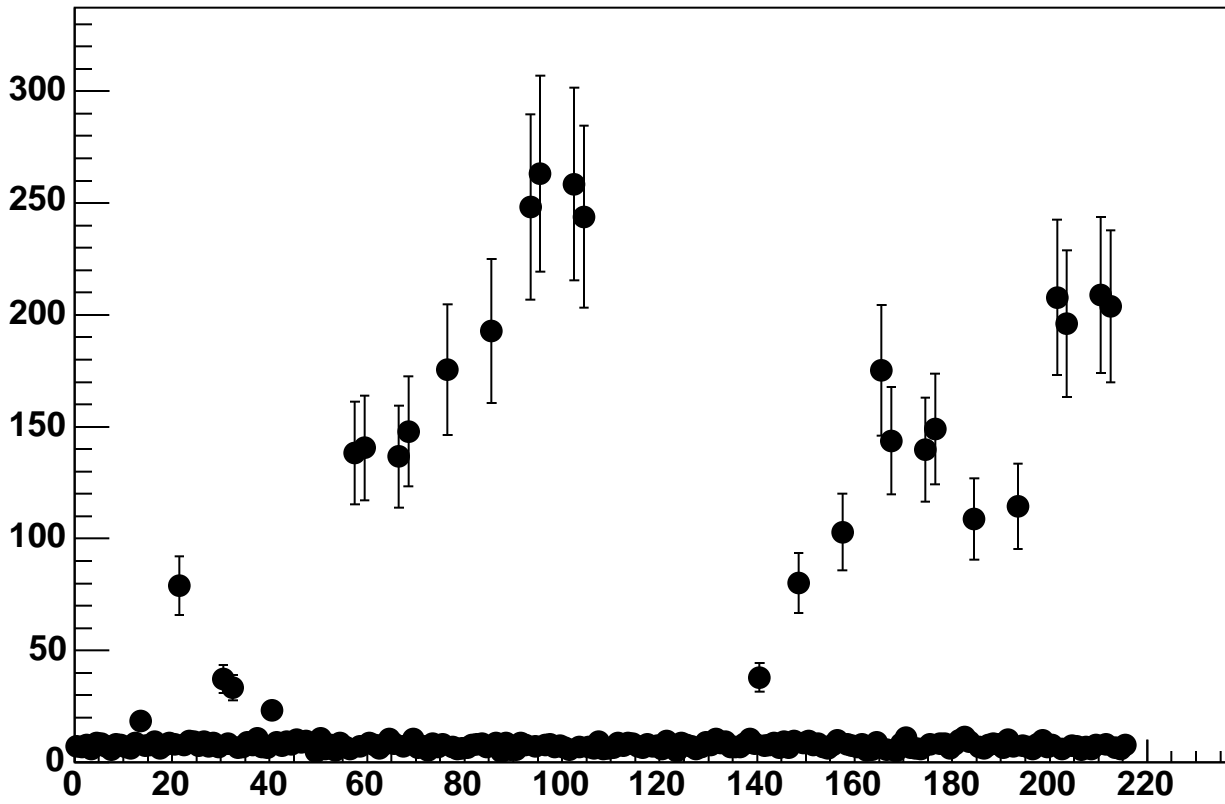
Enable 4, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



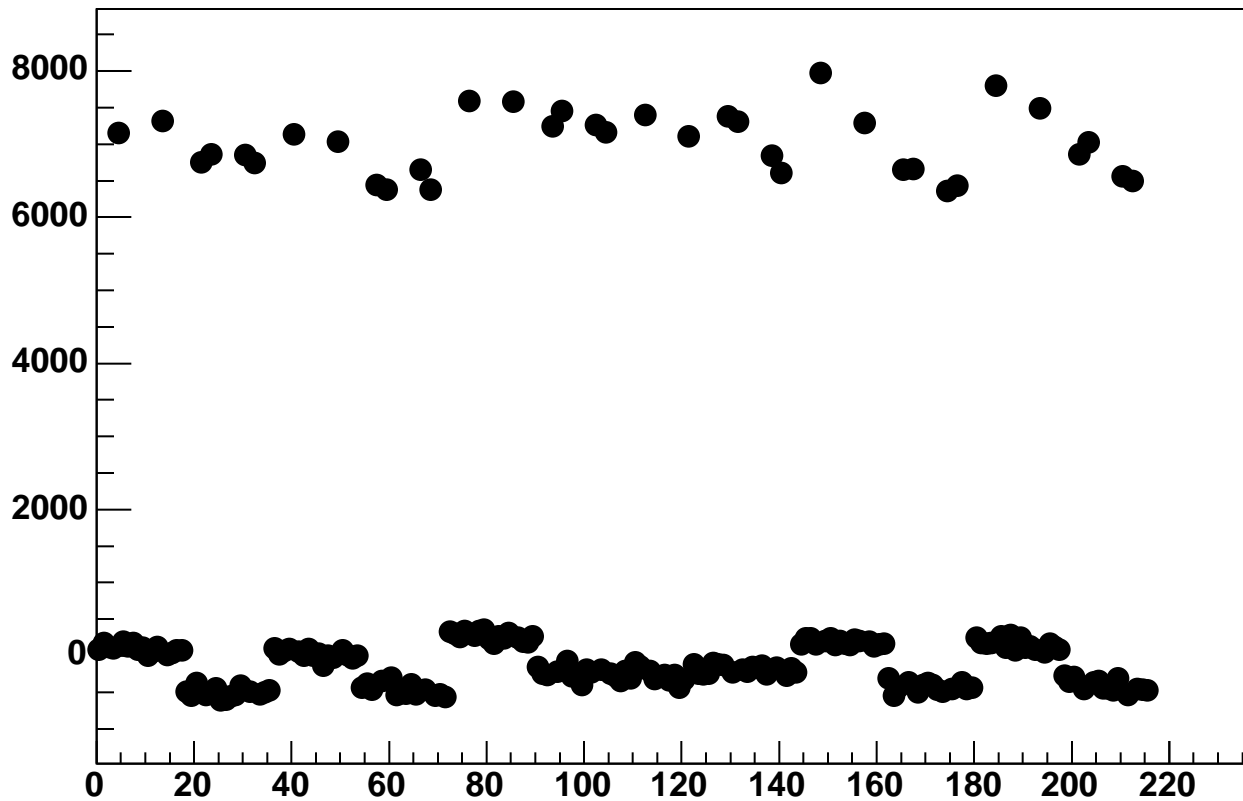
Enable 4, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



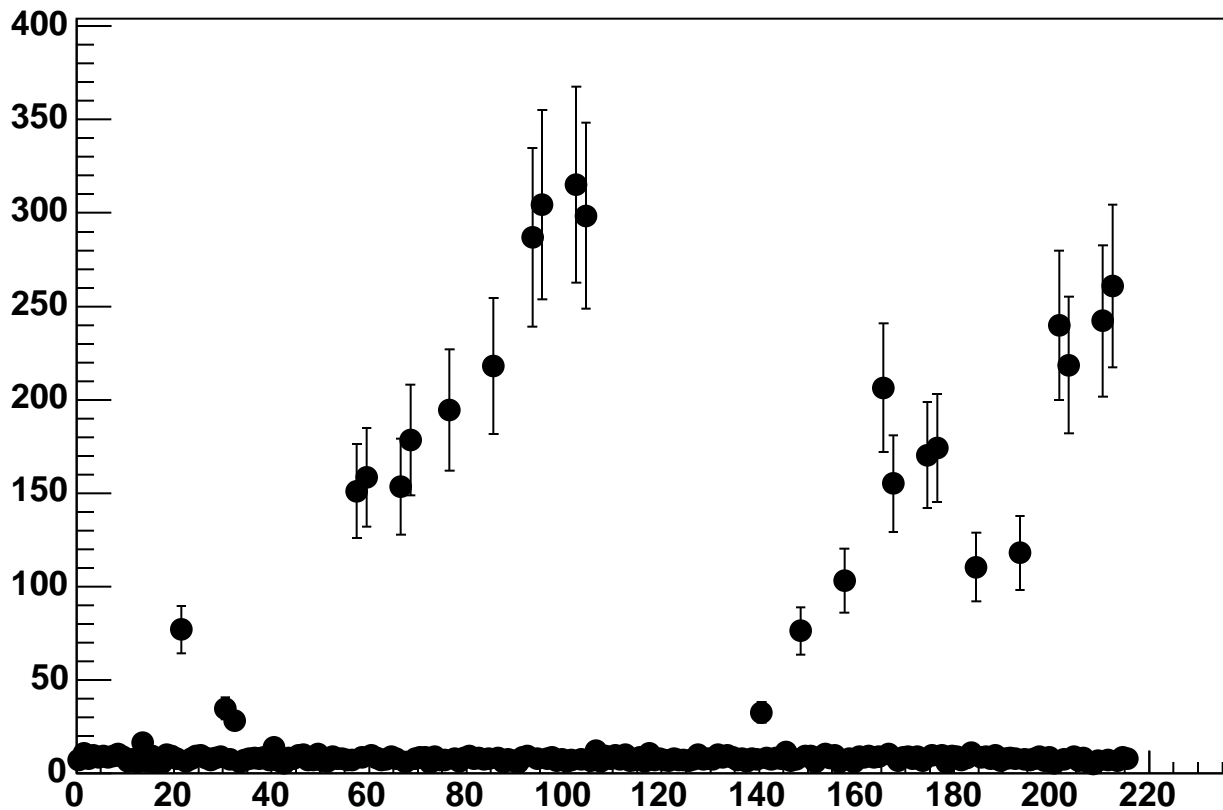
Enable 4, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



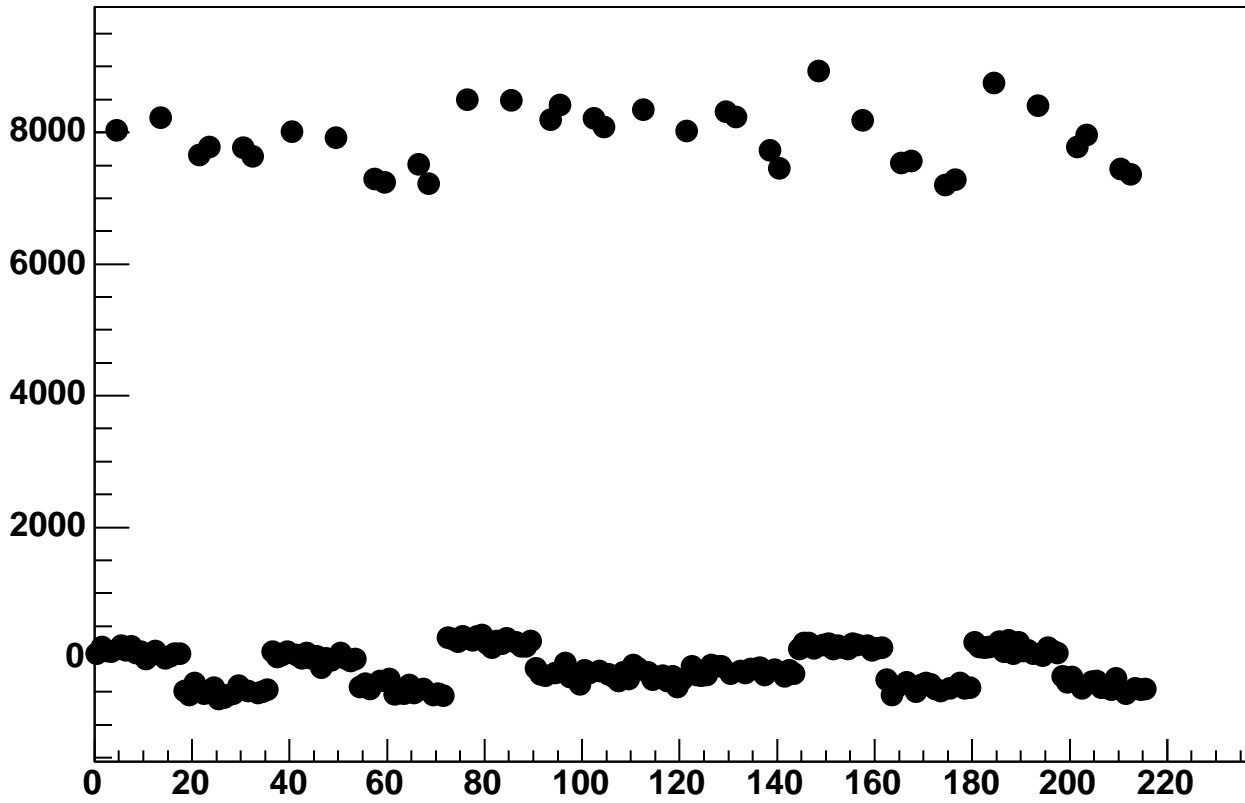
Enable 4, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



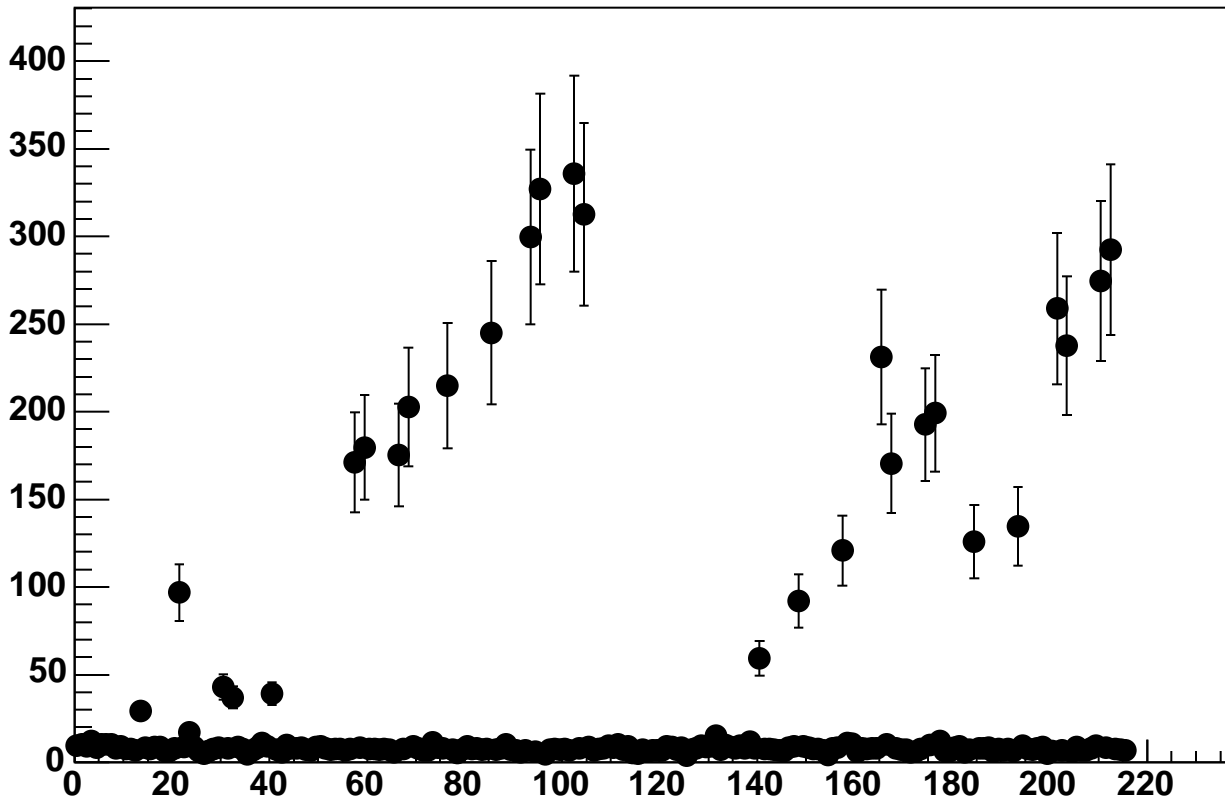
Enable 4, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



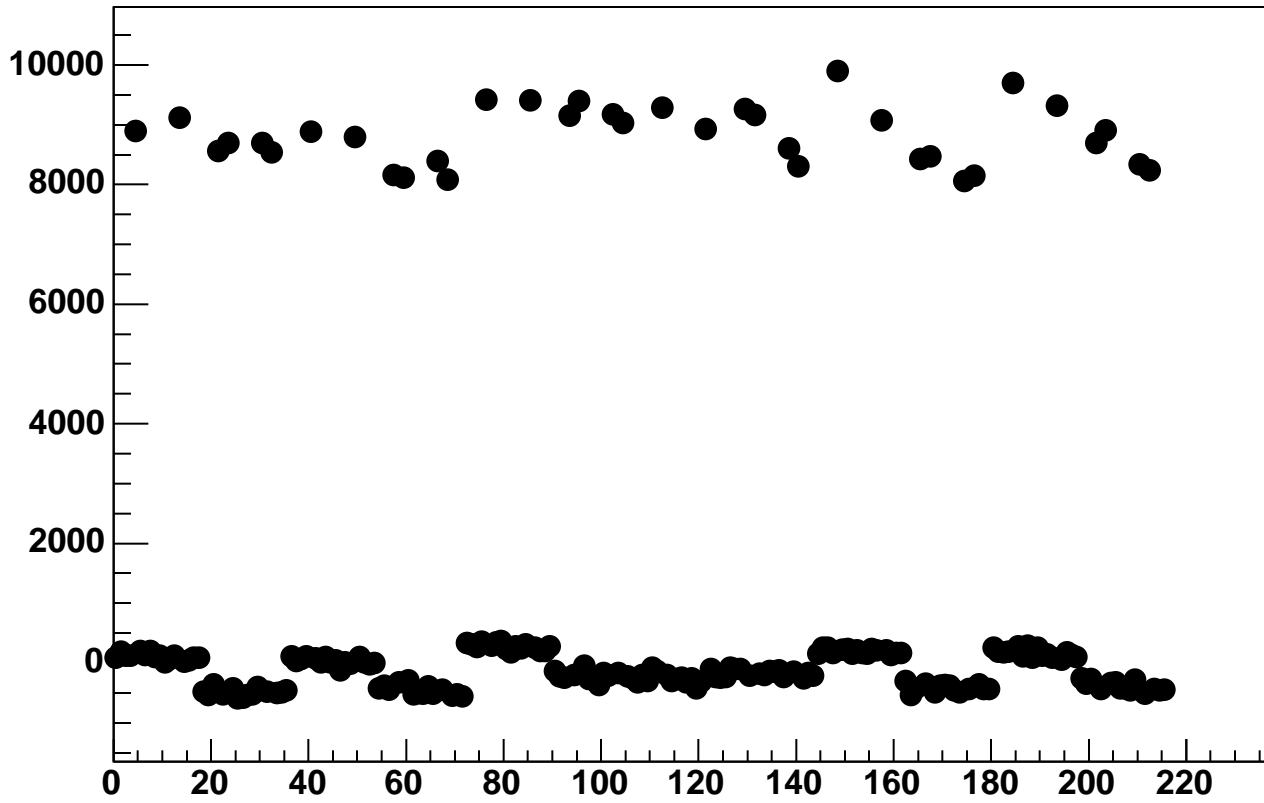
Enable 4, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



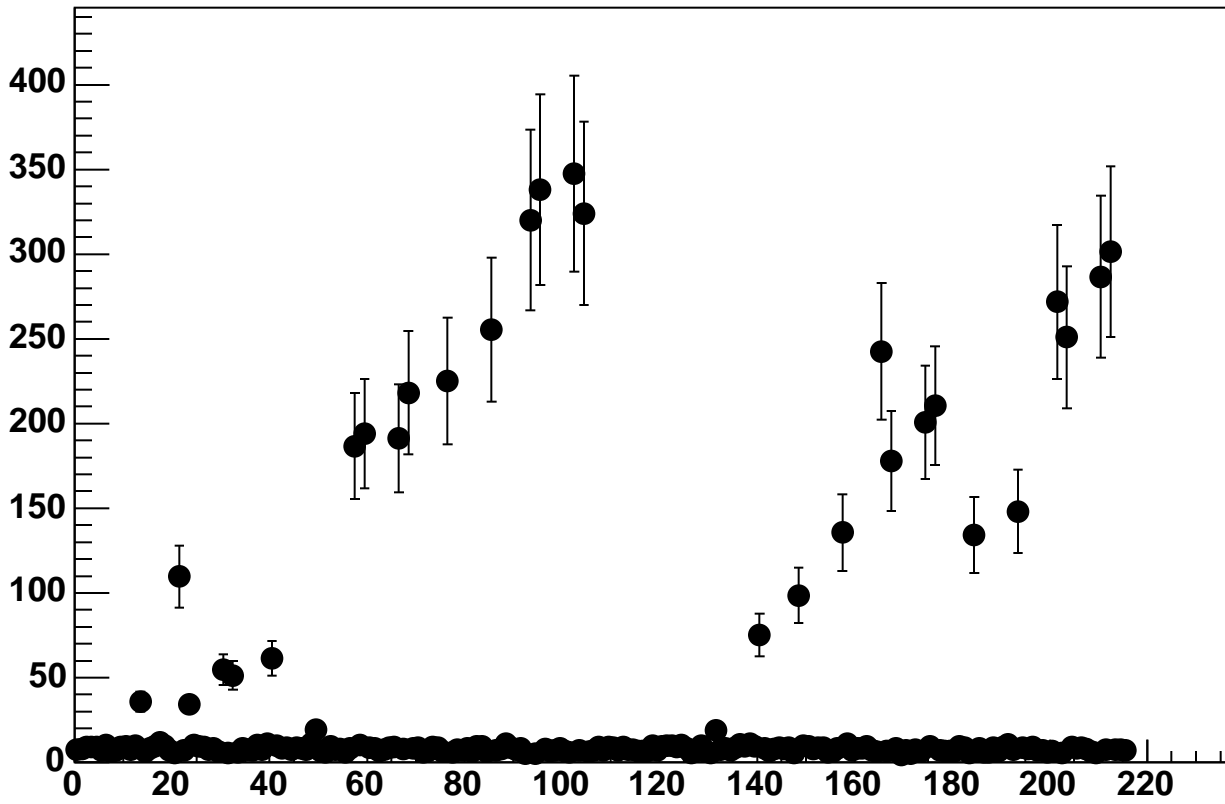
Enable 4, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



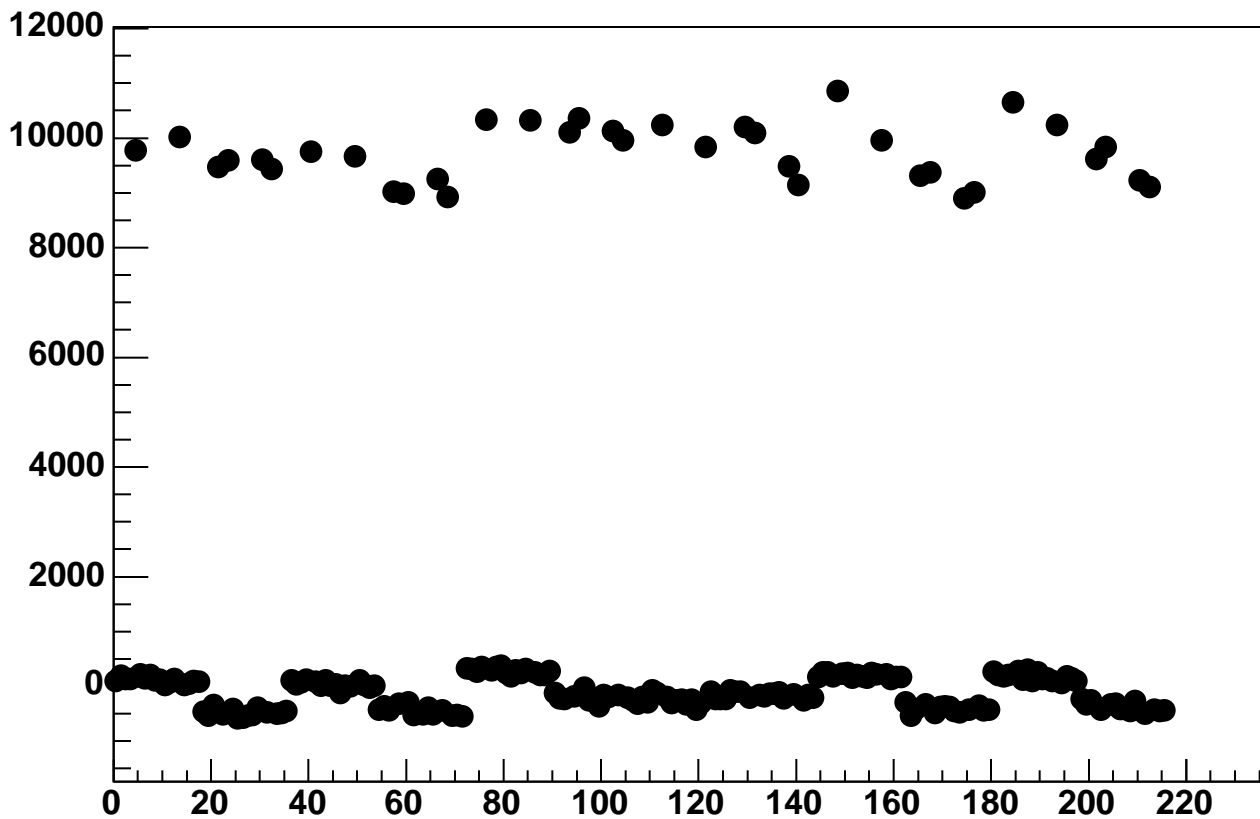
Enable 4, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



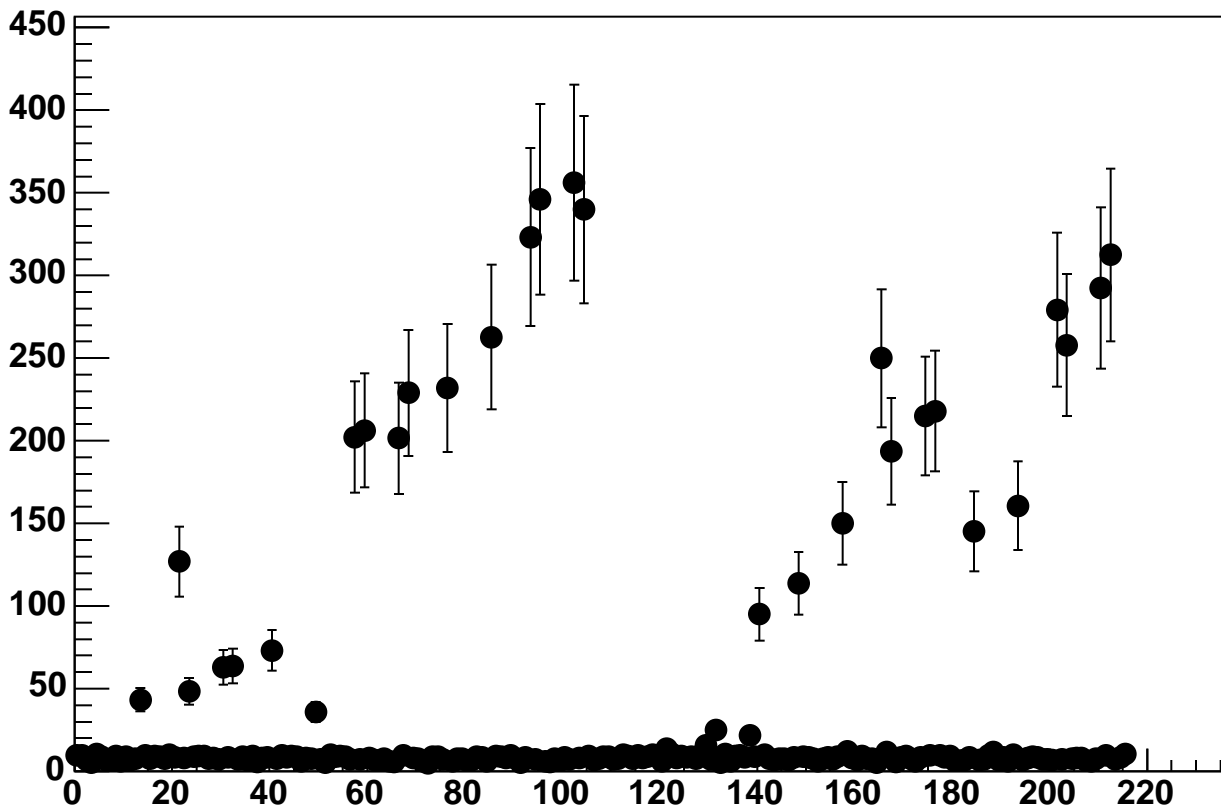
Enable 4, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



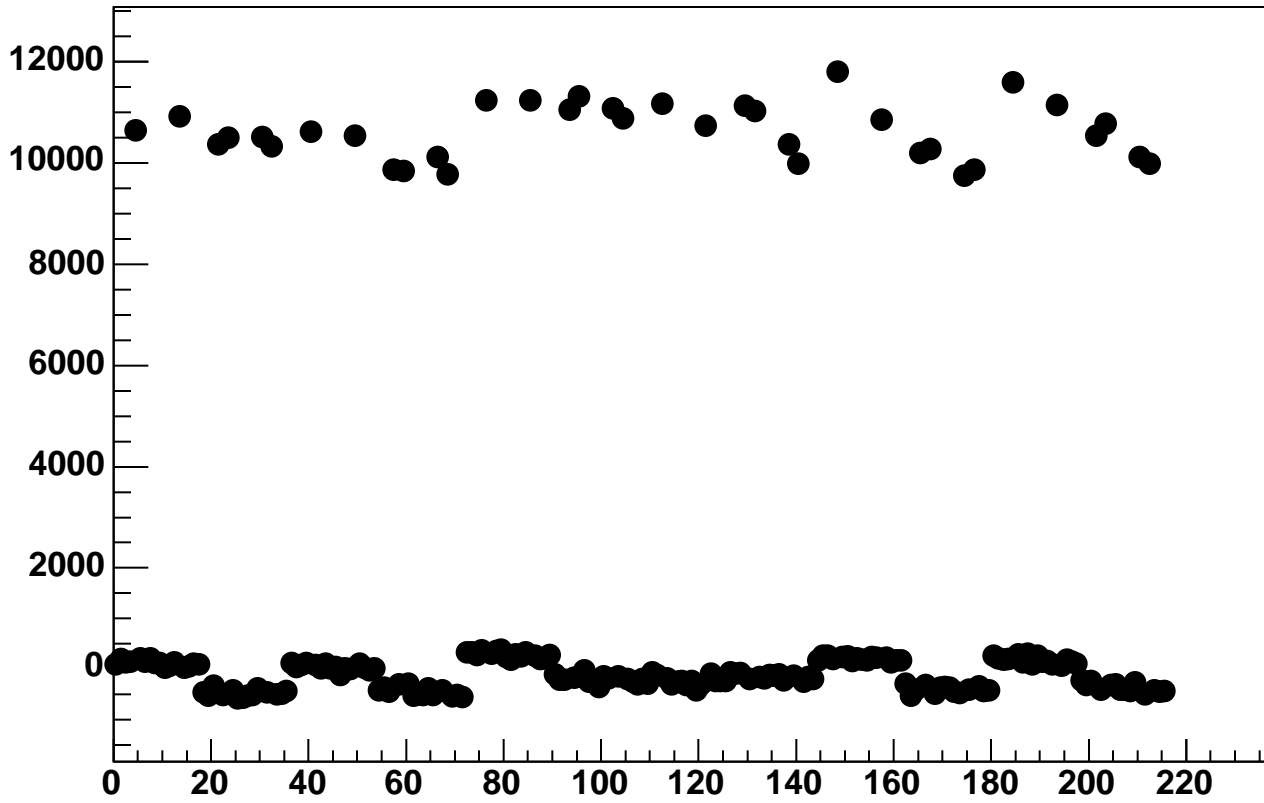
Enable 4, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



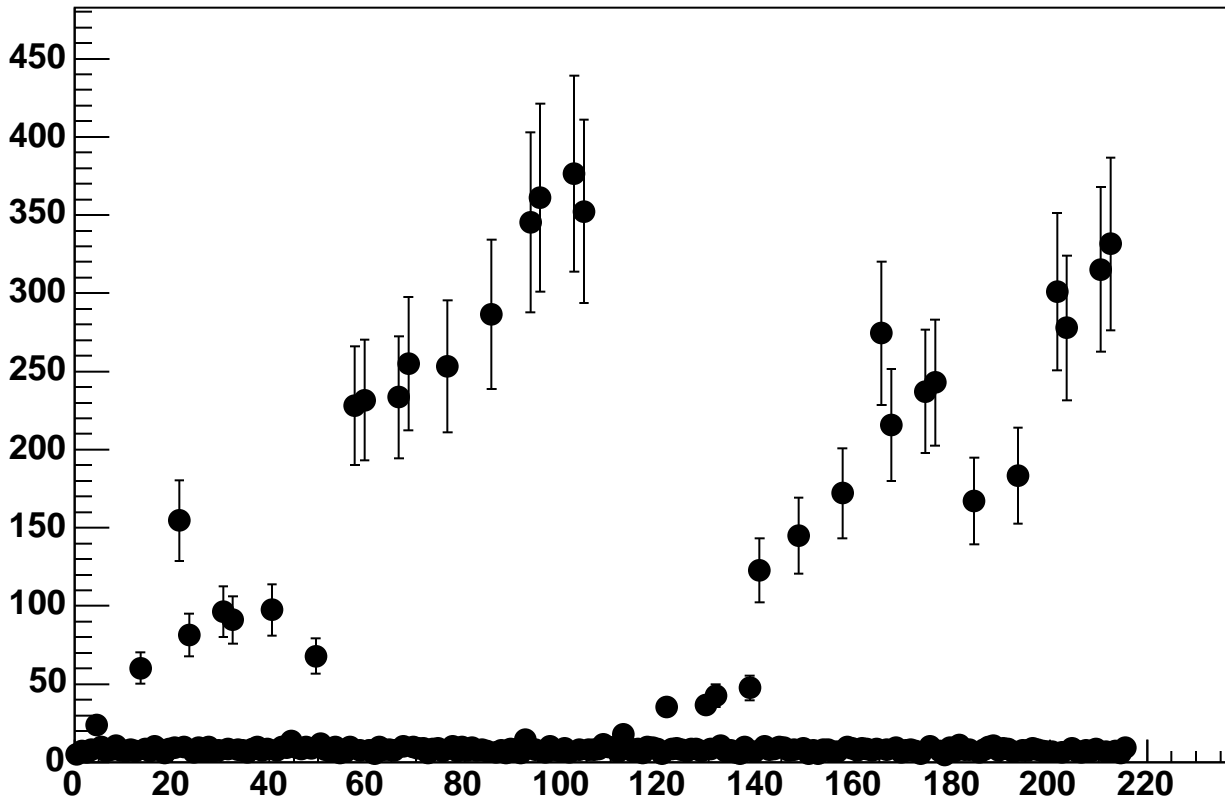
Enable 4, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



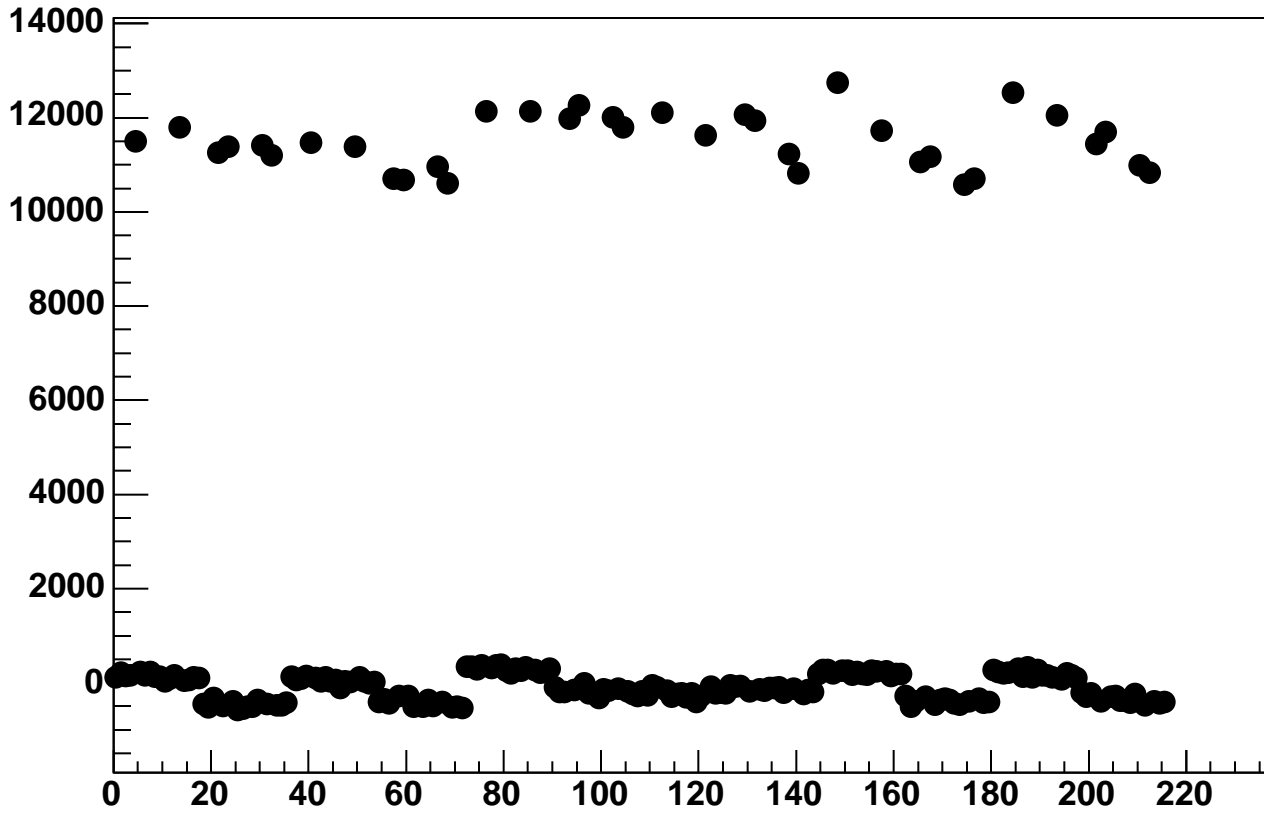
Enable 4, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



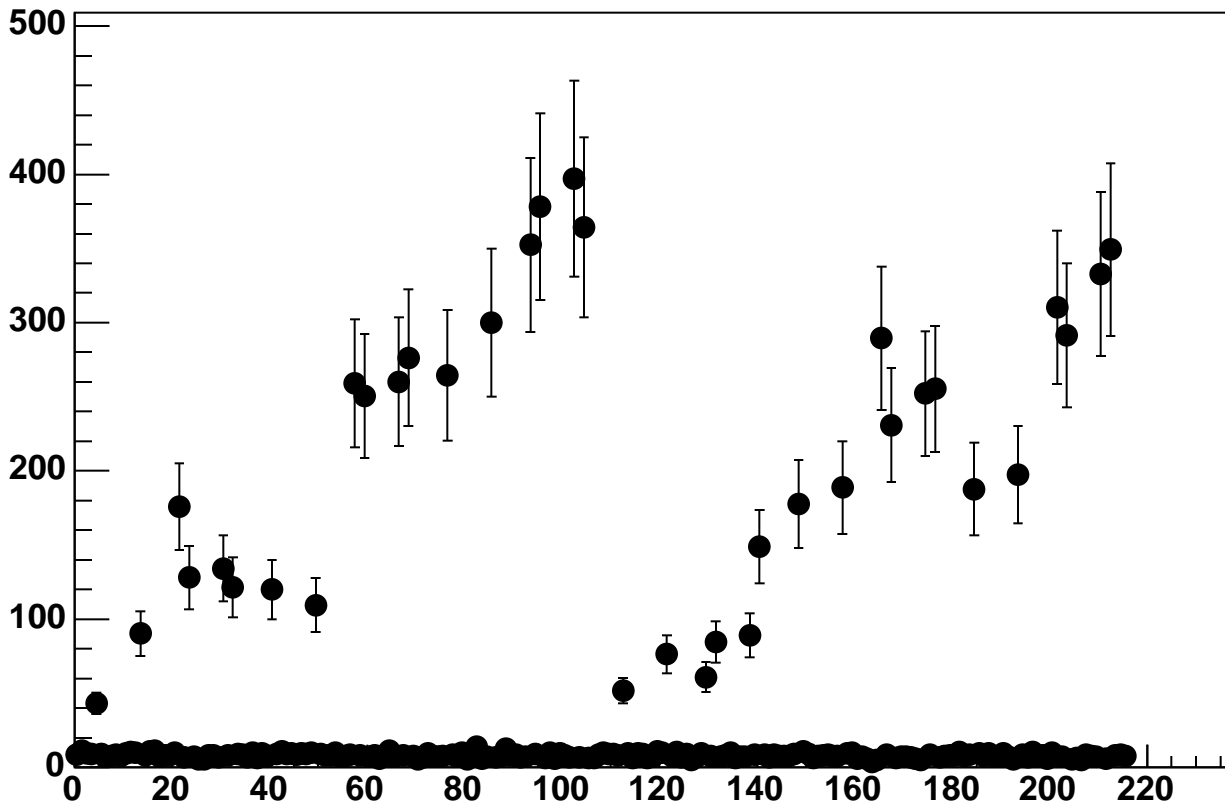
Enable 4, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



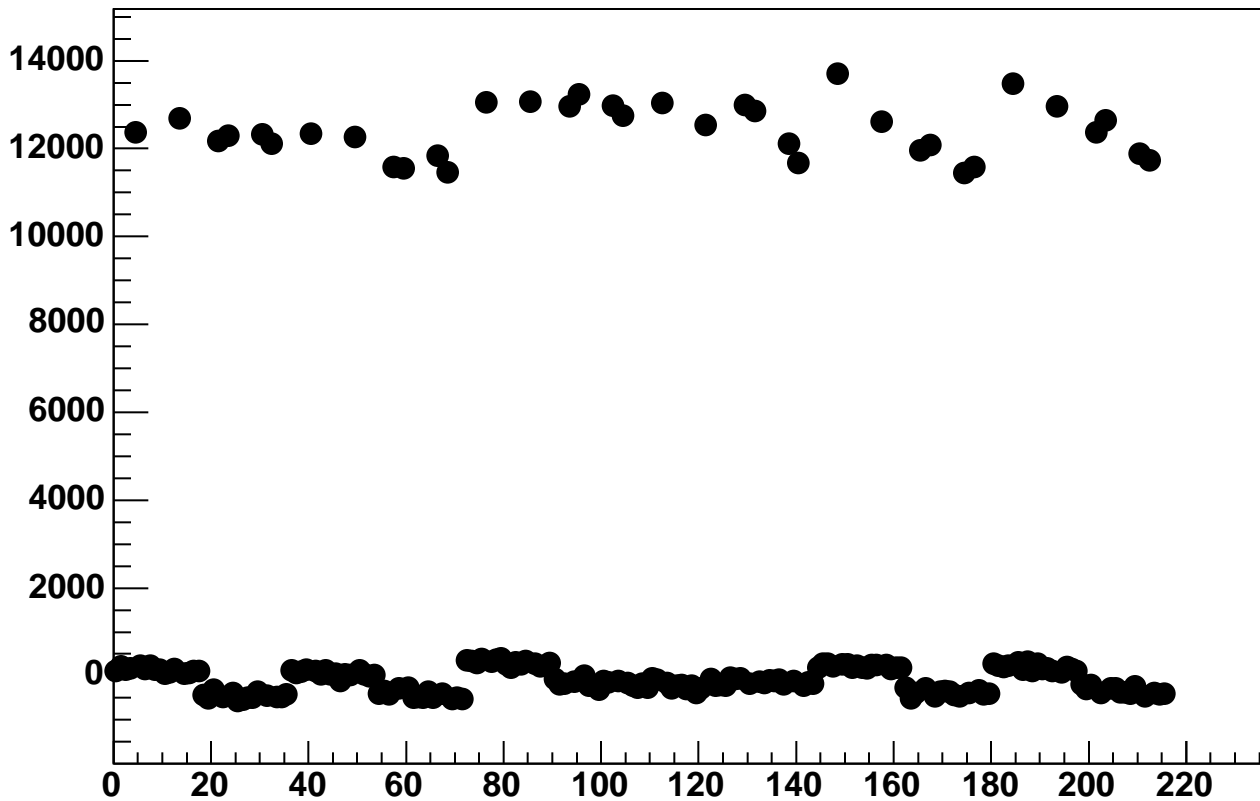
Enable 4, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



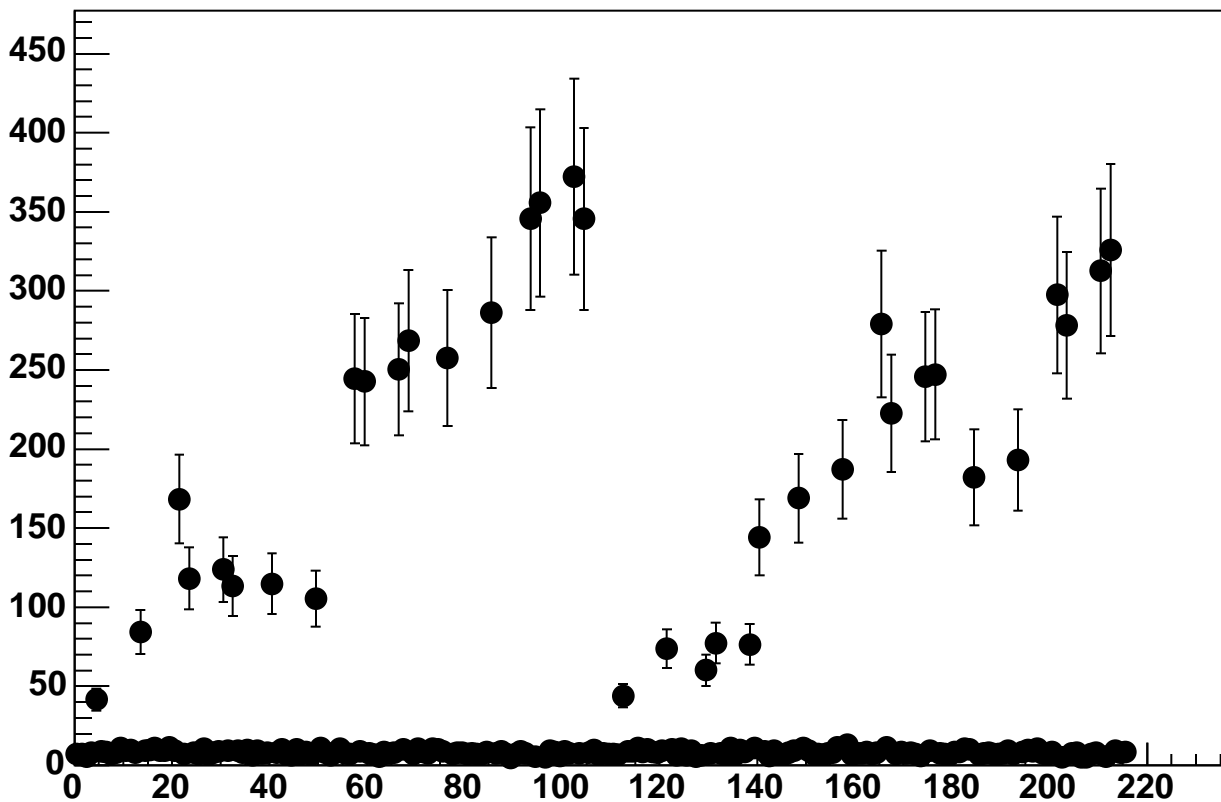
Enable 4, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



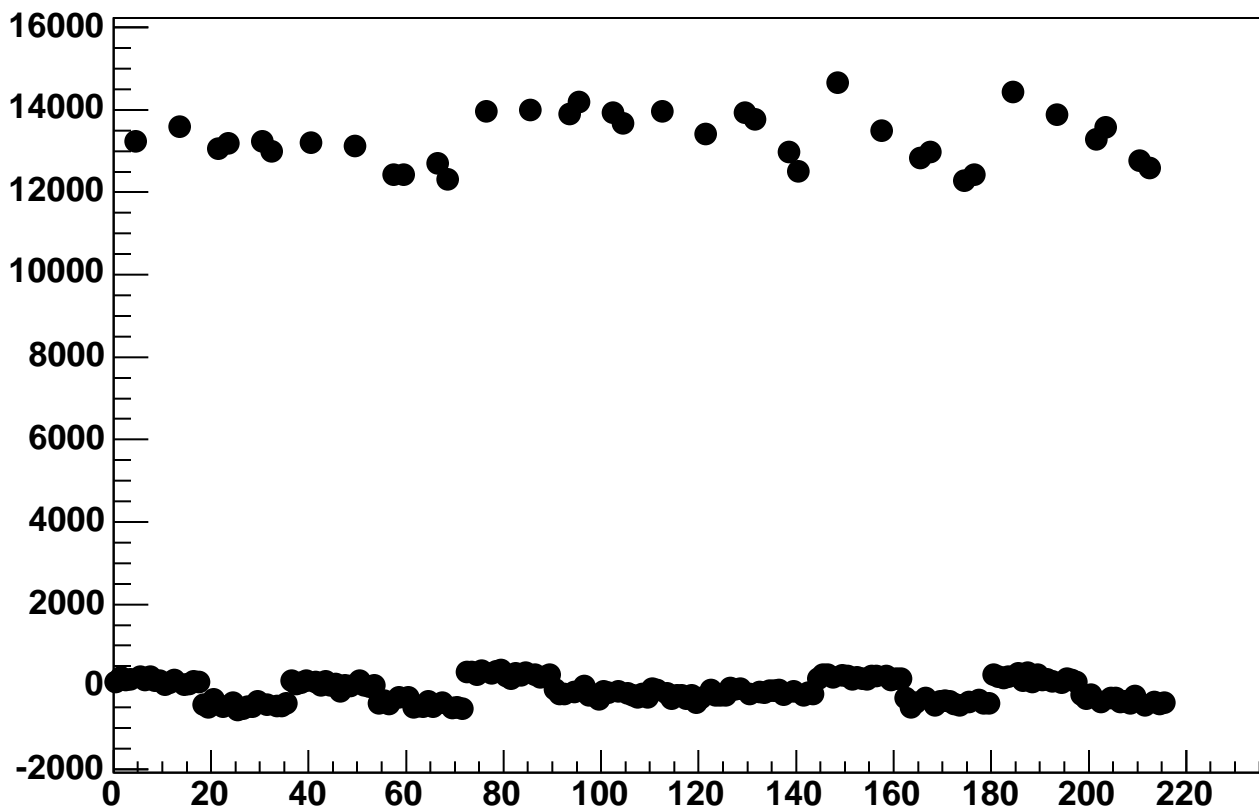
Enable 4, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



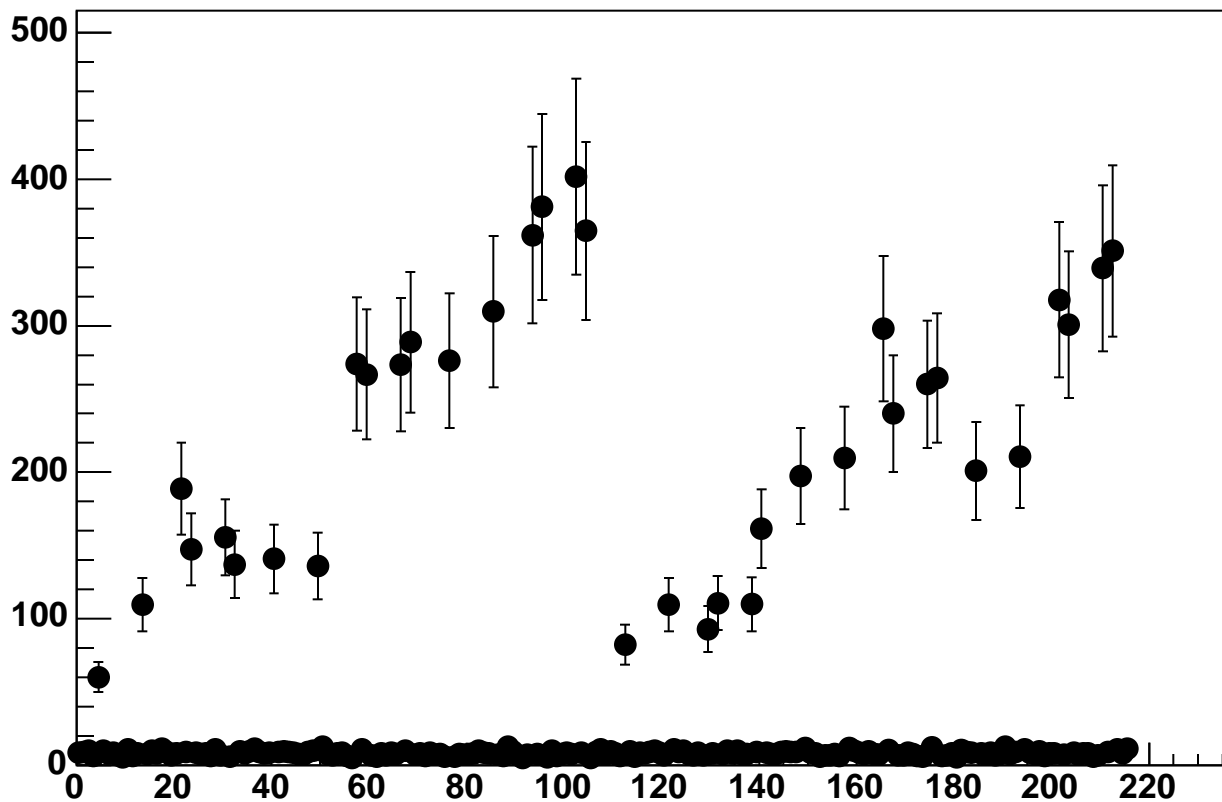
Enable 4, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



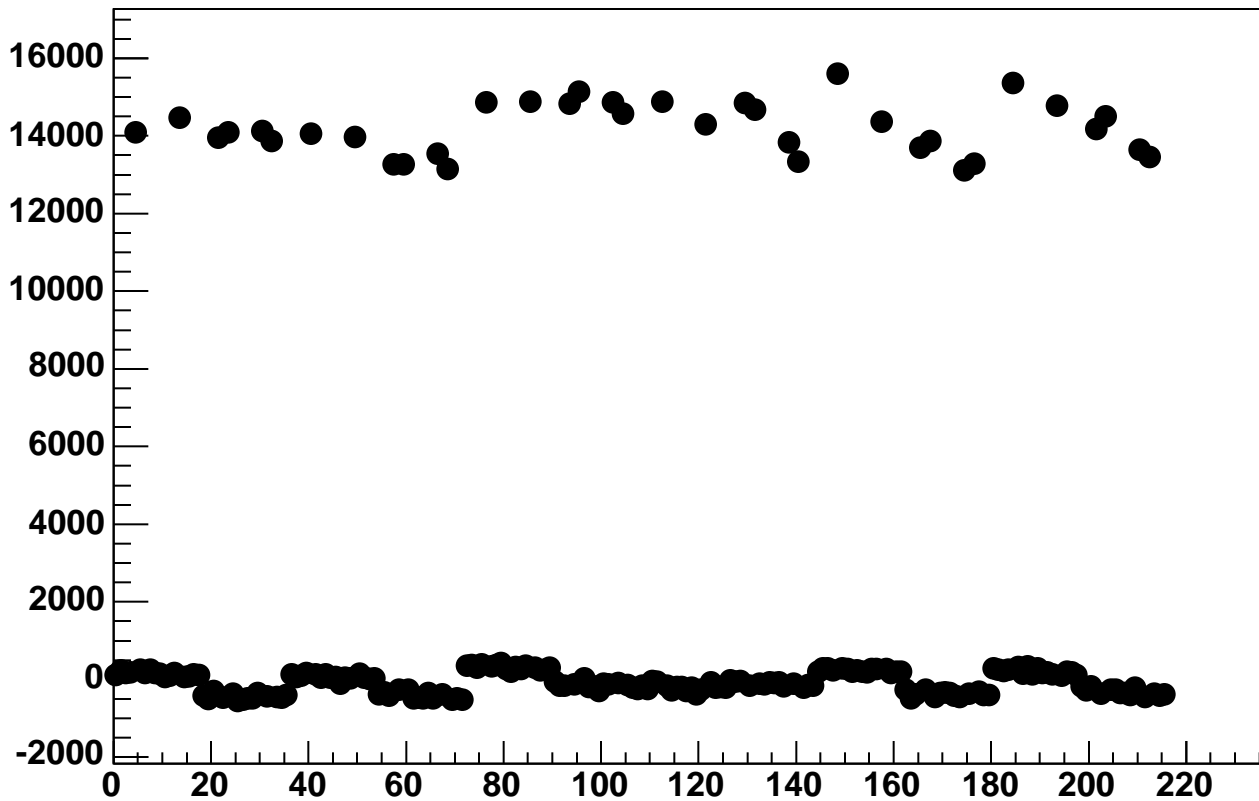
Enable 4, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



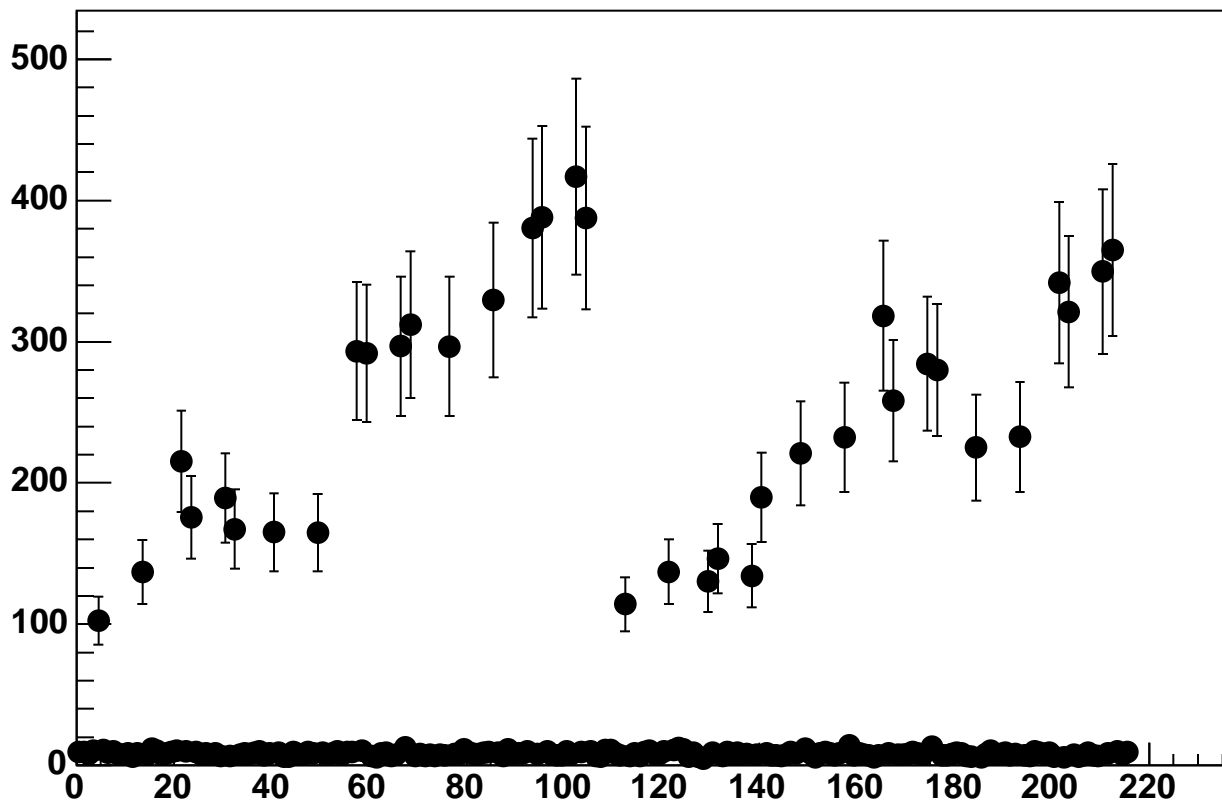
Enable 4, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



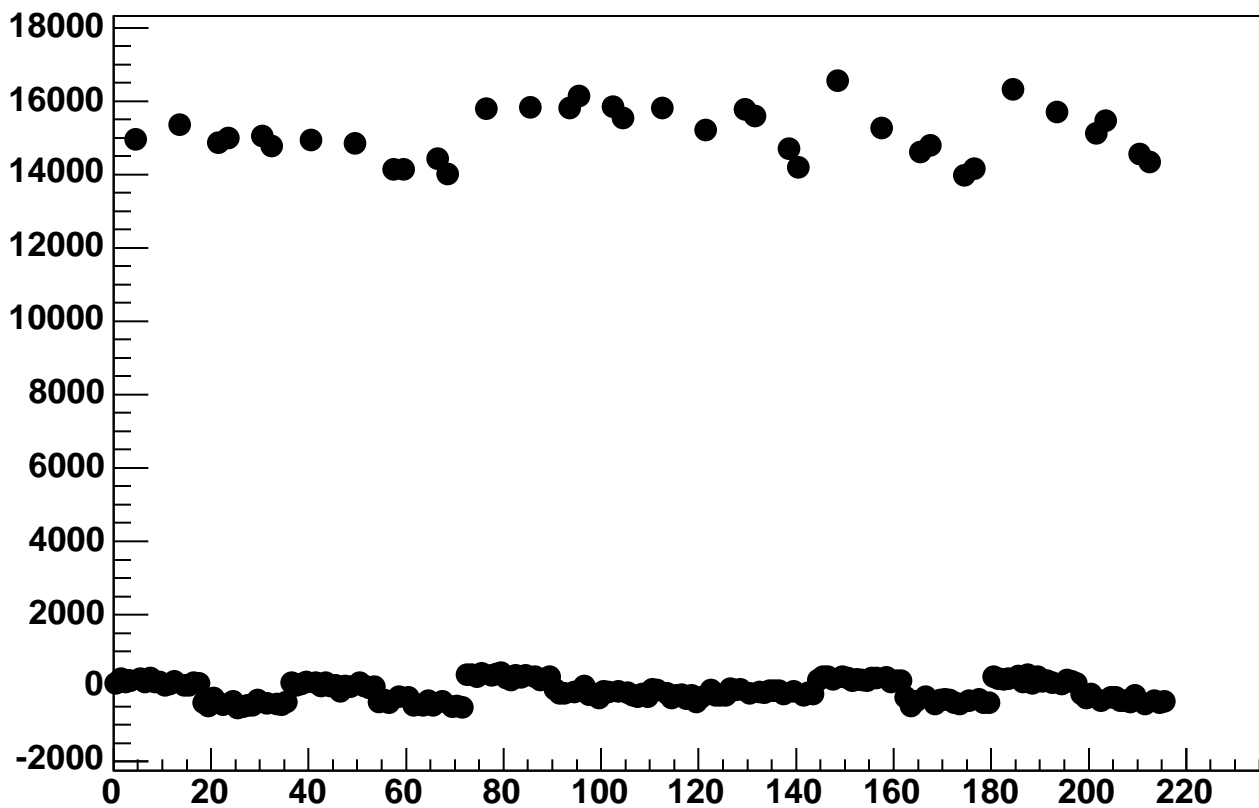
Enable 4, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



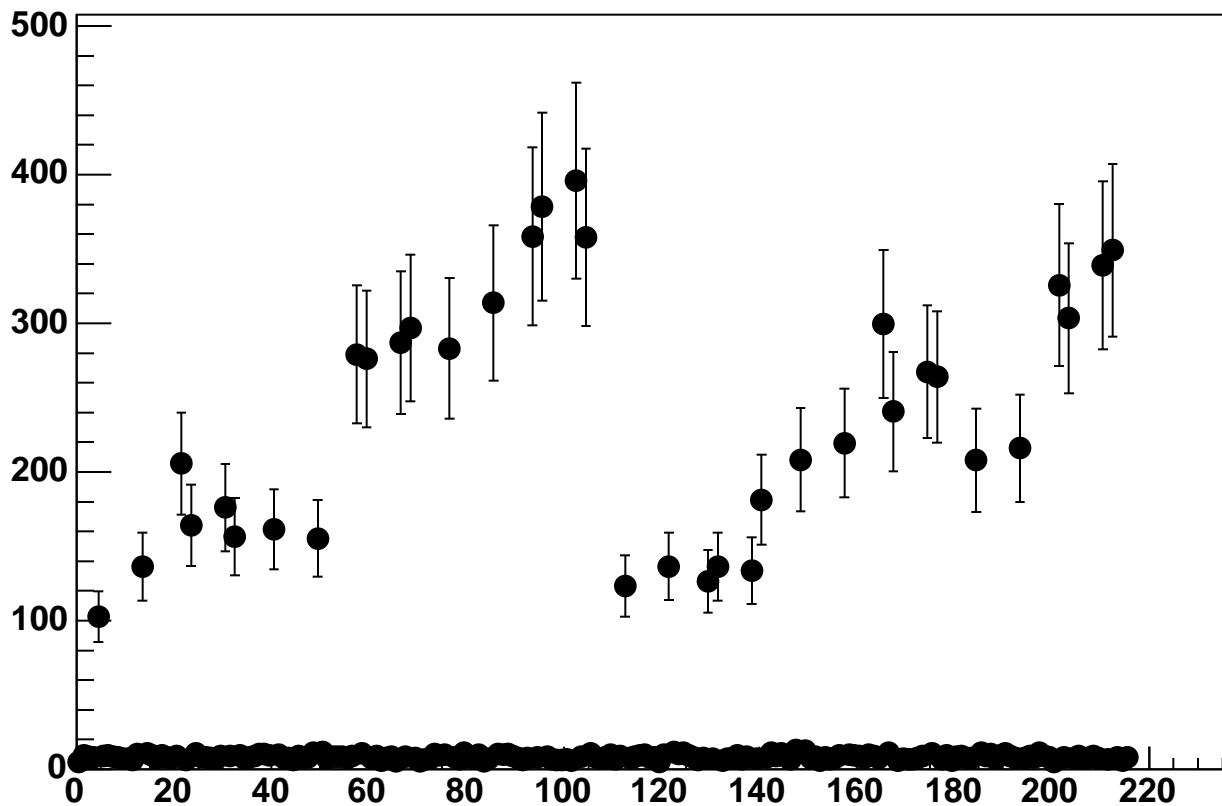
Enable 4, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



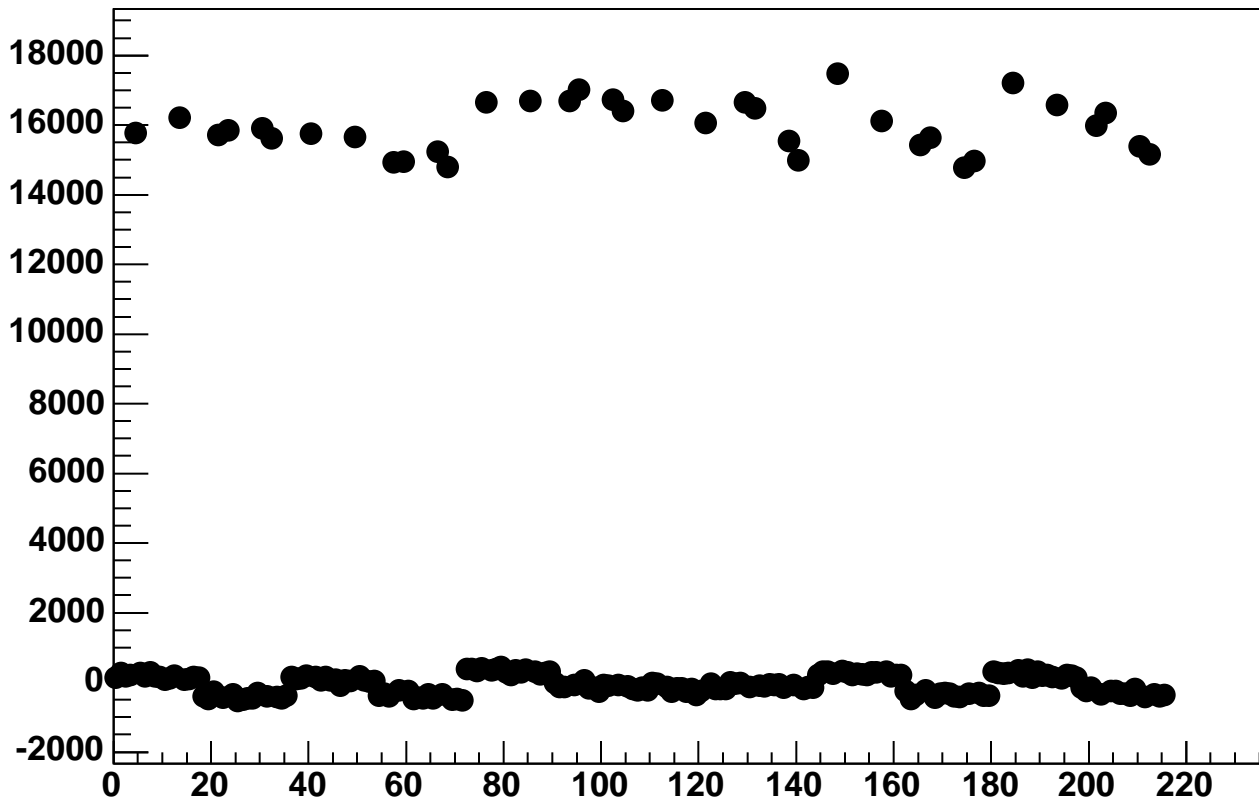
Enable 4, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



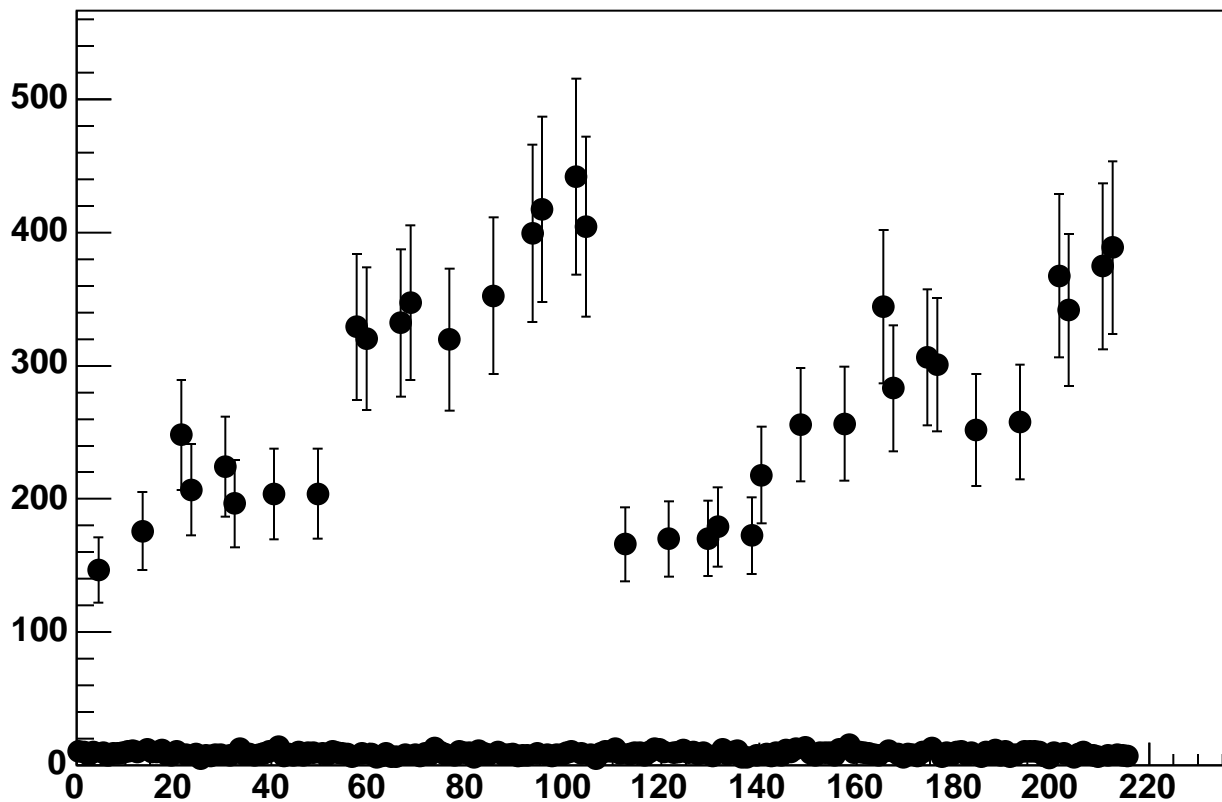
Enable 4, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



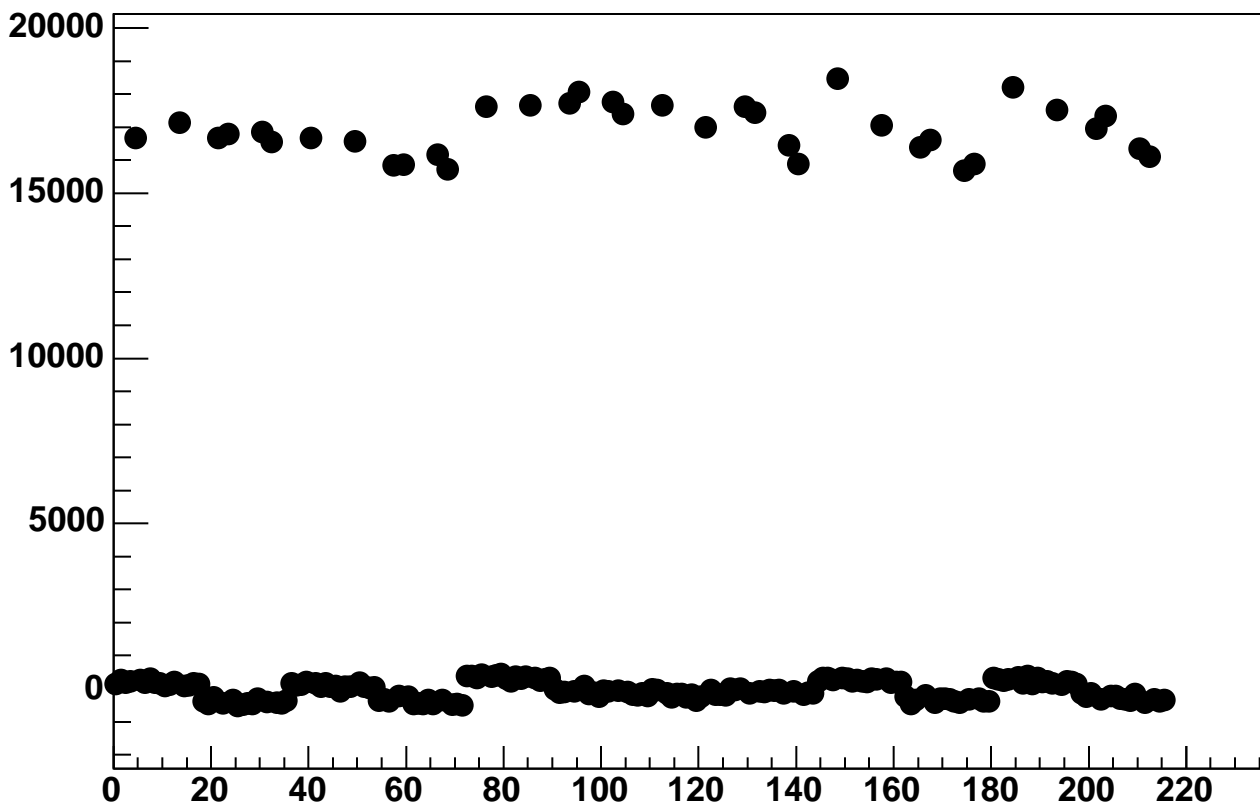
Enable 4, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



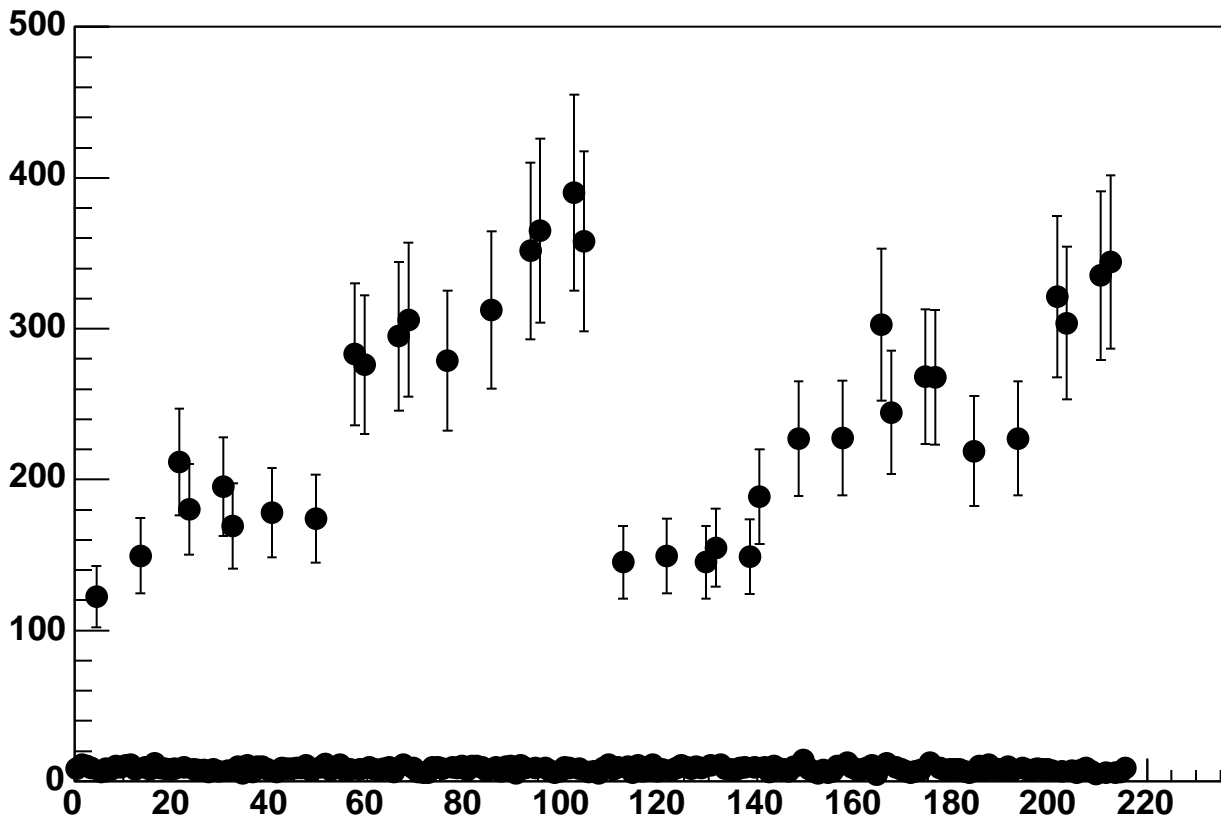
Enable 4, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



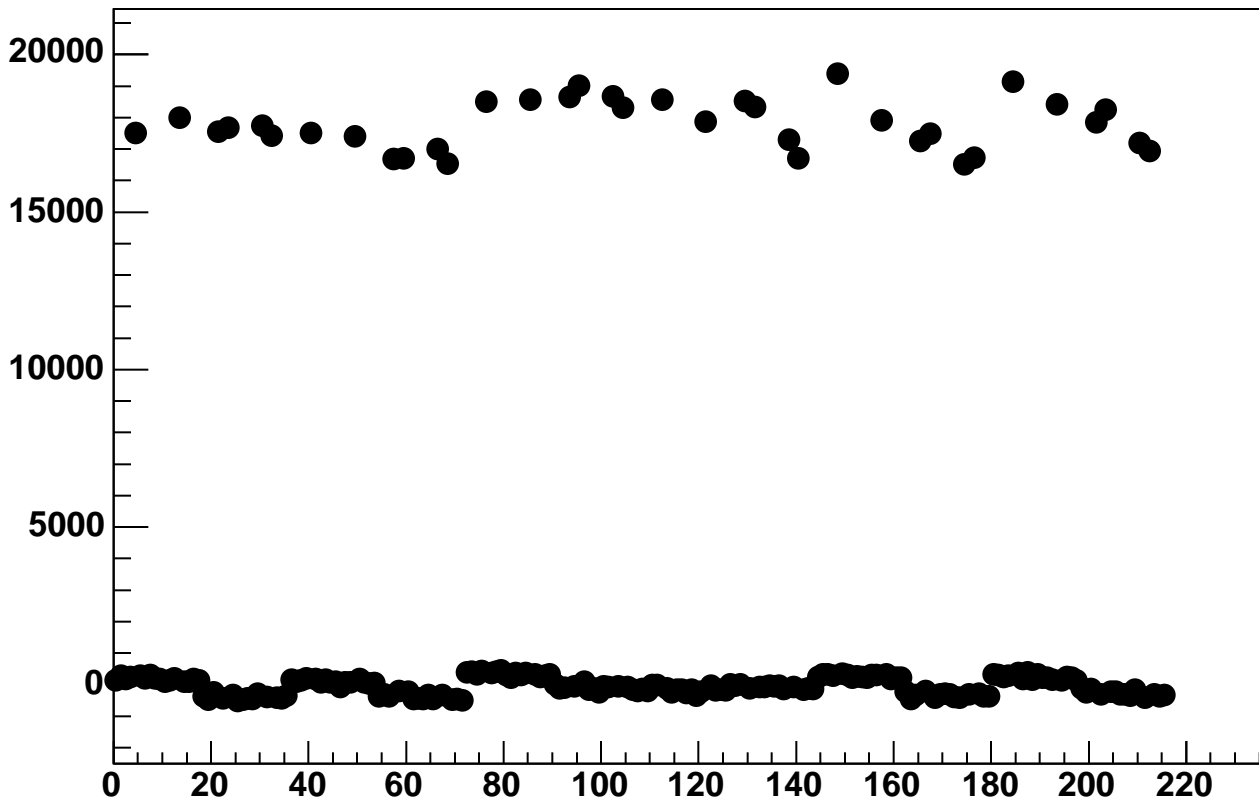
Enable 4, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



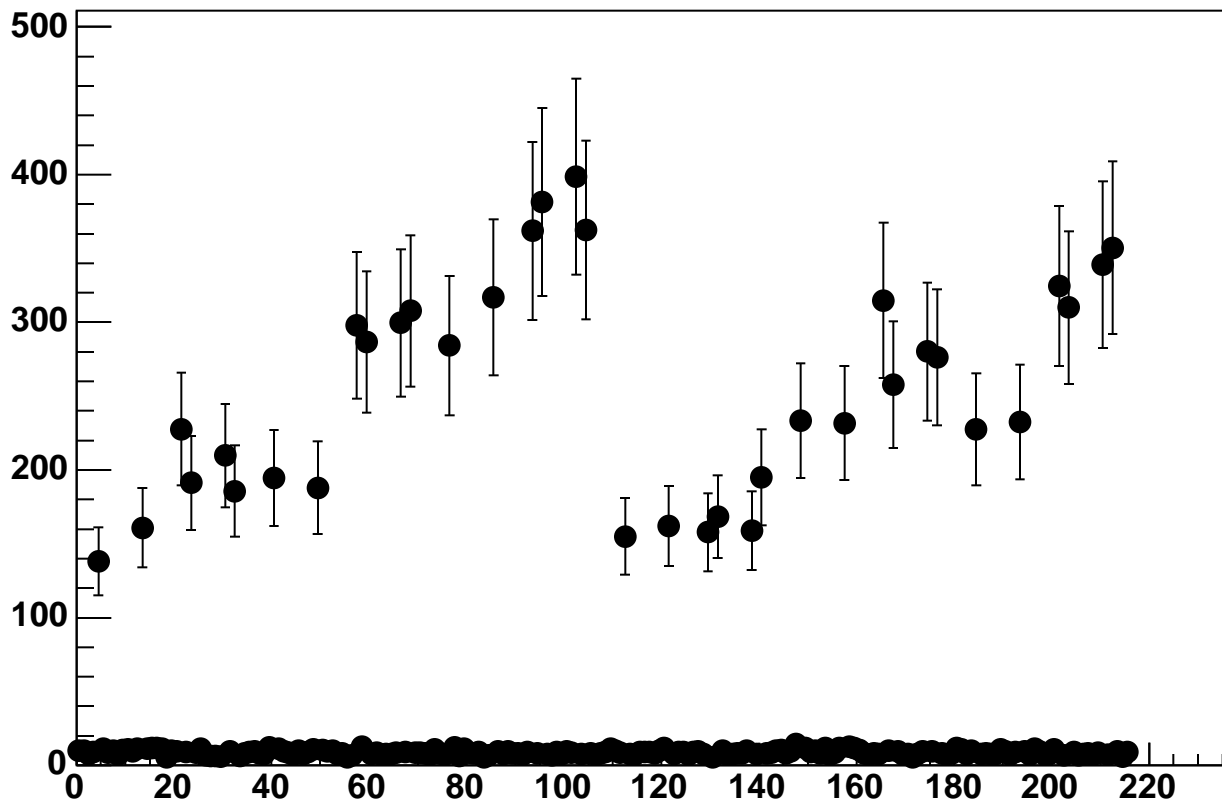
Enable 4, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



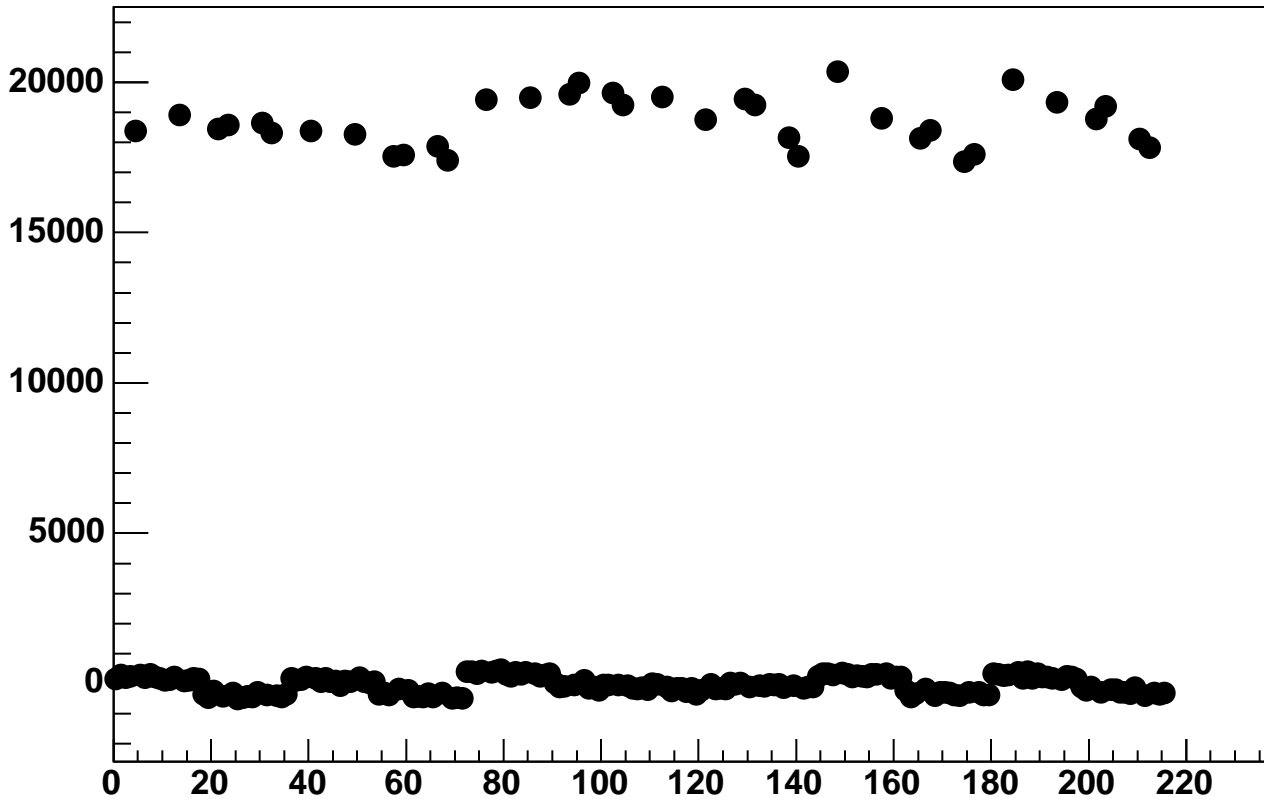
Enable 4, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



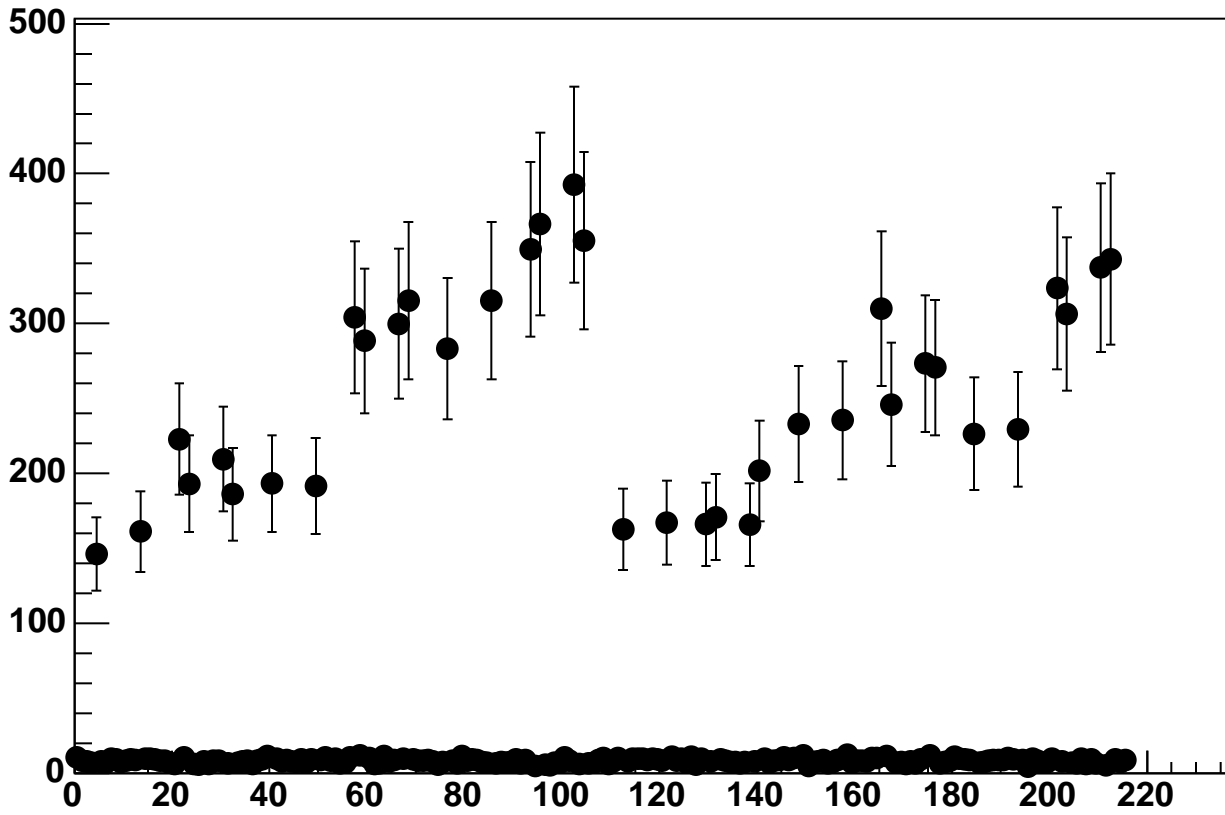
Enable 4, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



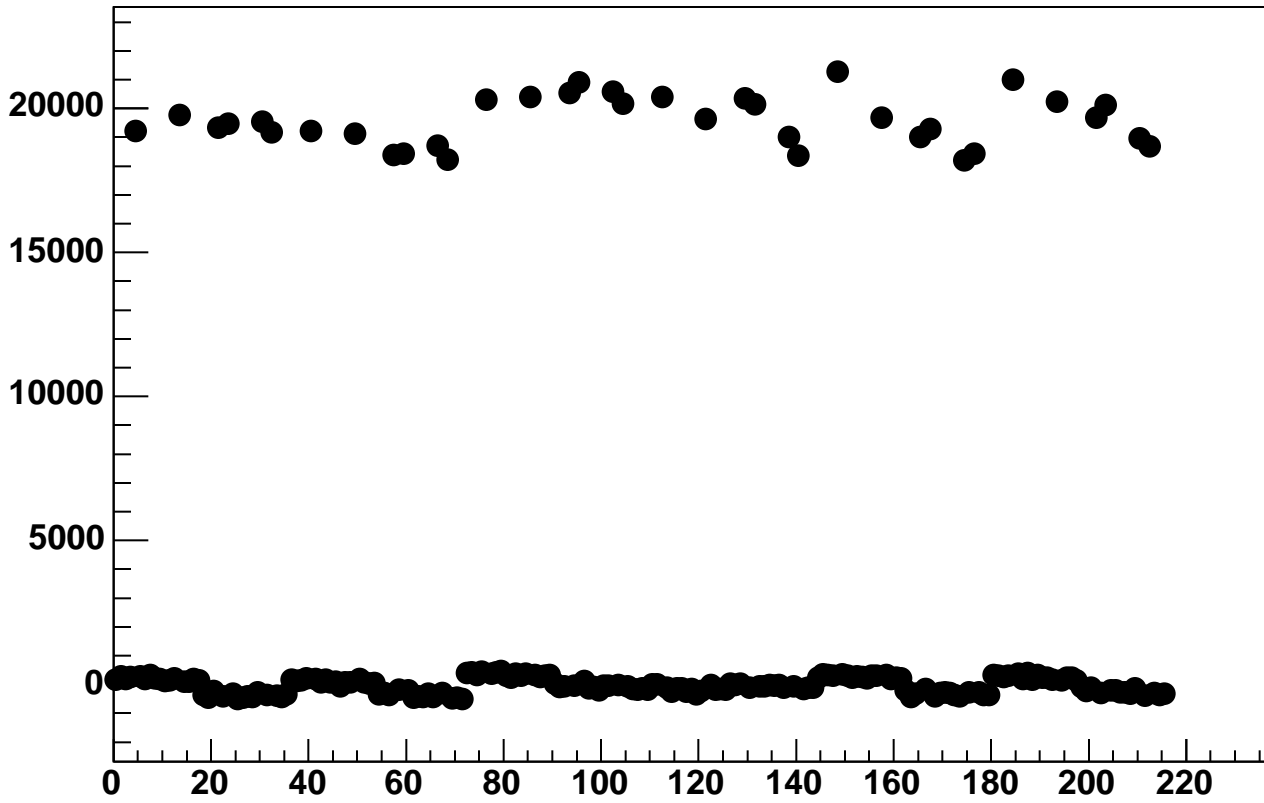
Enable 4, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



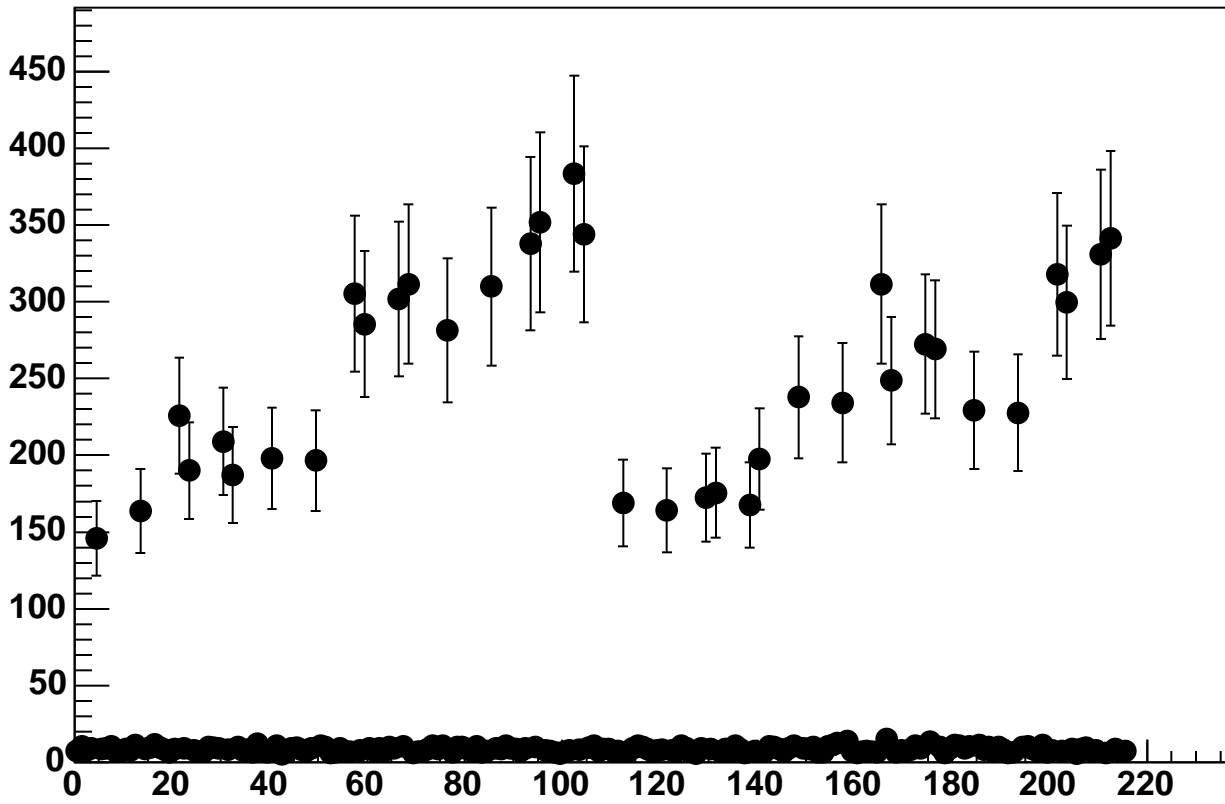
Enable 4, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



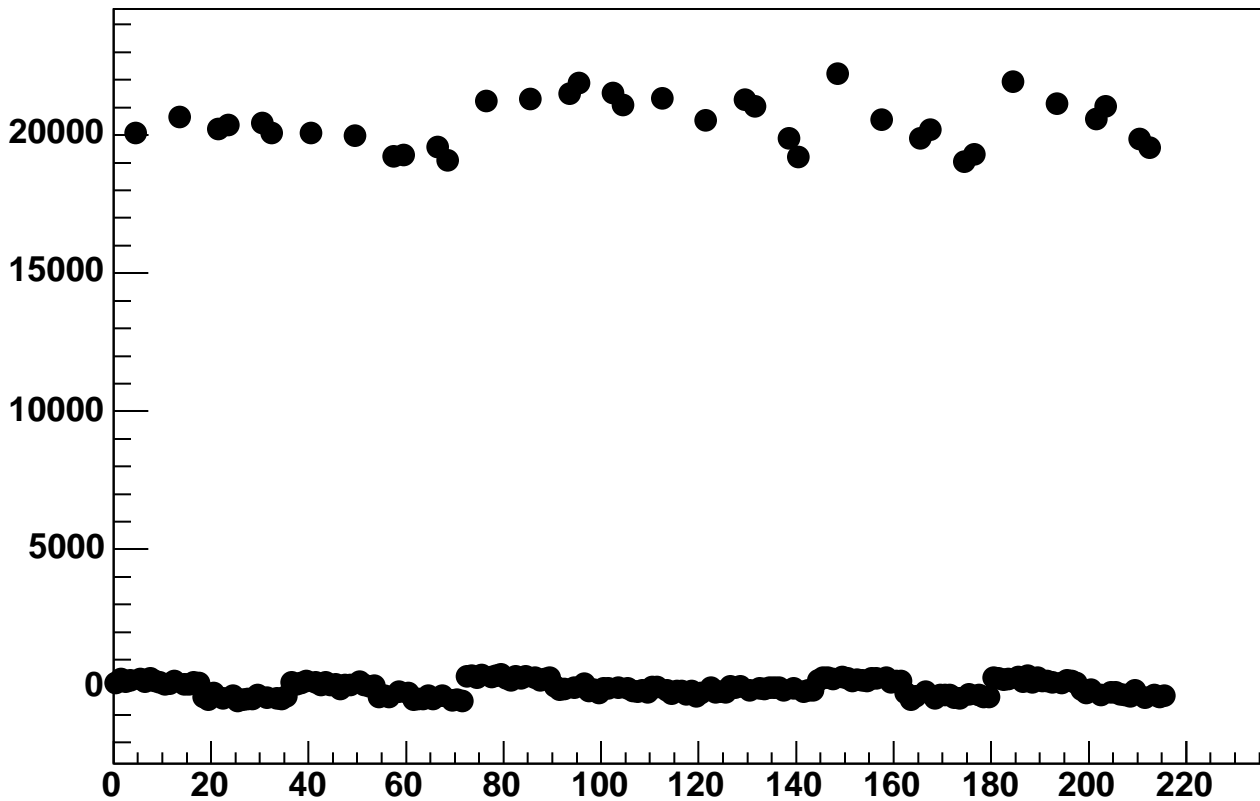
Enable 4, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



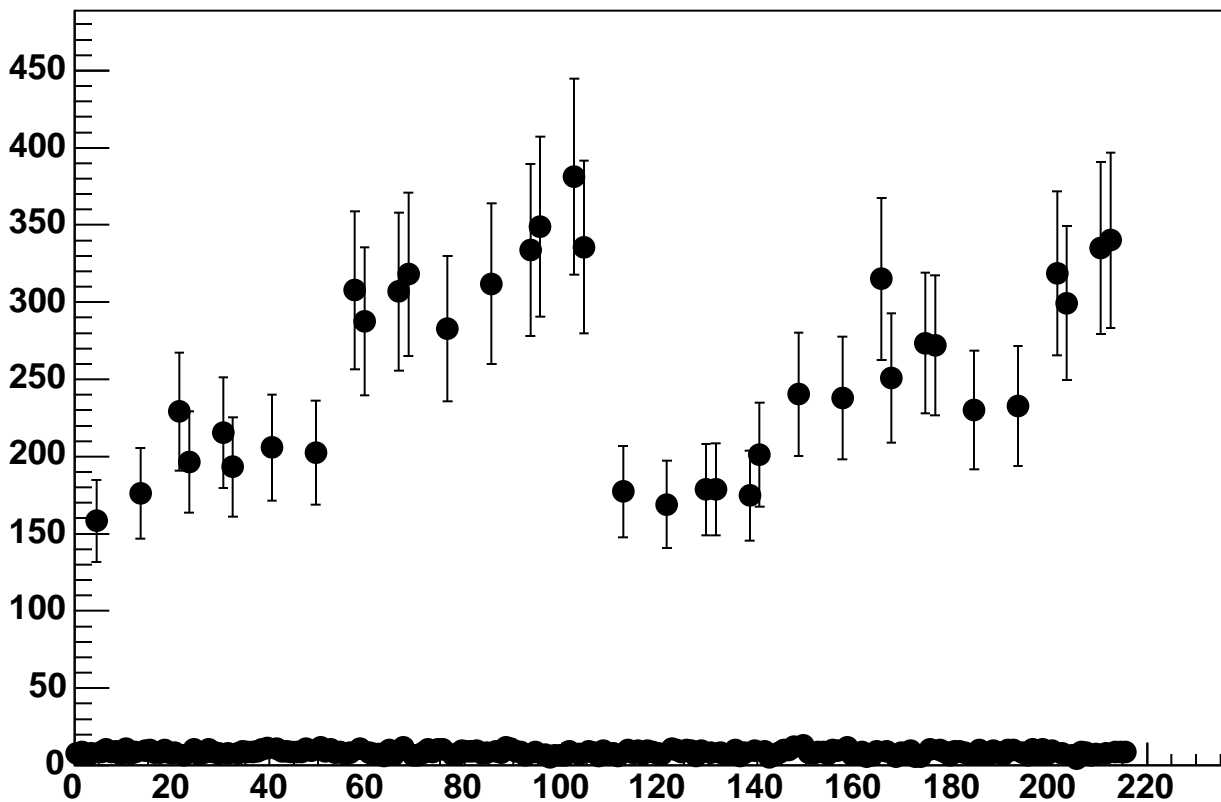
Enable 4, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



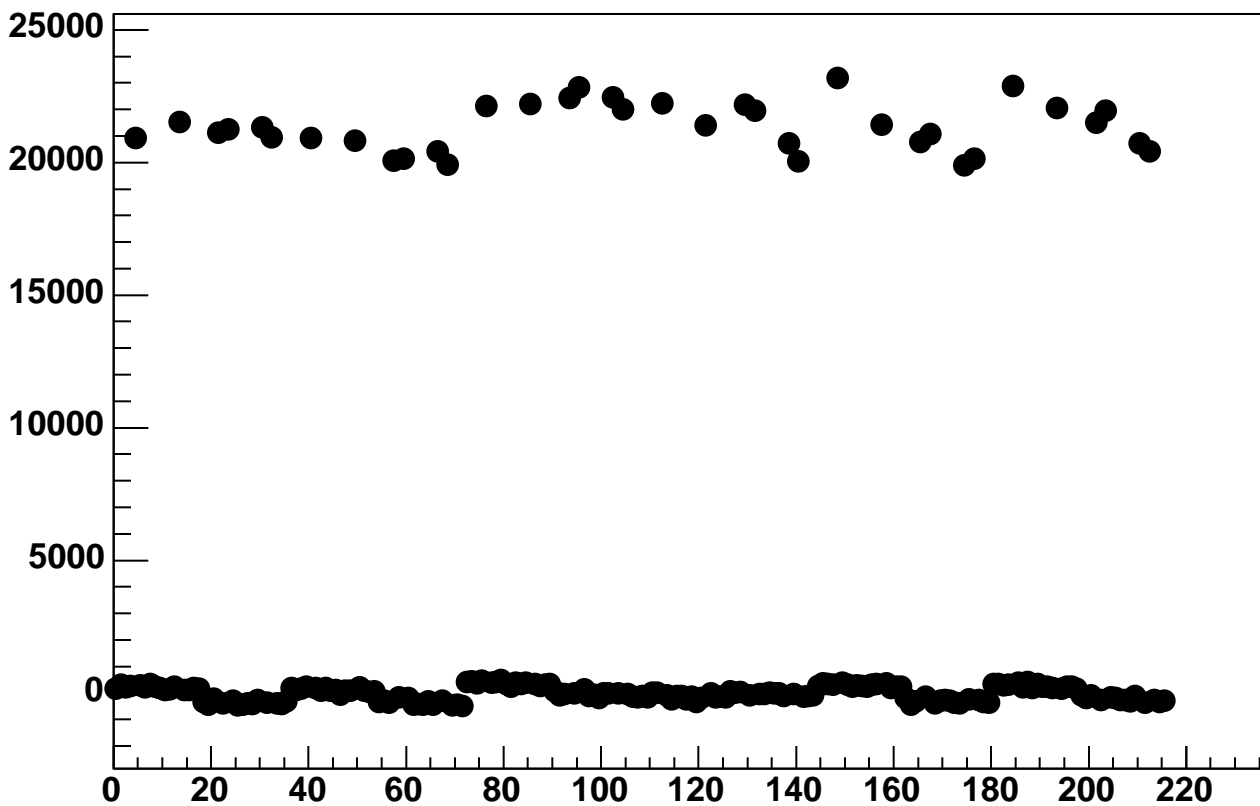
Enable 4, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



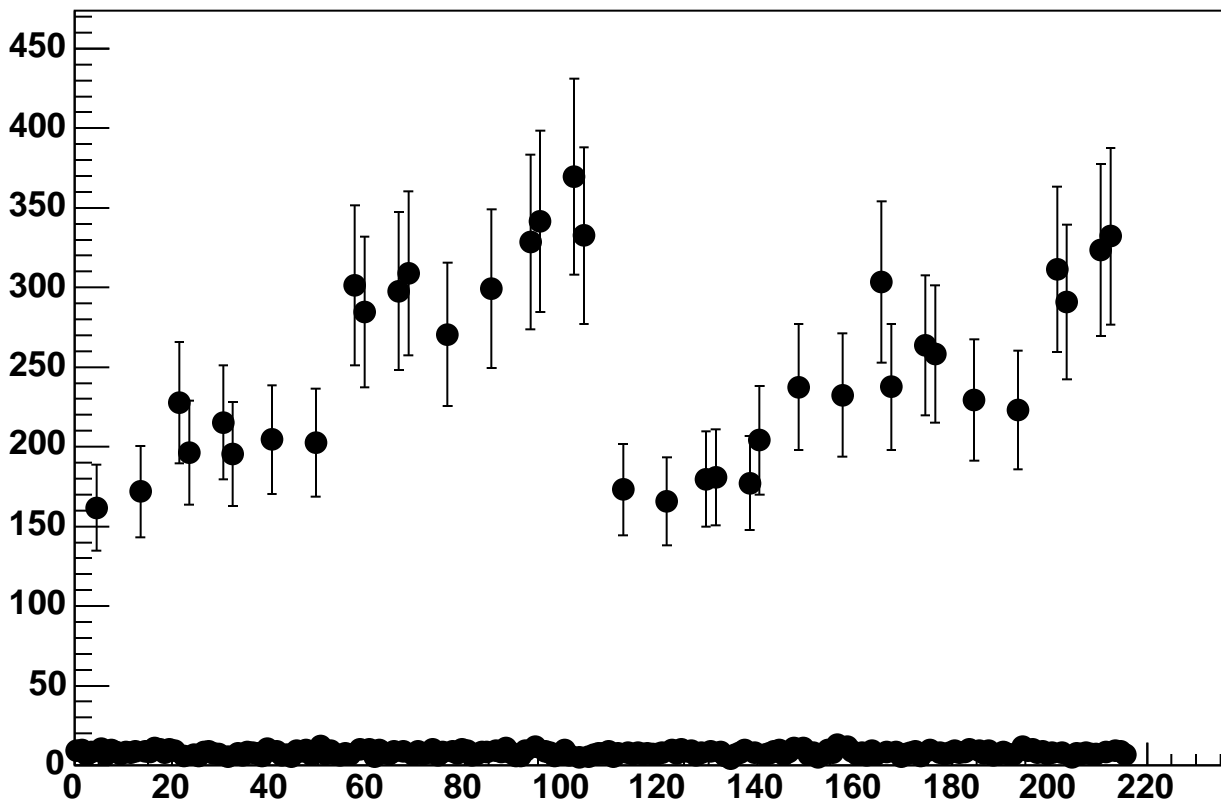
Enable 4, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



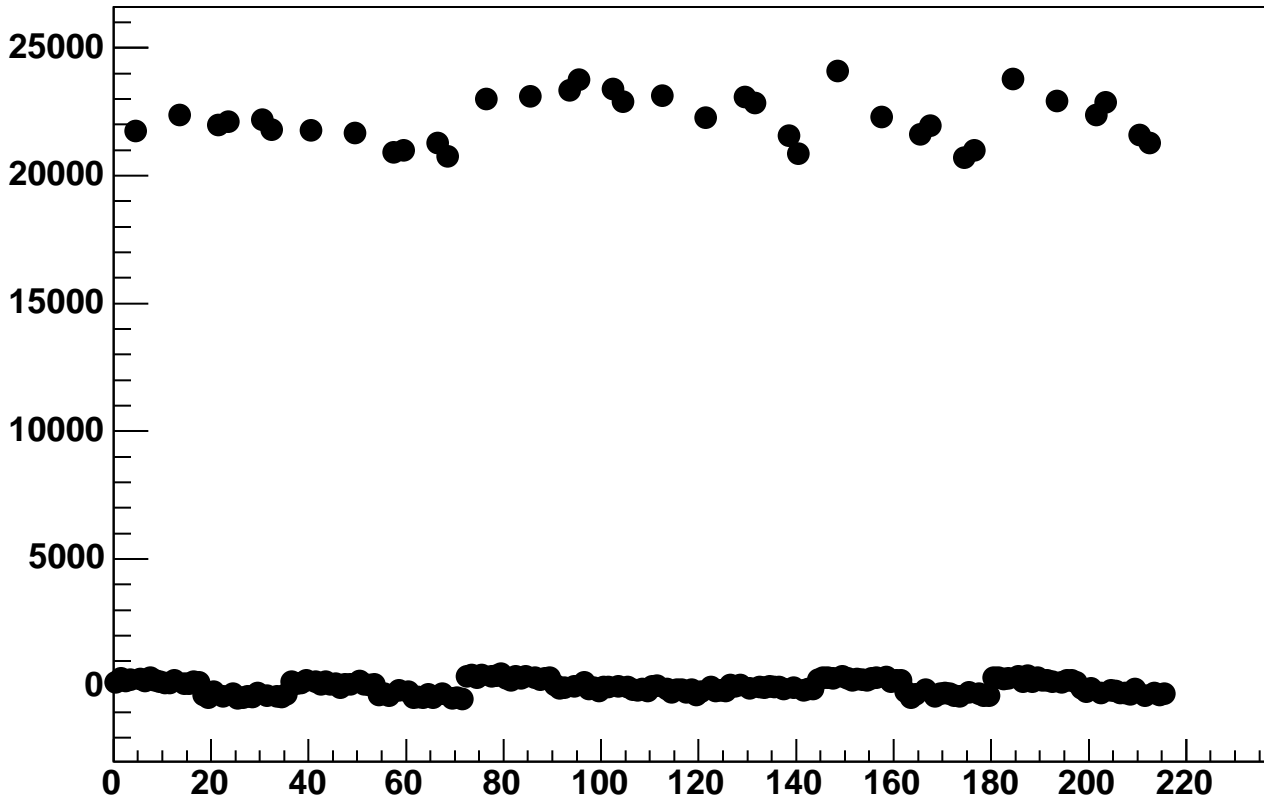
Enable 4, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



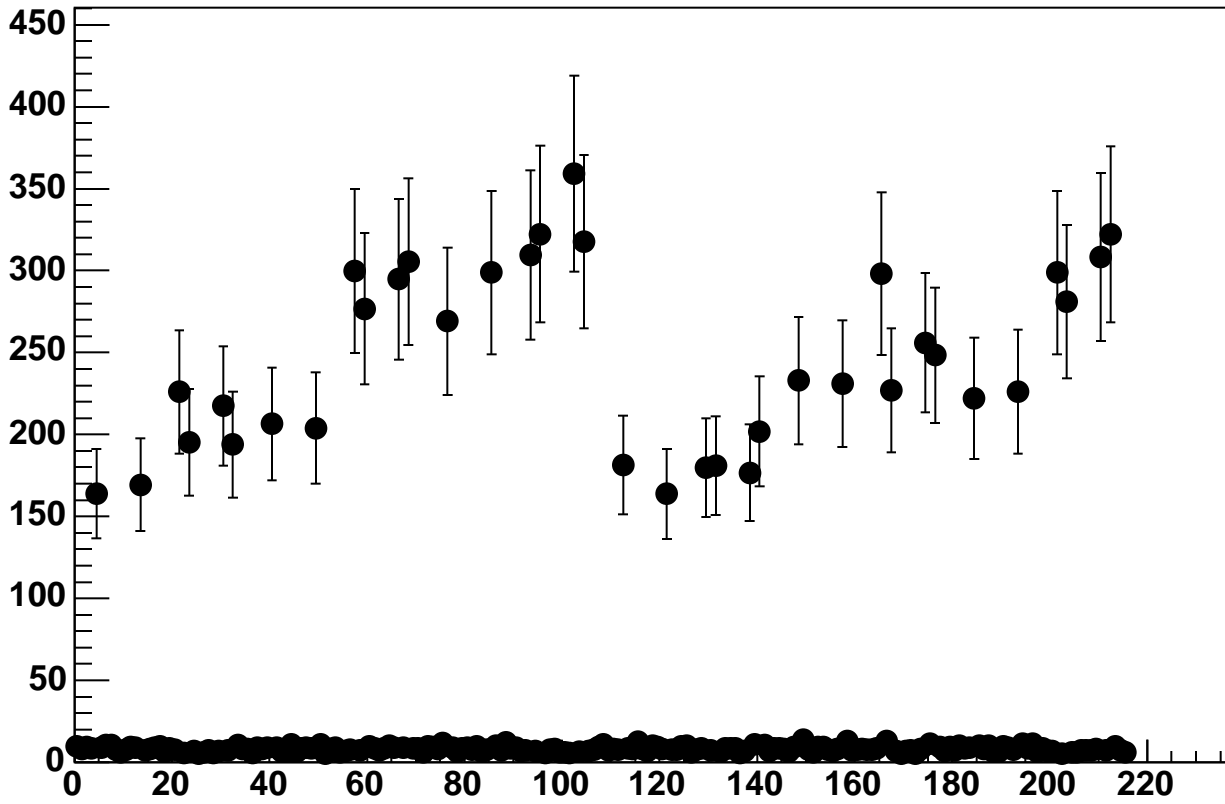
Enable 4, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



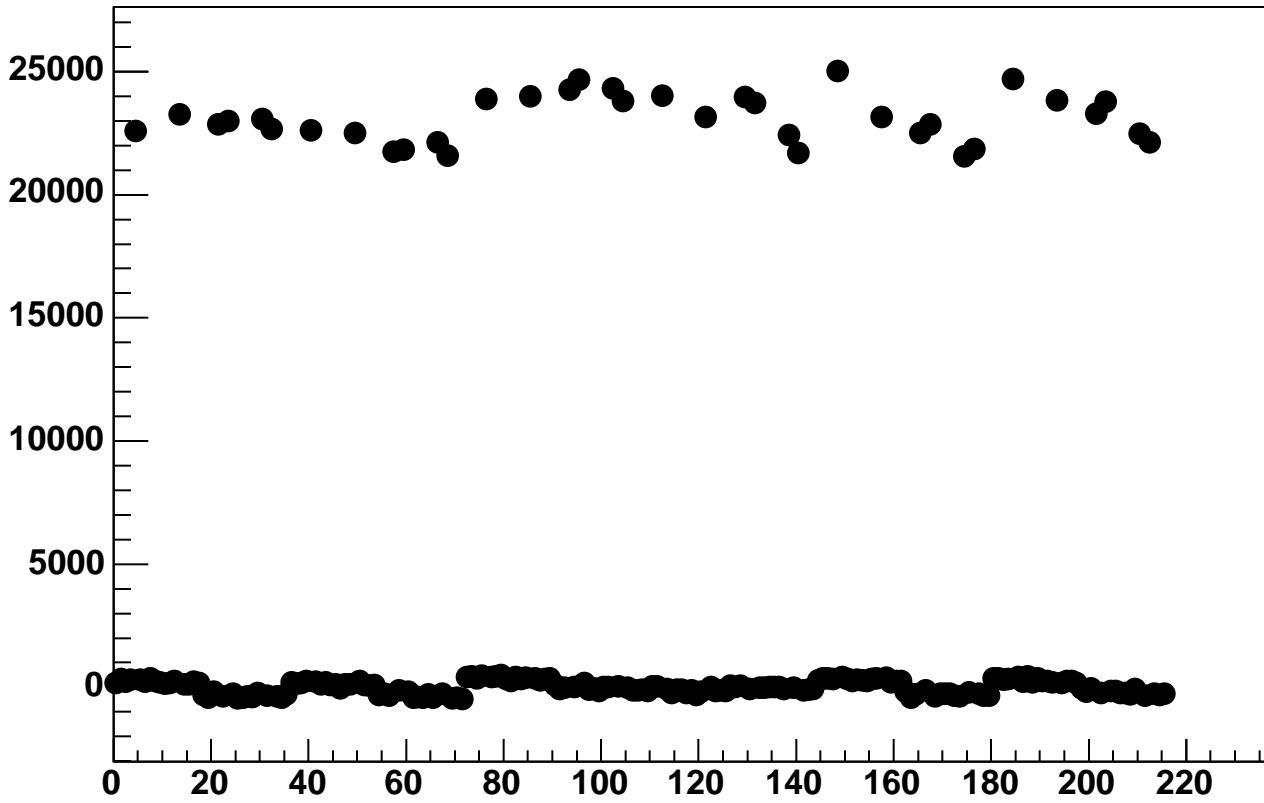
Enable 4, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



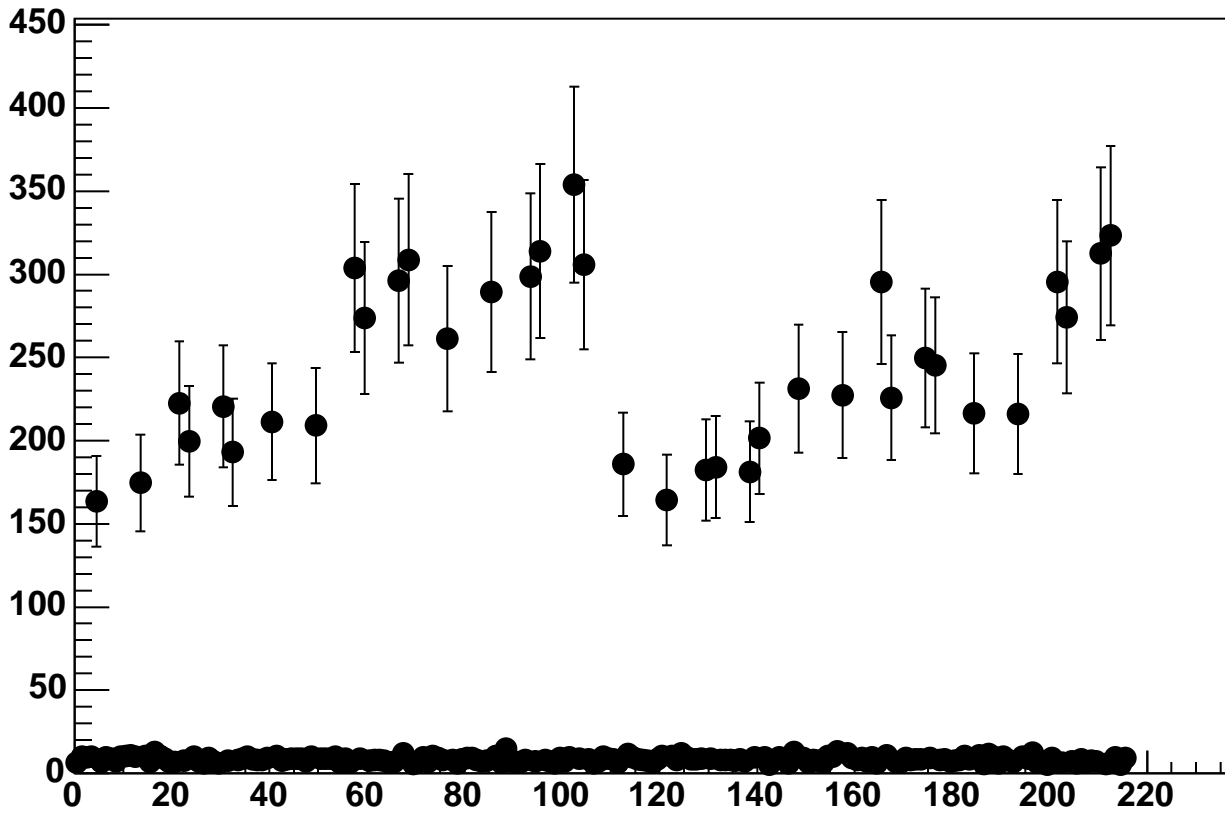
Enable 4, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



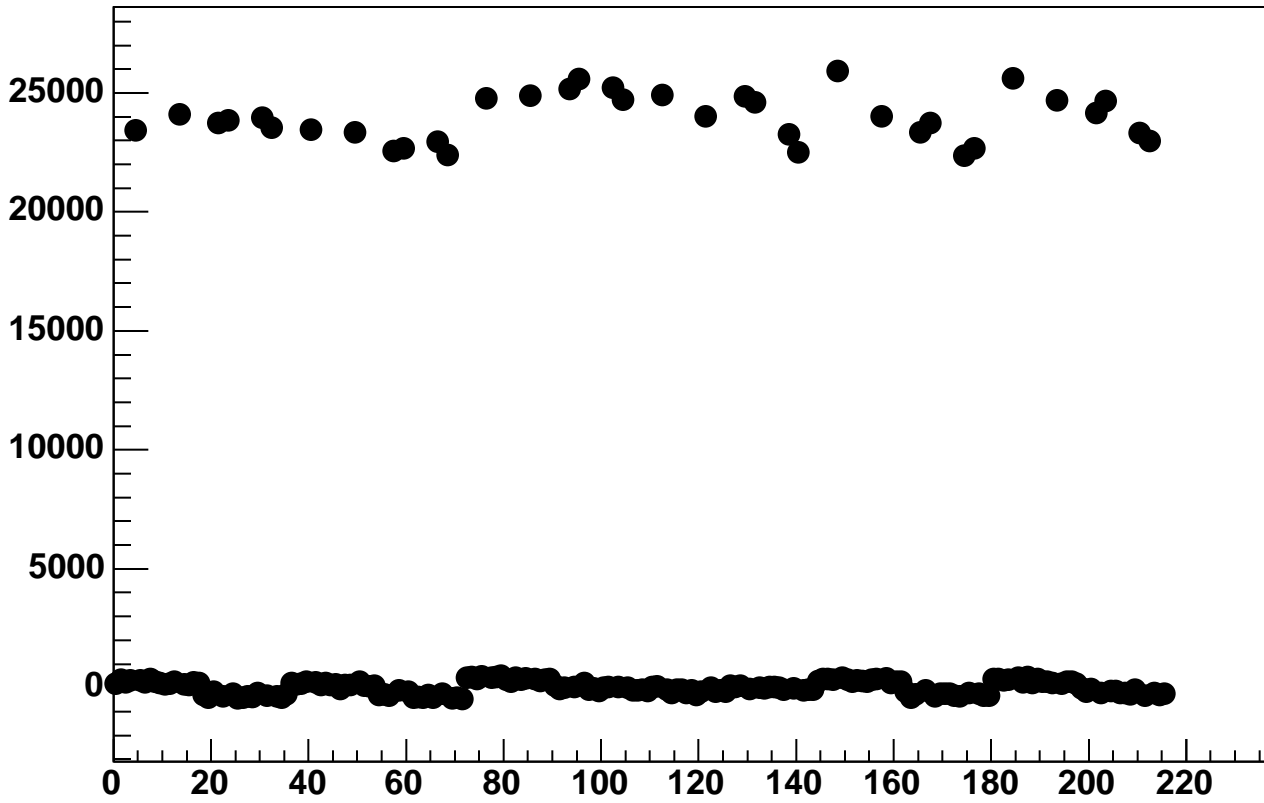
Enable 4, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



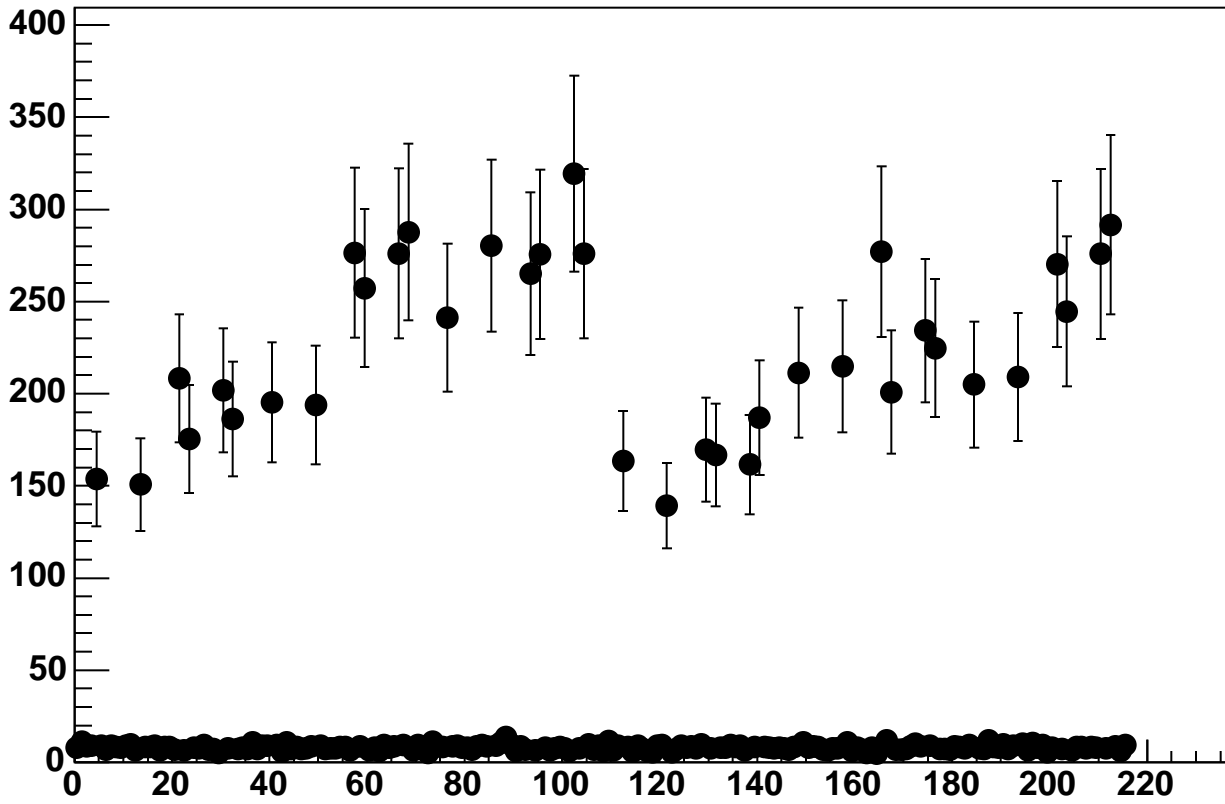
Enable 4, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



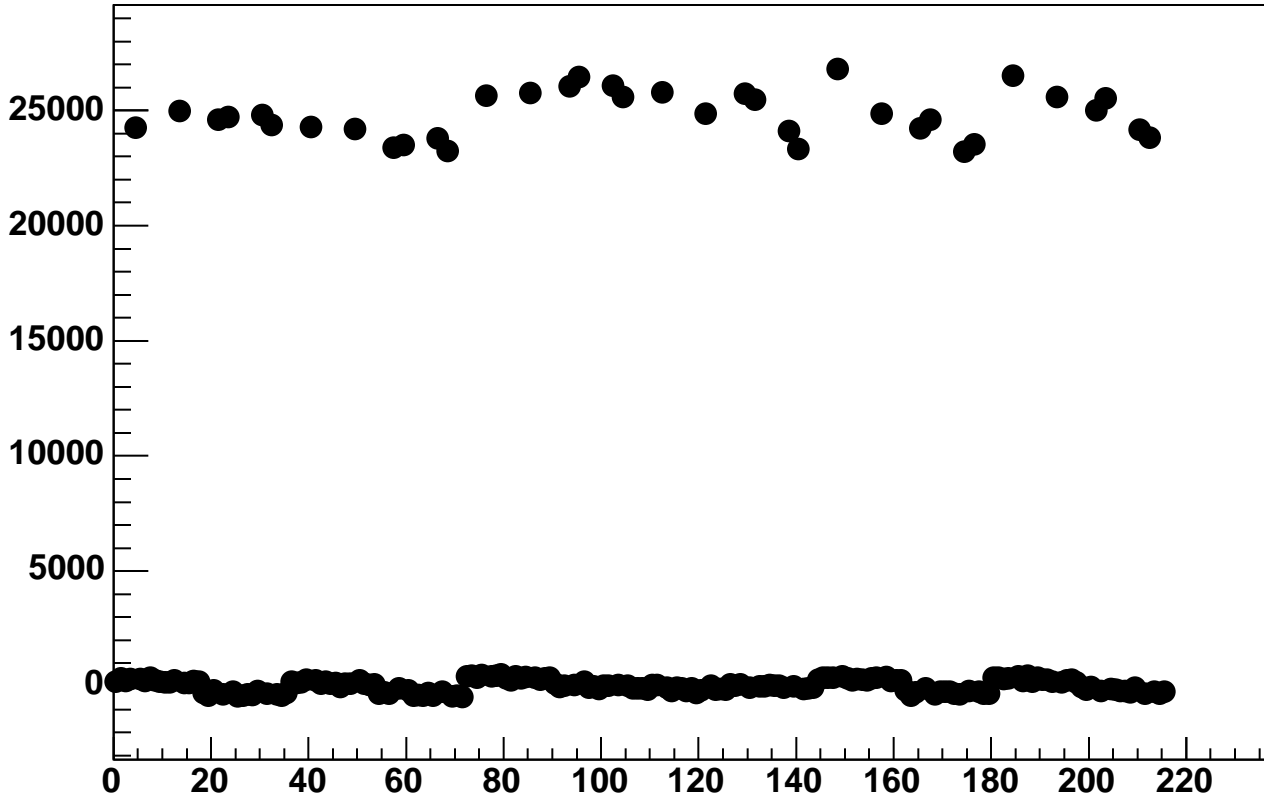
Enable 4, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



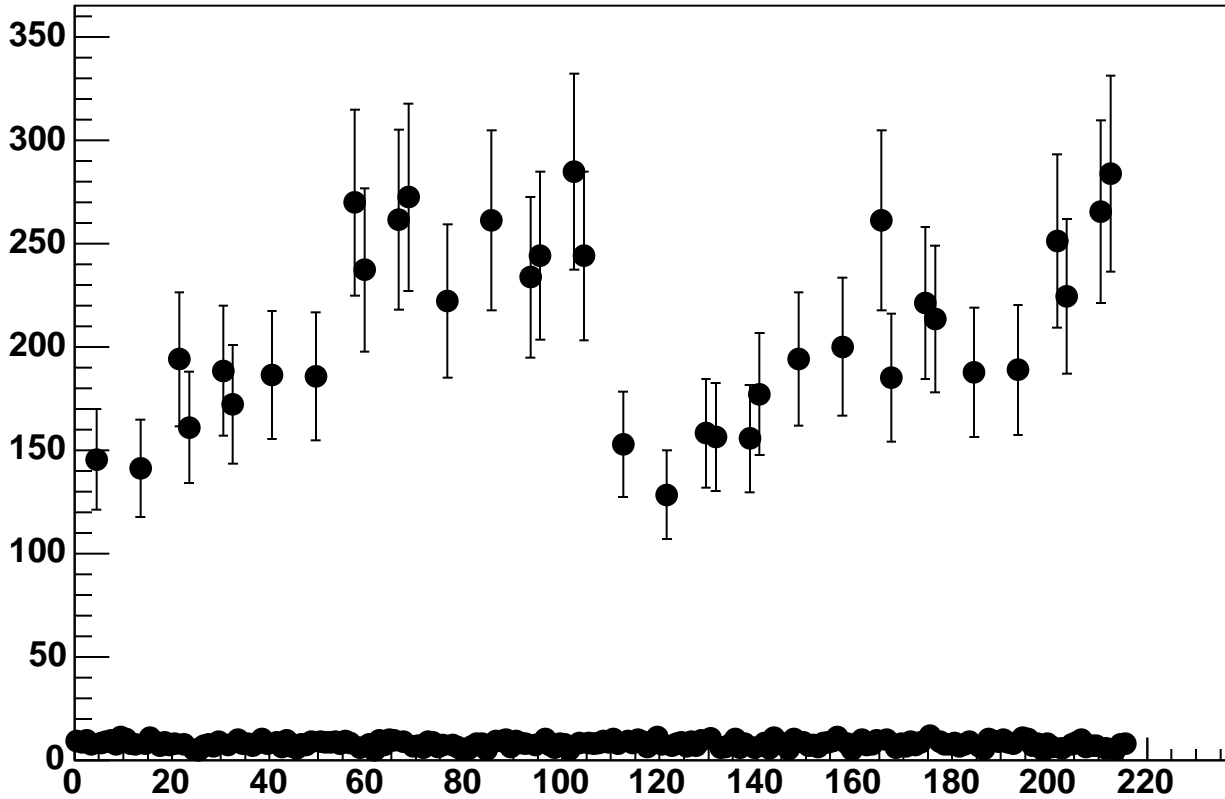
Enable 4, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



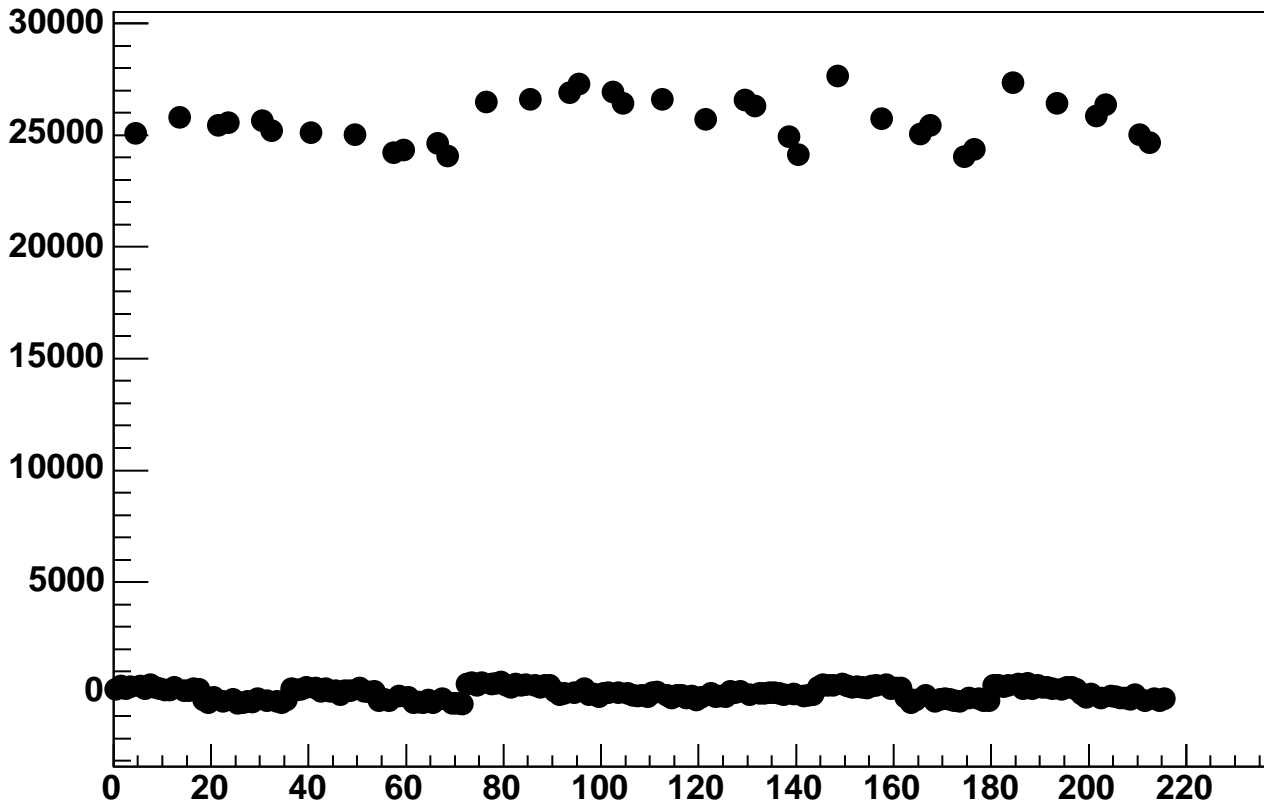
Enable 4, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



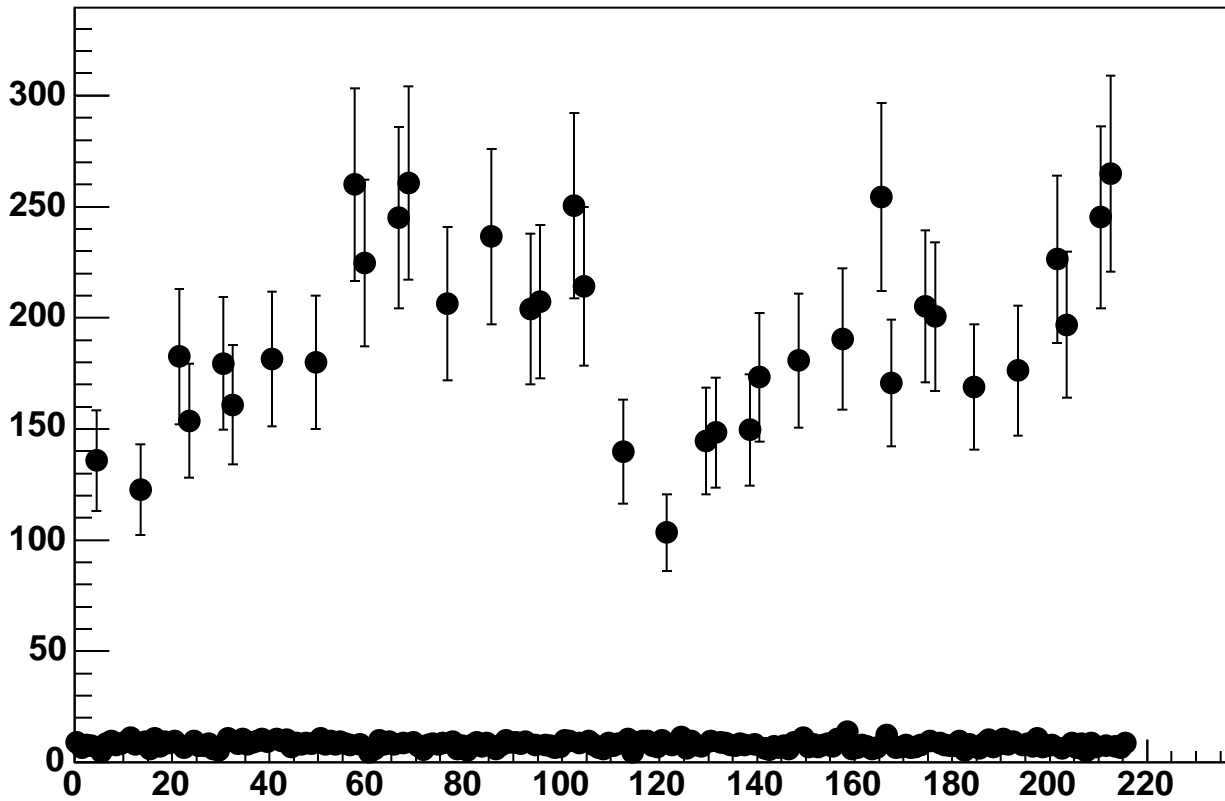
Enable 4, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



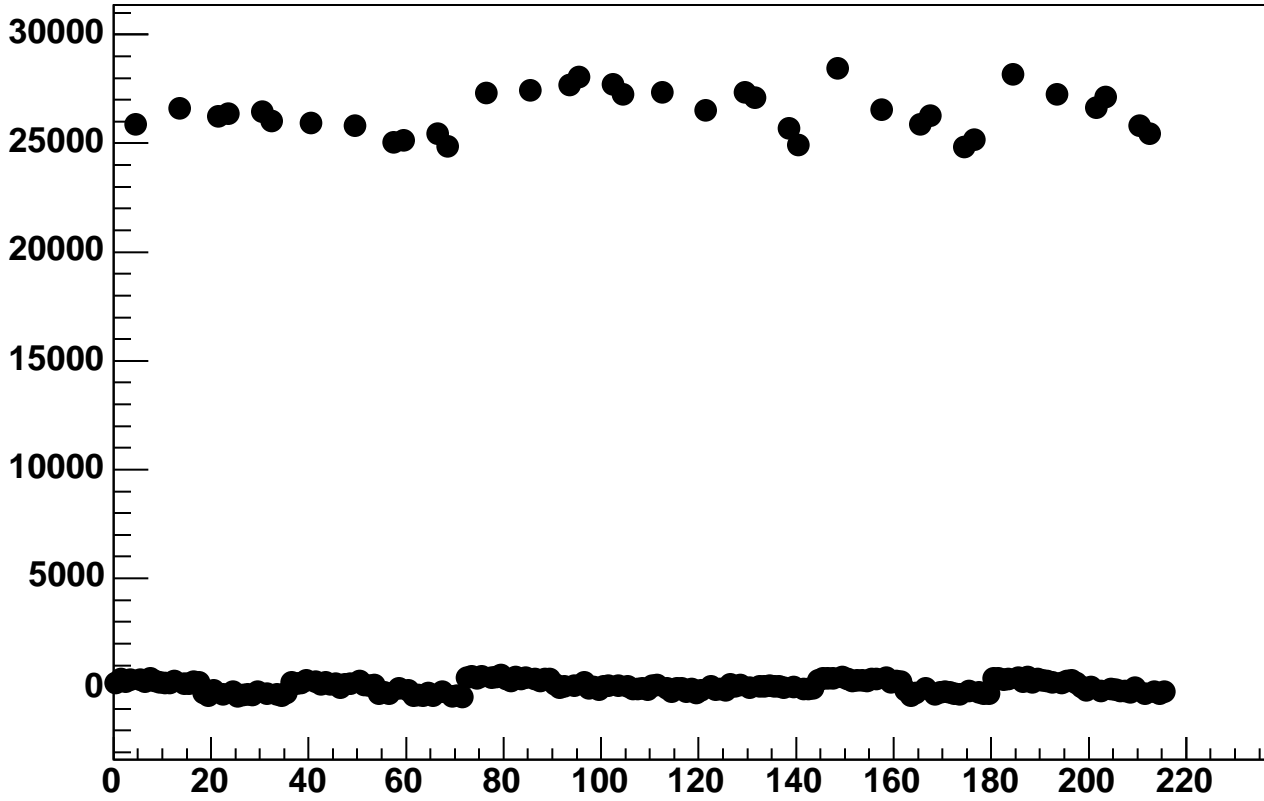
Enable 4, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



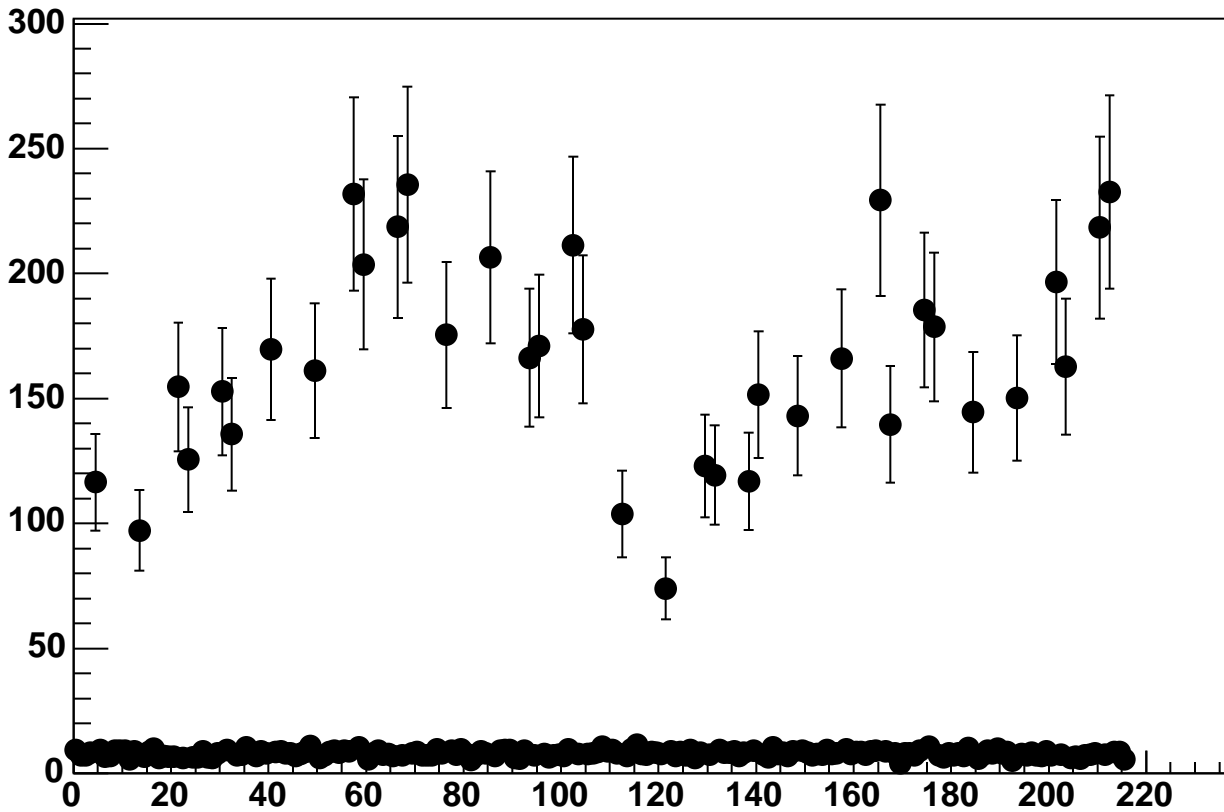
Enable 4, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



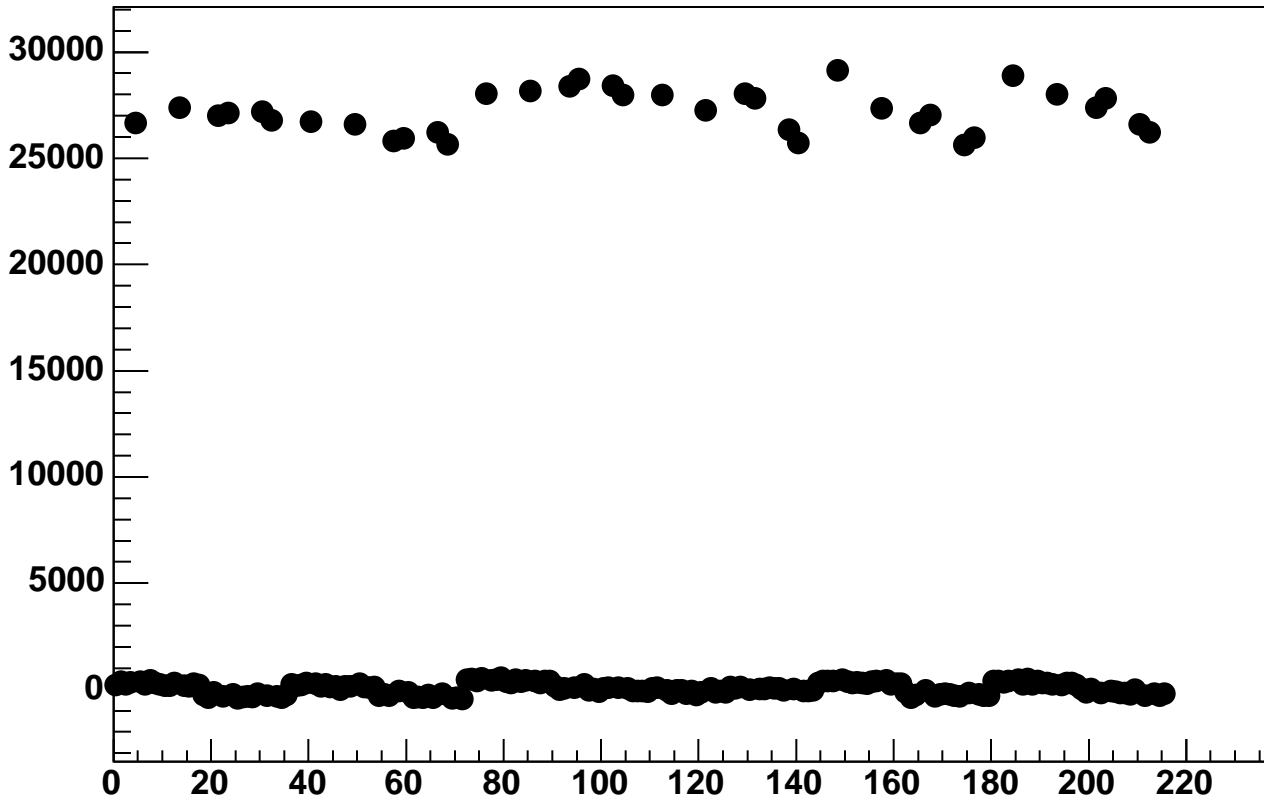
Enable 4, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



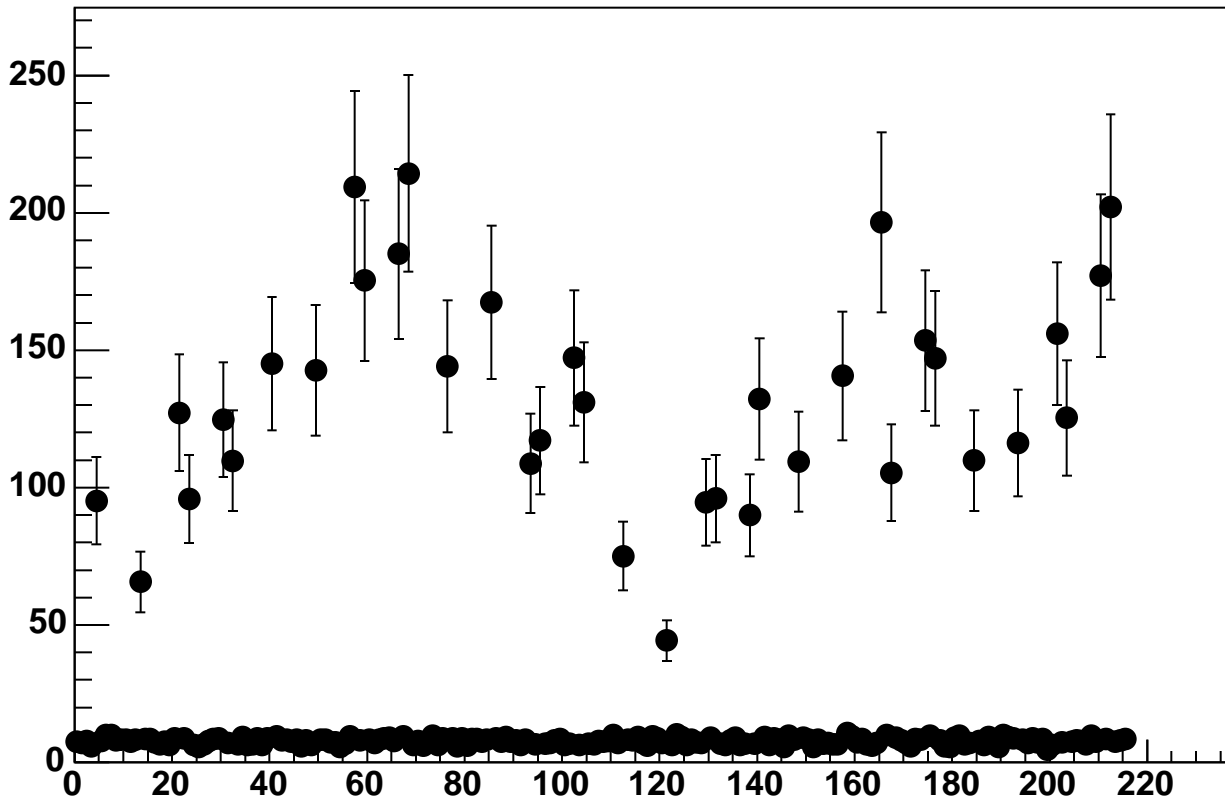
Enable 4, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



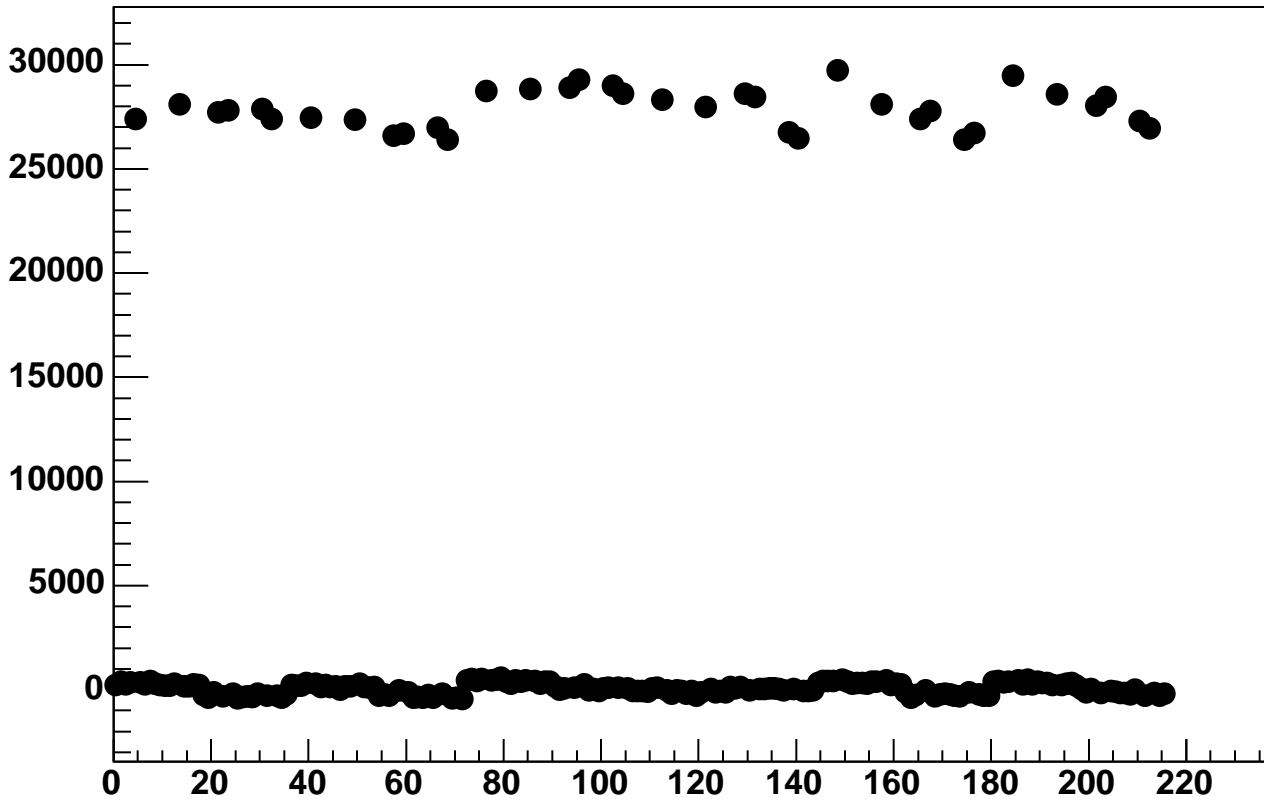
Enable 4, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



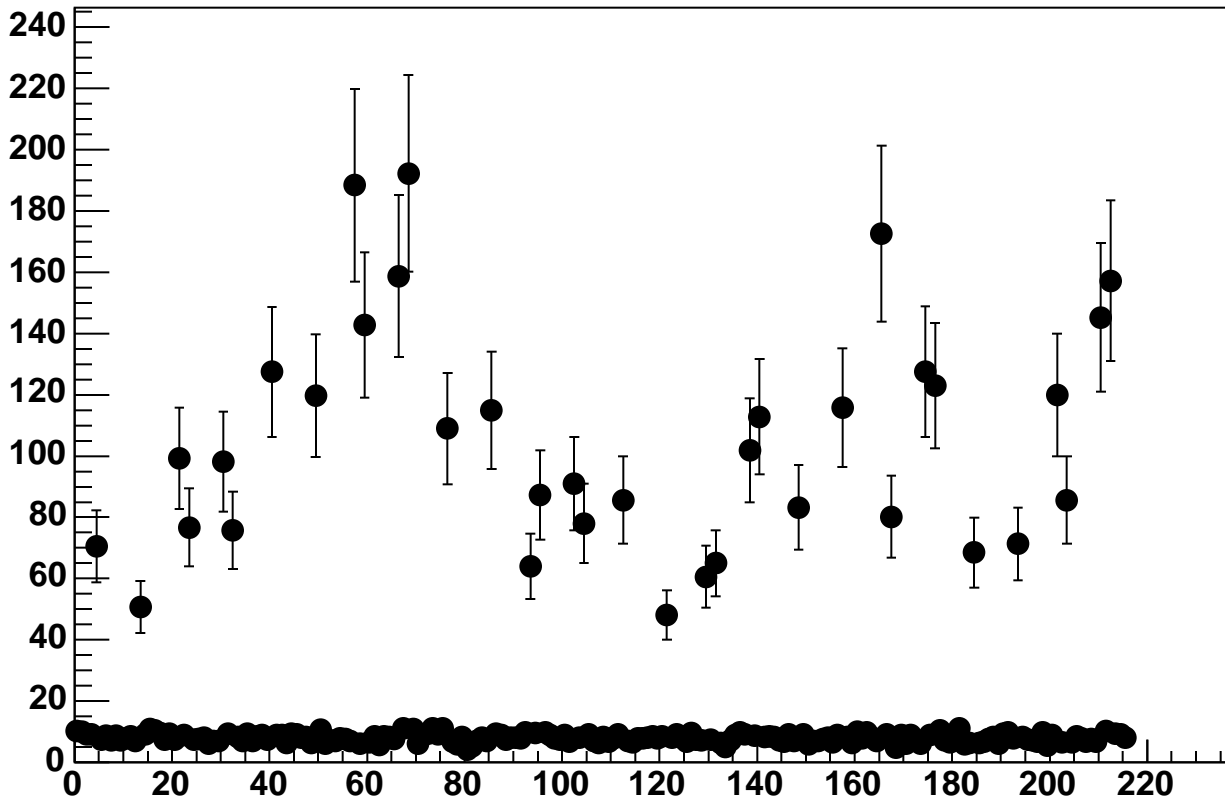
Enable 4, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



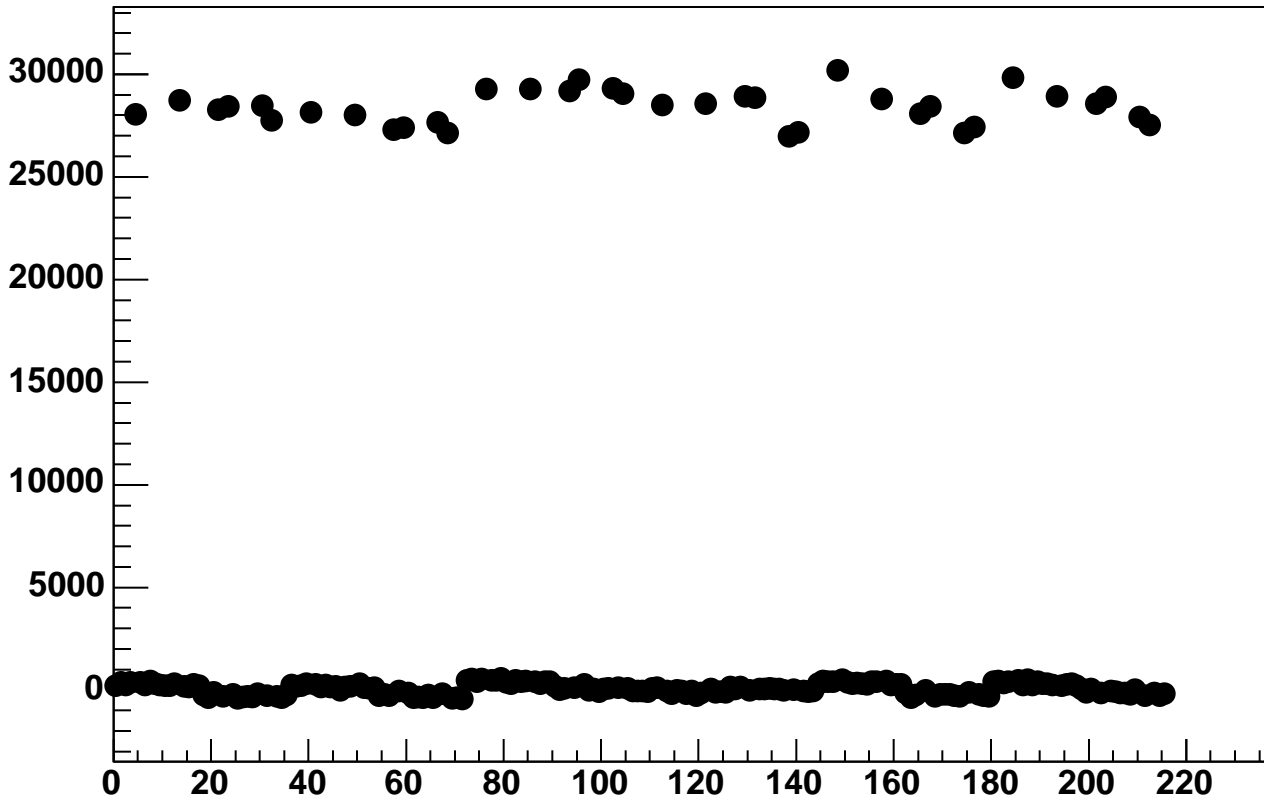
Enable 4, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



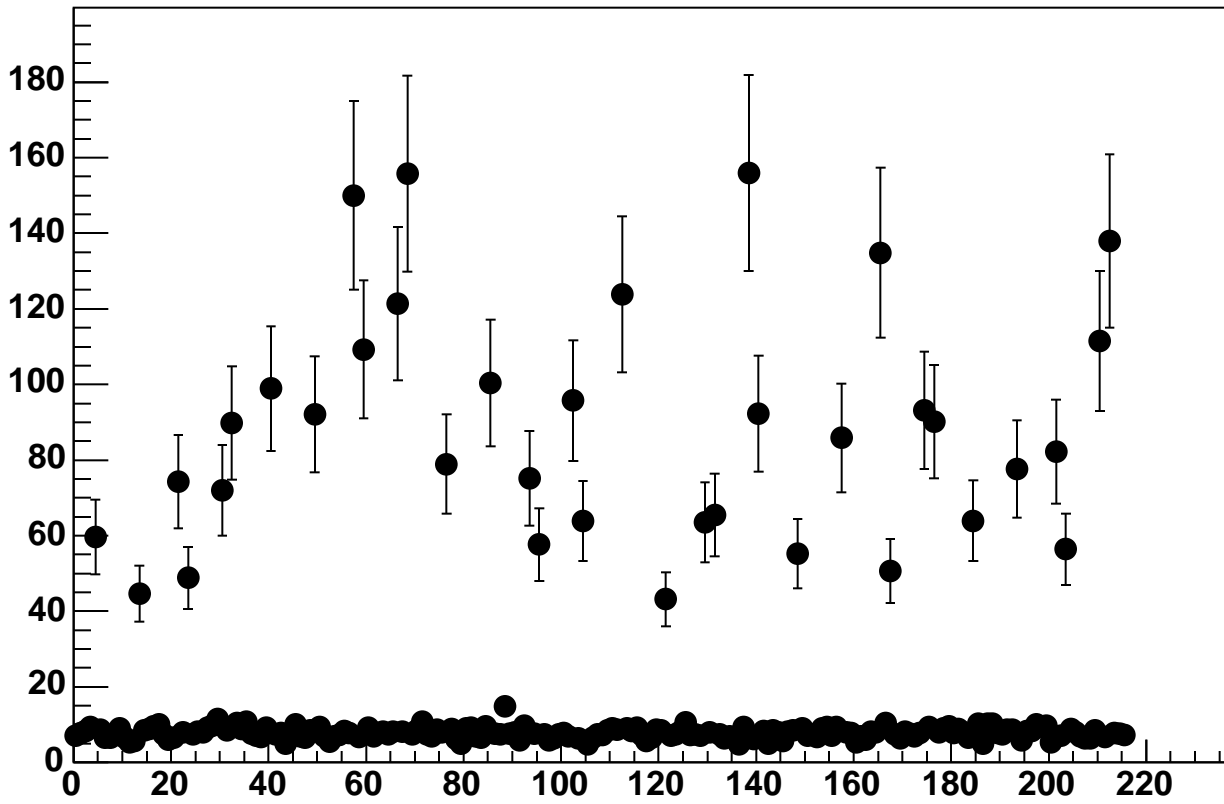
Enable 4, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



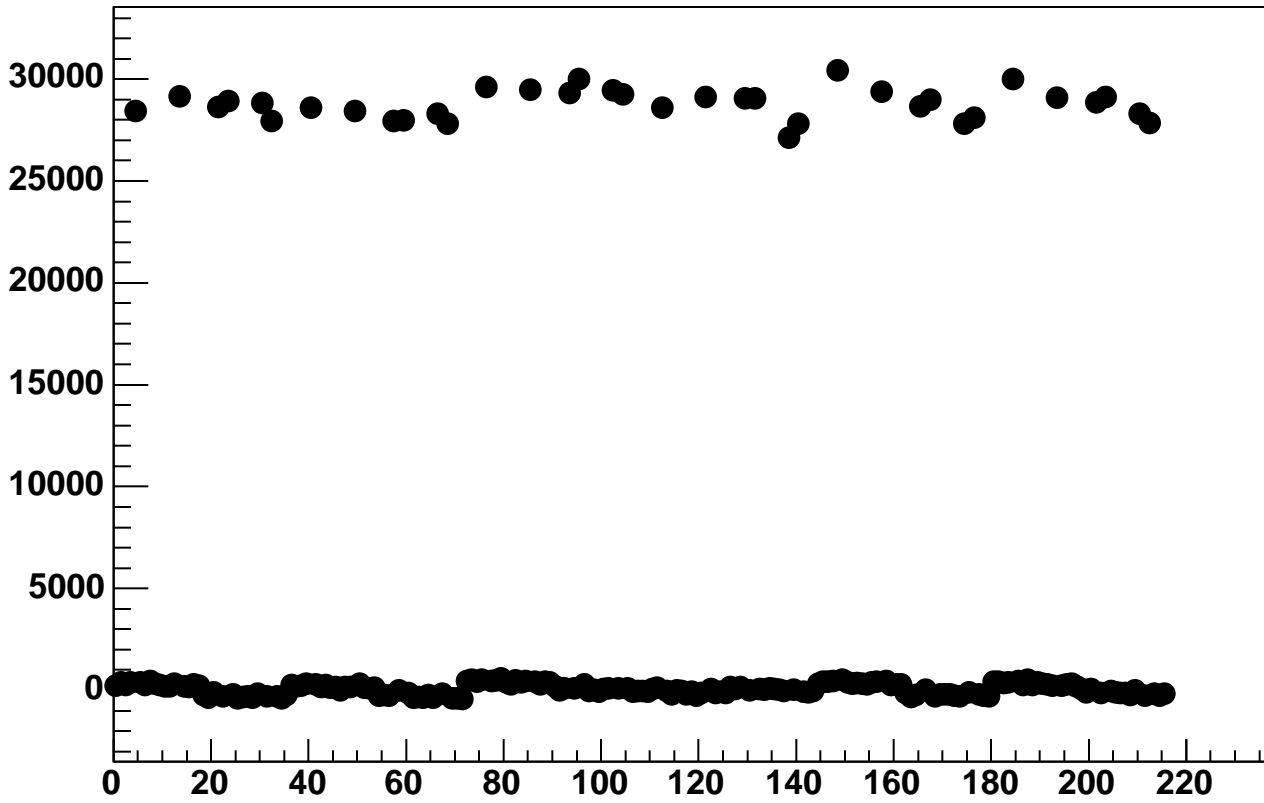
Enable 4, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



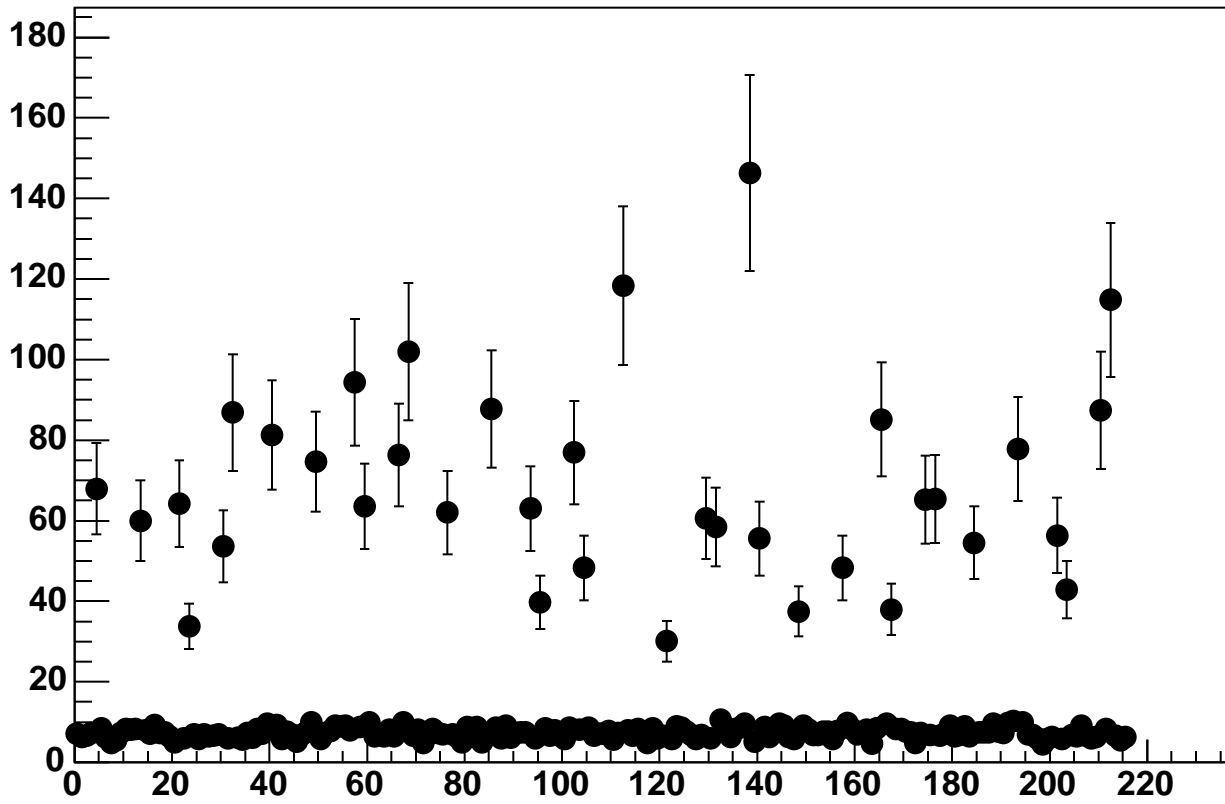
Enable 4, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



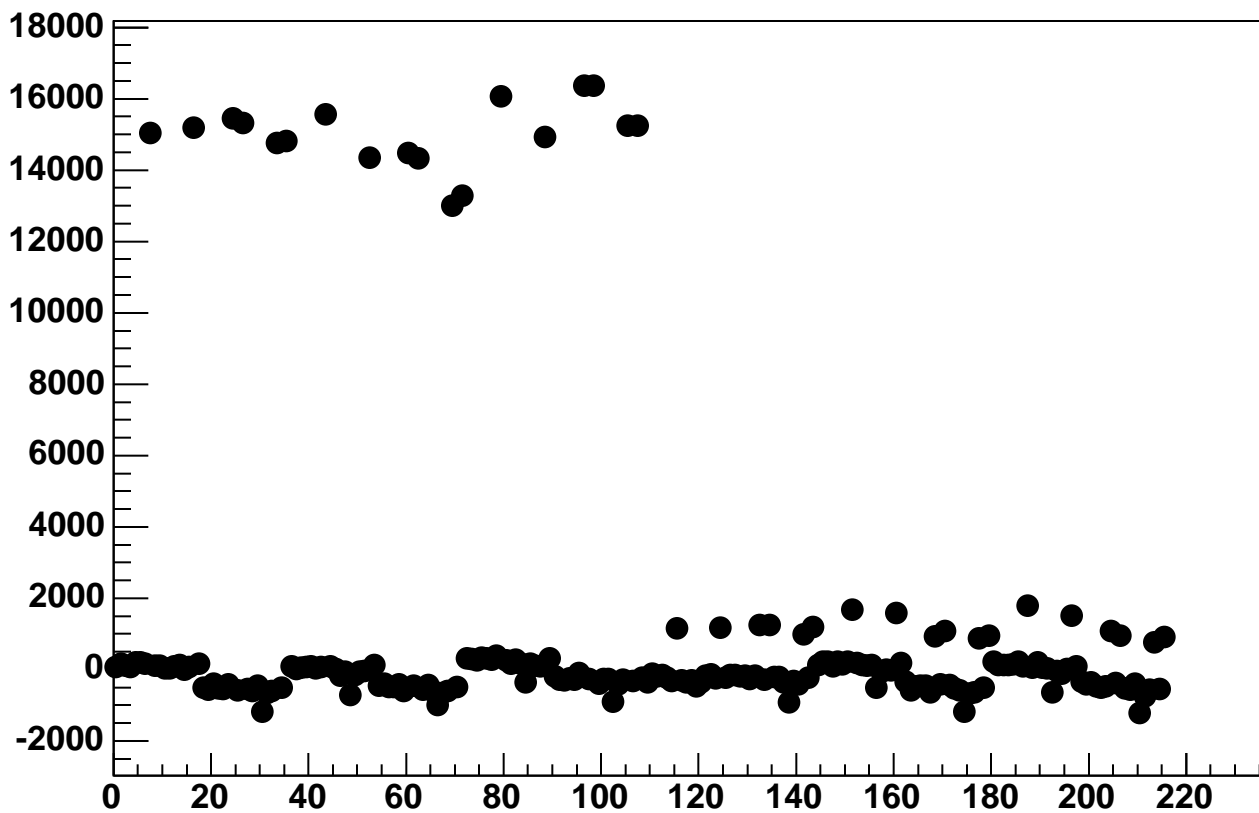
Enable 4, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



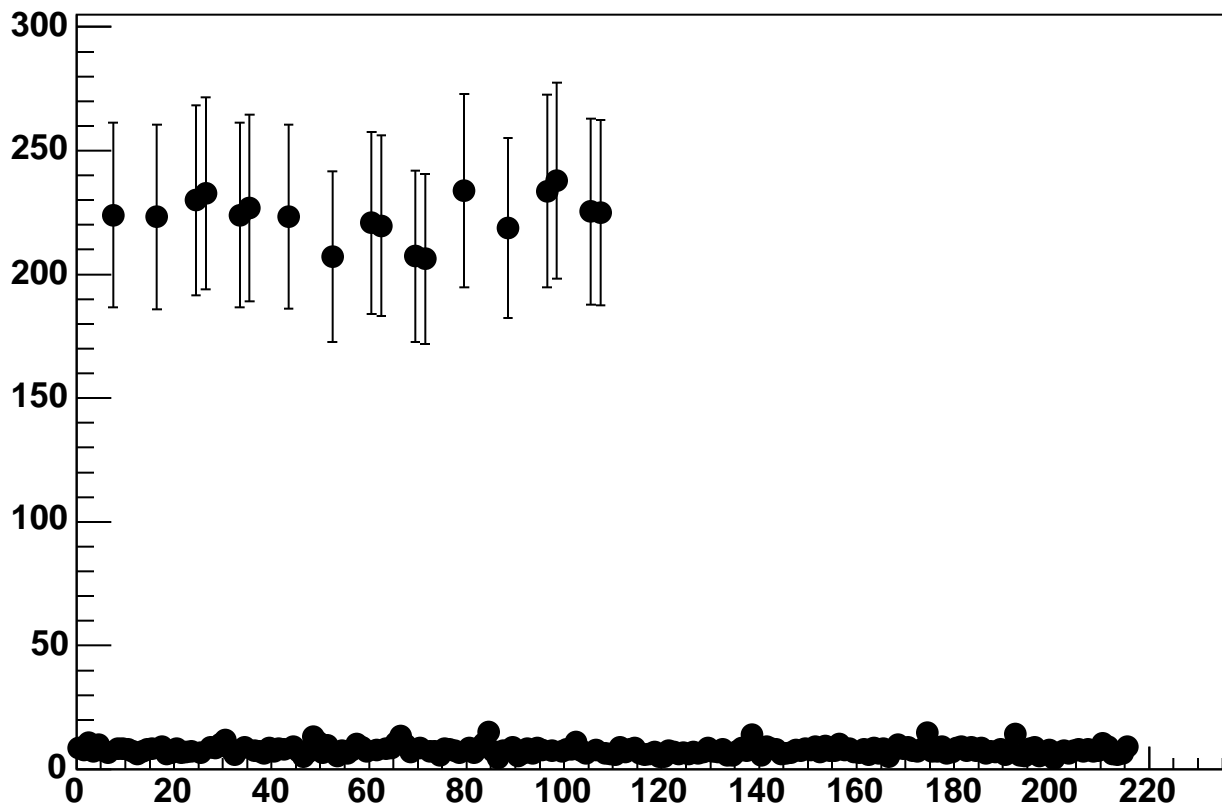
Enable 4, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



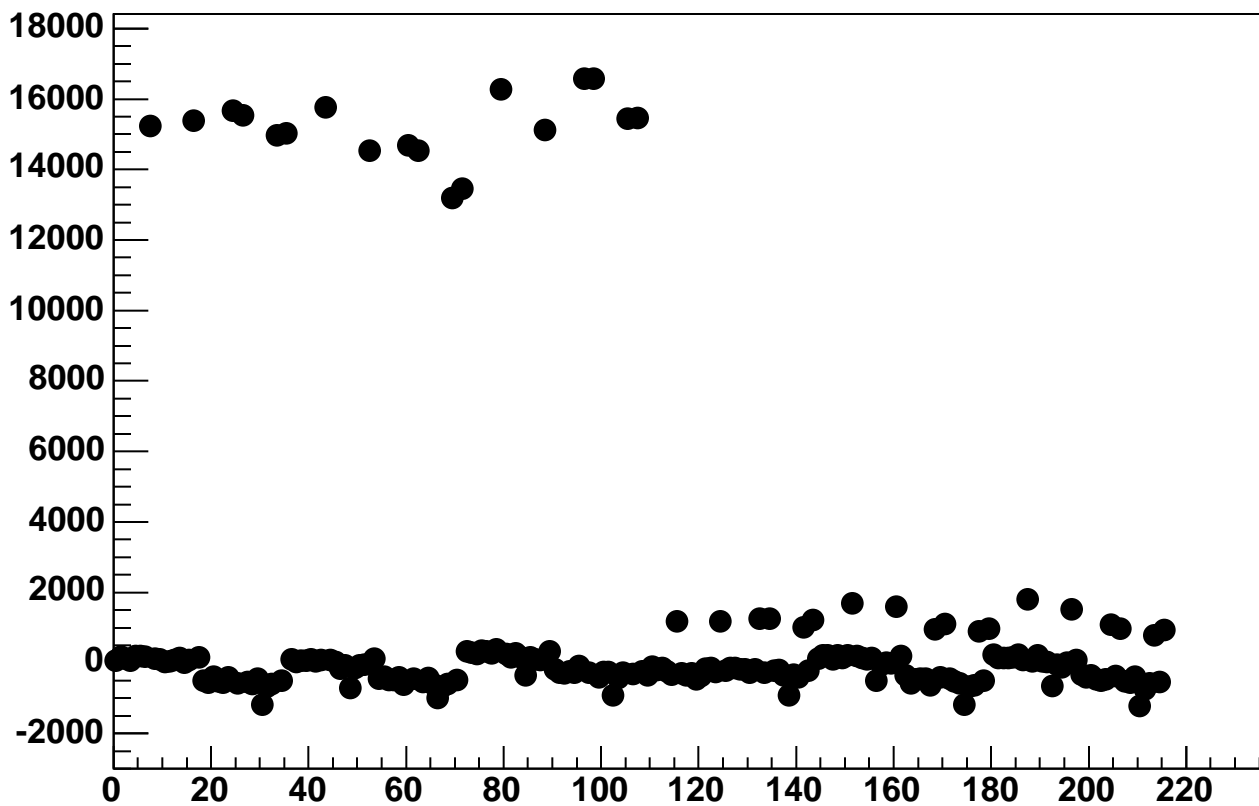
Enable 5, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



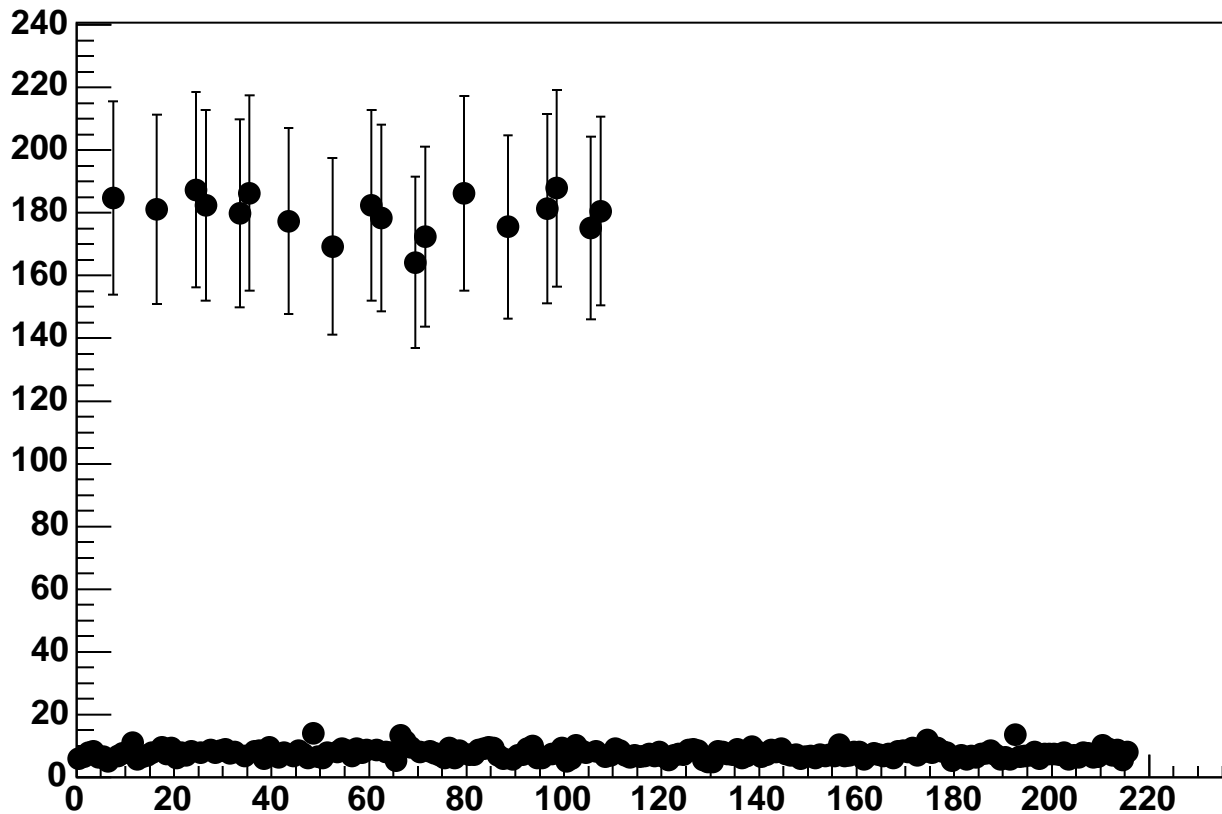
Enable 5, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



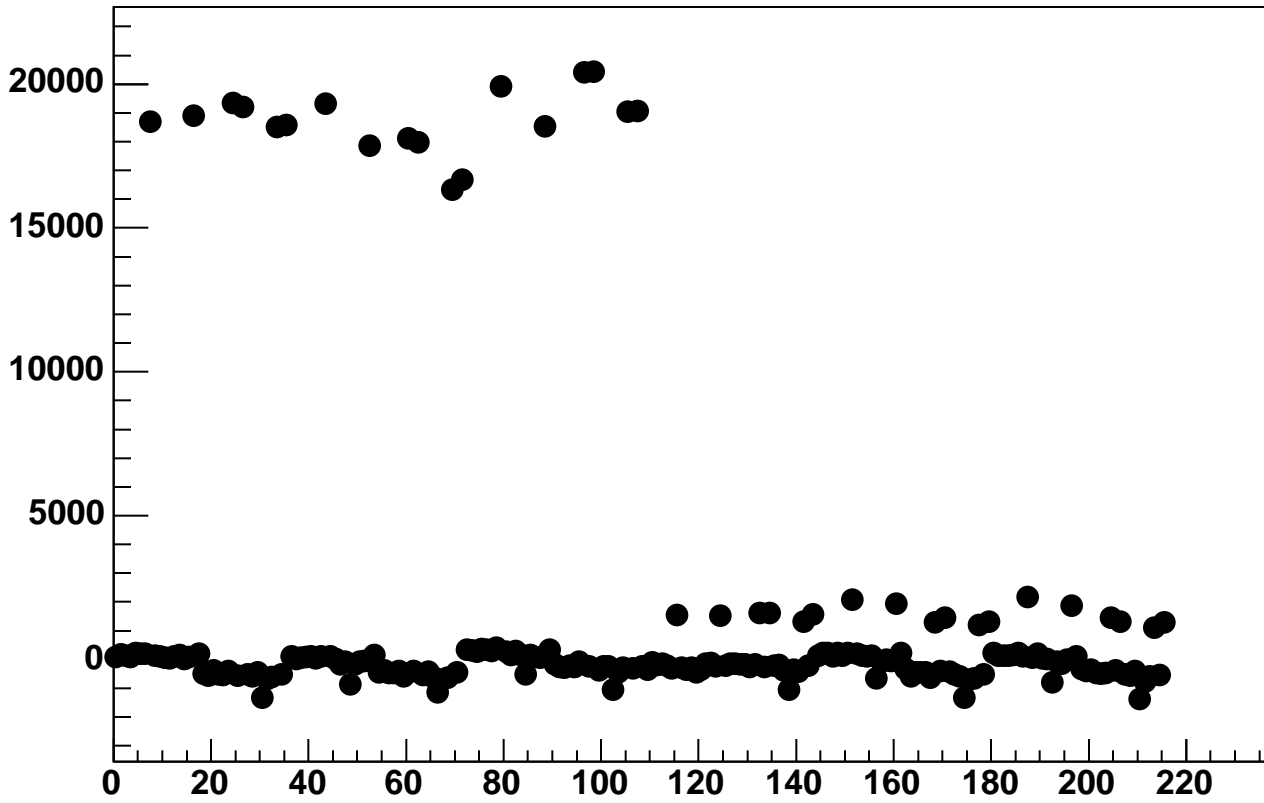
Enable 5, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



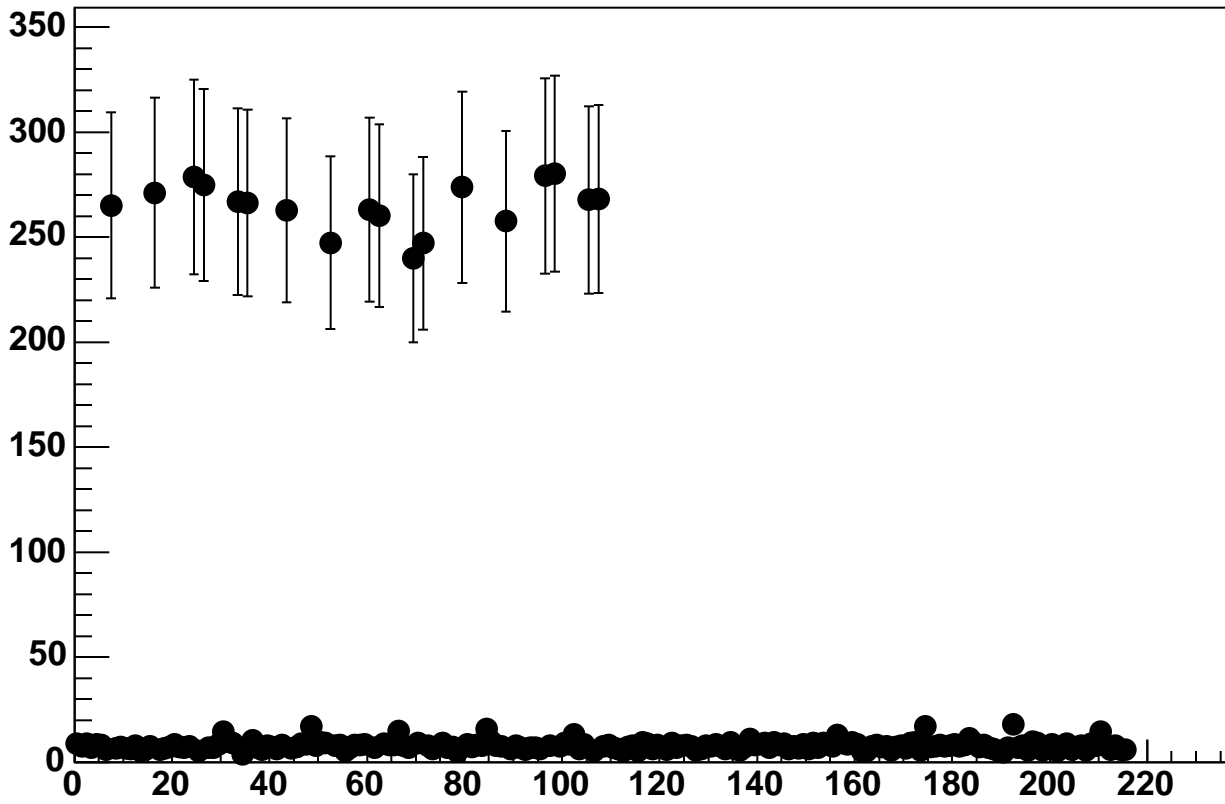
Enable 5, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



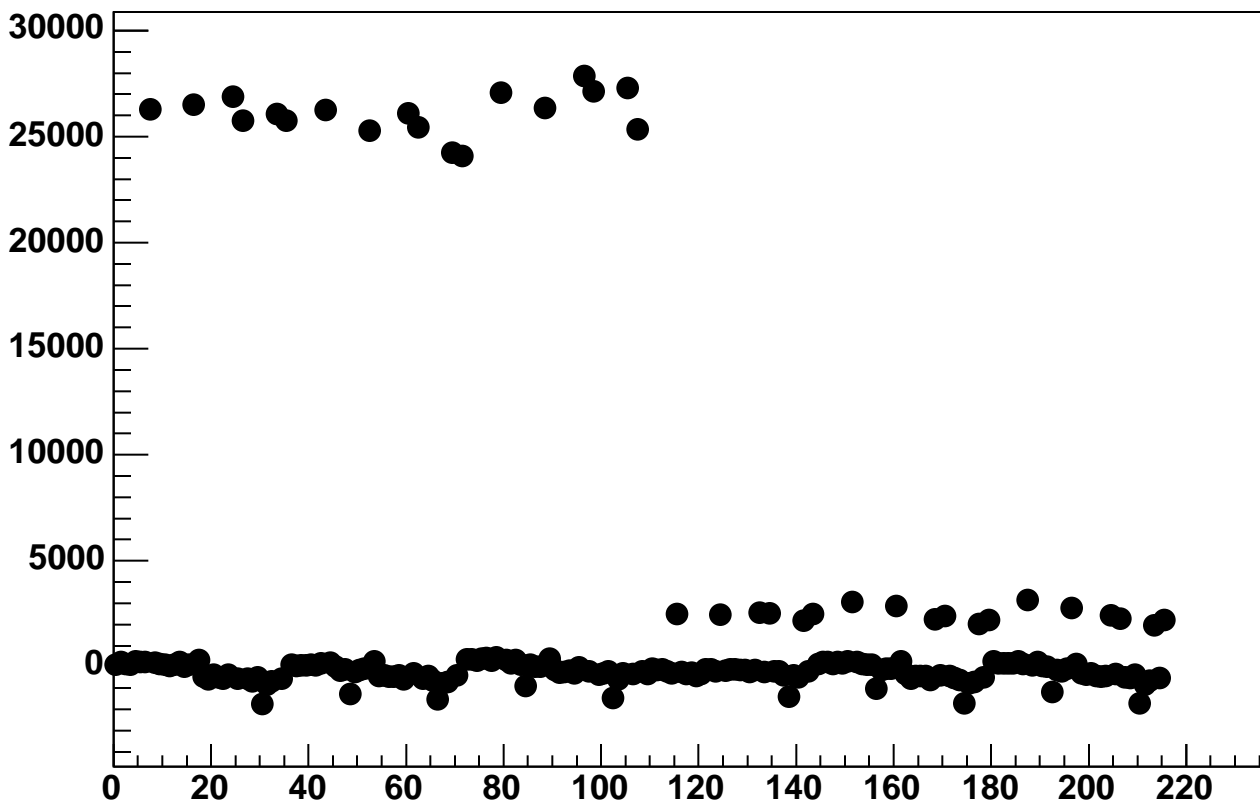
Enable 5, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



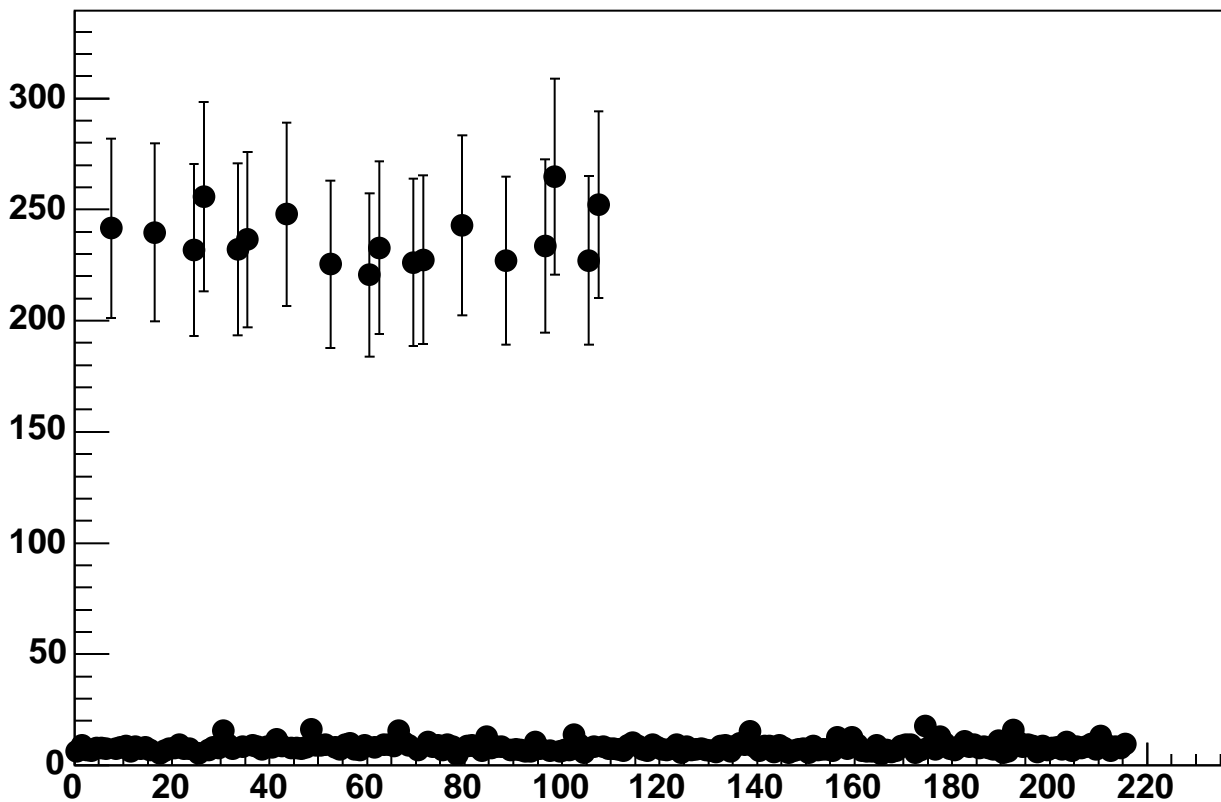
Enable 5, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



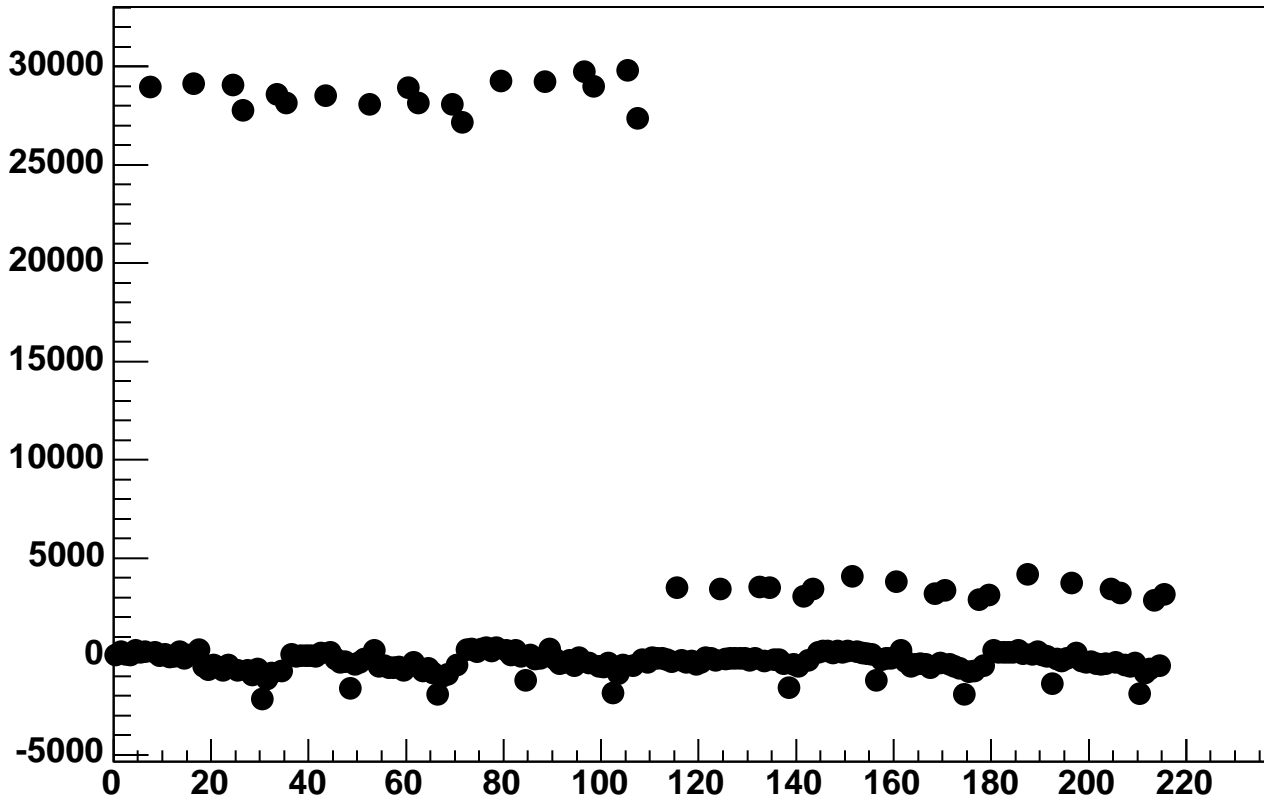
Enable 5, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



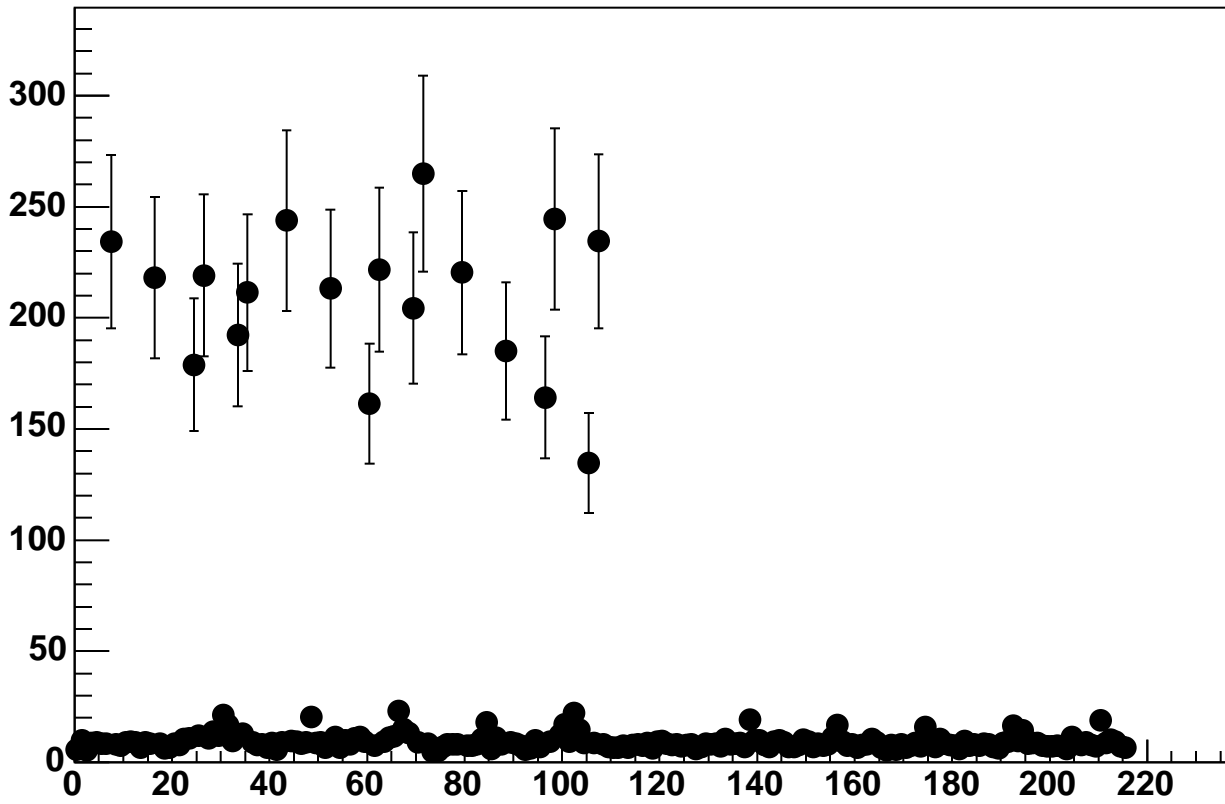
Enable 5, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



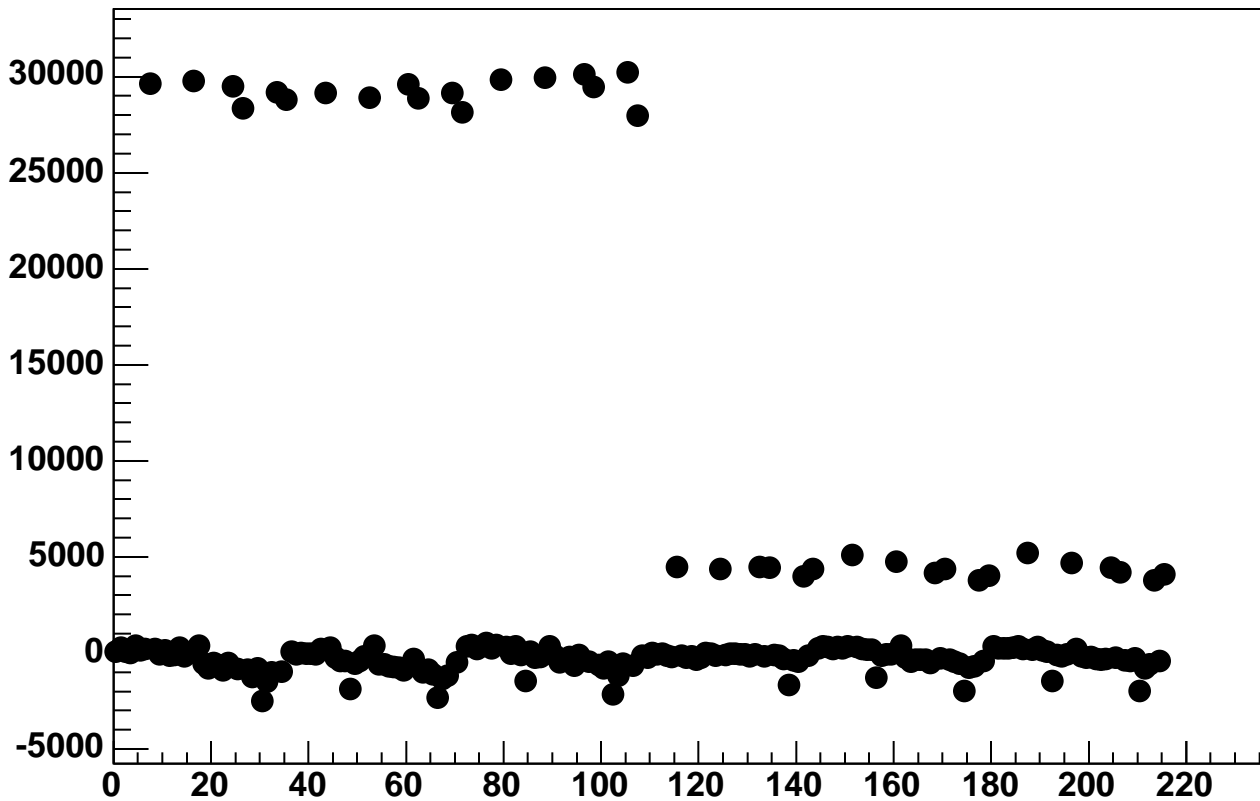
Enable 5, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



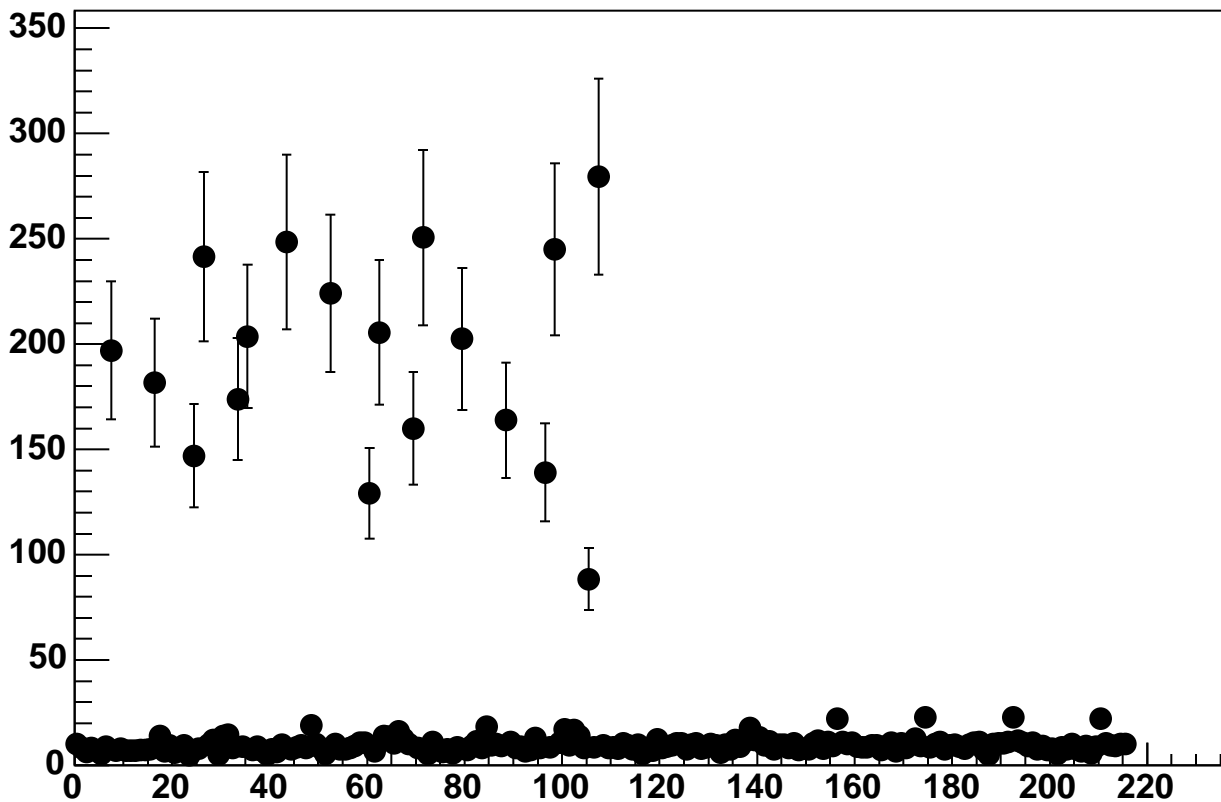
Enable 5, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



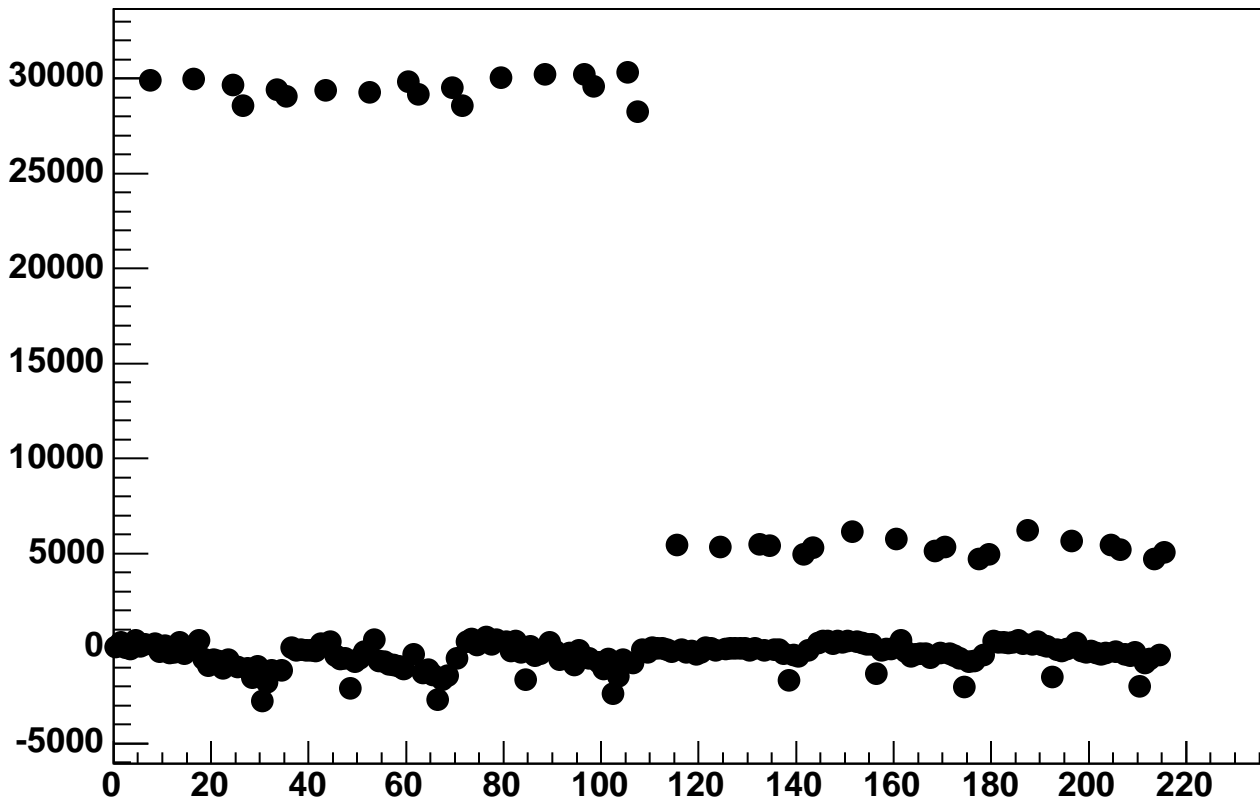
Enable 5, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



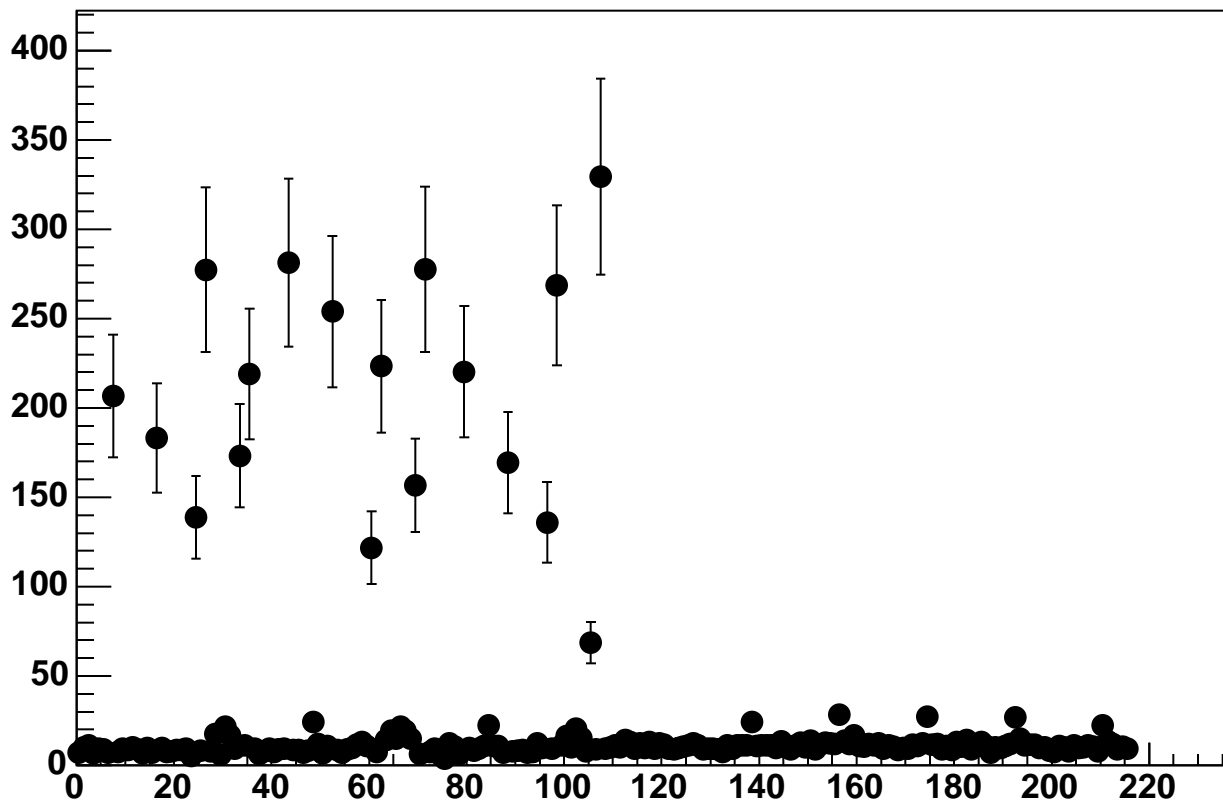
Enable 5, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



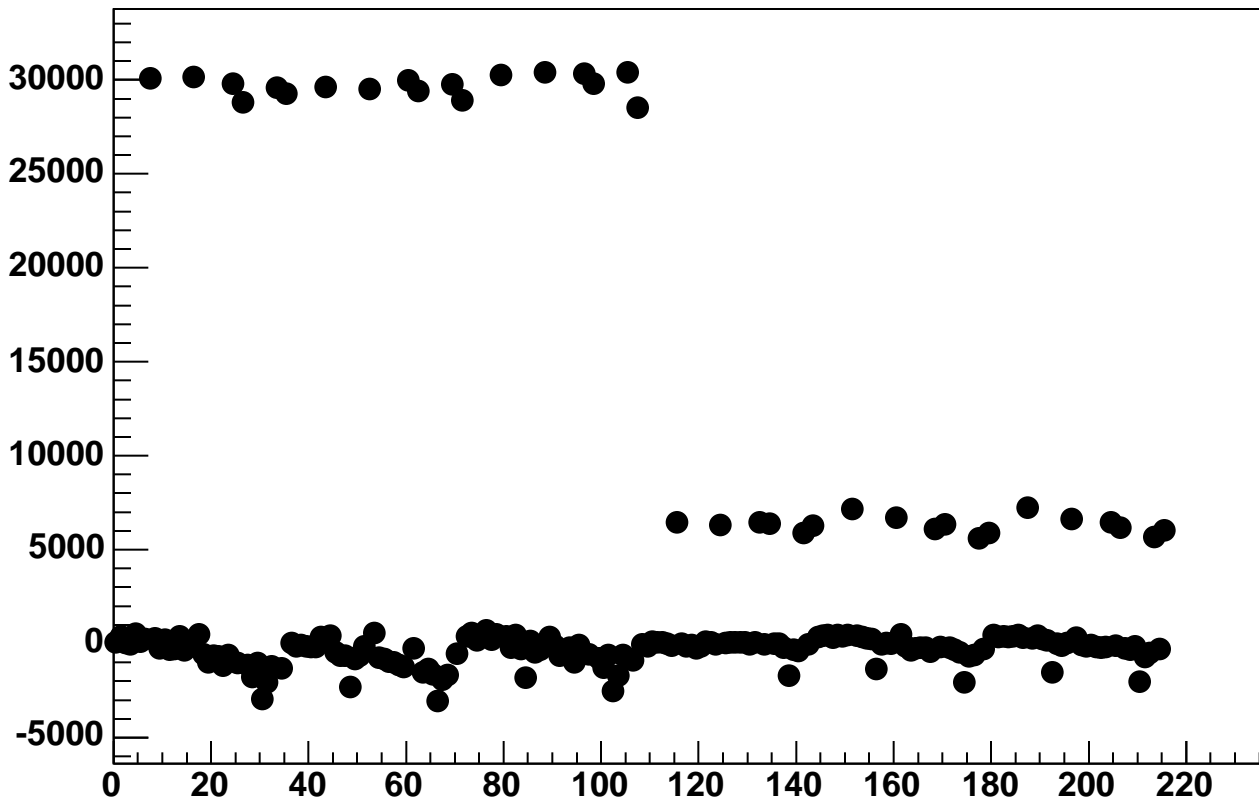
Enable 5, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



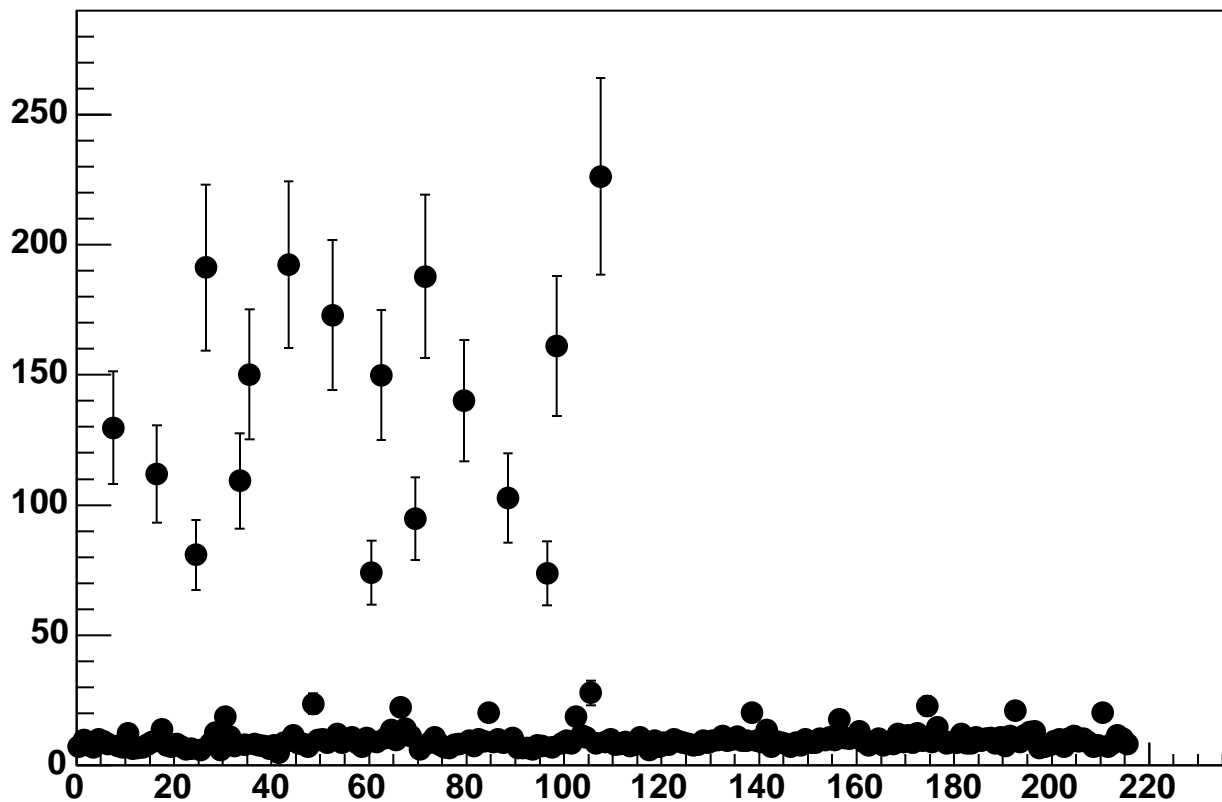
Enable 5, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



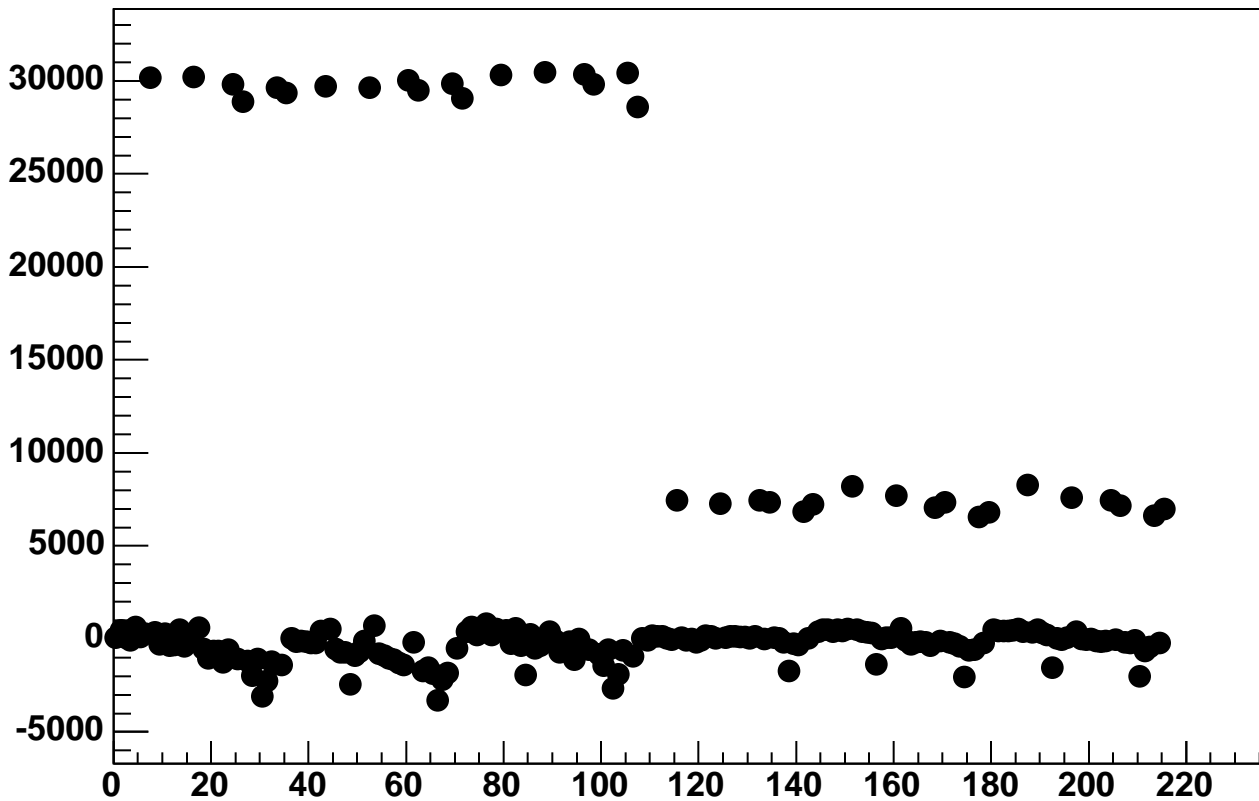
Enable 5, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



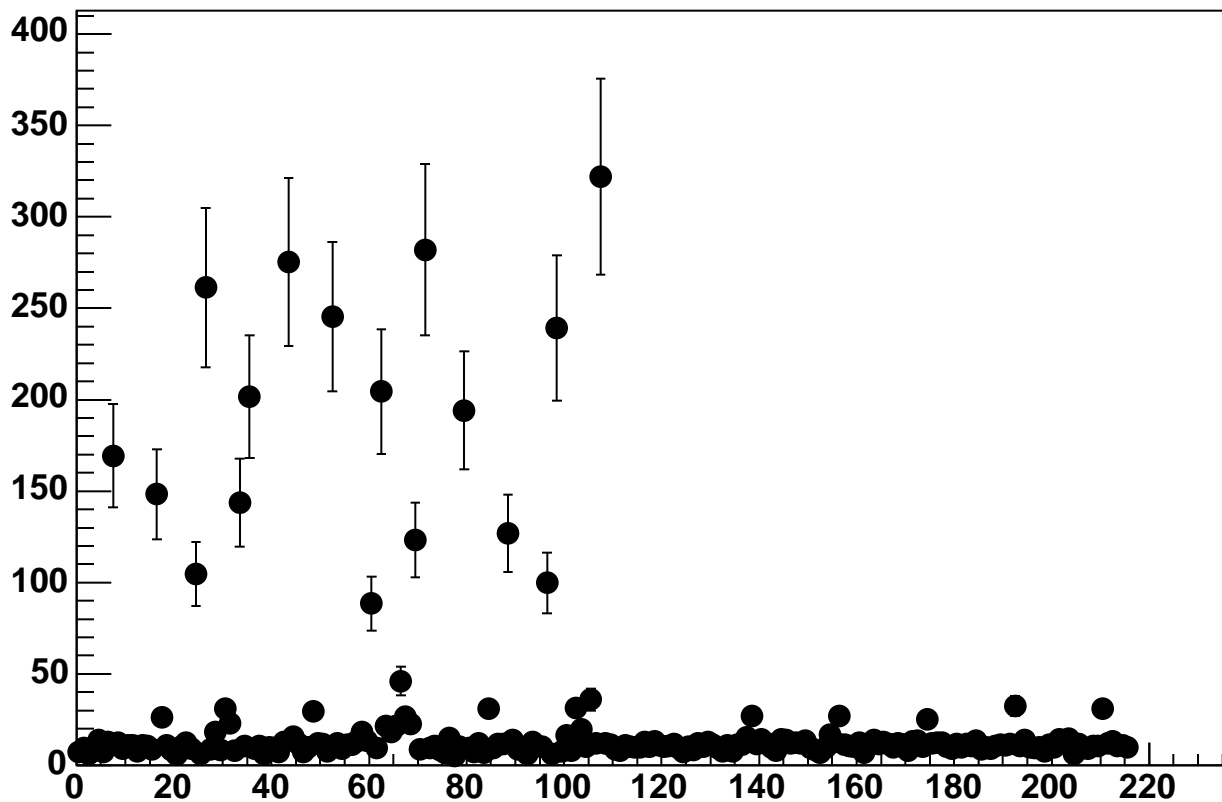
Enable 5, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



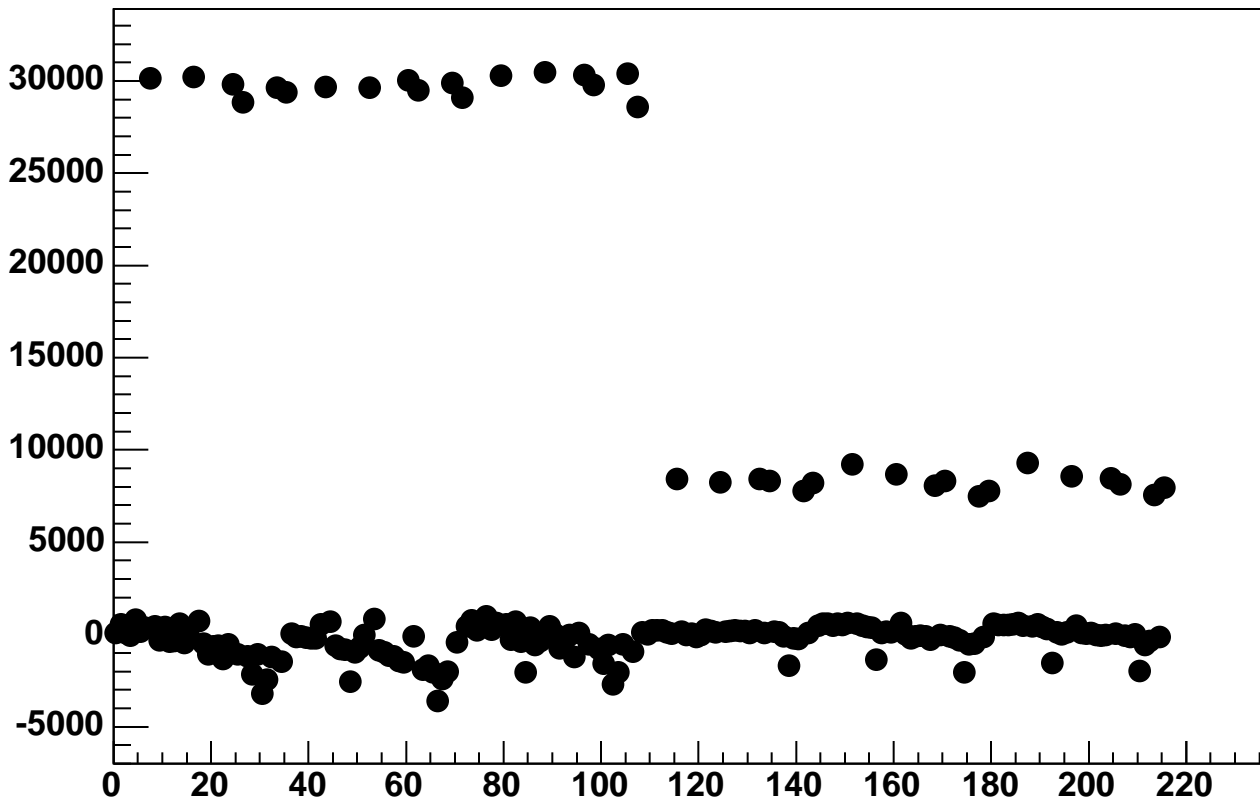
Enable 5, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



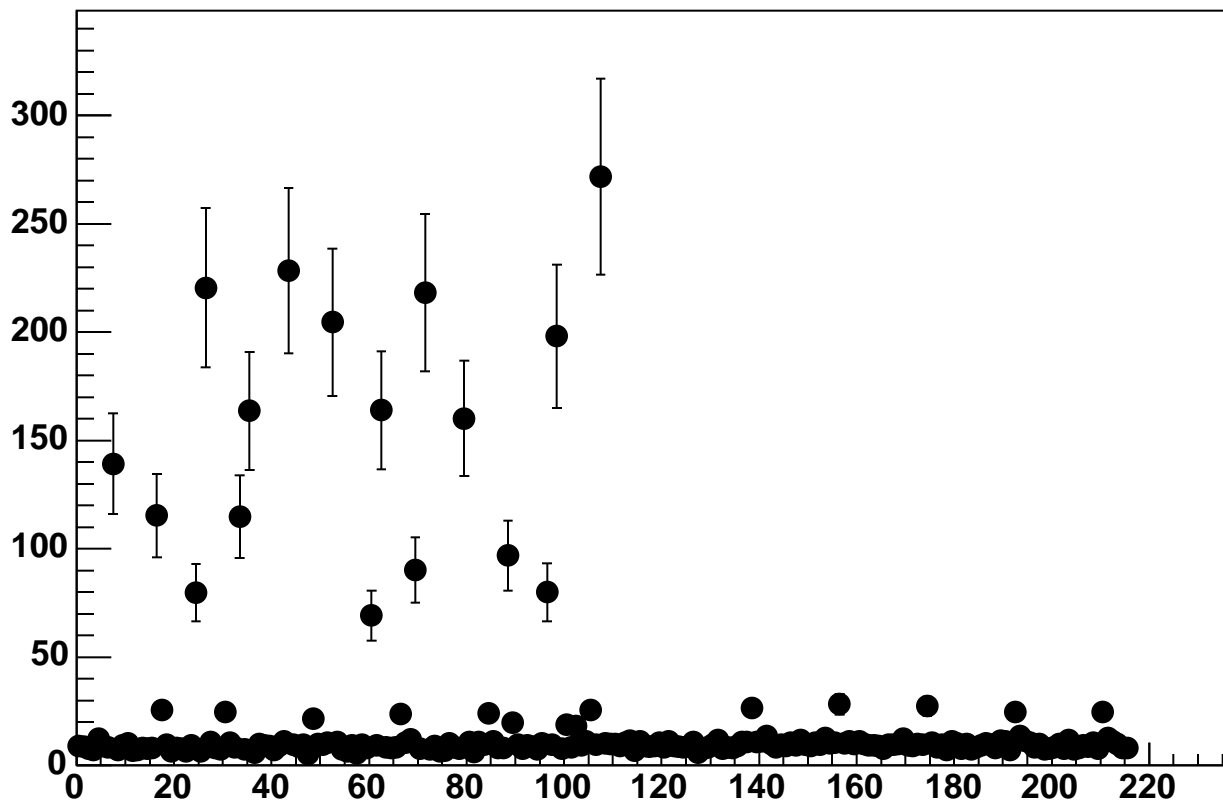
Enable 5, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



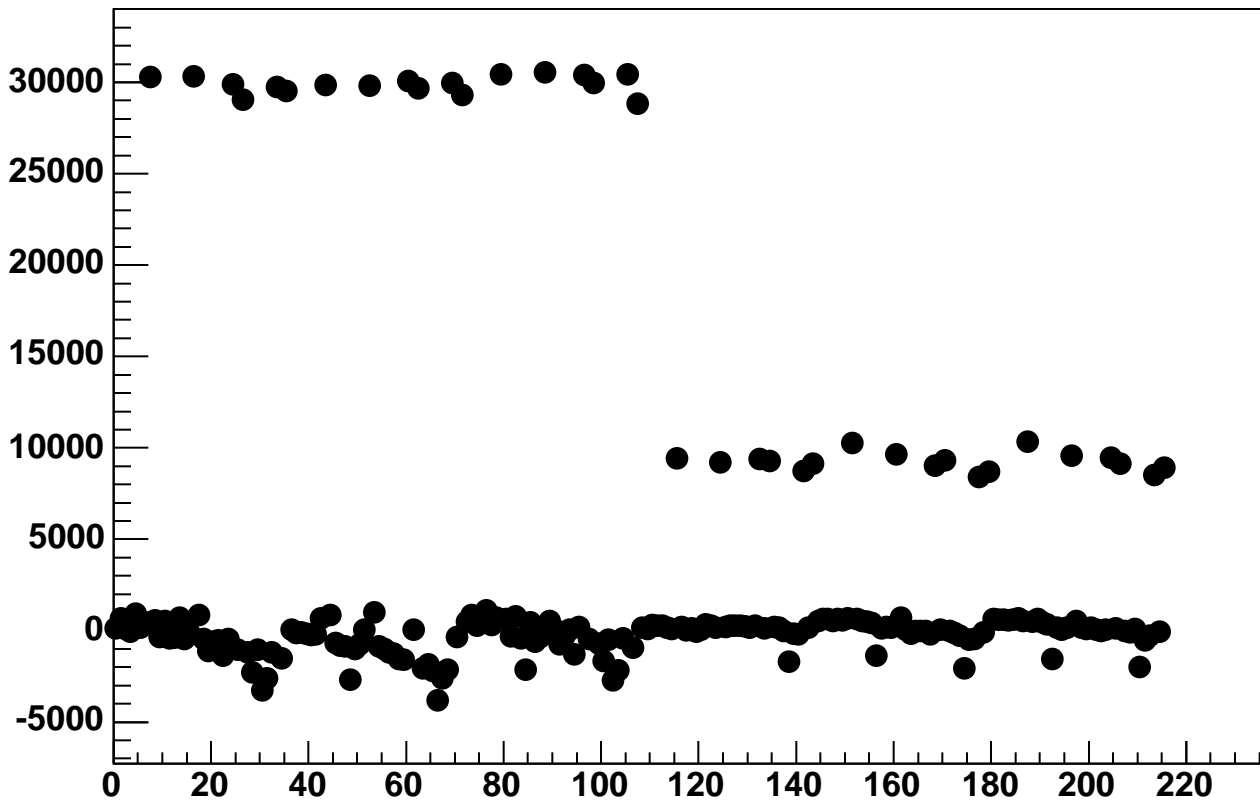
Enable 5, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



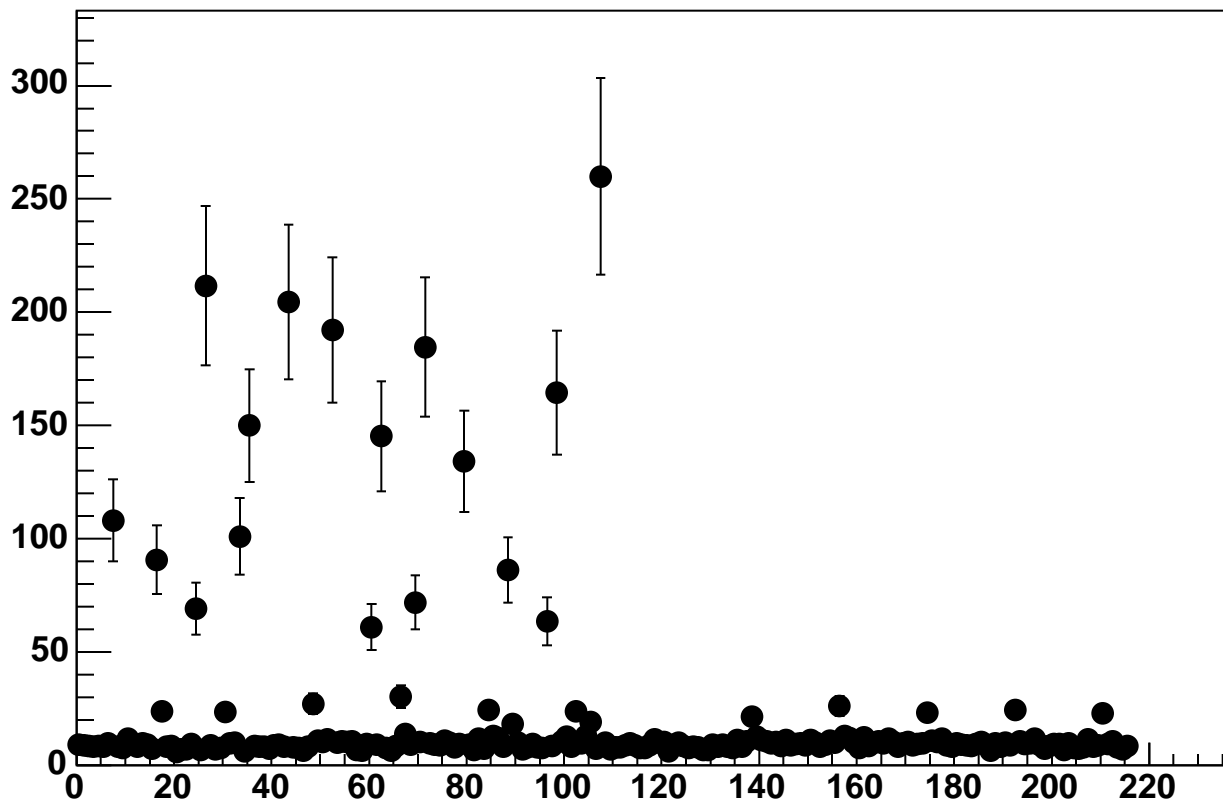
Enable 5, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



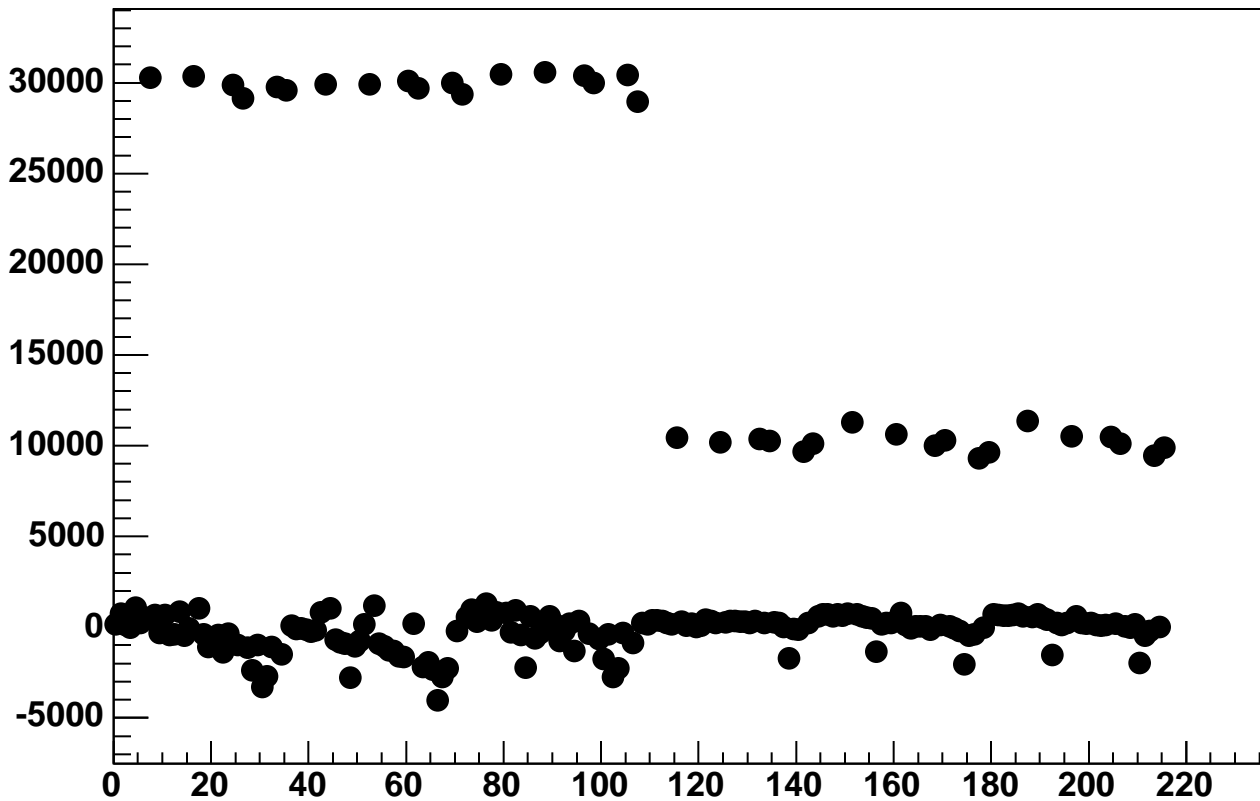
Enable 5, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



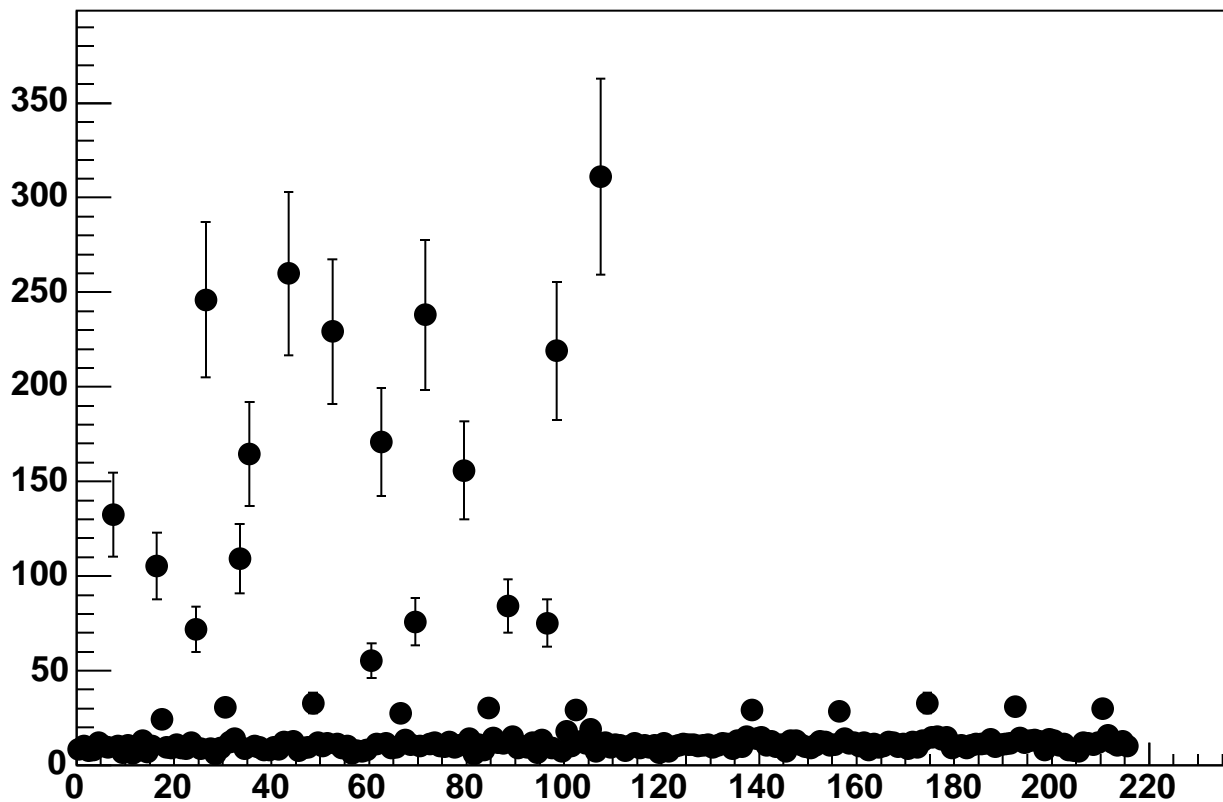
Enable 5, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



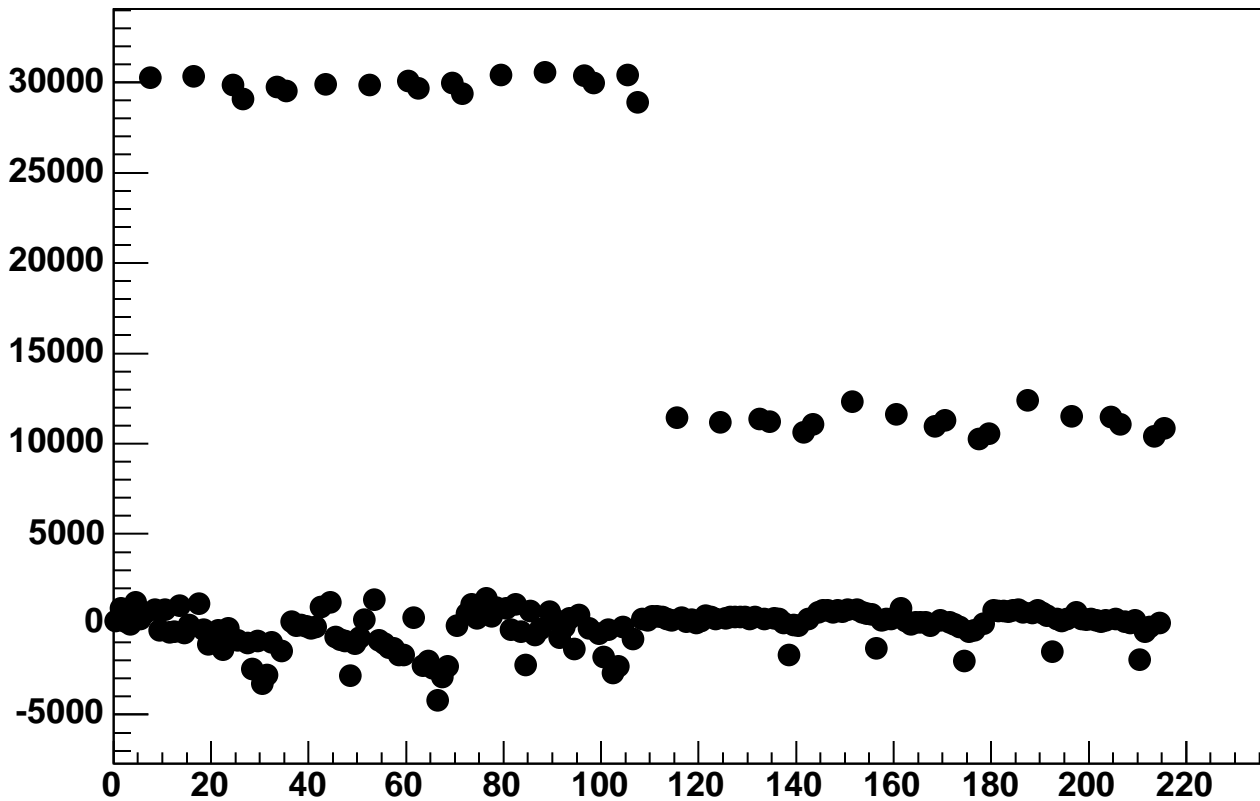
Enable 5, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



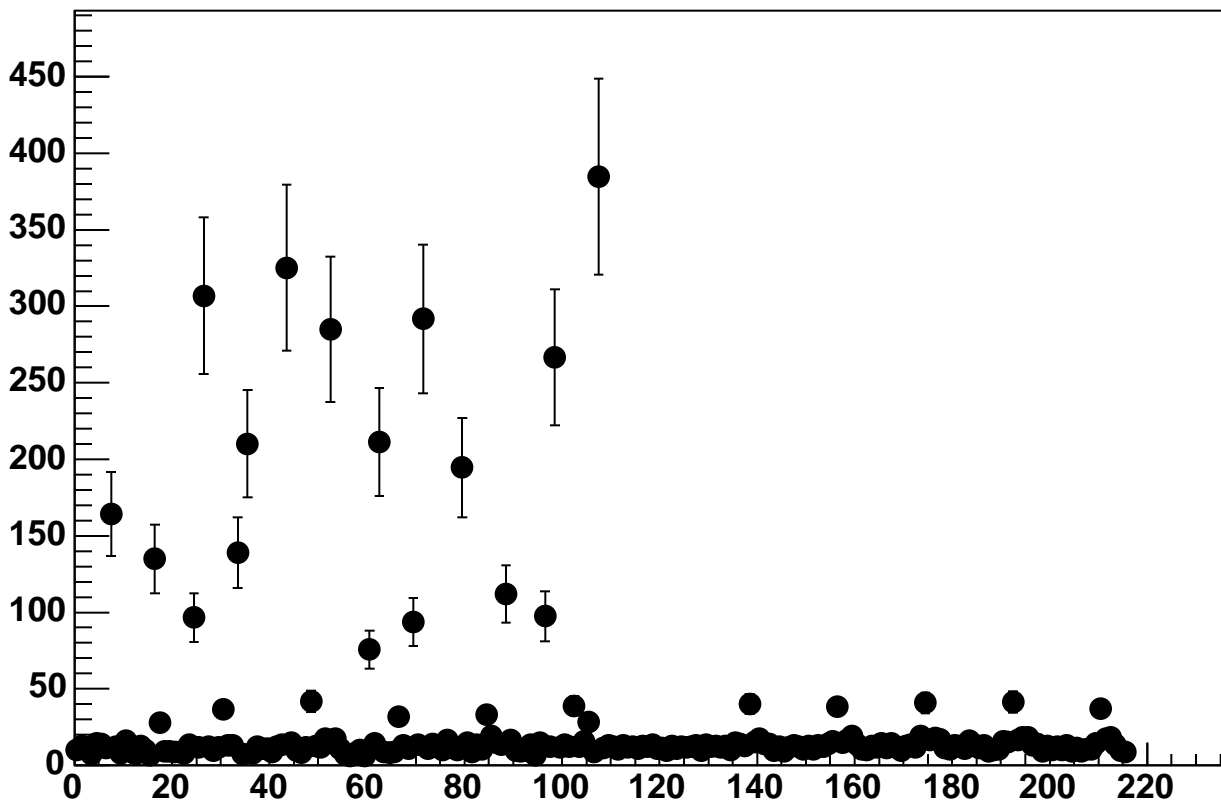
Enable 5, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



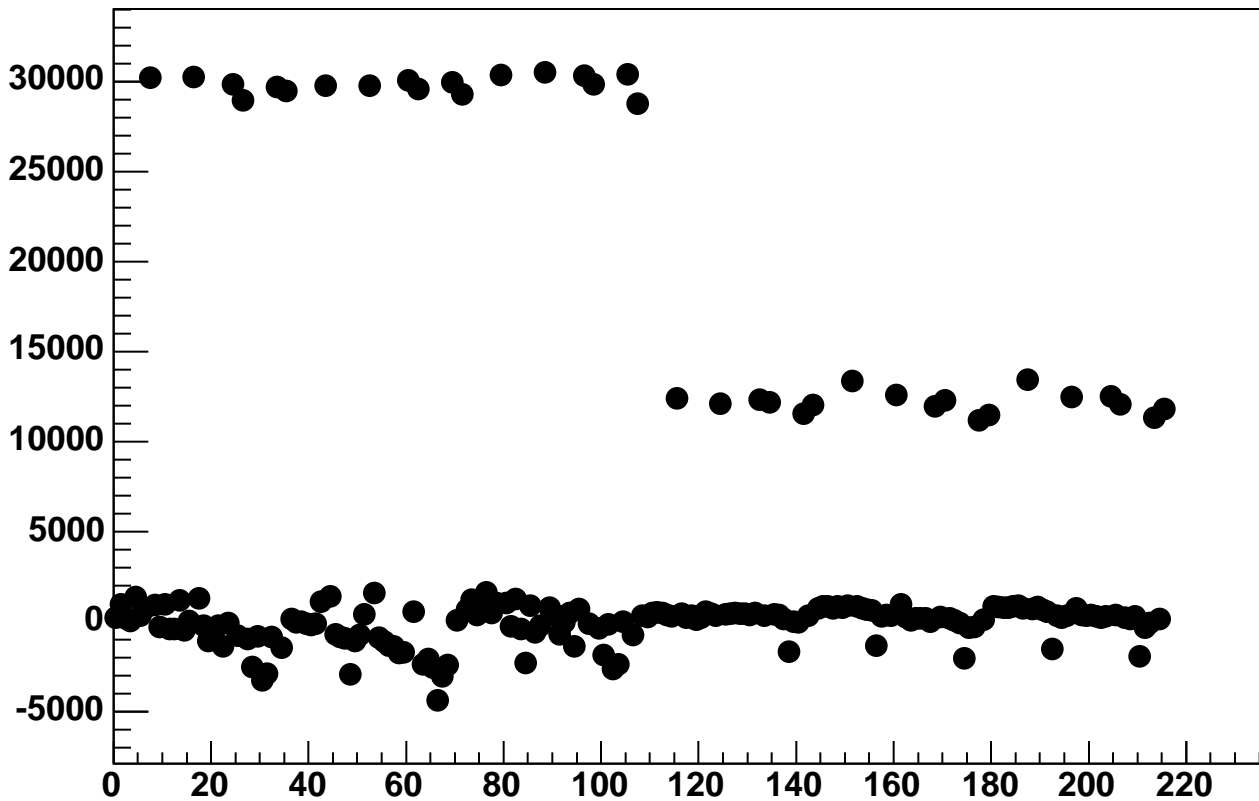
Enable 5, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



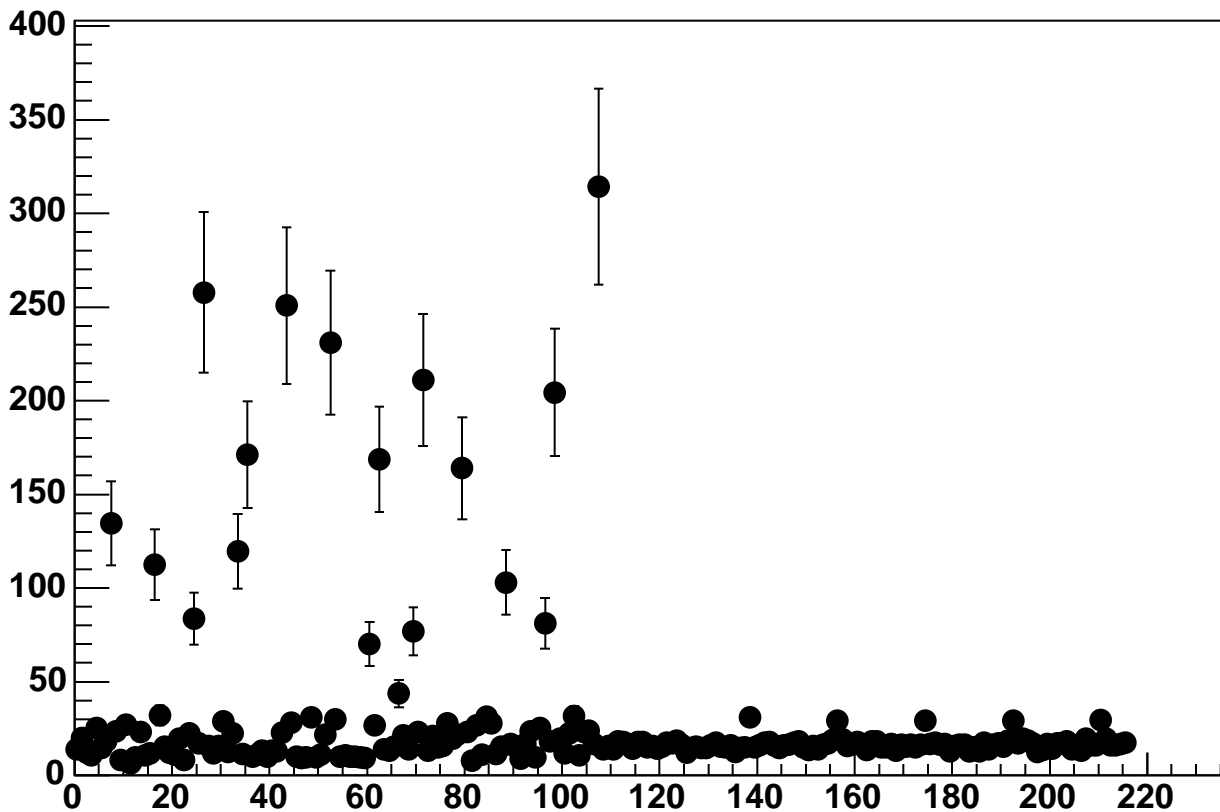
Enable 5, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



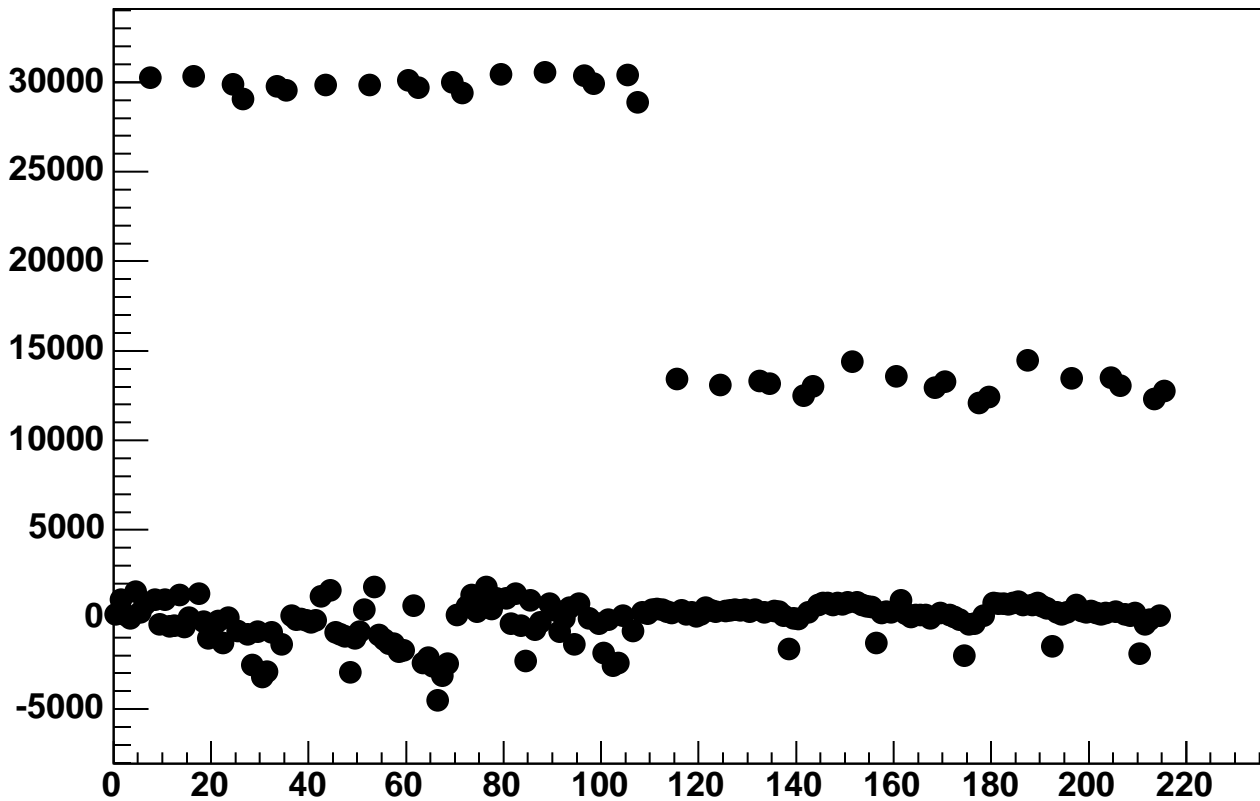
Enable 5, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



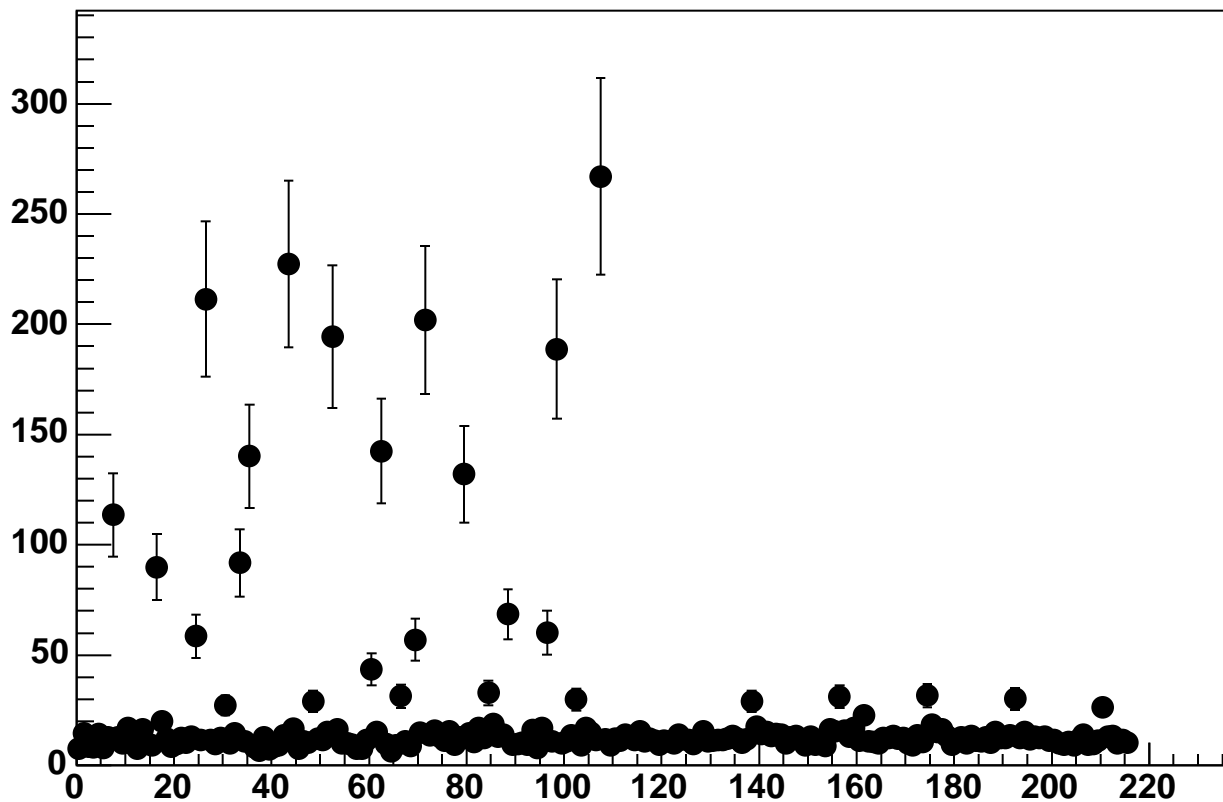
Enable 5, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



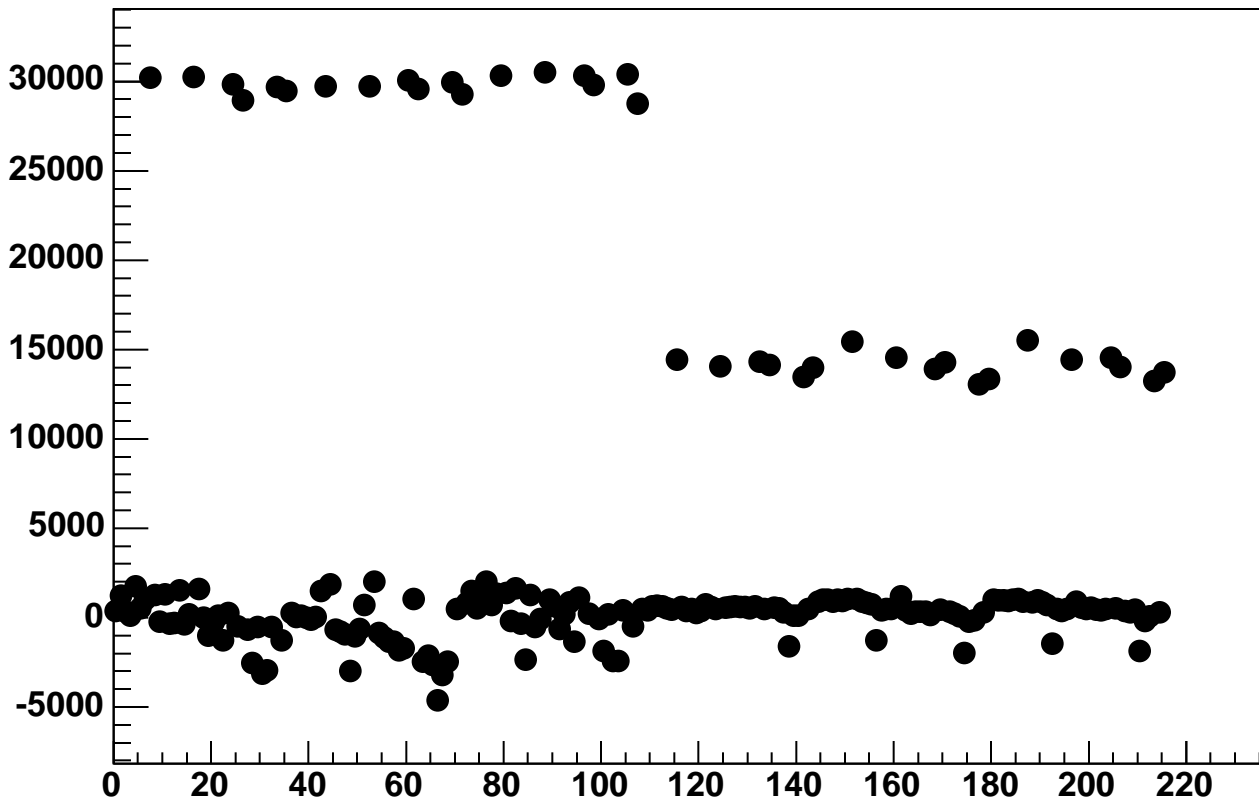
Enable 5, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



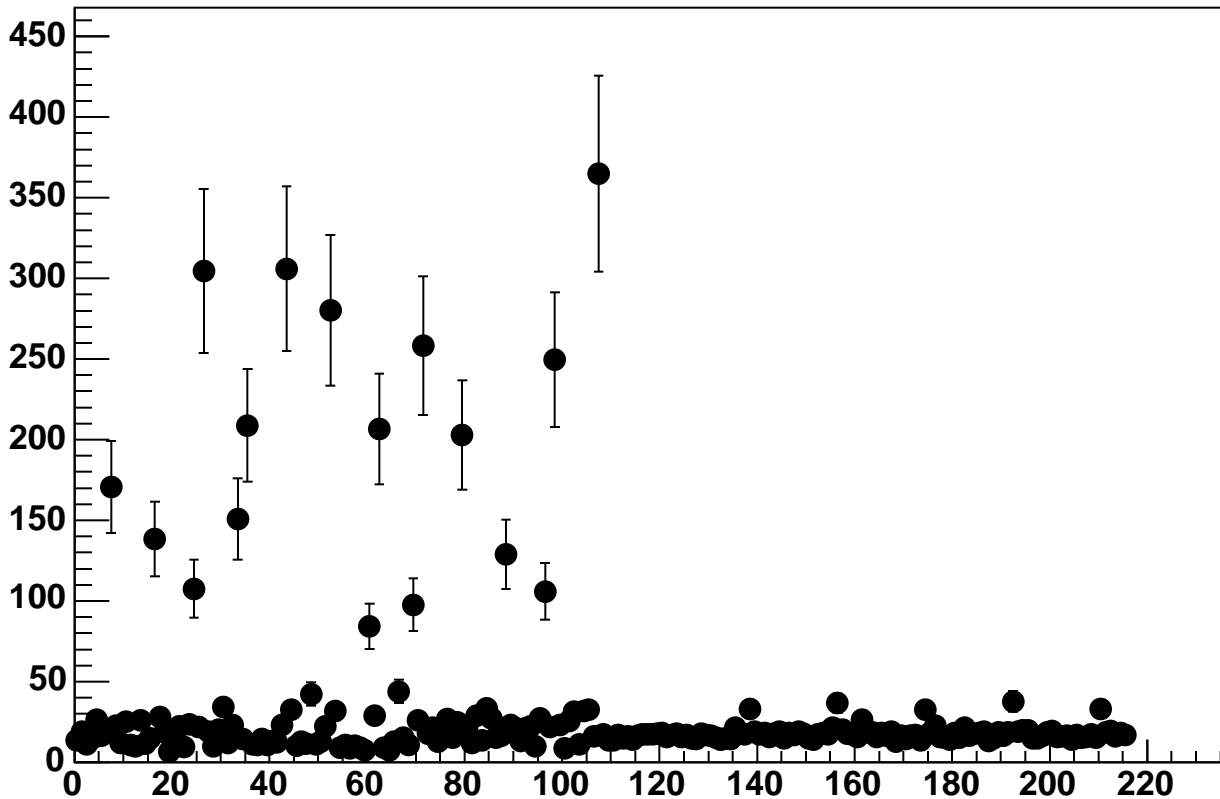
Enable 5, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



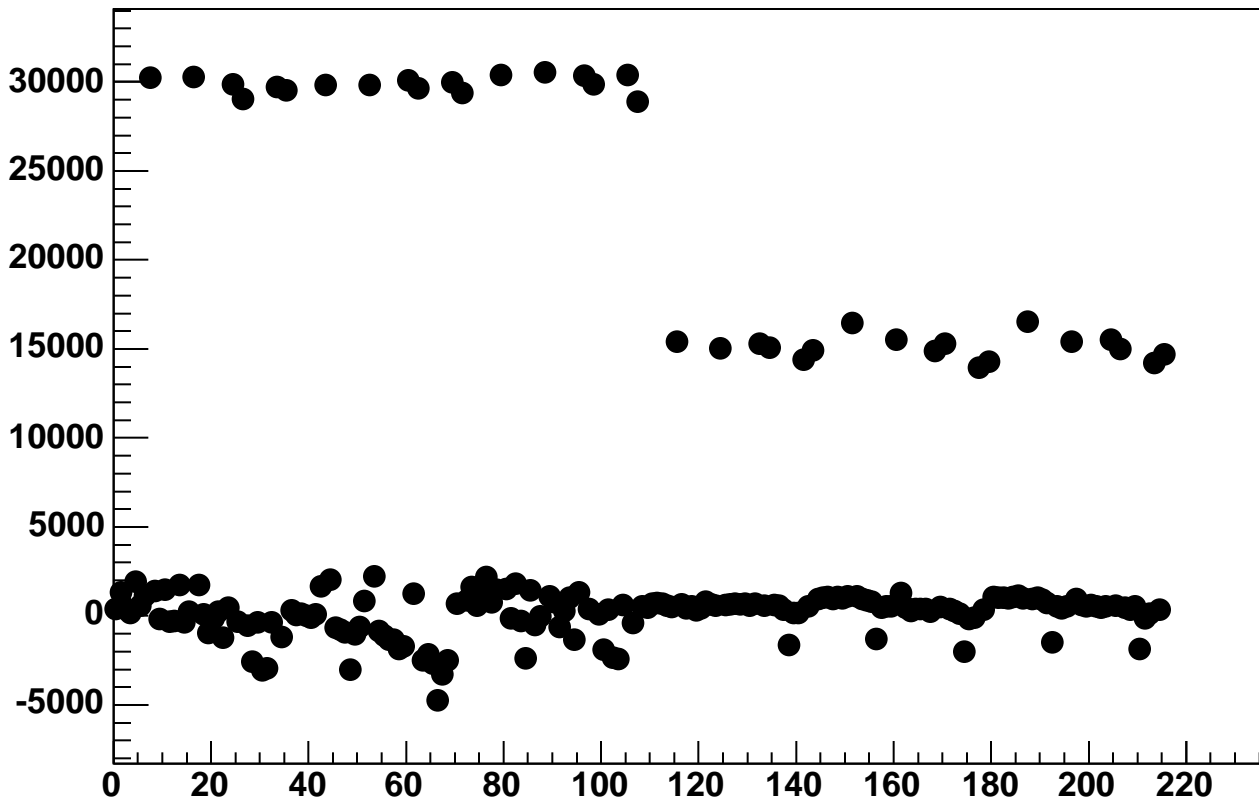
Enable 5, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



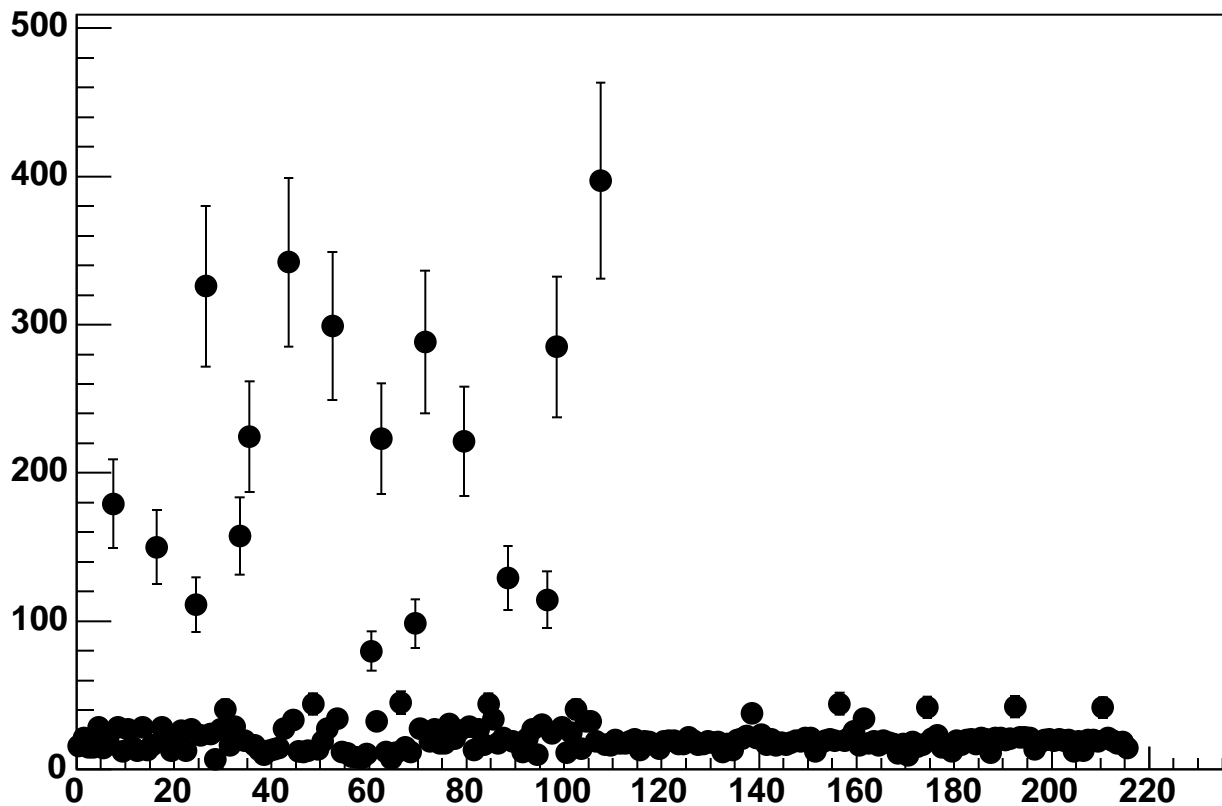
Enable 5, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



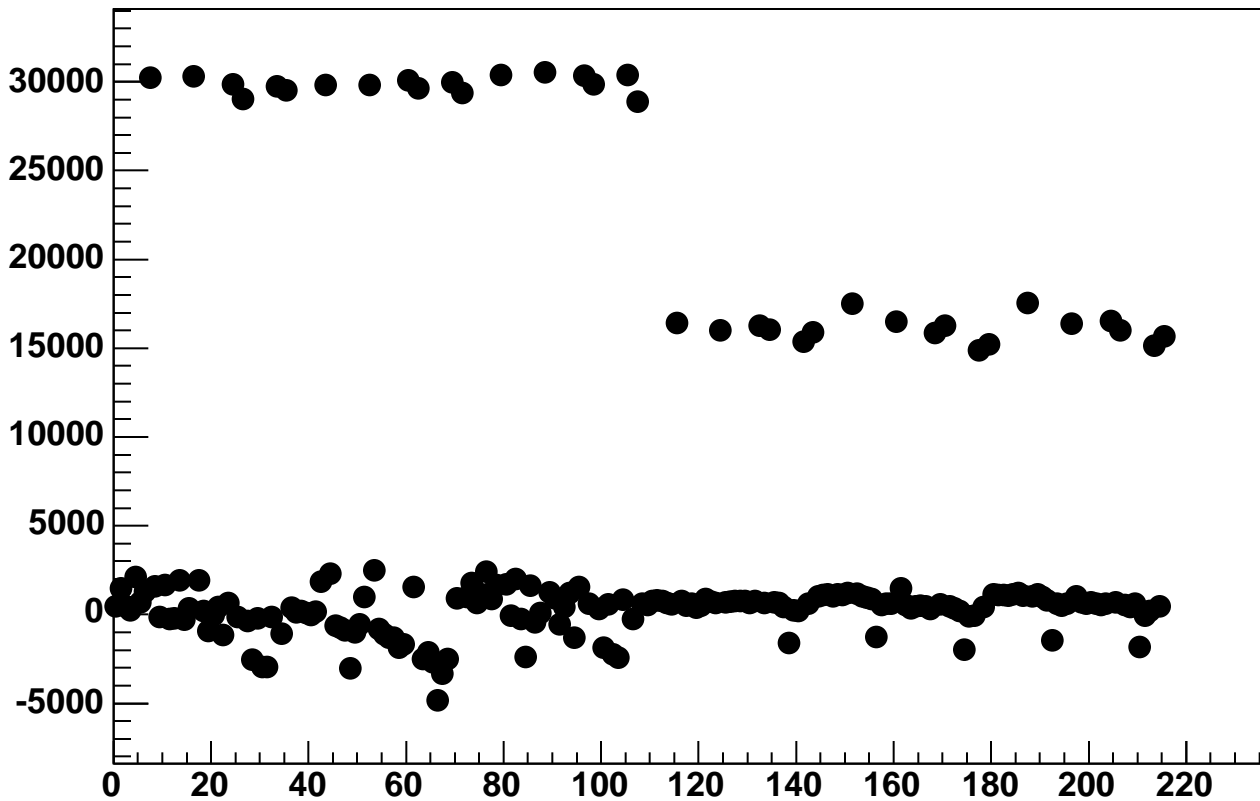
Enable 5, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



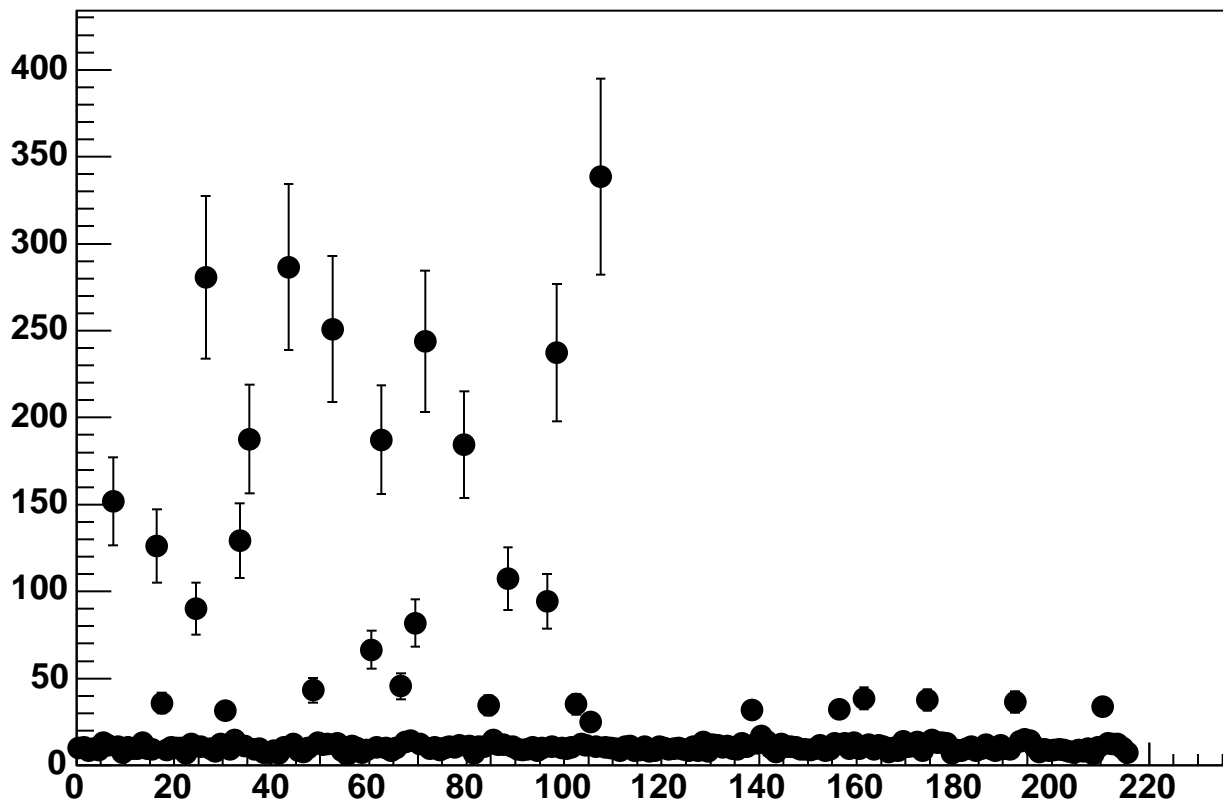
Enable 5, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



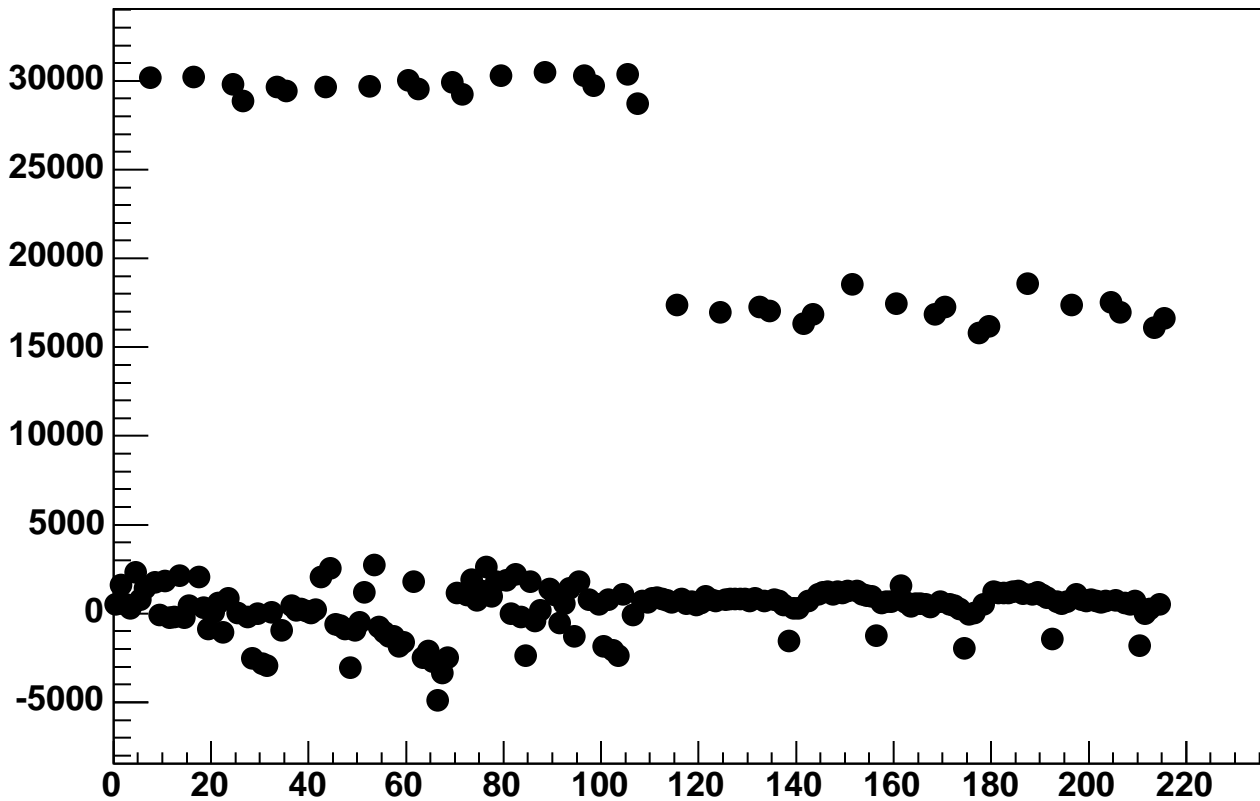
Enable 5, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



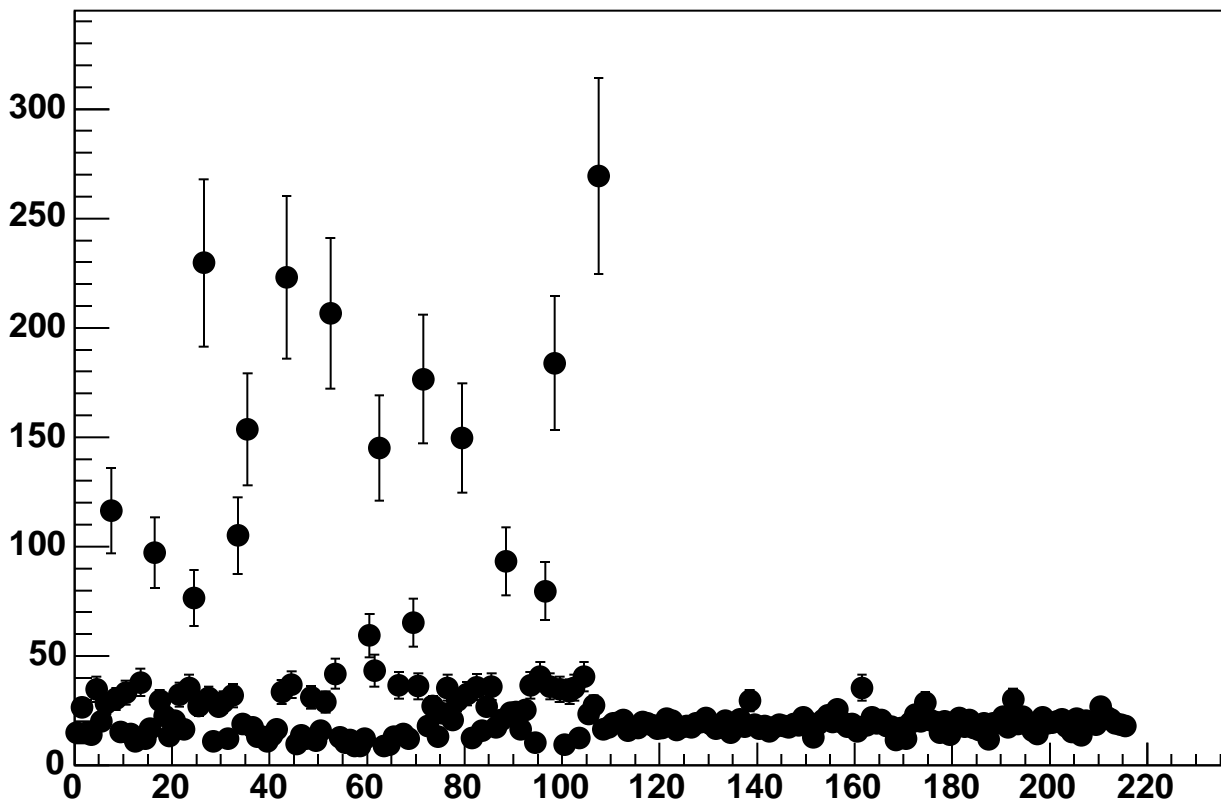
Enable 5, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



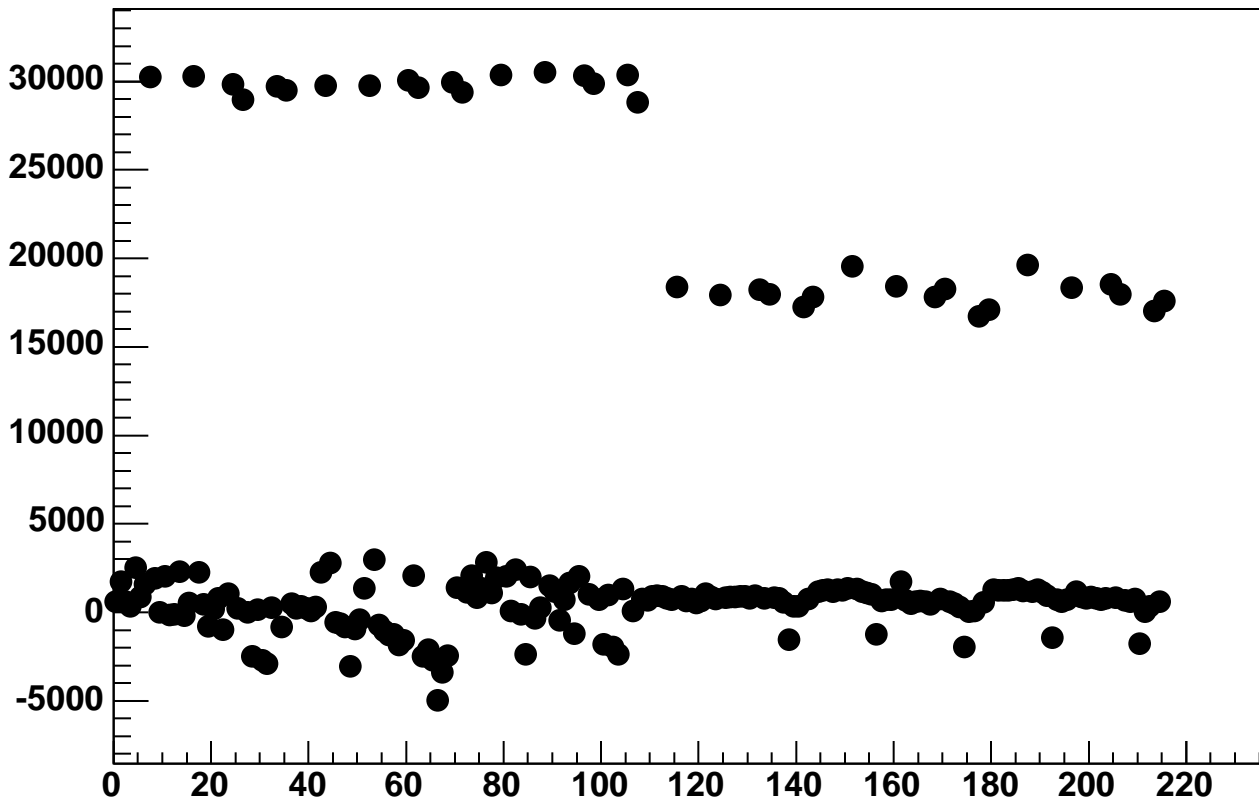
Enable 5, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



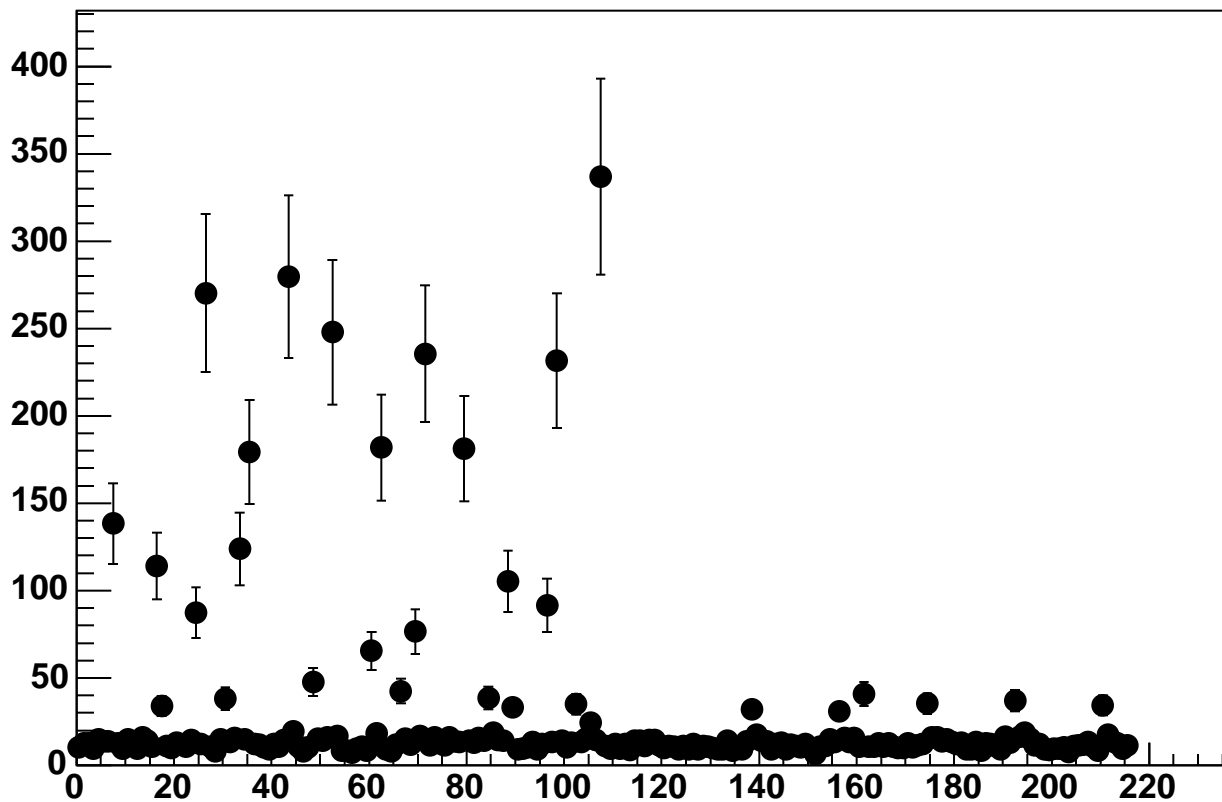
Enable 5, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



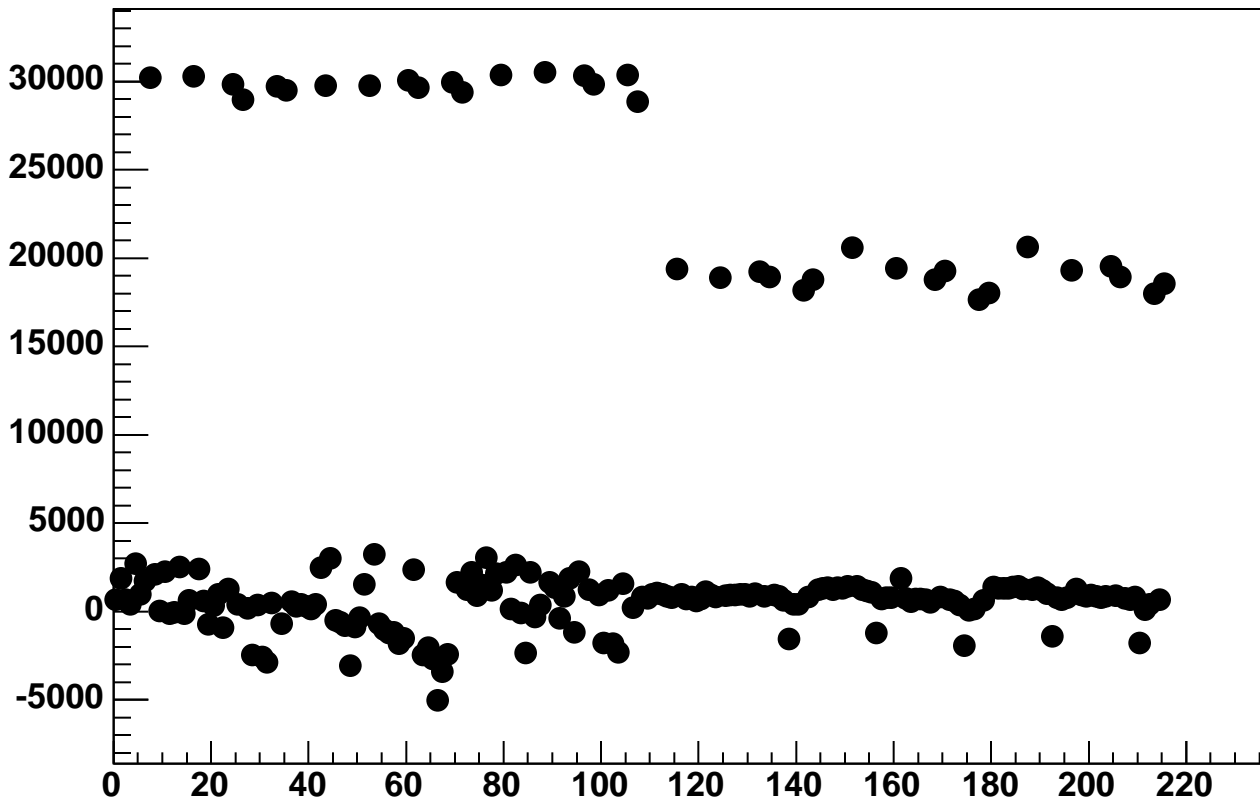
Enable 5, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



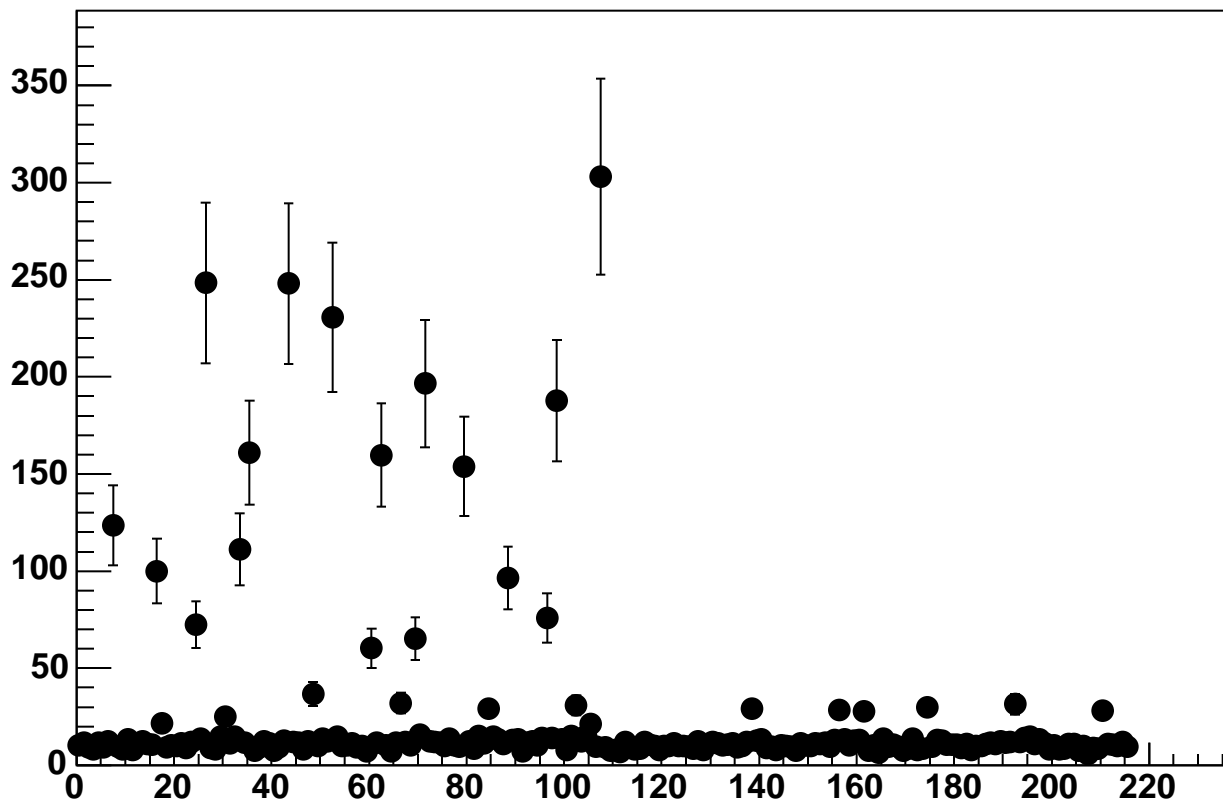
Enable 5, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



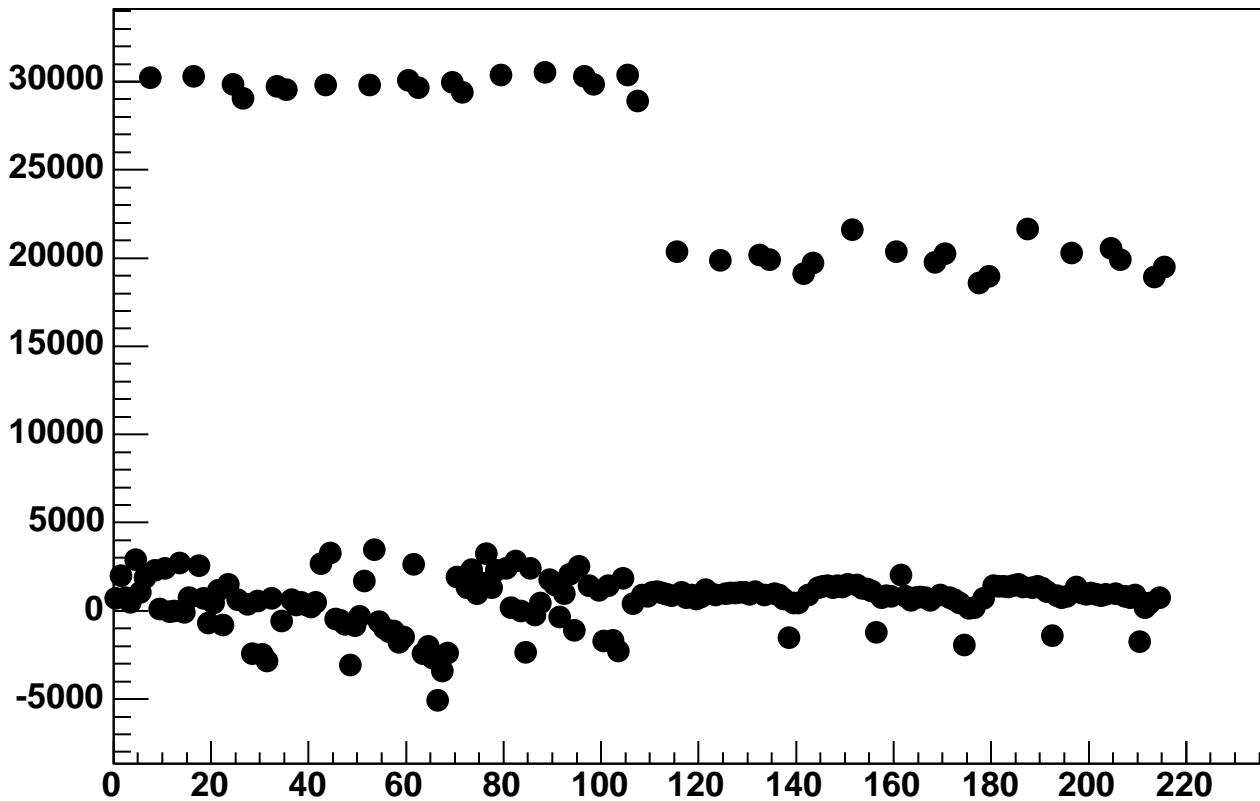
Enable 5, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



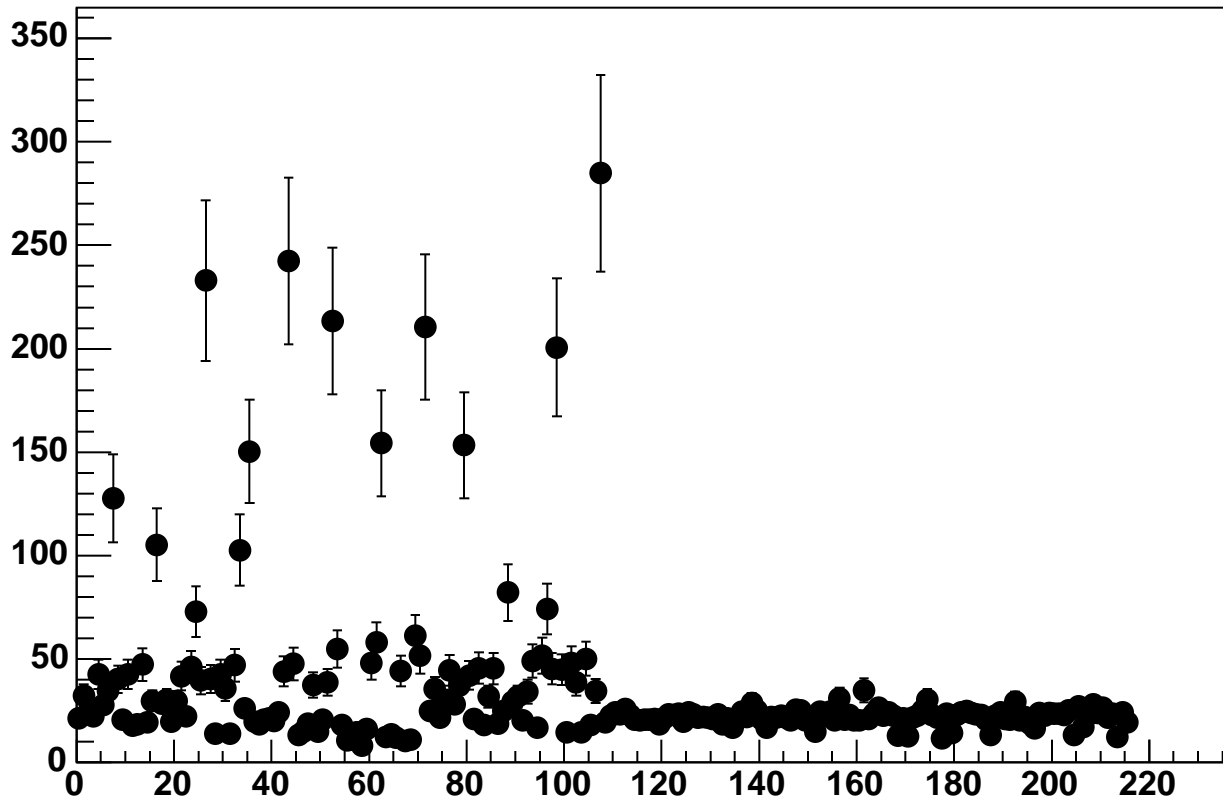
Enable 5, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



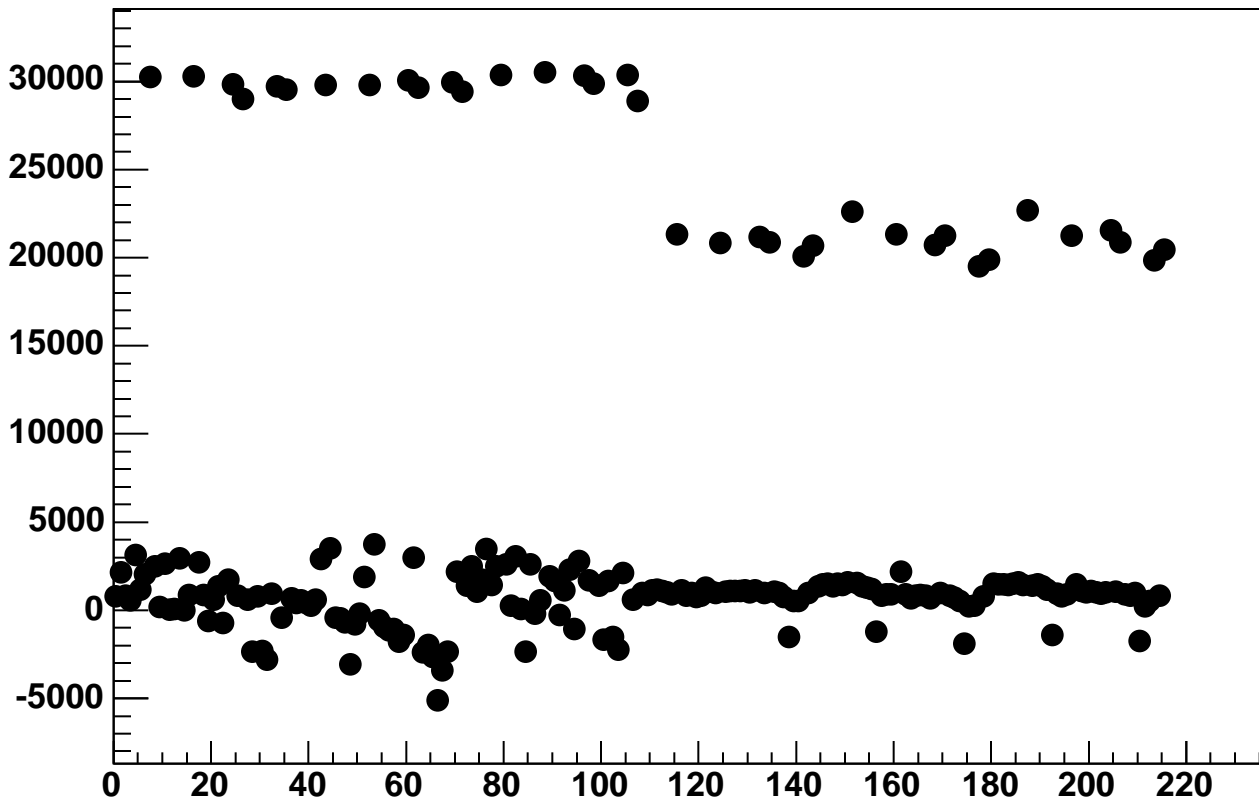
Enable 5, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



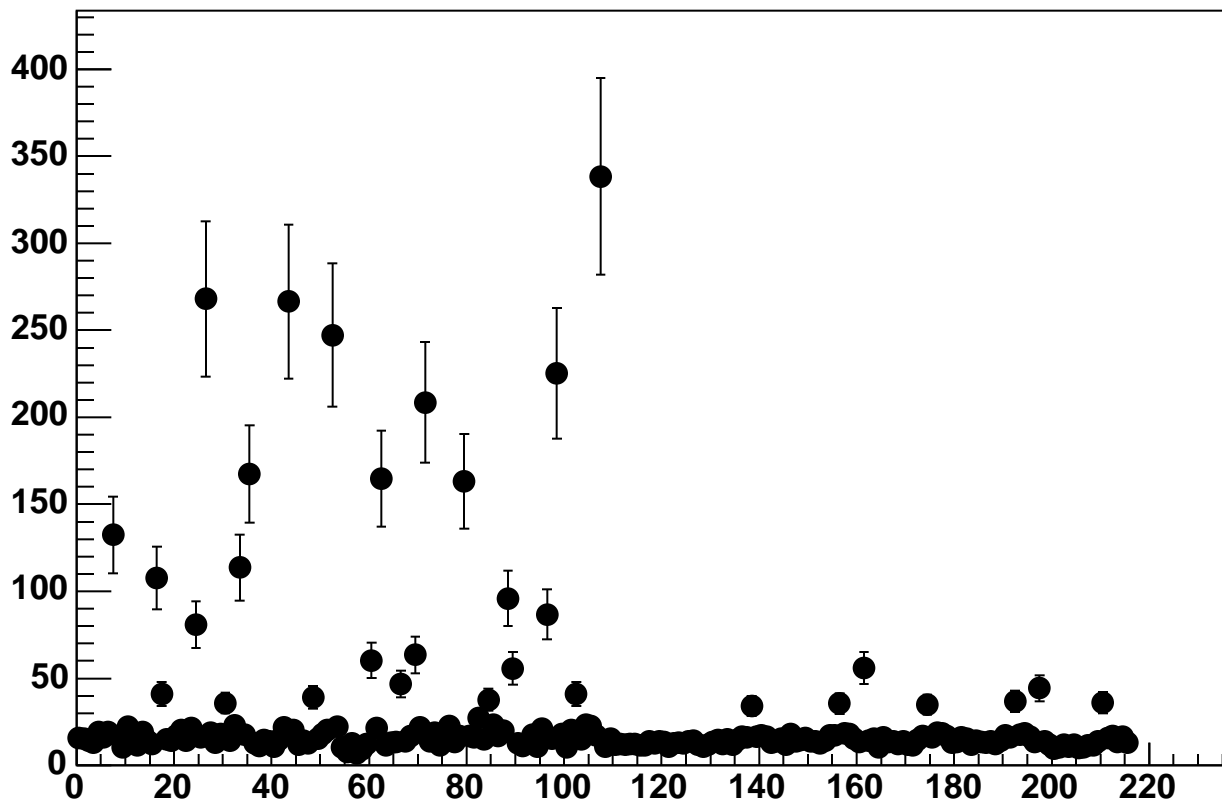
Enable 5, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



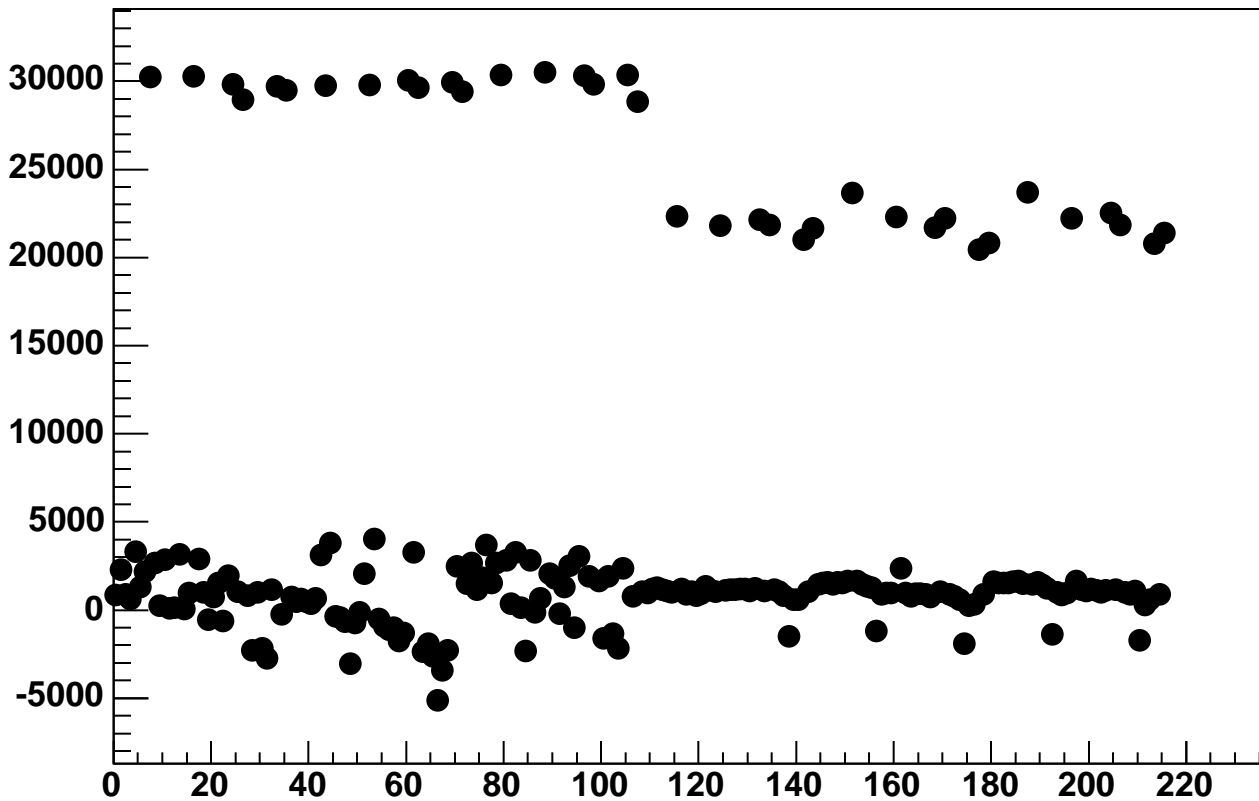
Enable 5, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



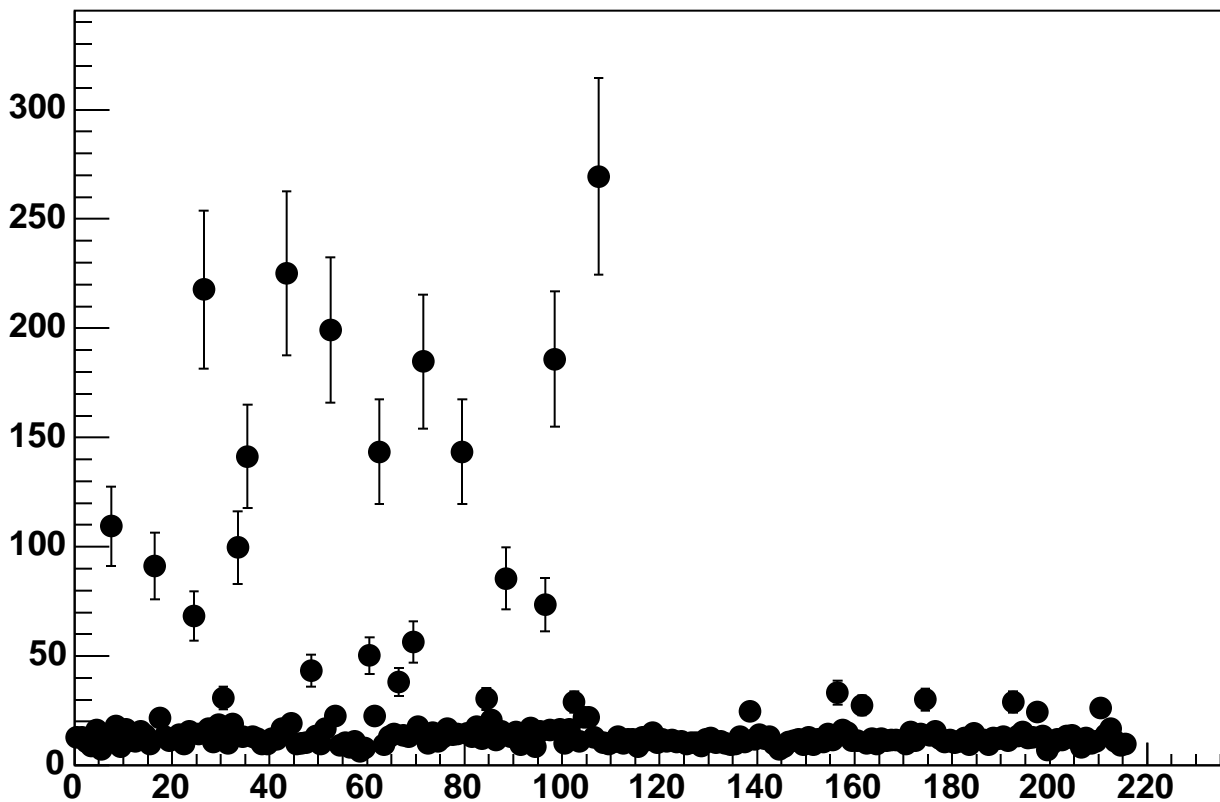
Enable 5, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



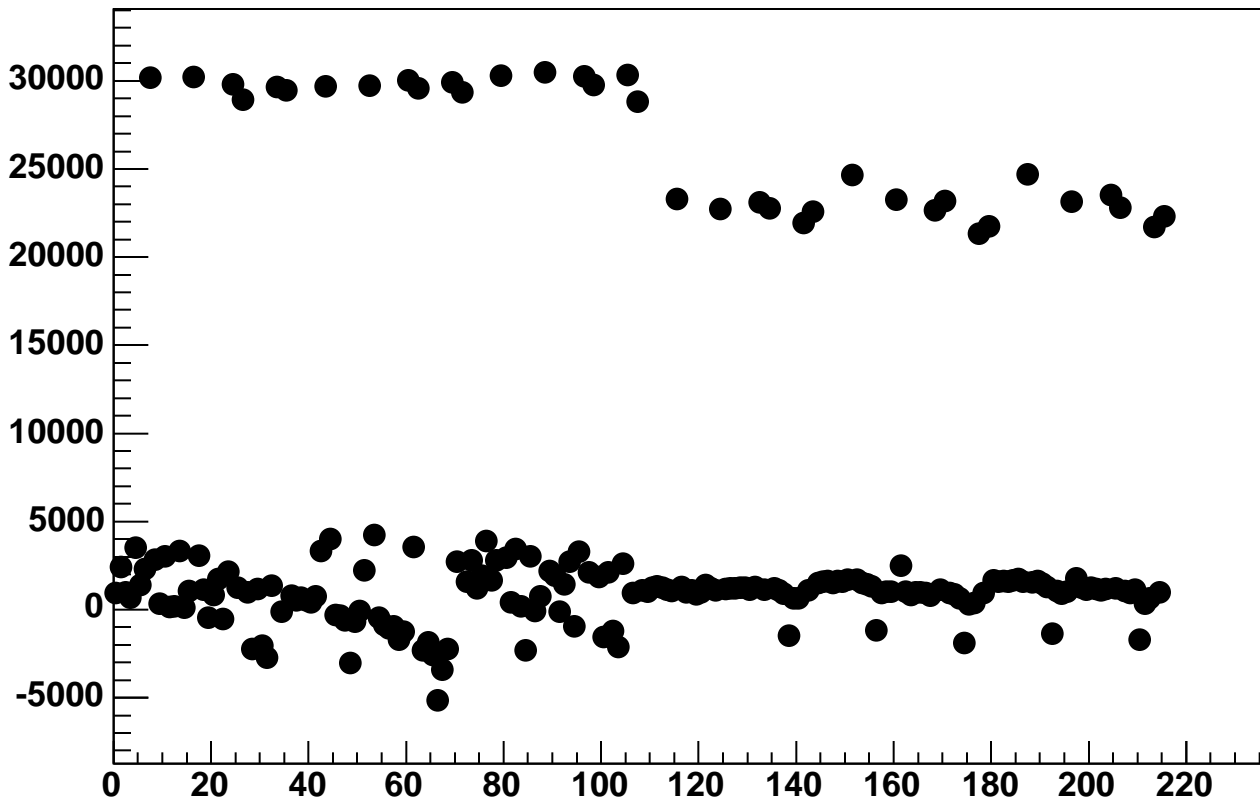
Enable 5, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



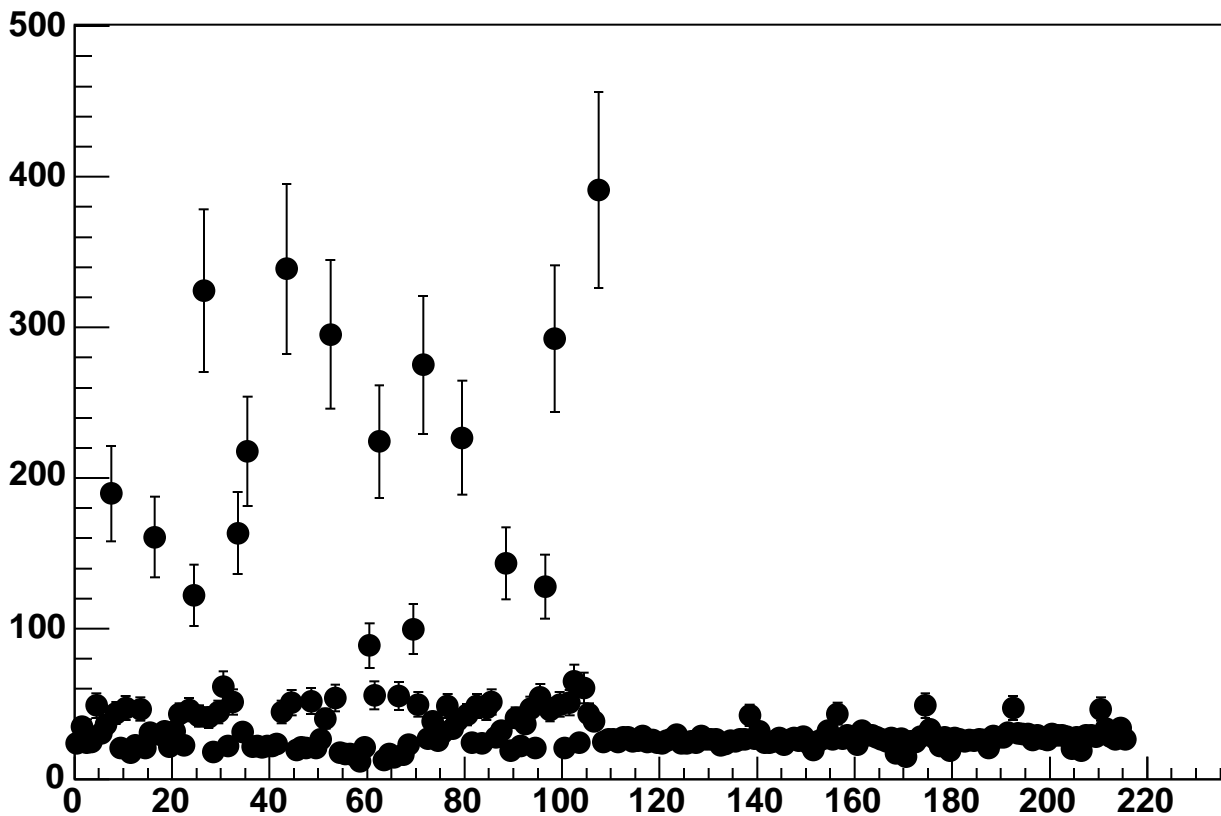
Enable 5, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



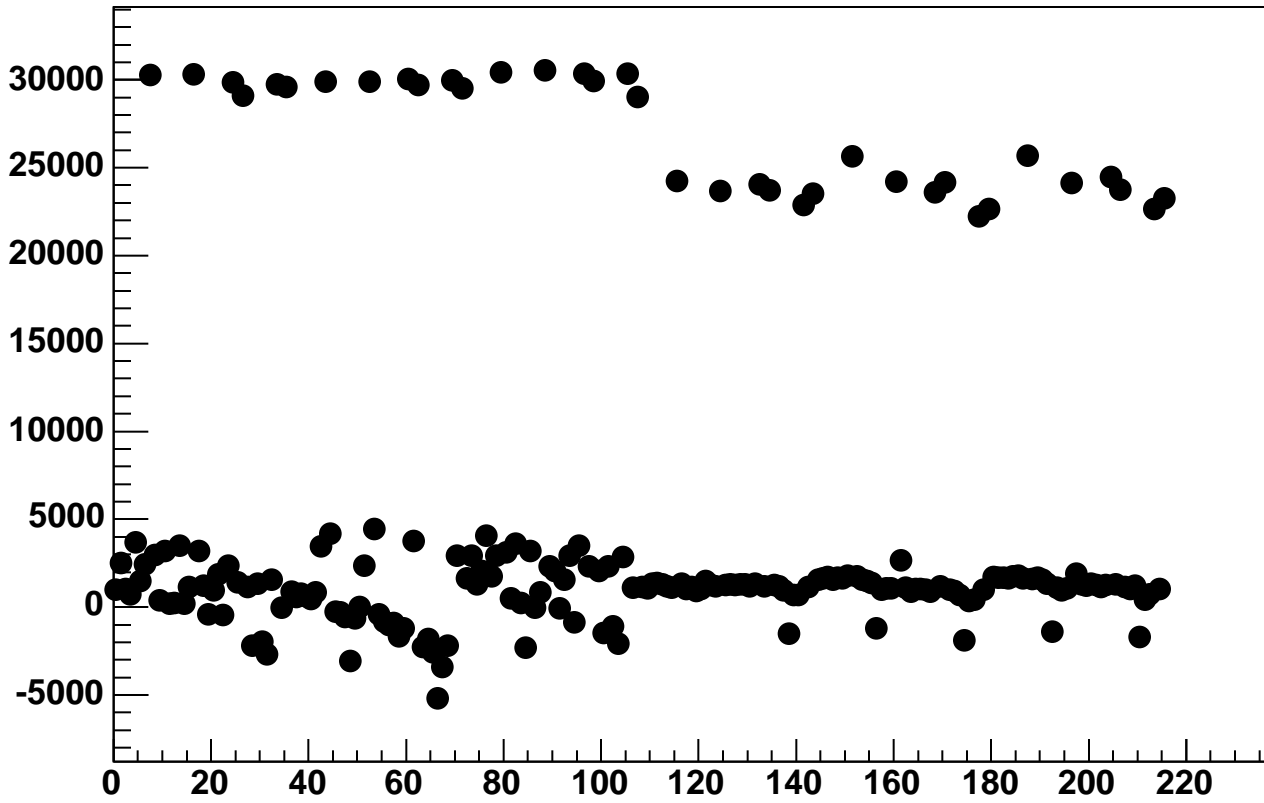
Enable 5, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



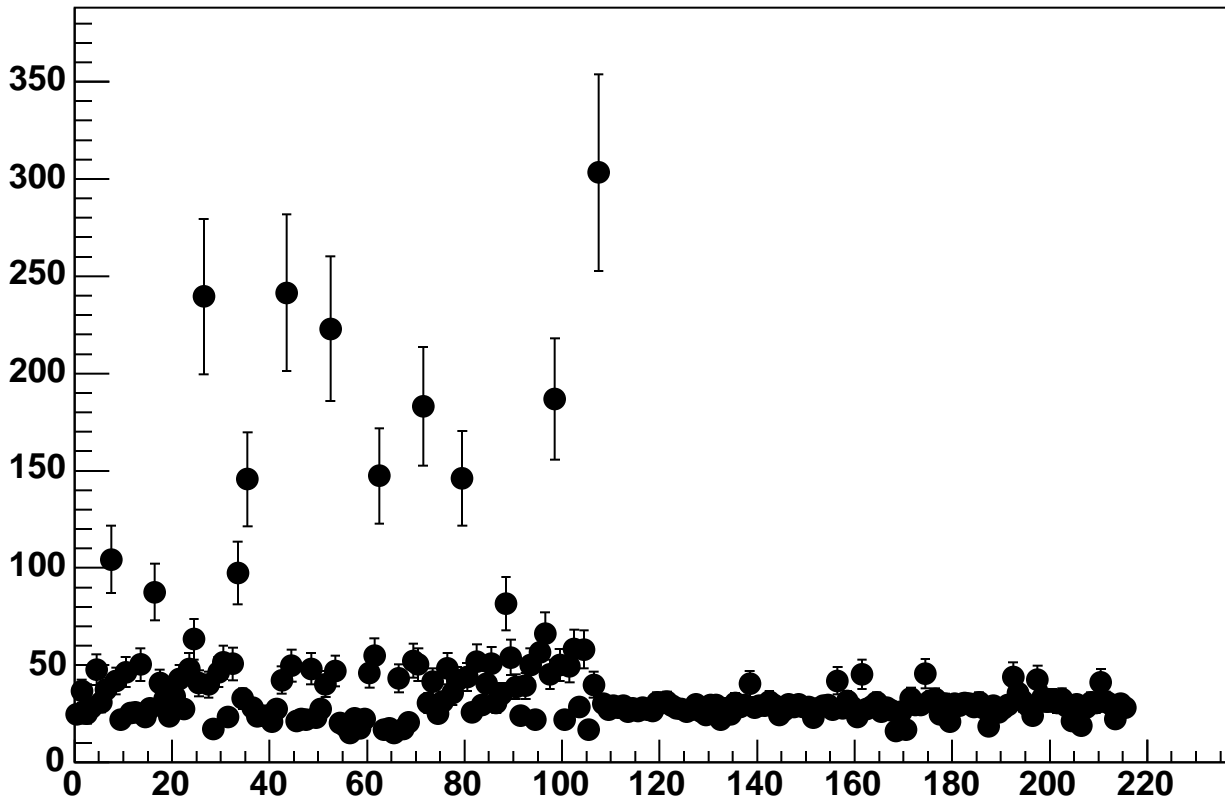
Enable 5, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



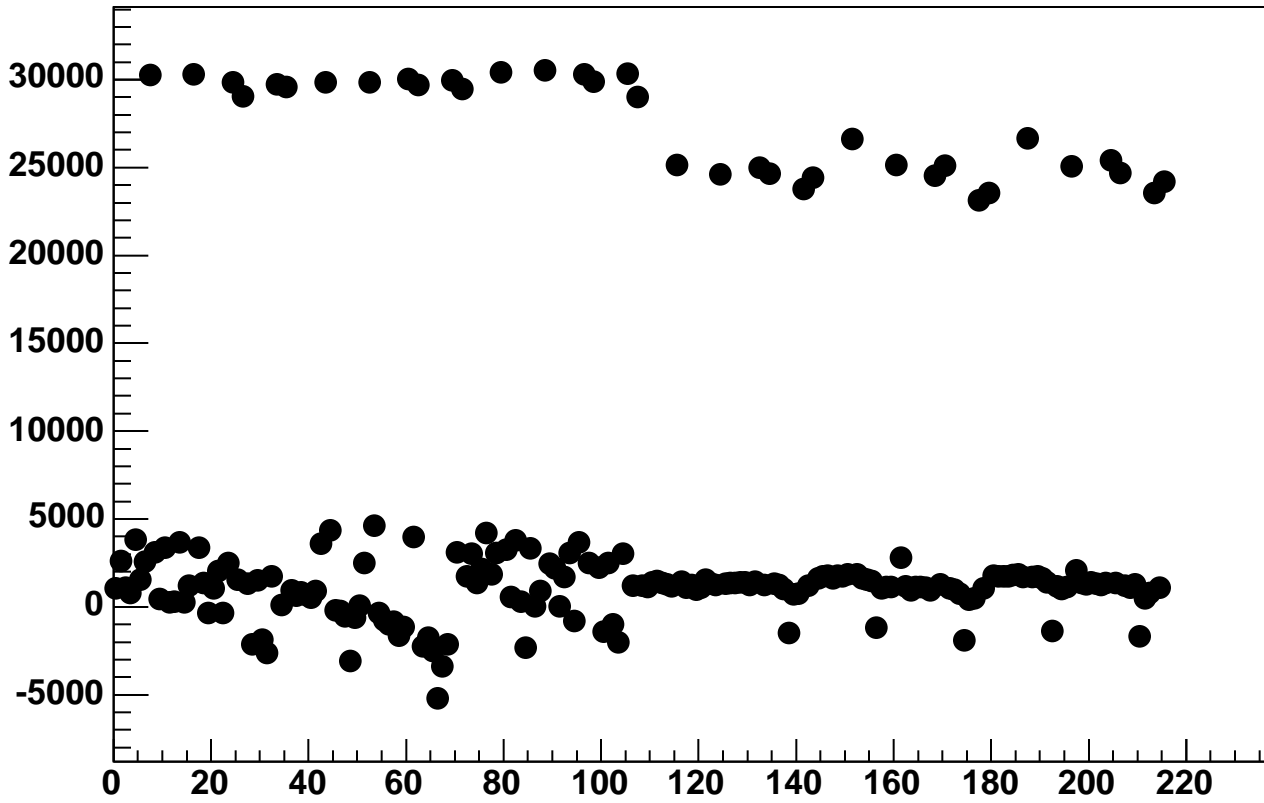
Enable 5, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



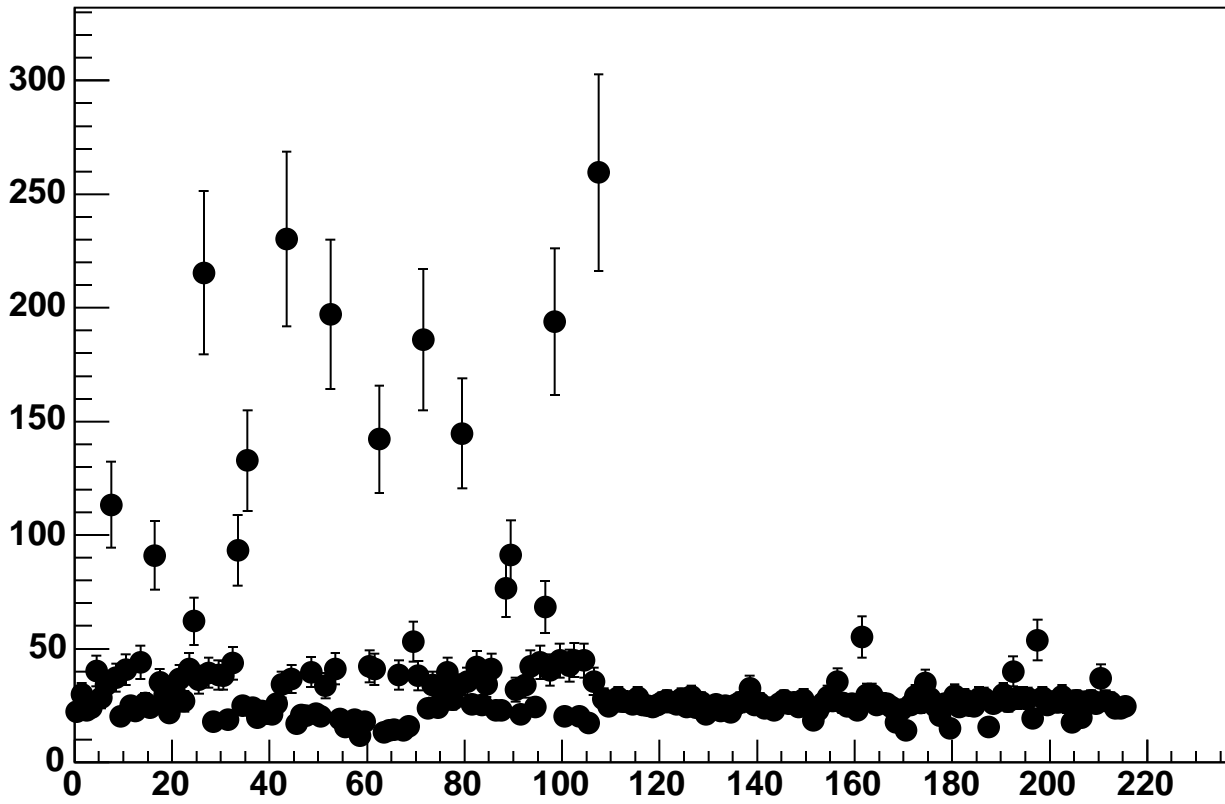
Enable 5, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



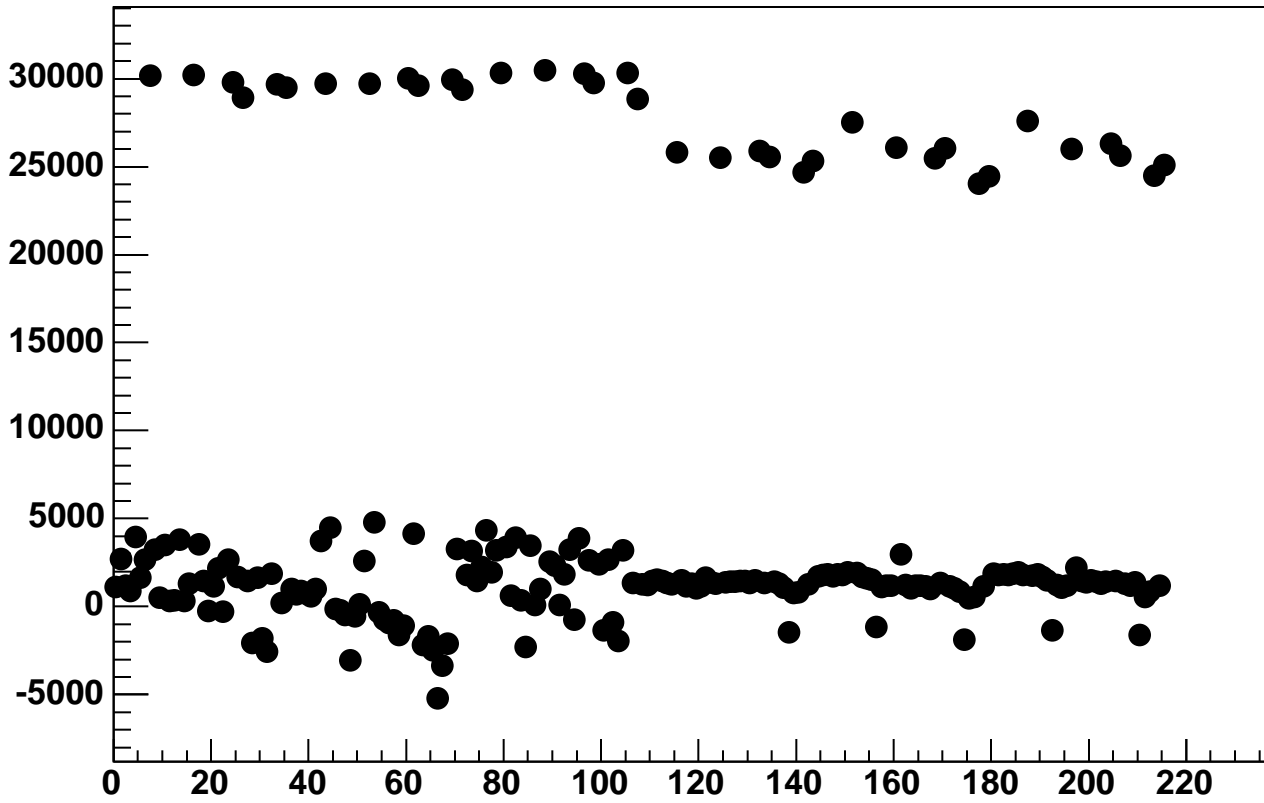
Enable 5, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



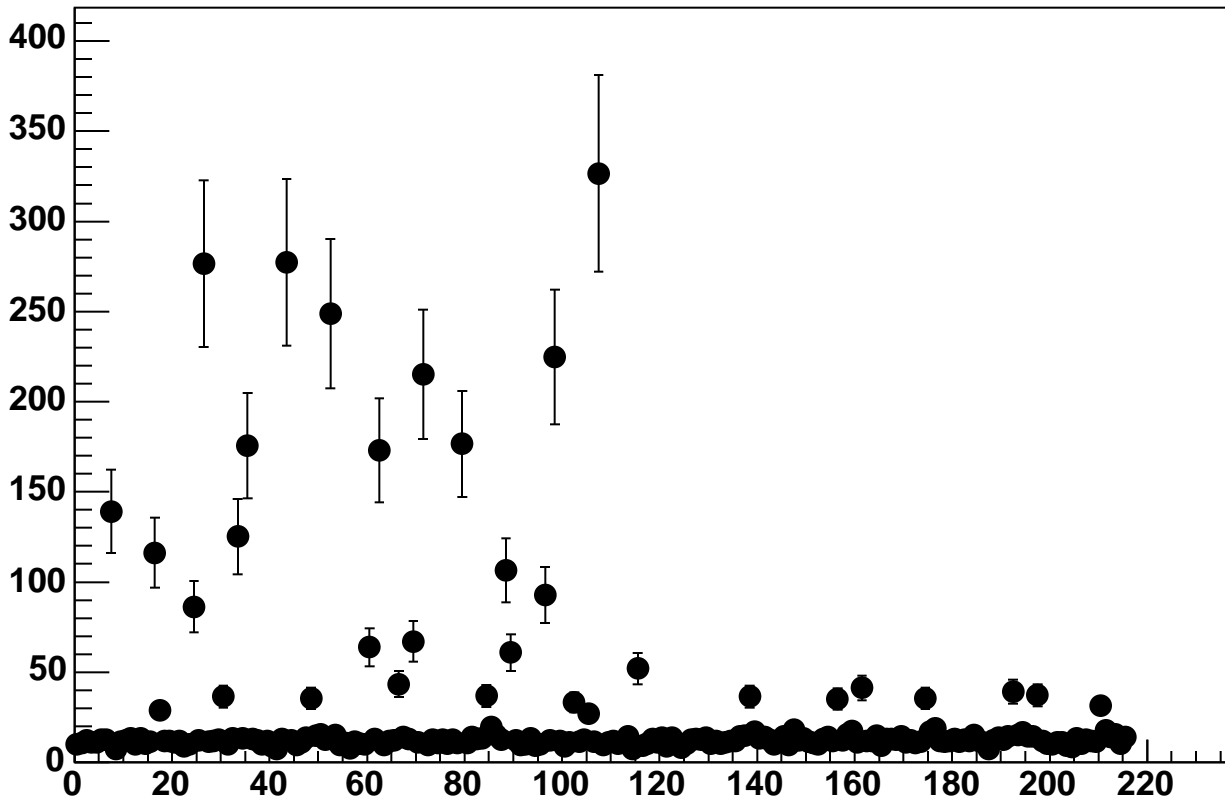
Enable 5, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



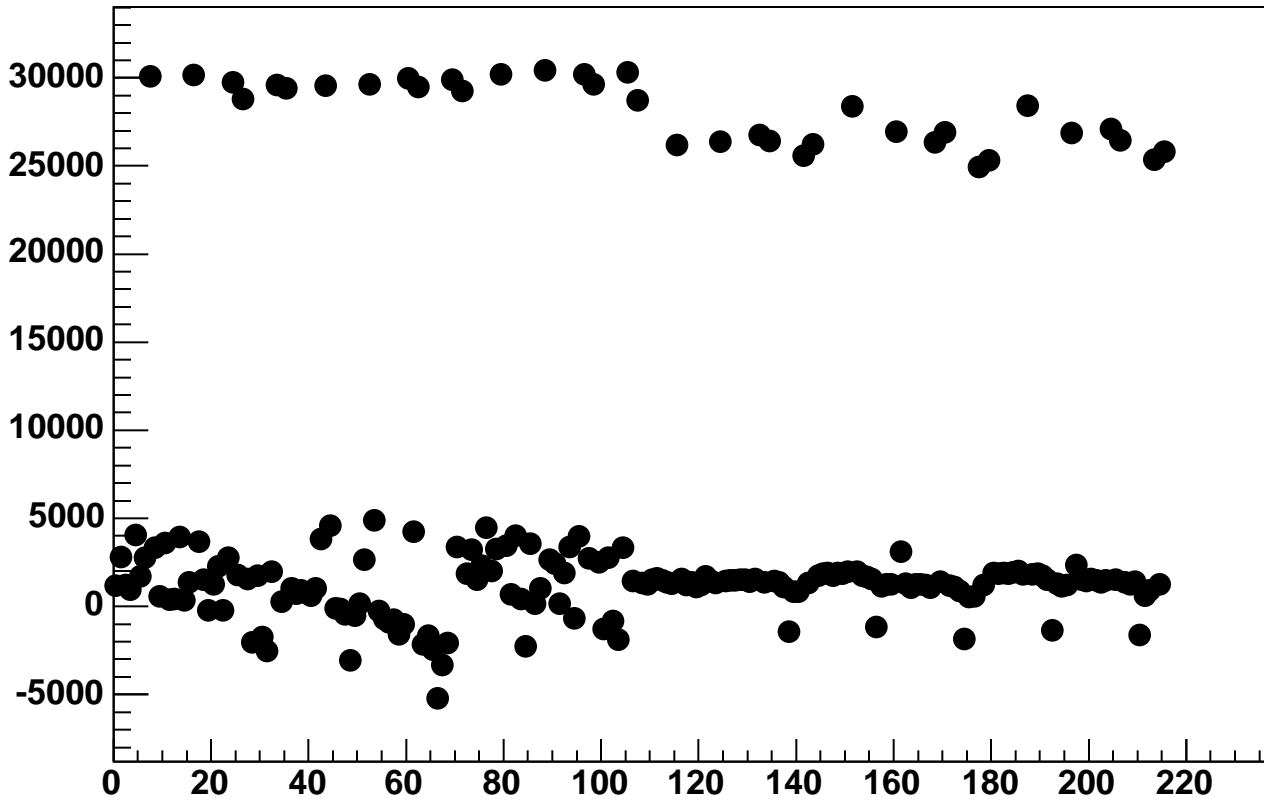
Enable 5, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



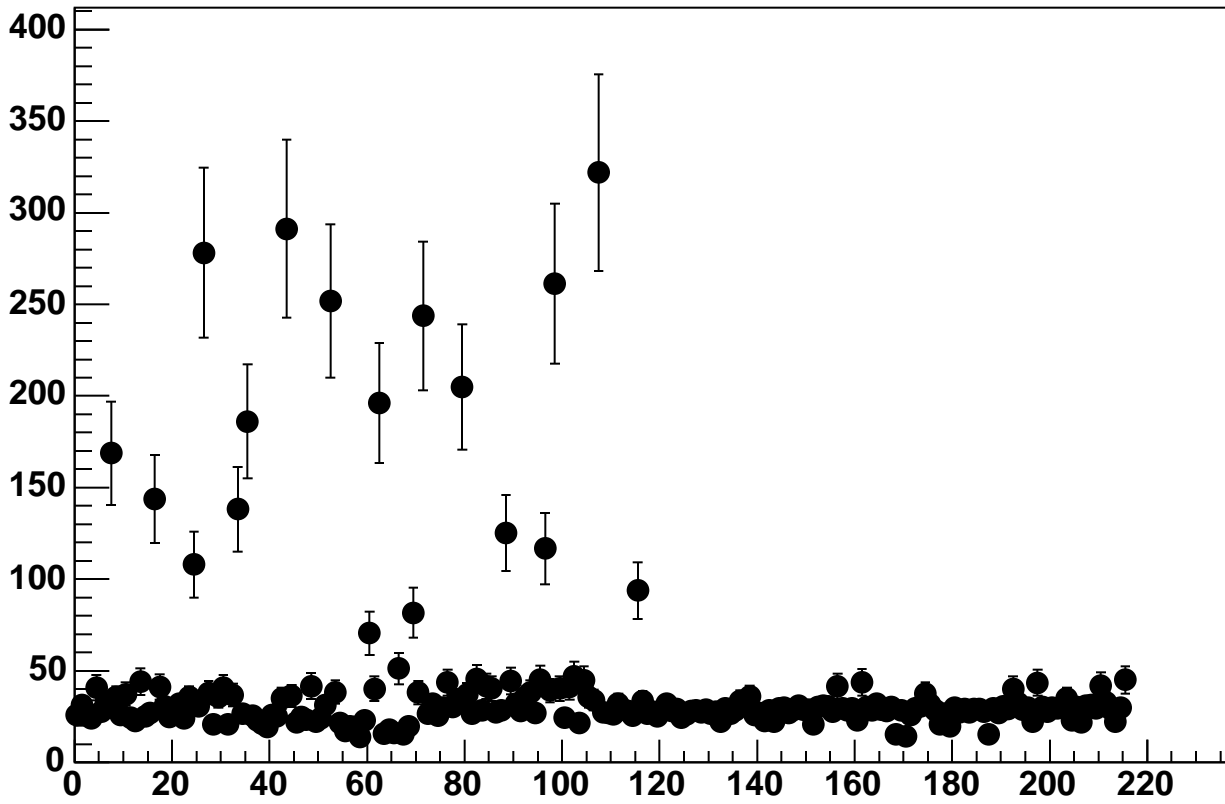
Enable 5, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



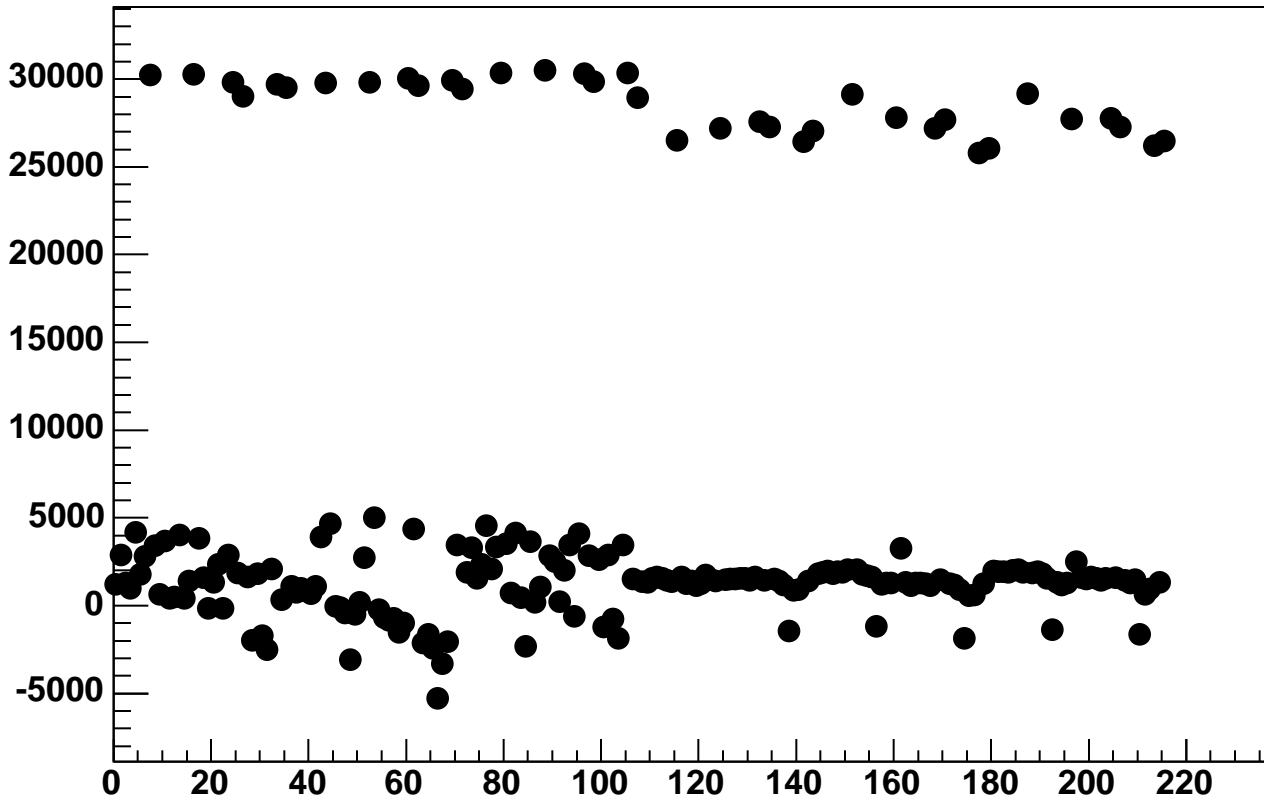
Enable 5, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



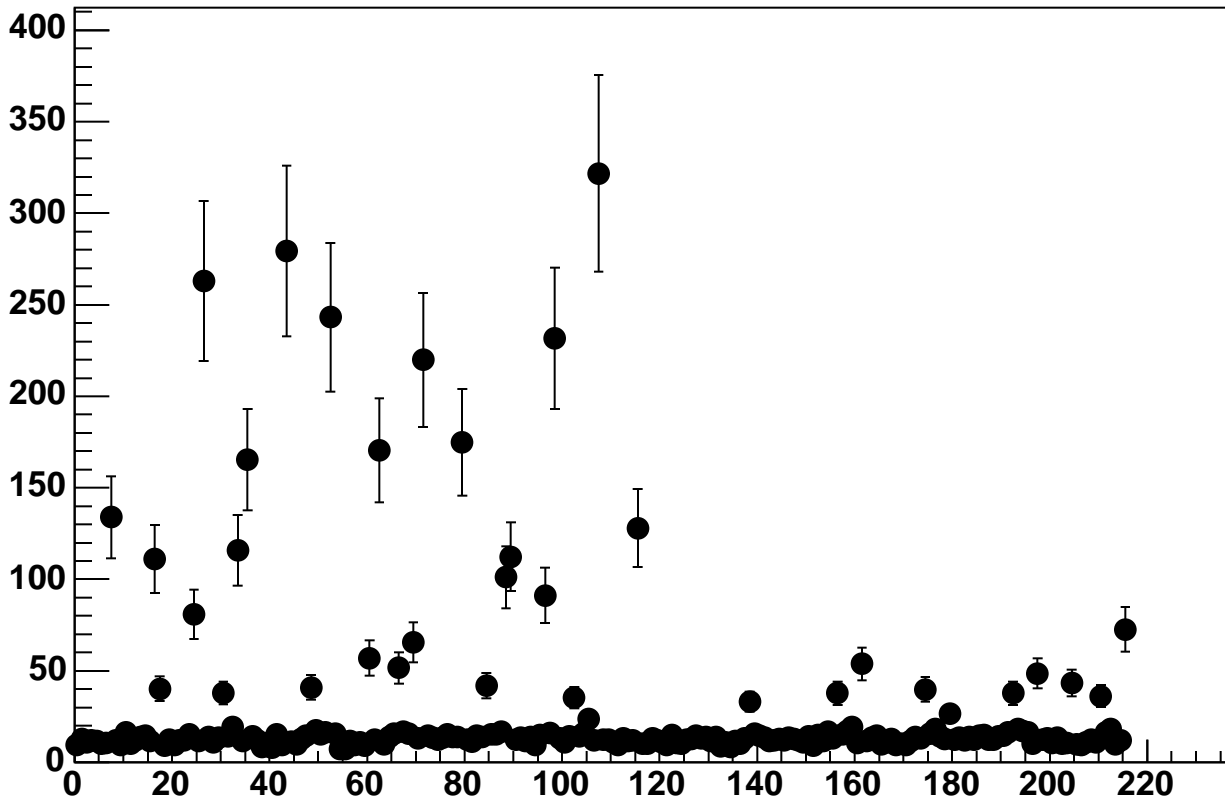
Enable 5, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



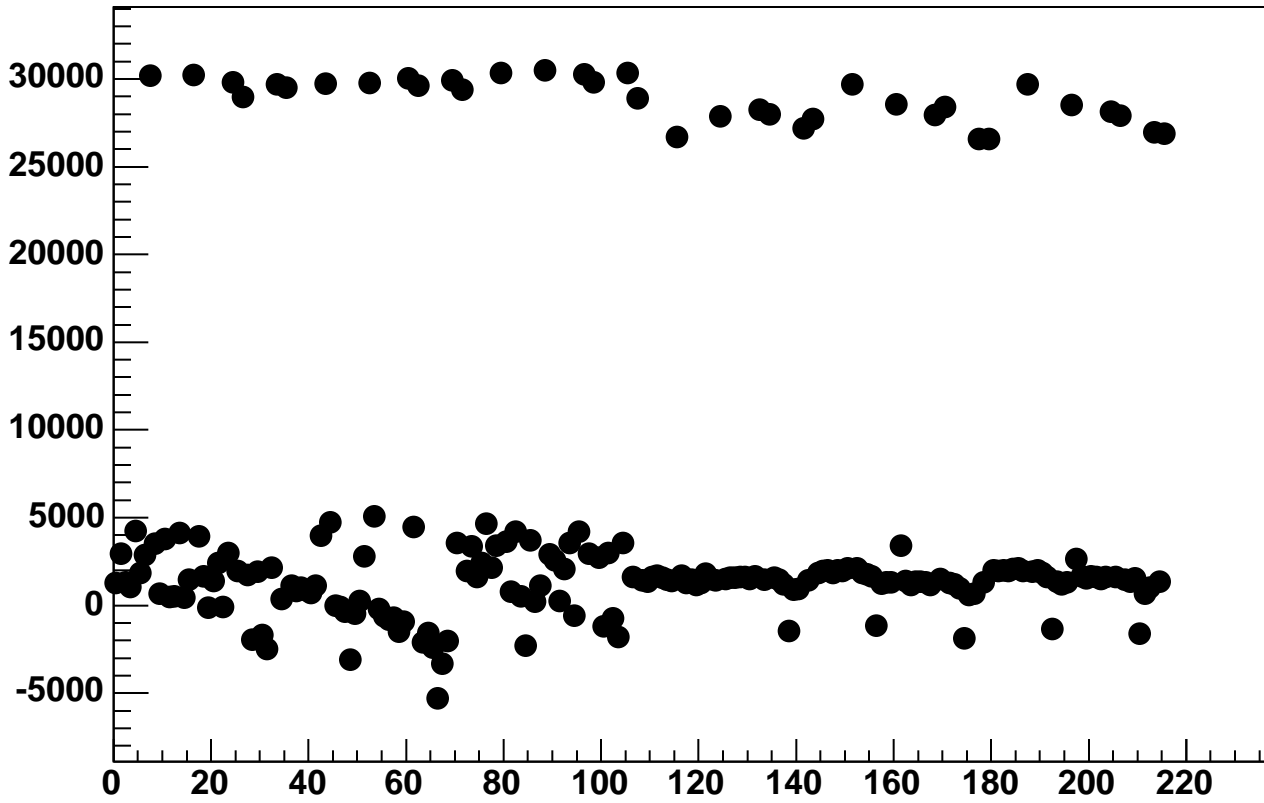
Enable 5, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



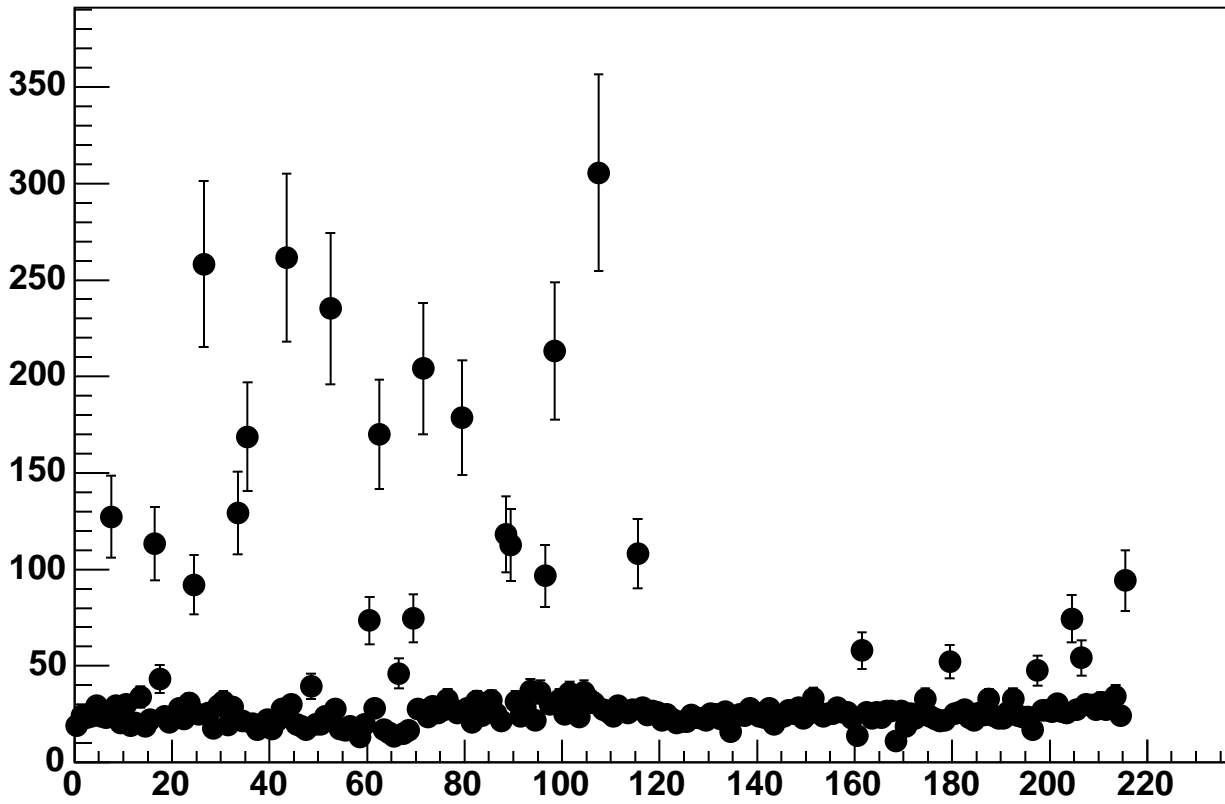
Enable 5, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



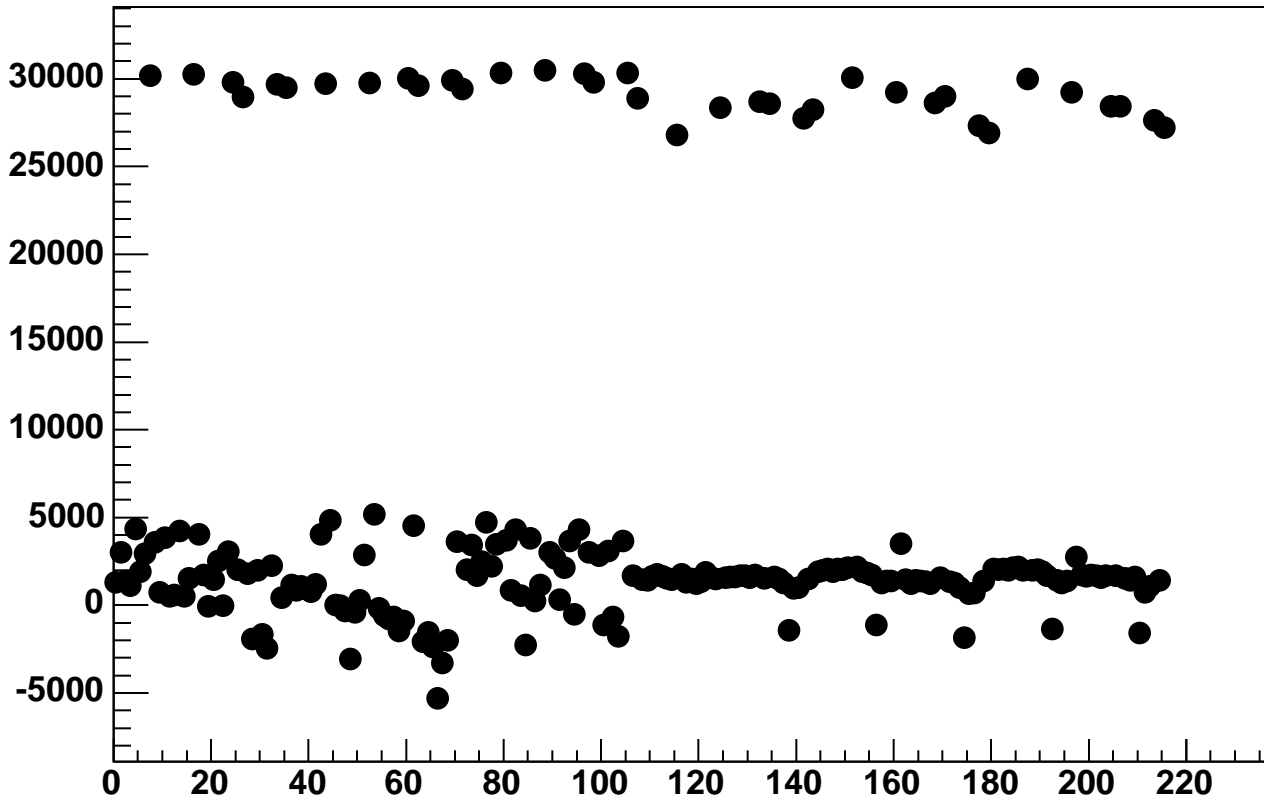
Enable 5, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



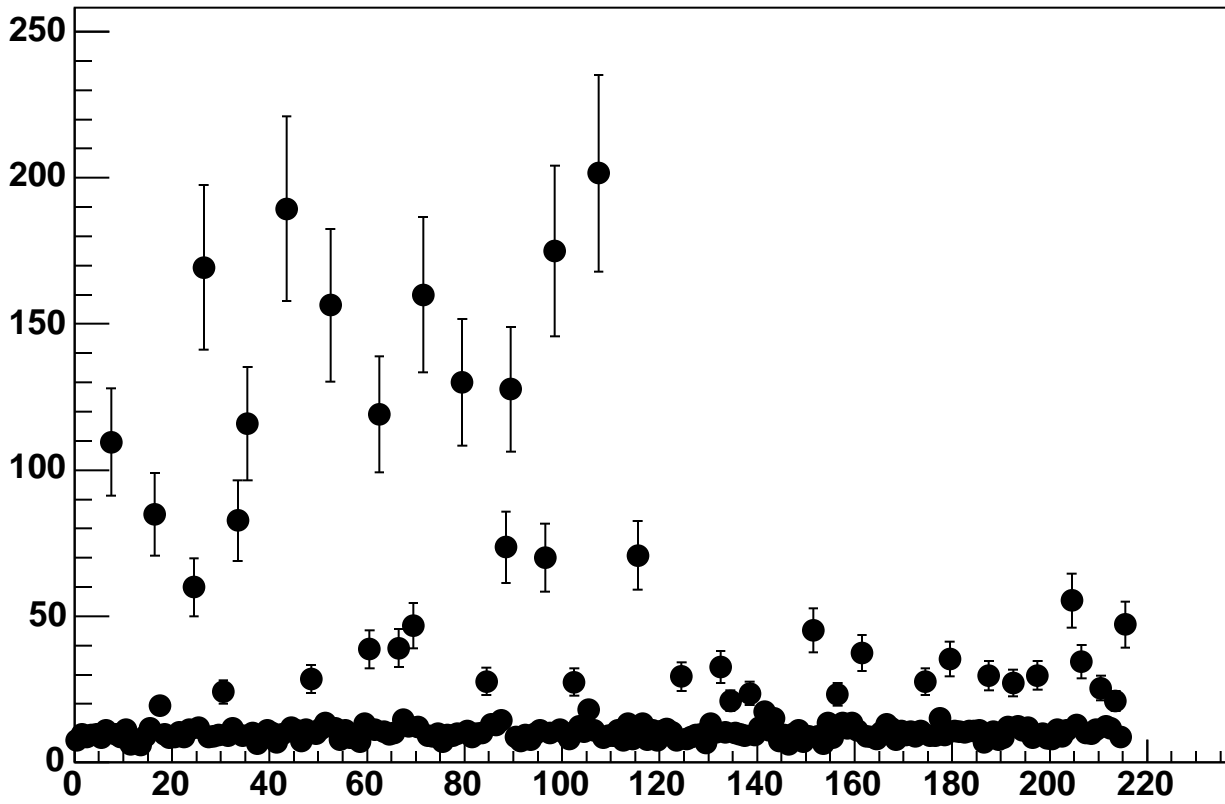
Enable 5, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



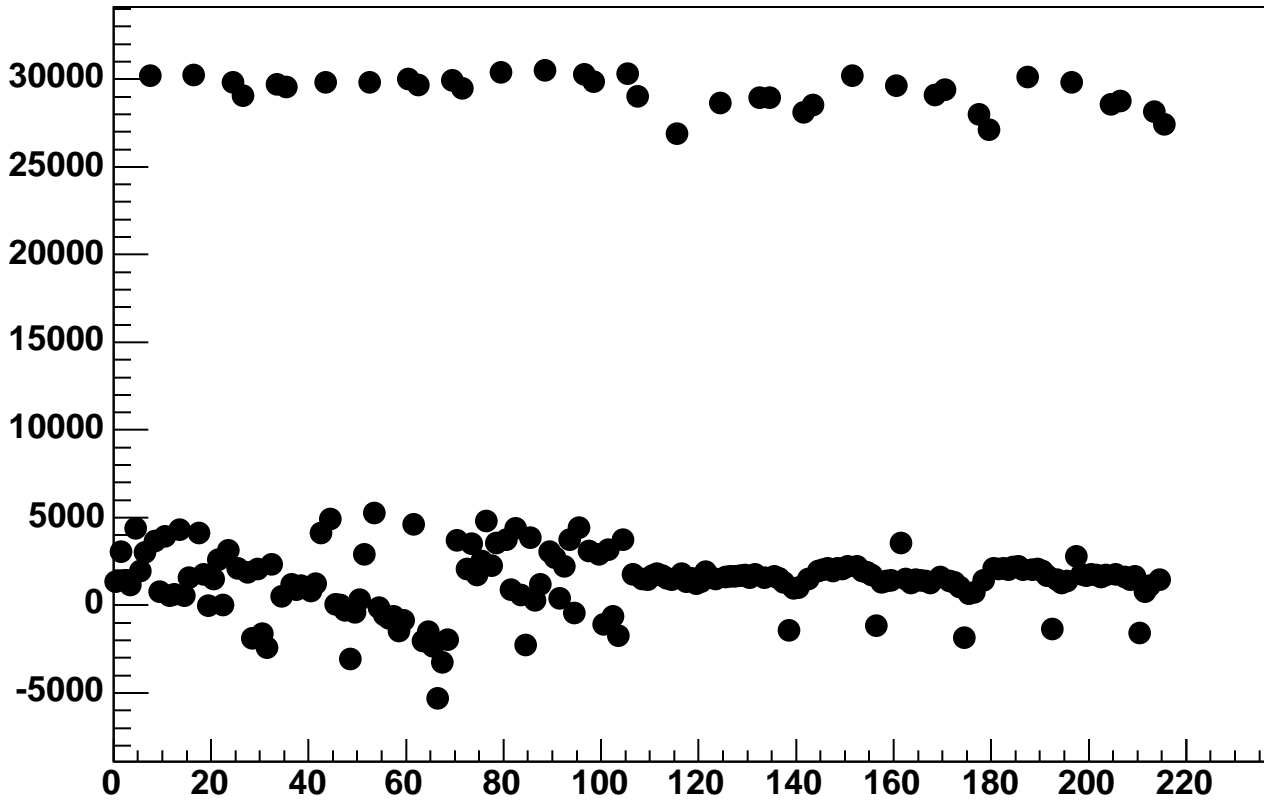
Enable 5, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



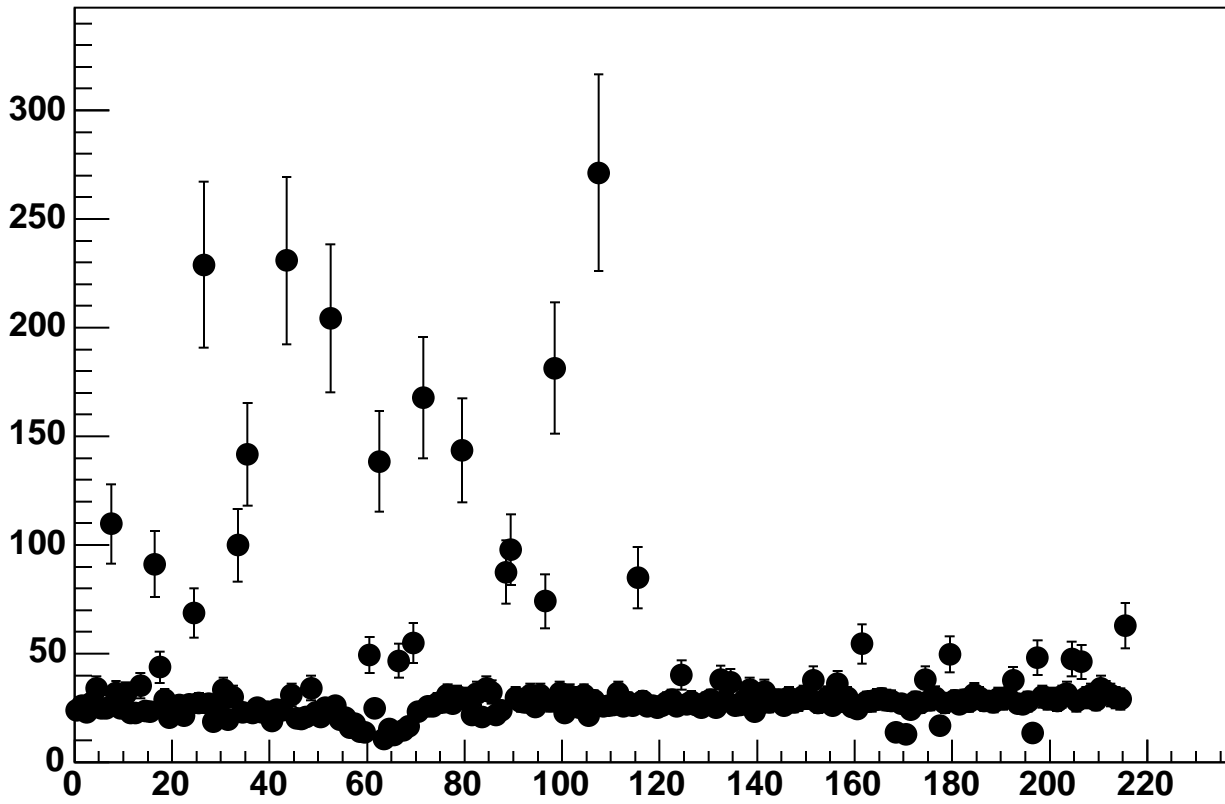
Enable 5, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



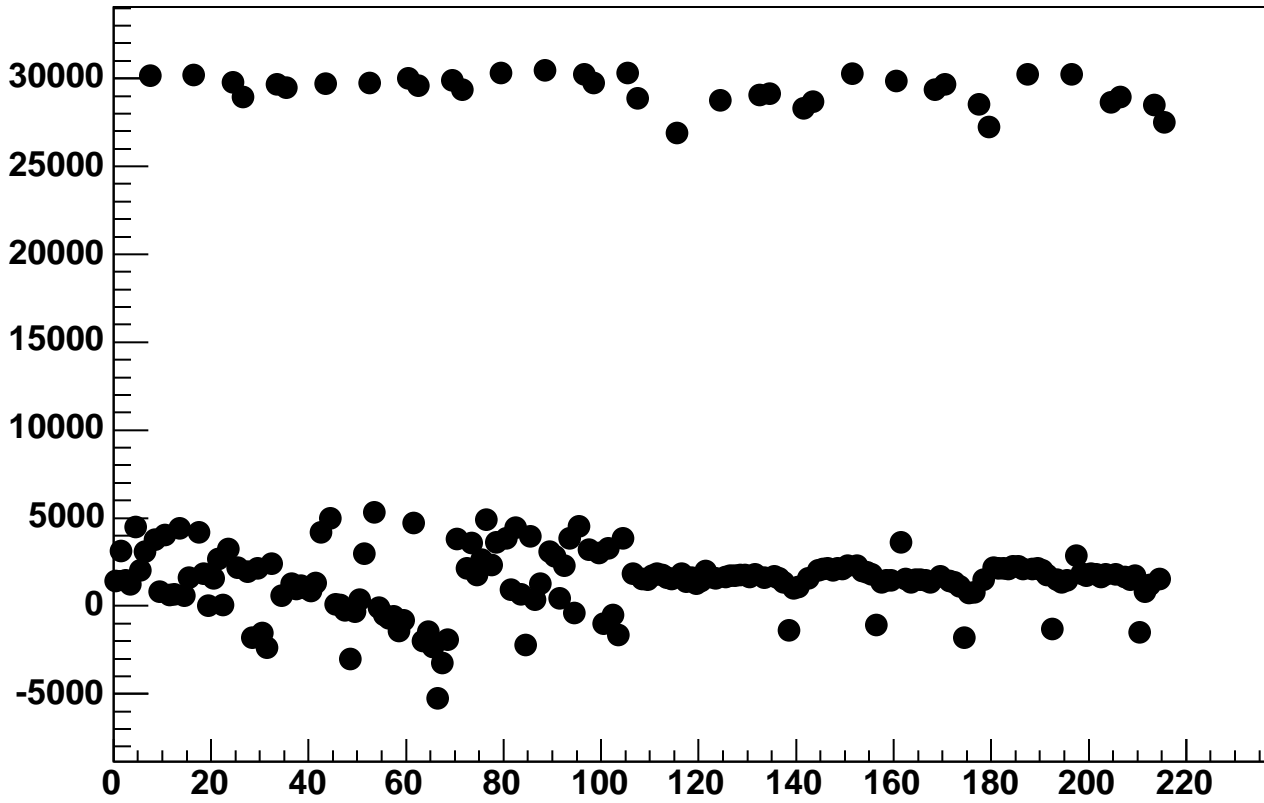
Enable 5, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



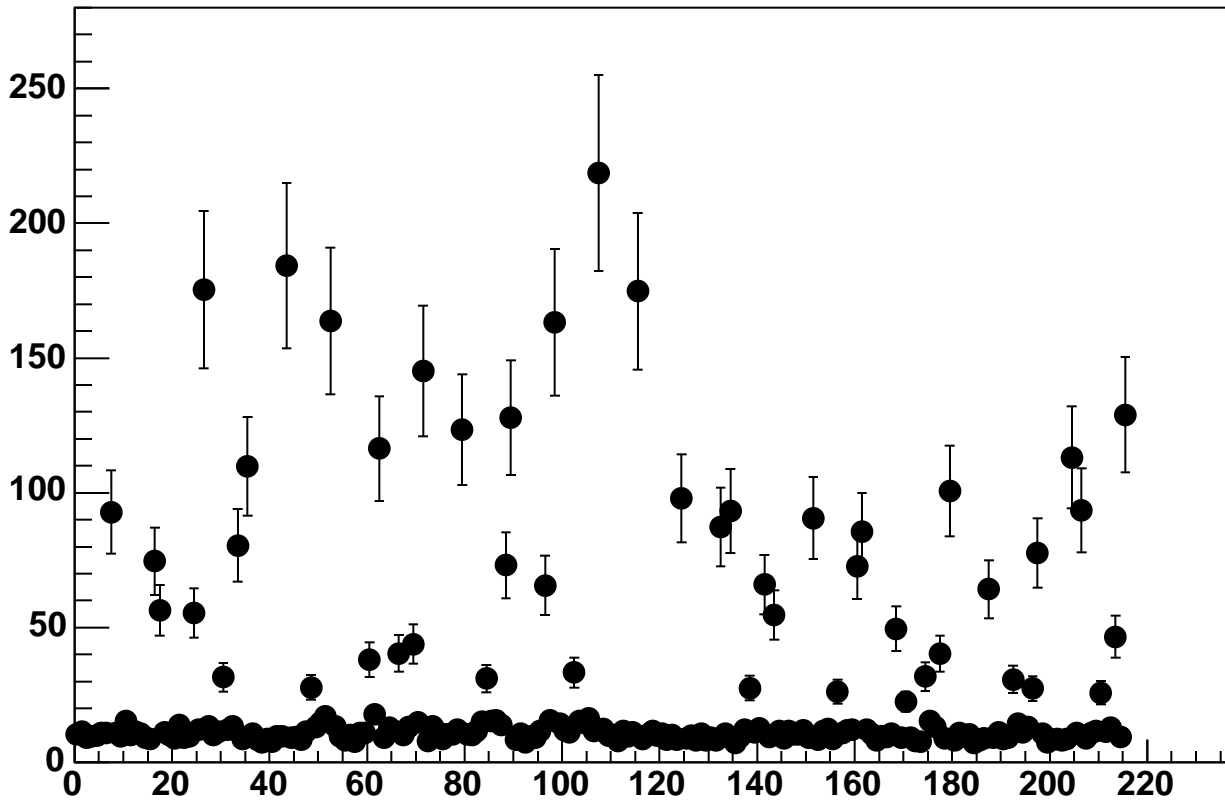
Enable 5, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



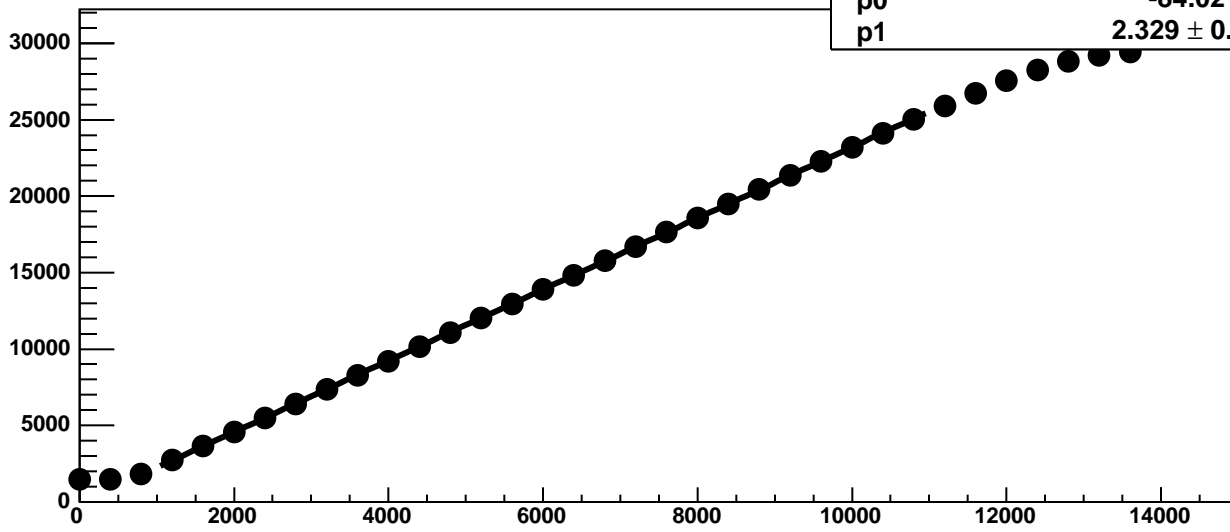
Enable 5, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



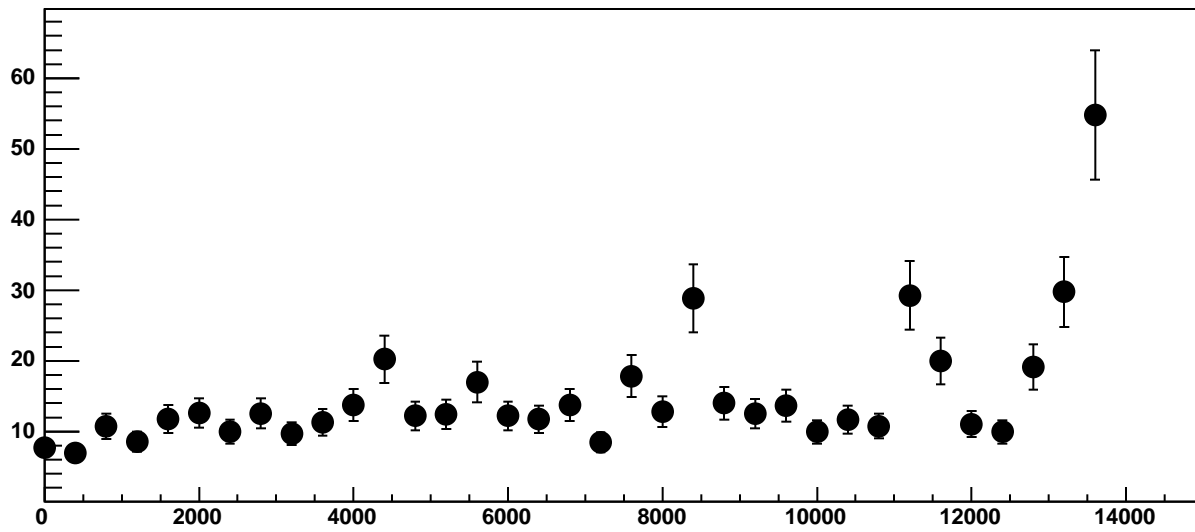
Enable 5, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



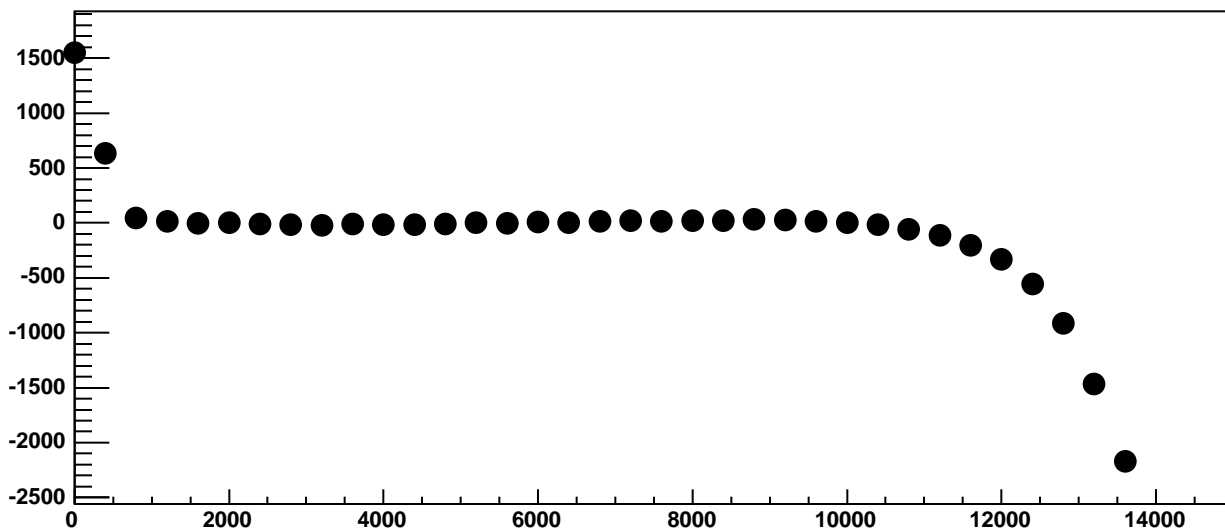
Chip 0, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



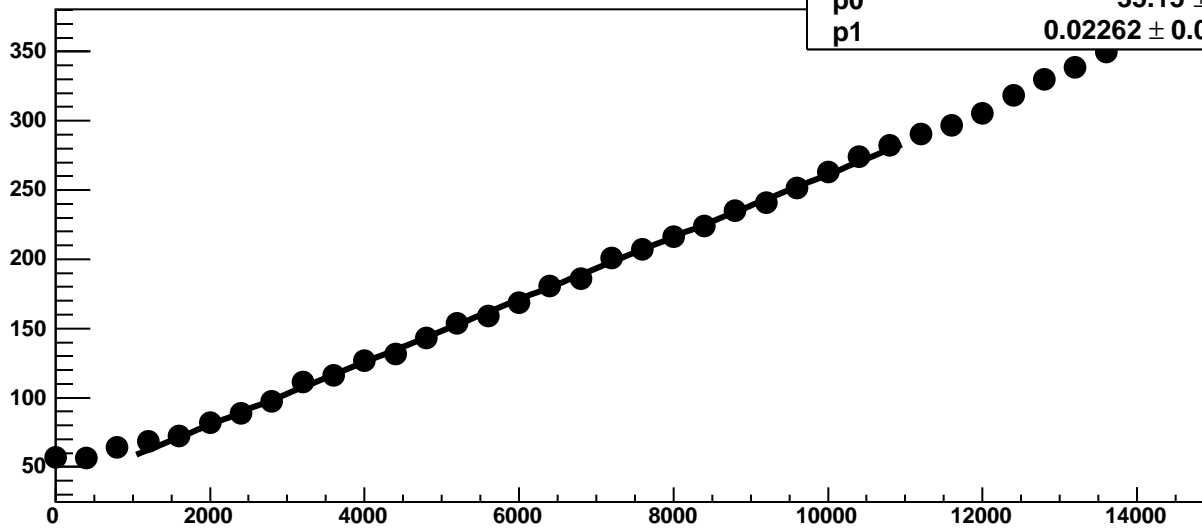
Chip 0, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

39.69 / 23

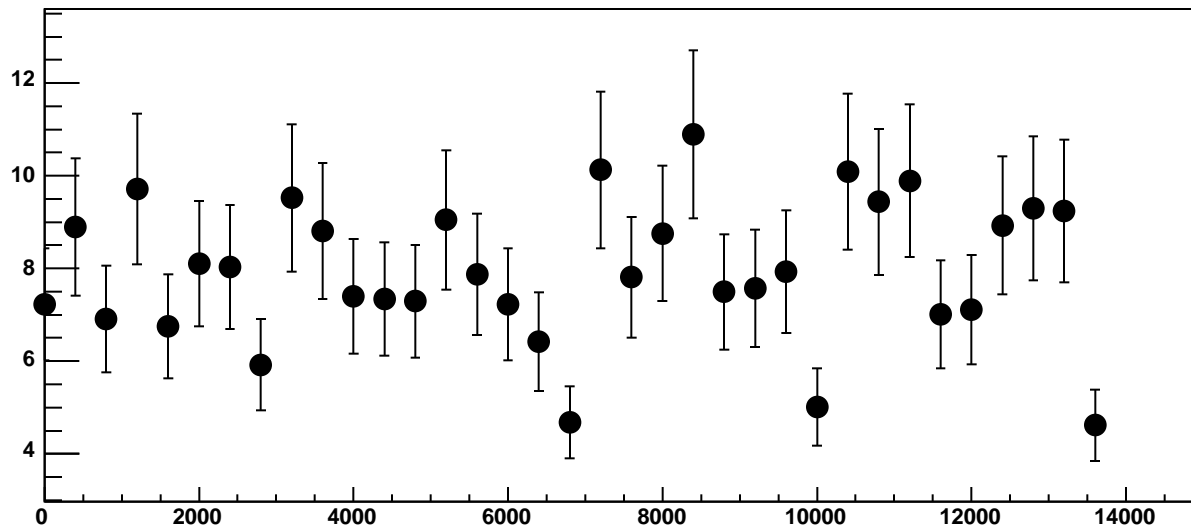
p0

35.15 ± 0.8163

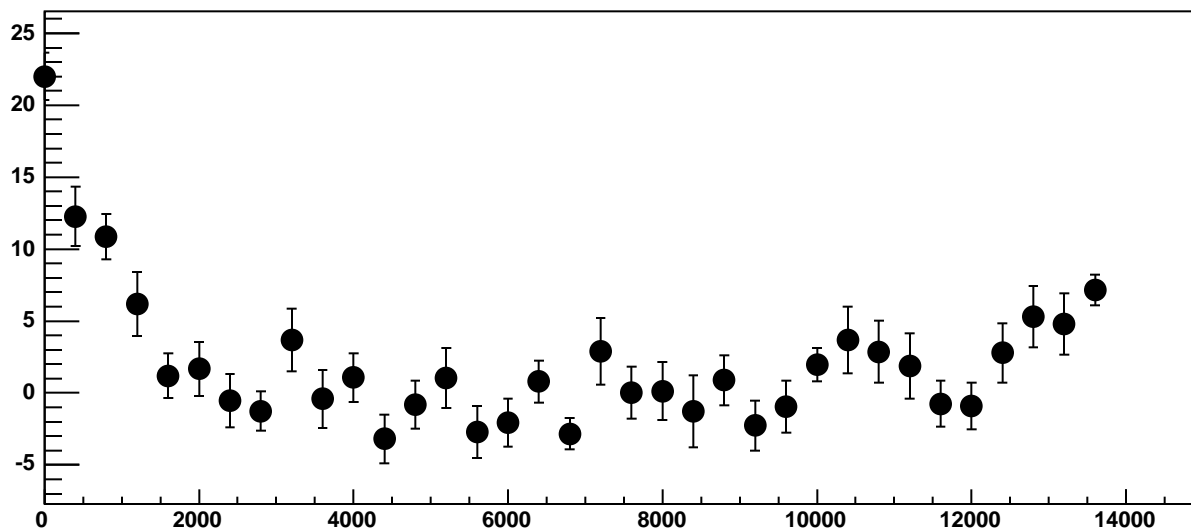
p1

0.02262 ± 0.0001222

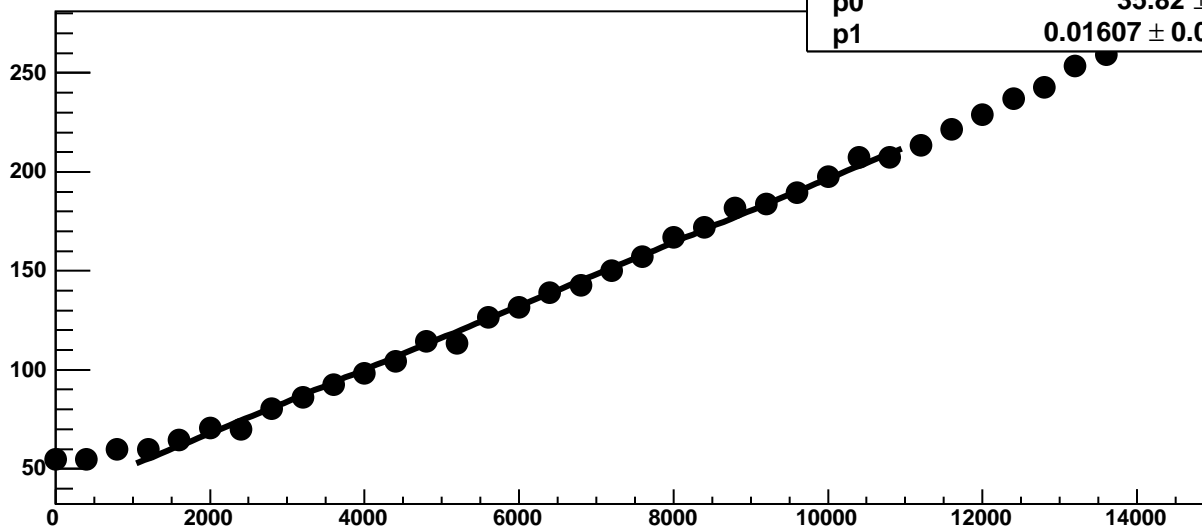
Chip 0, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

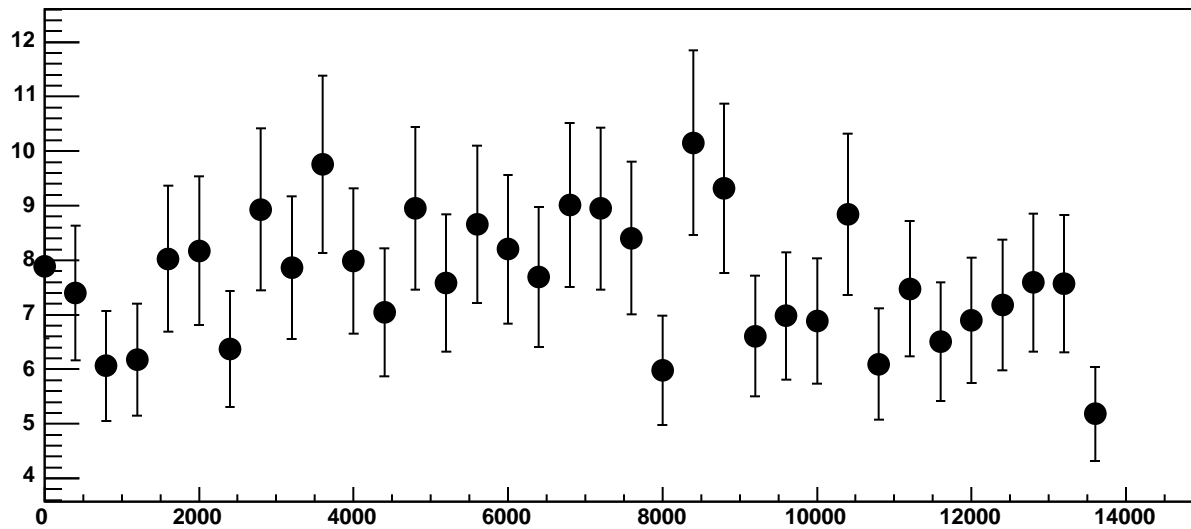


Chip 0, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

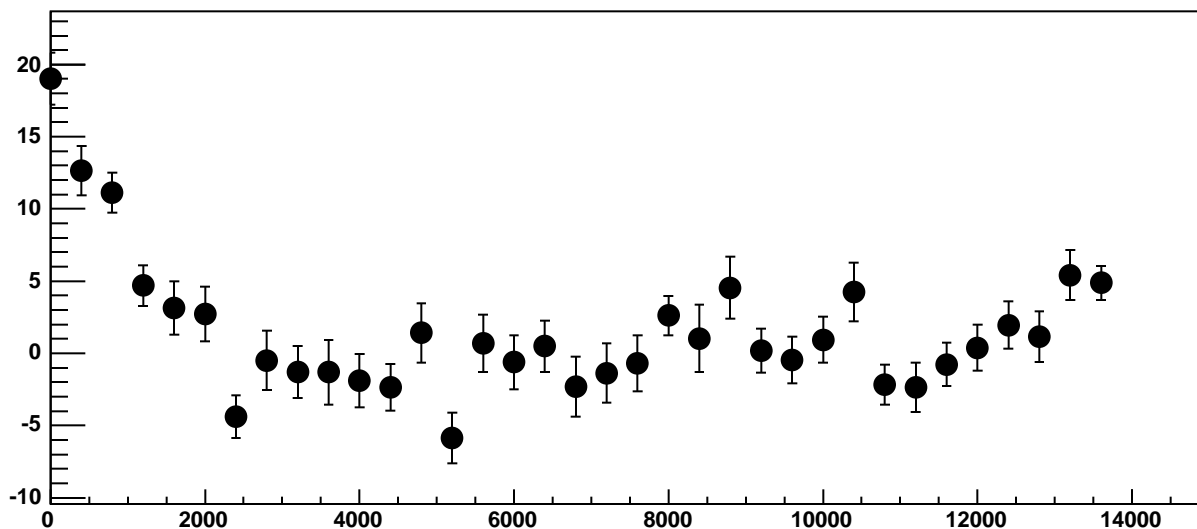


χ^2 / ndf 58.68 / 23
p0 35.82 ± 0.7849
p1 0.01607 ± 0.0001155

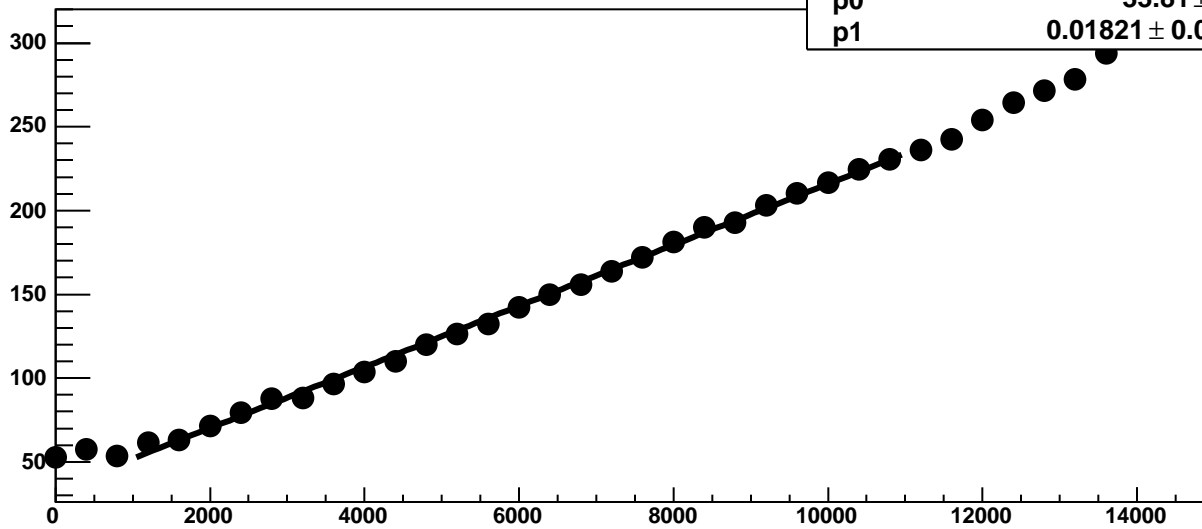
Chip 0, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

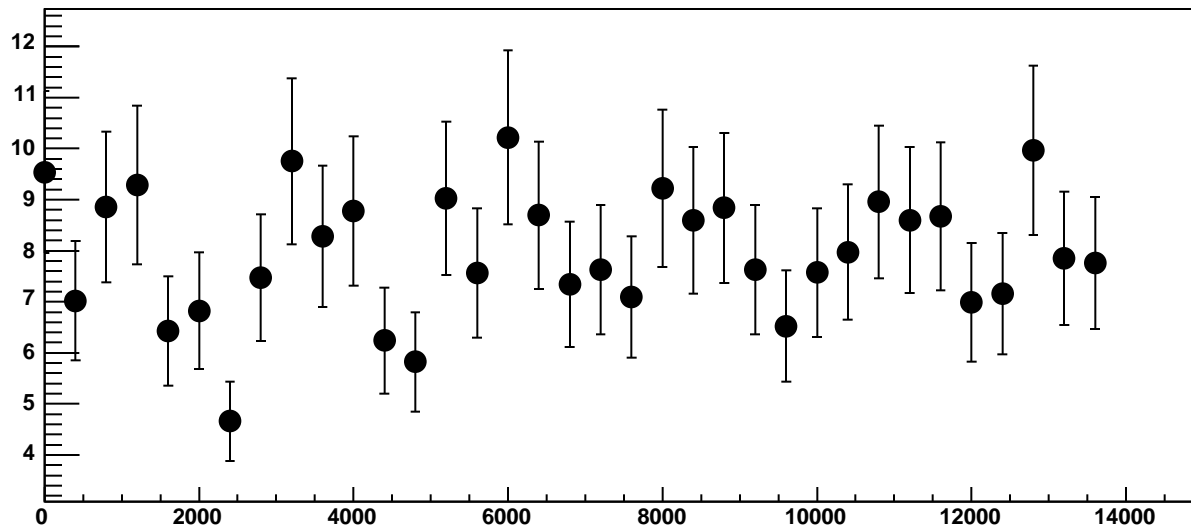


Chip 0, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

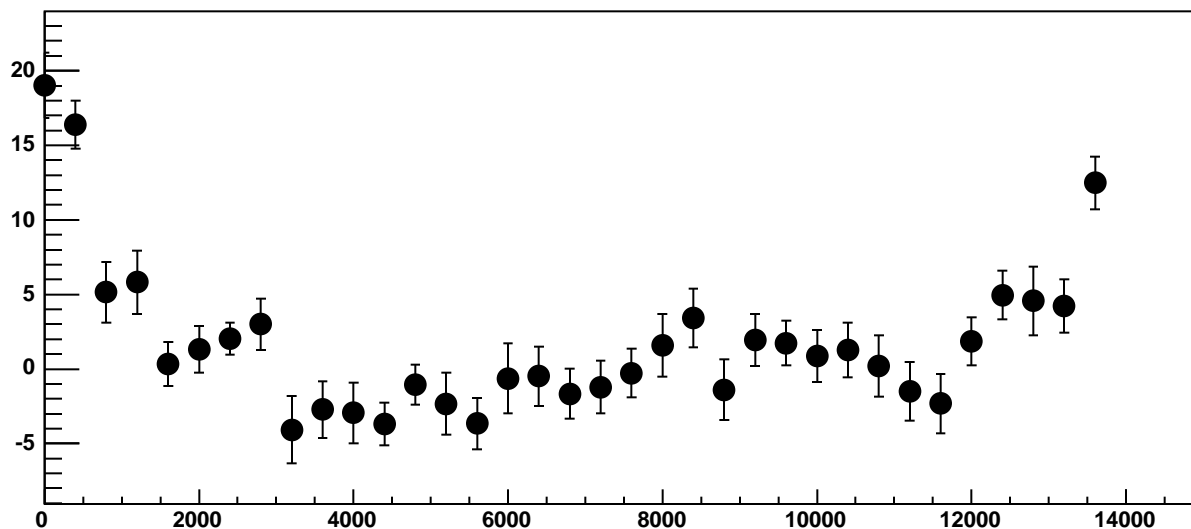


χ^2 / ndf 44.33 / 23
p0 33.81 ± 0.7484
p1 0.01821 ± 0.0001175

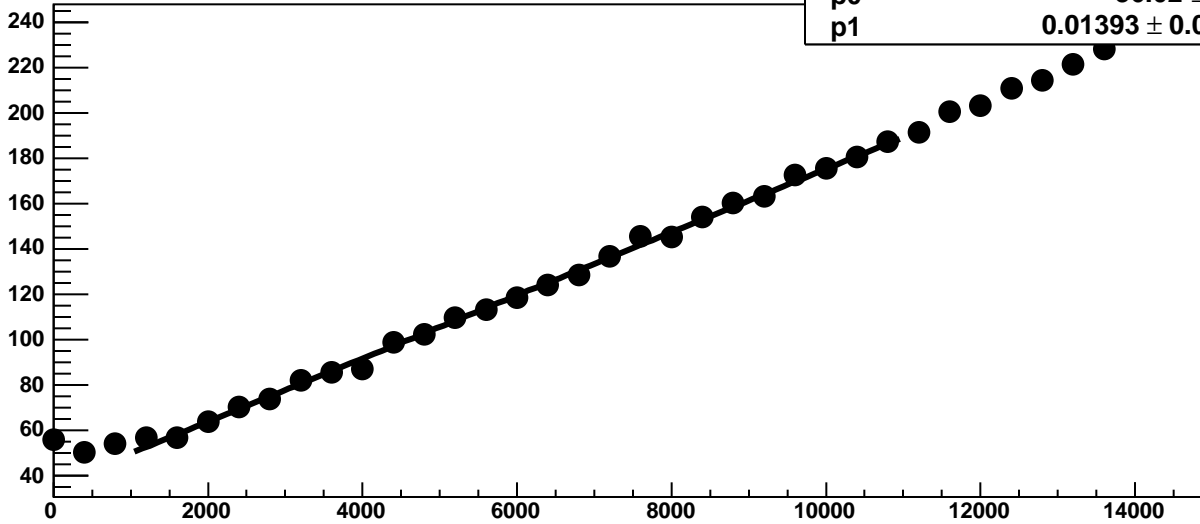
Chip 0, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC

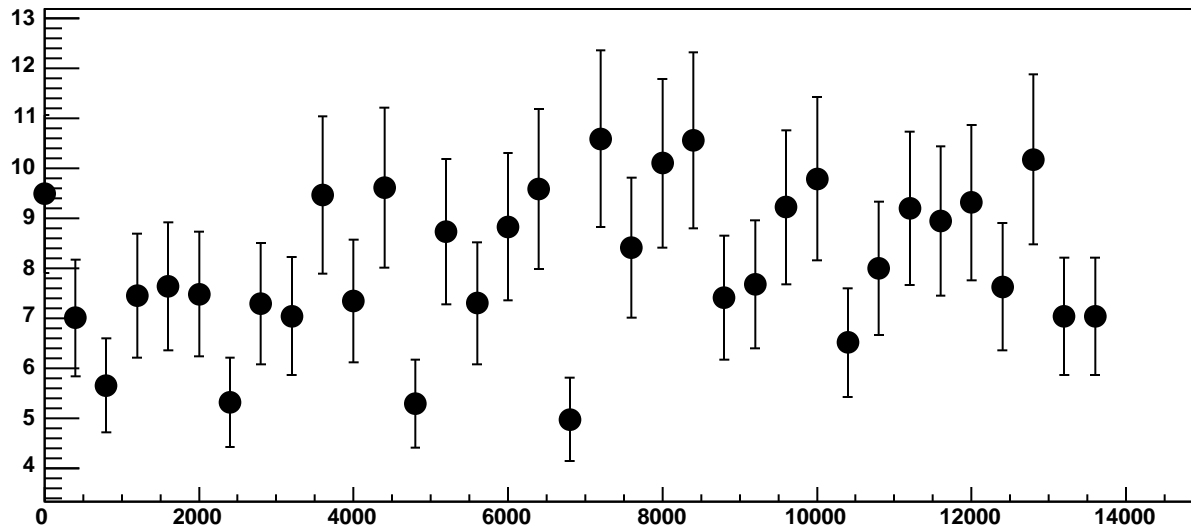


Chip 0, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

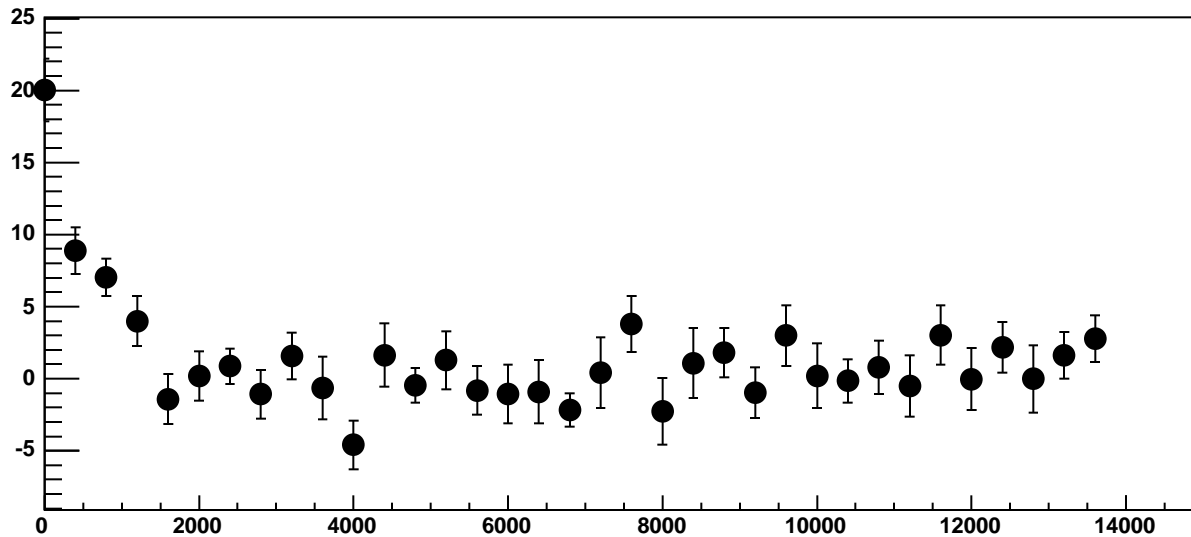


χ^2 / ndf 29.5 / 23
p0 36.02 ± 0.7662
p1 0.01393 ± 0.0001203

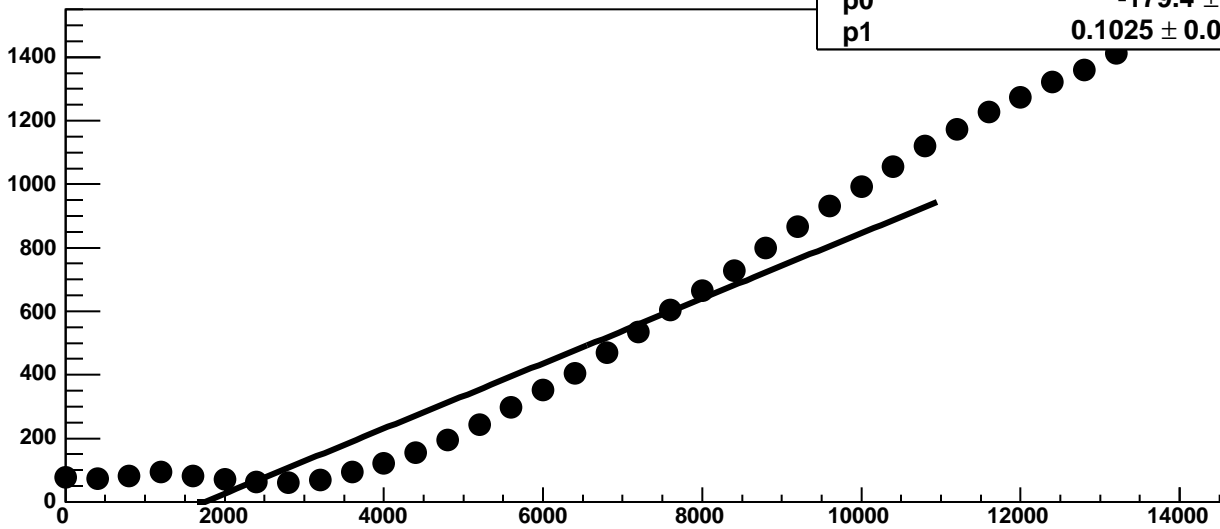
Chip 0, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



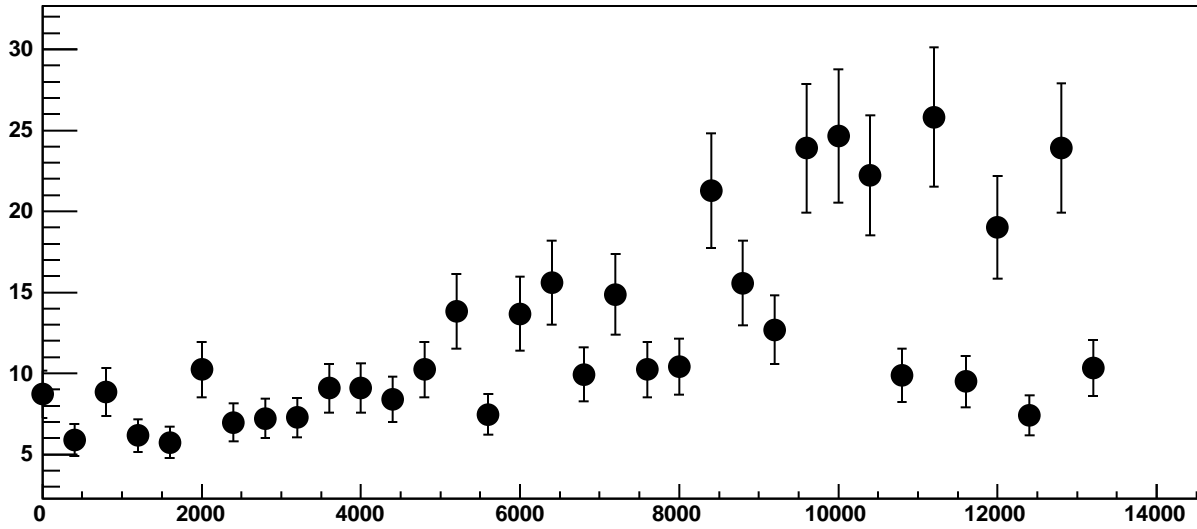
Chip 0, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



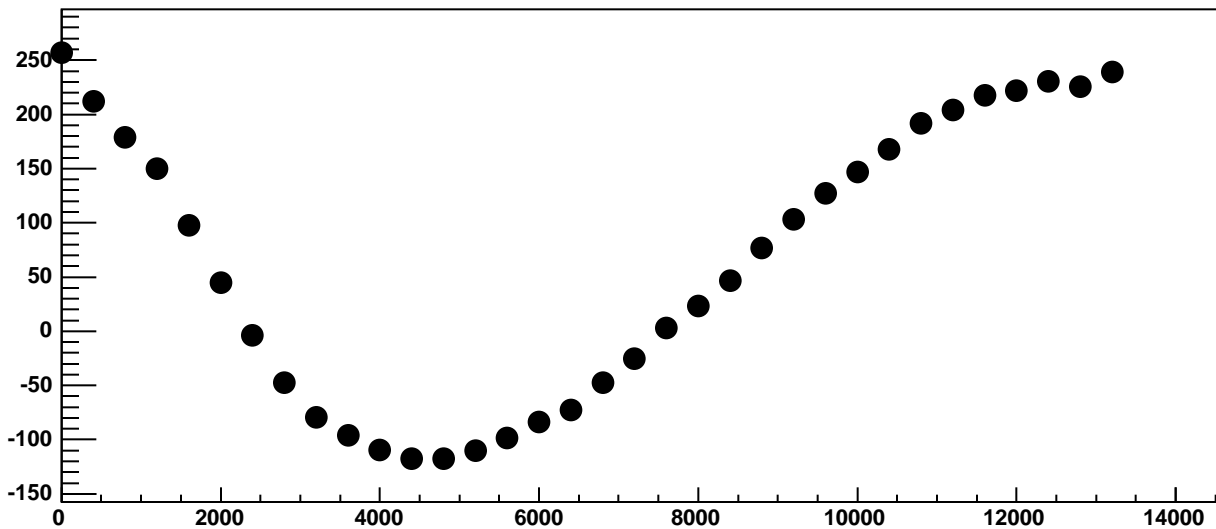
Chip 0, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



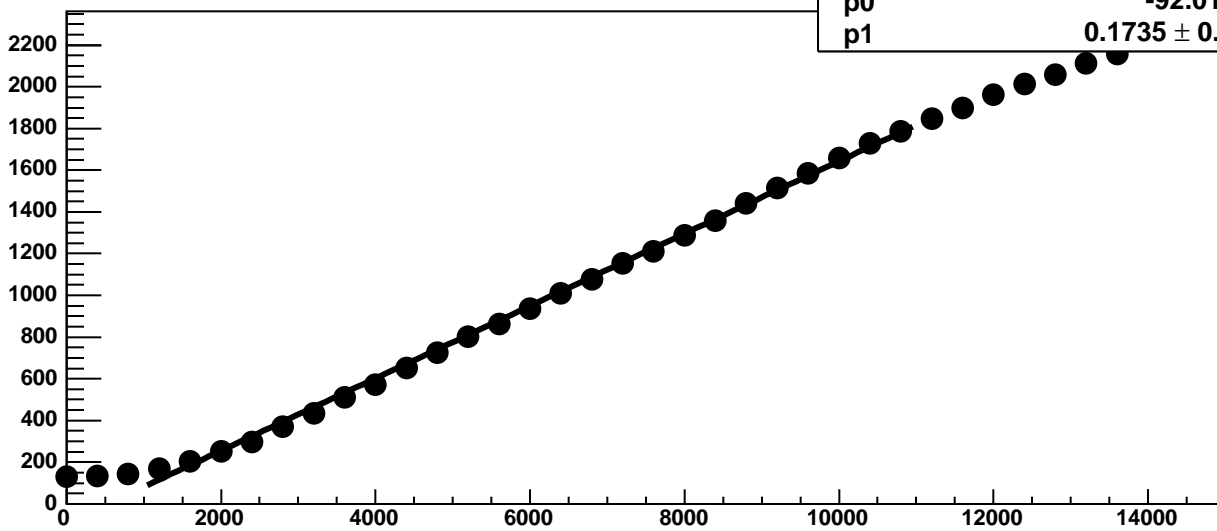
Chip 0, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



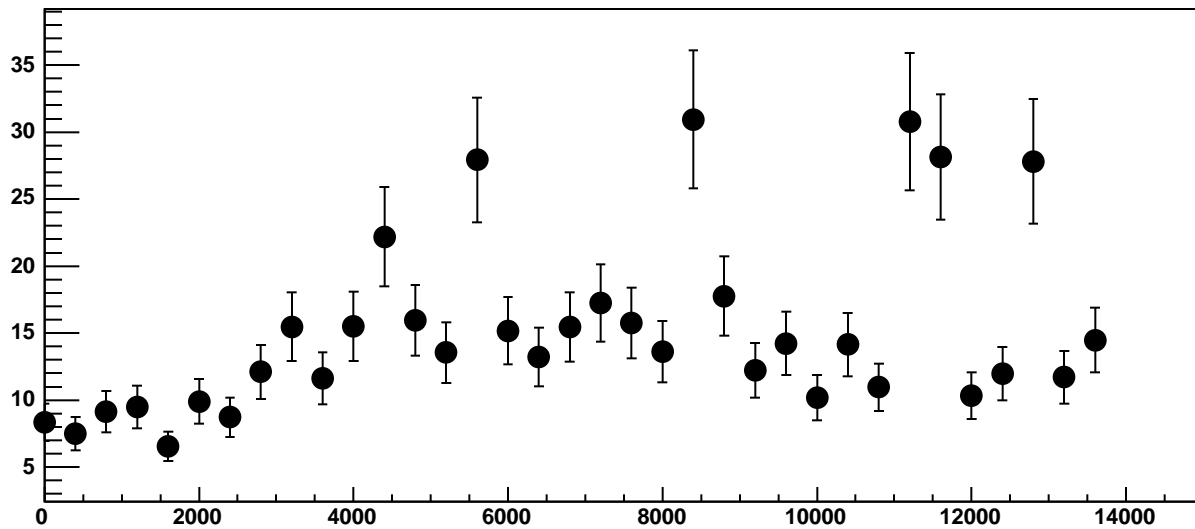
Chip 0, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



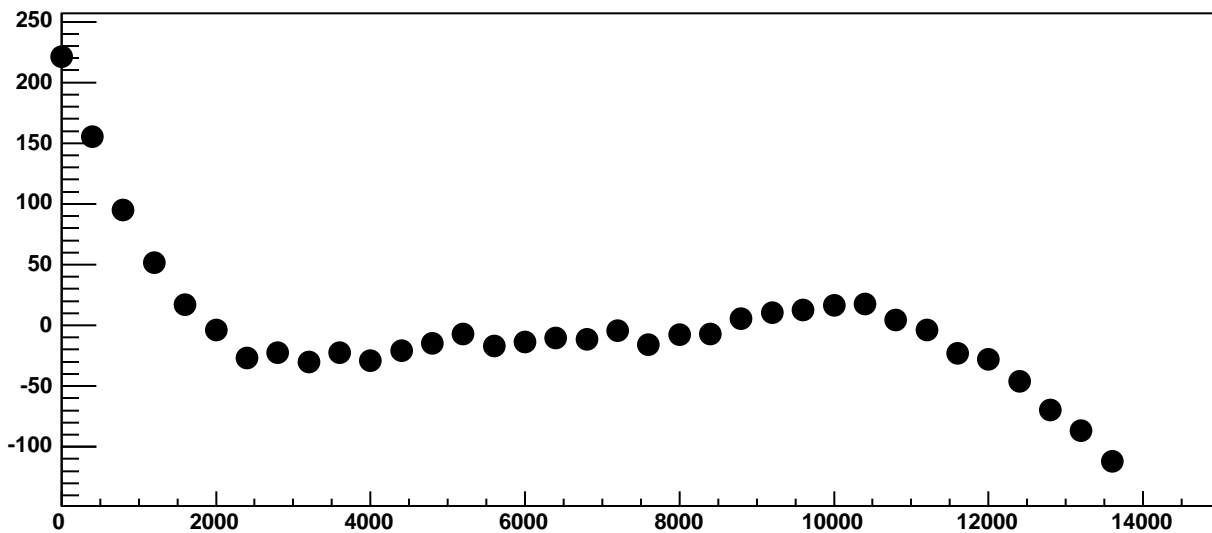
Chip 0, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC



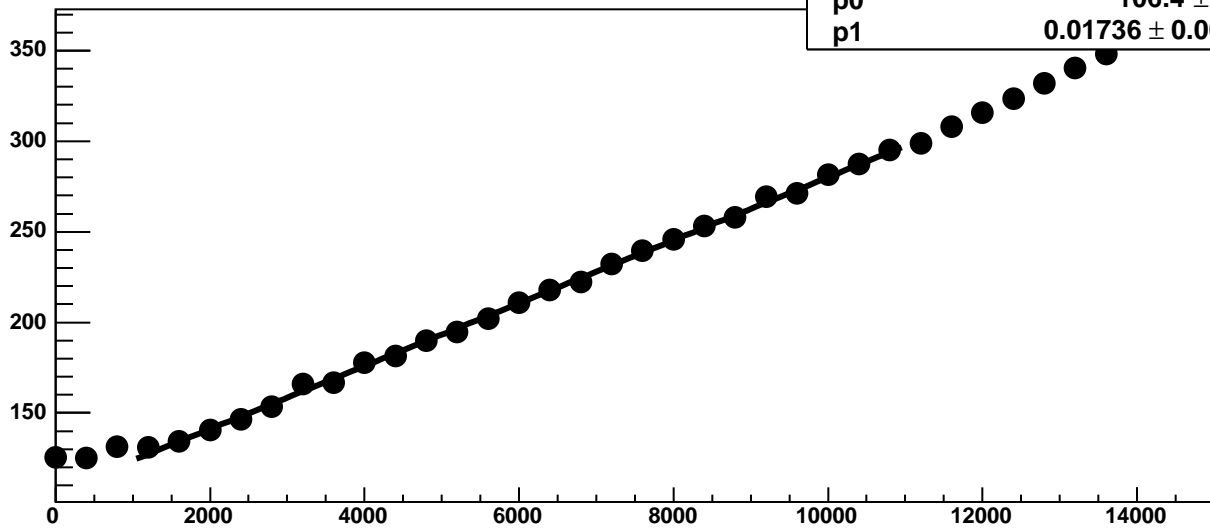
Chip 0, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC

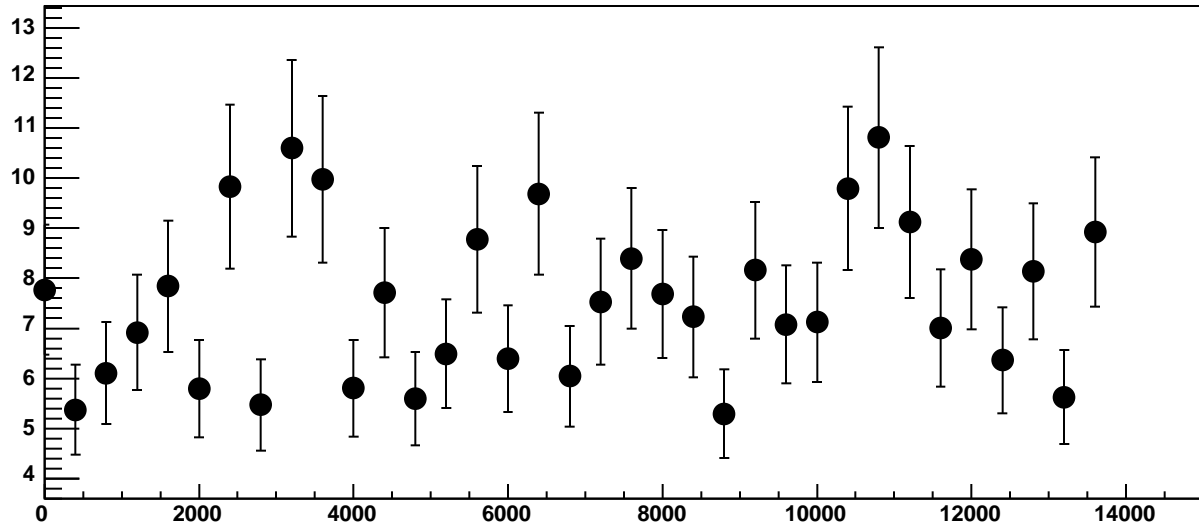


Chip 0, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

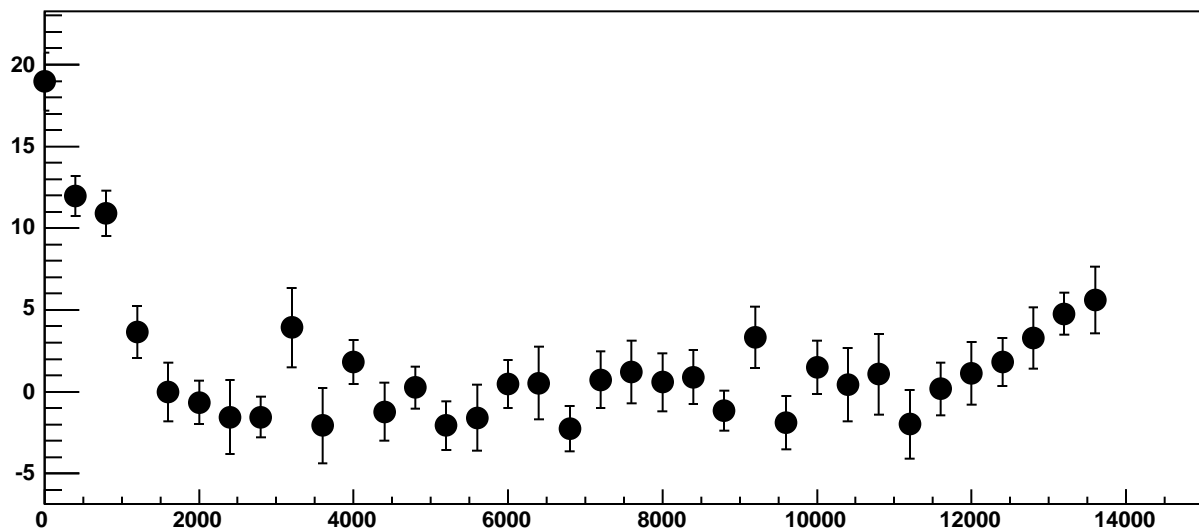


χ^2 / ndf 26.17 / 23
p0 106.4 ± 0.7622
p1 0.01736 ± 0.0001185

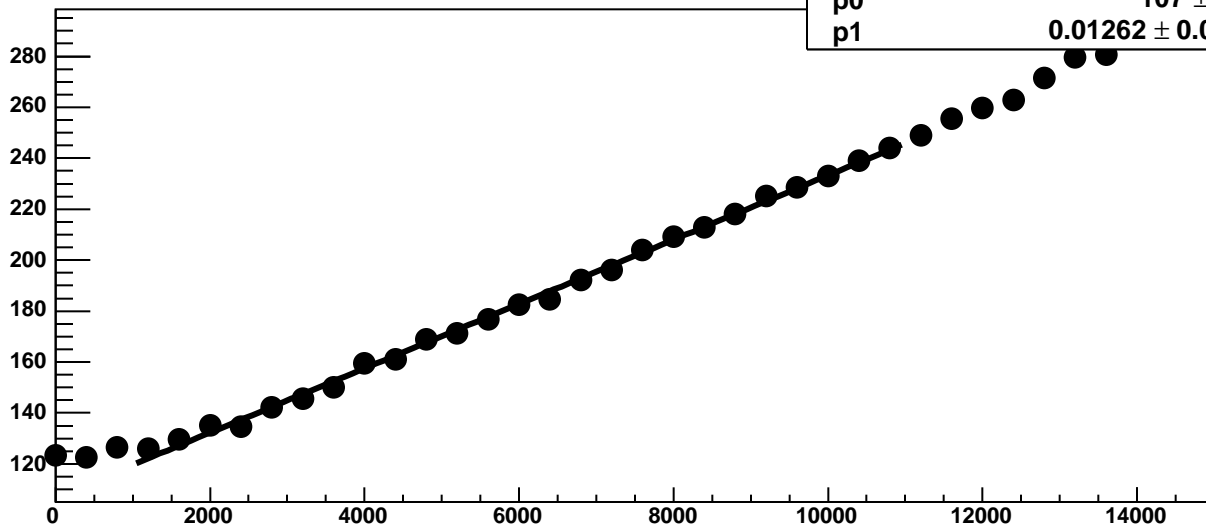
Chip 0, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

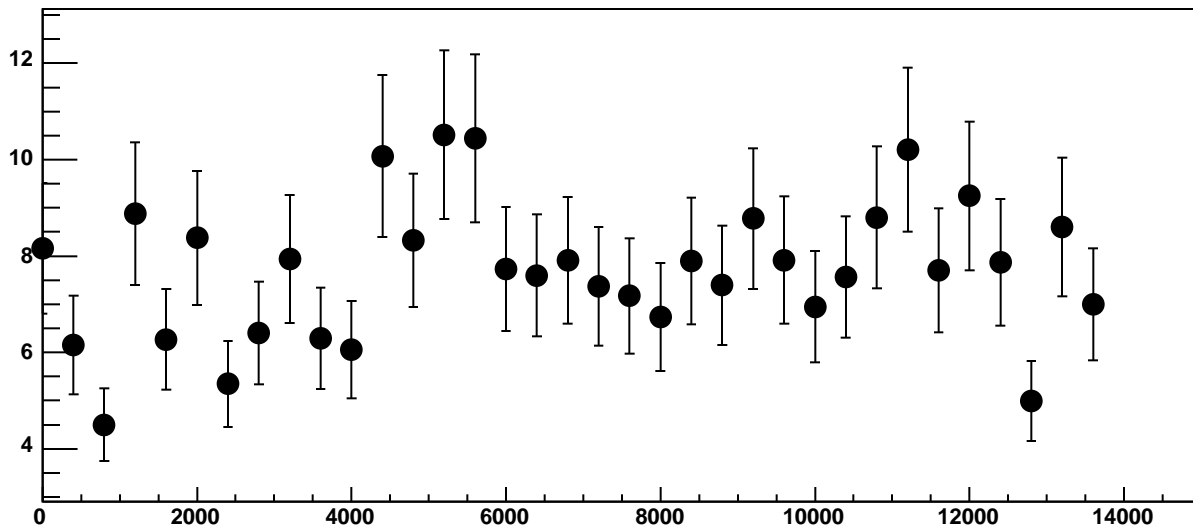


Chip 0, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

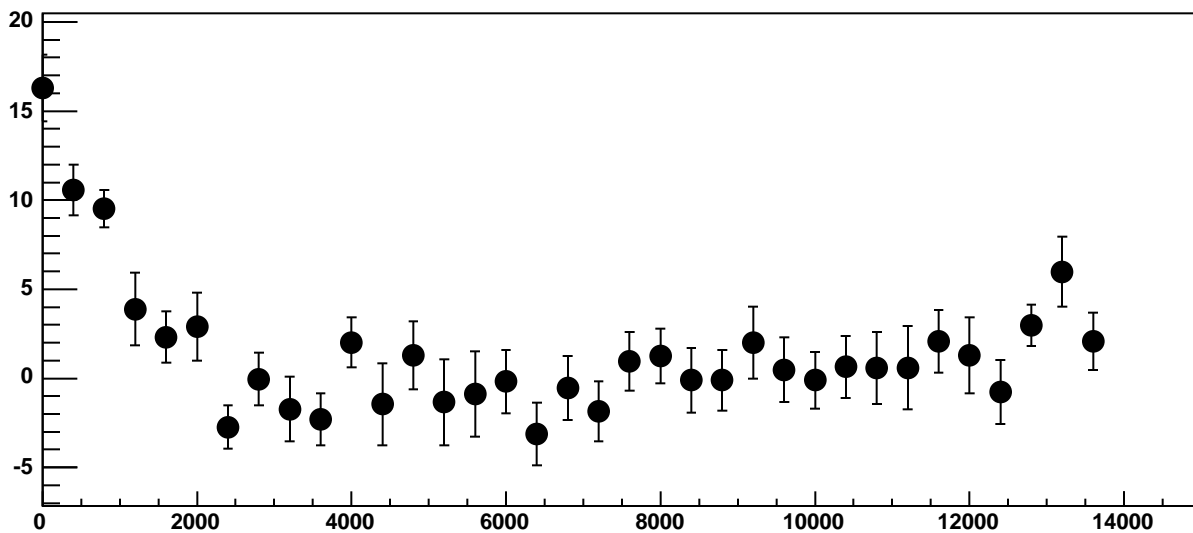


χ^2 / ndf 27.1 / 23
p0 107 ± 0.7559
p1 0.01262 ± 0.0001171

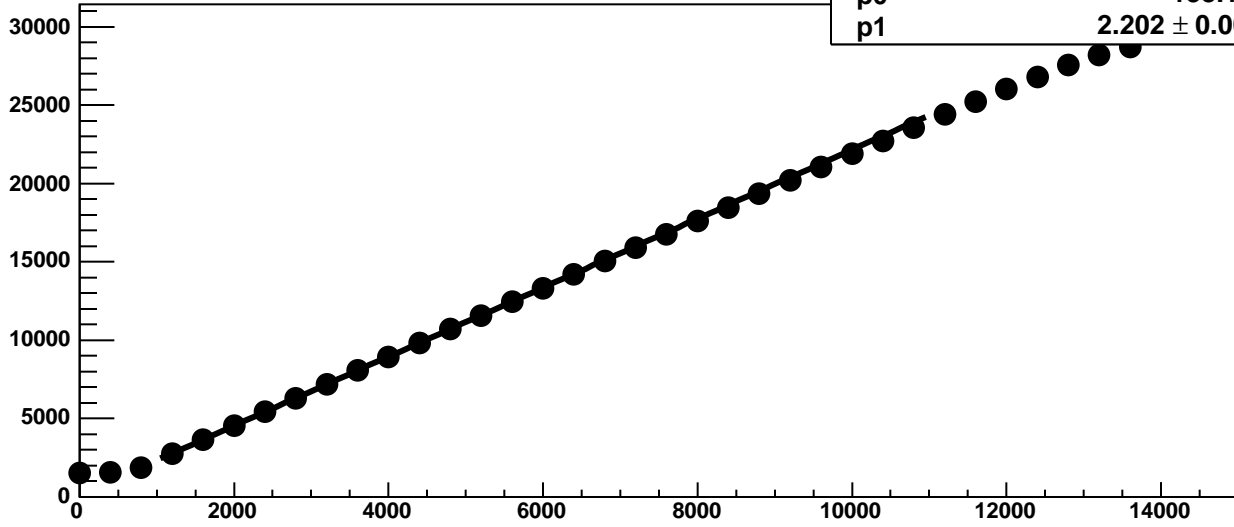
Chip 0, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

555.5 / 23

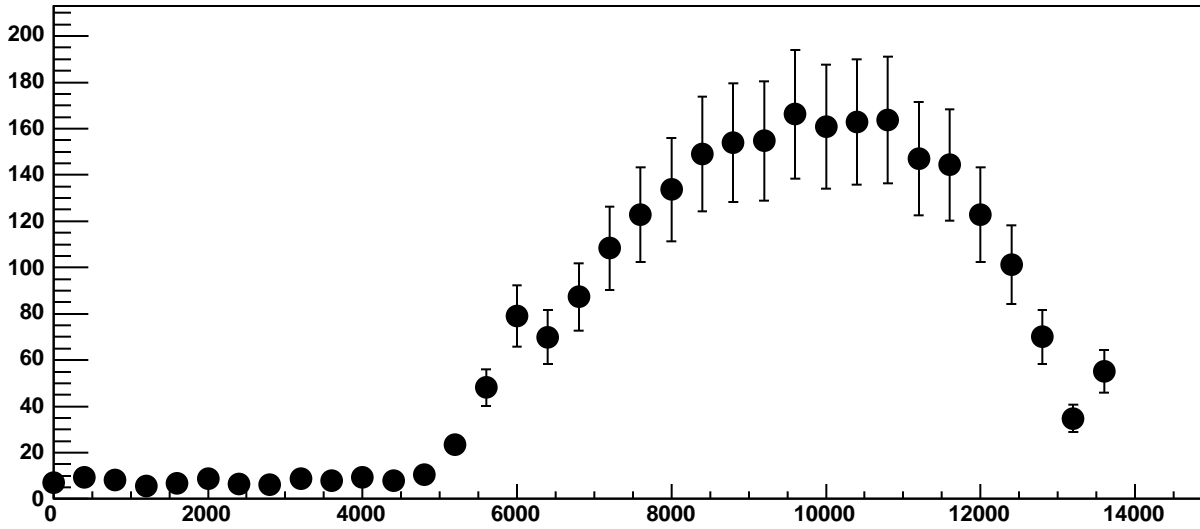
p0

138.1 ± 1.32

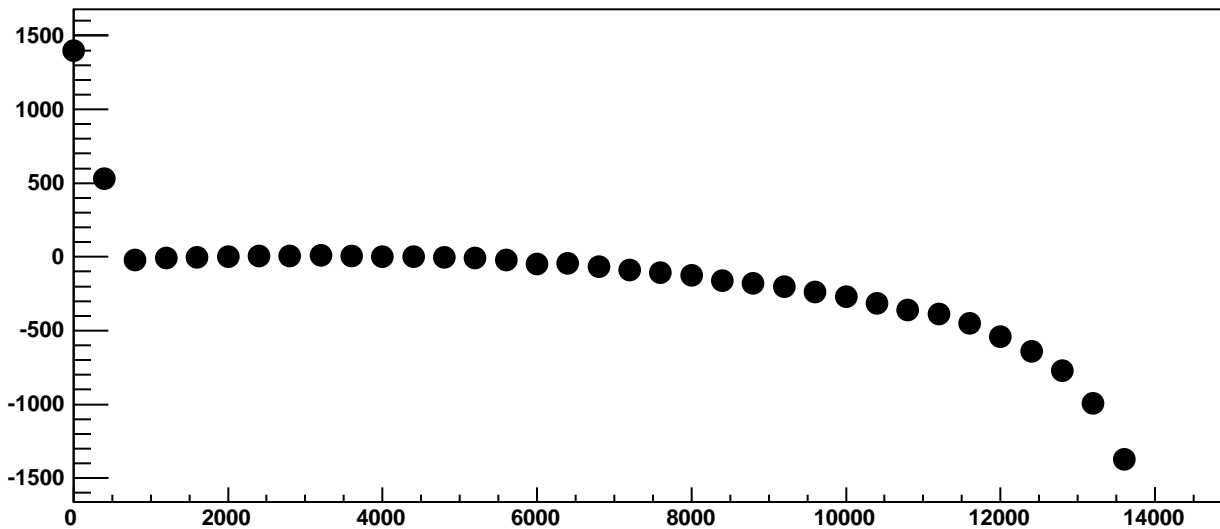
p1

2.202 ± 0.0004416

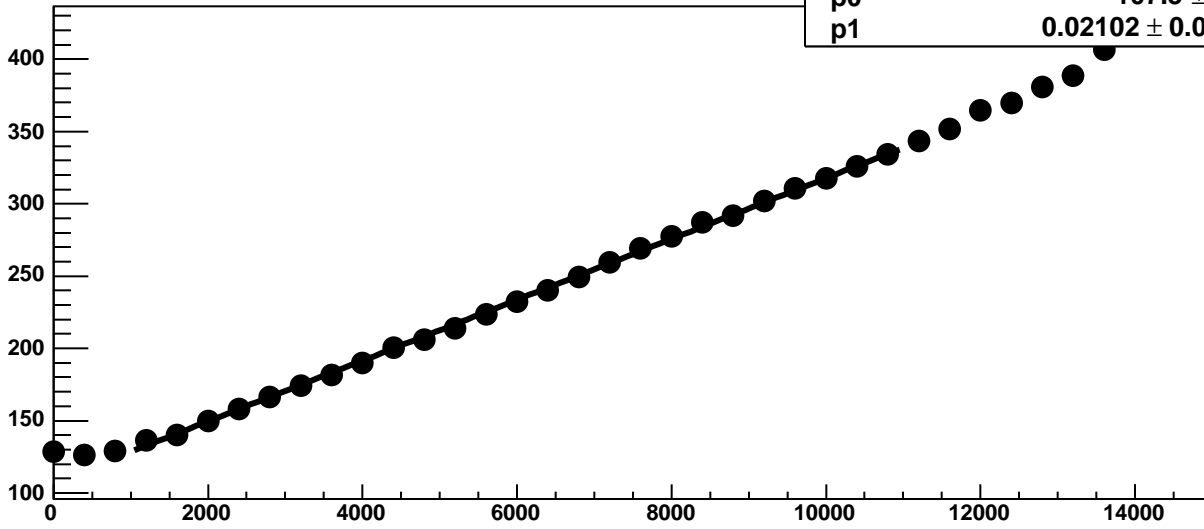
Chip 0, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC

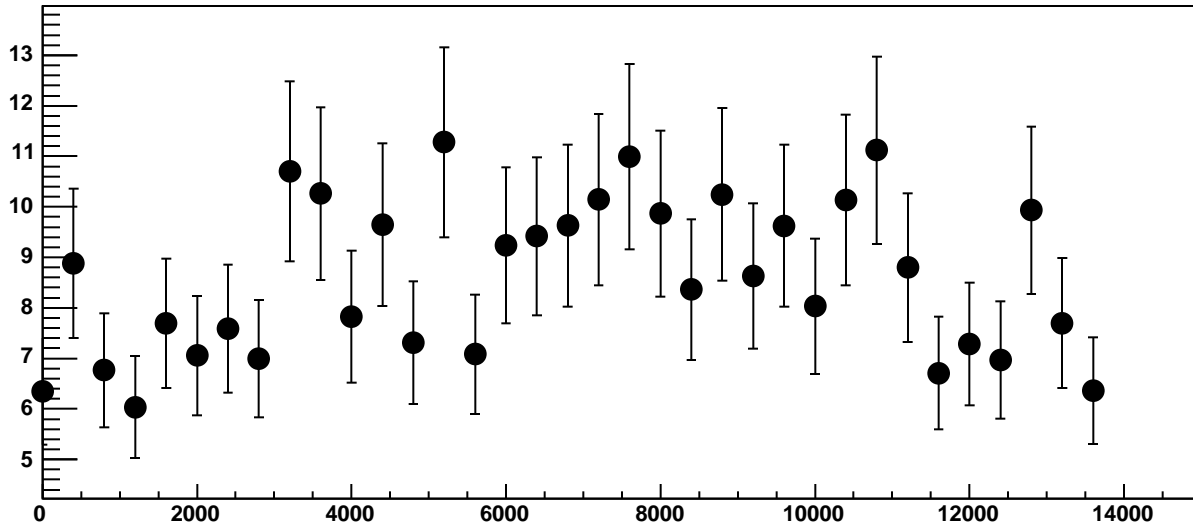


Chip 0, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

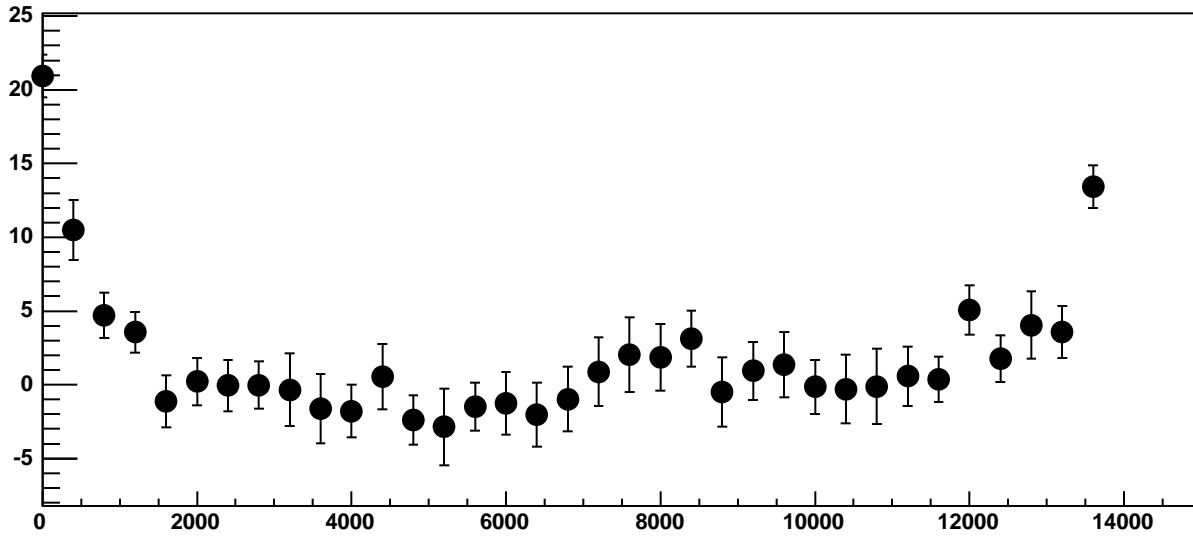


χ^2 / ndf 18.89 / 23
p0 107.5 ± 0.8183
p1 0.02102 ± 0.0001328

Chip 0, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

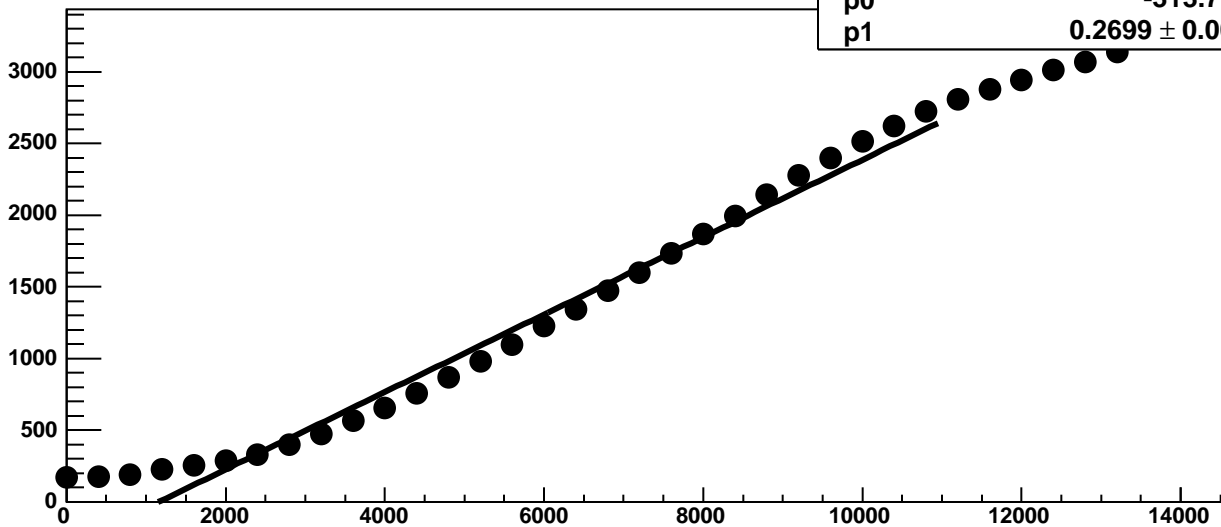


Chip 0, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

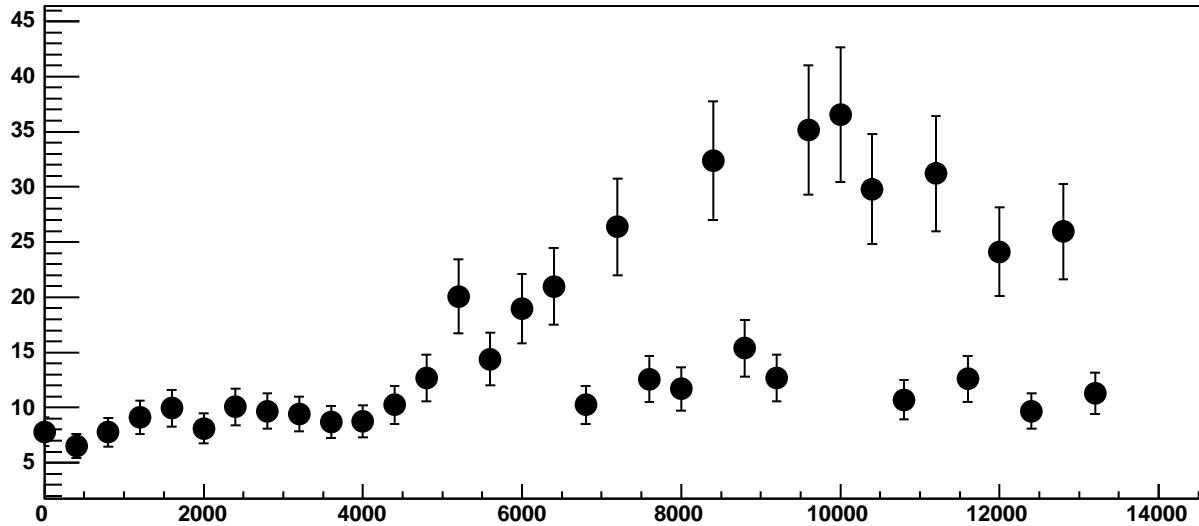


Chip 0, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

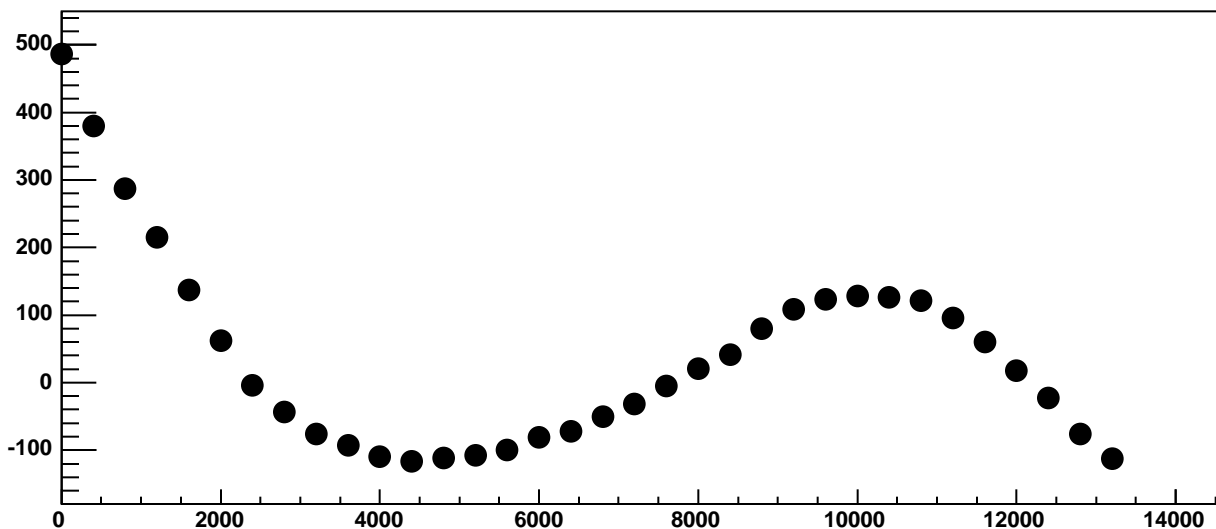
χ^2 / ndf 3.38e+04 / 23
p0 -313.7 ± 1.09
p1 0.2699 ± 0.0002007



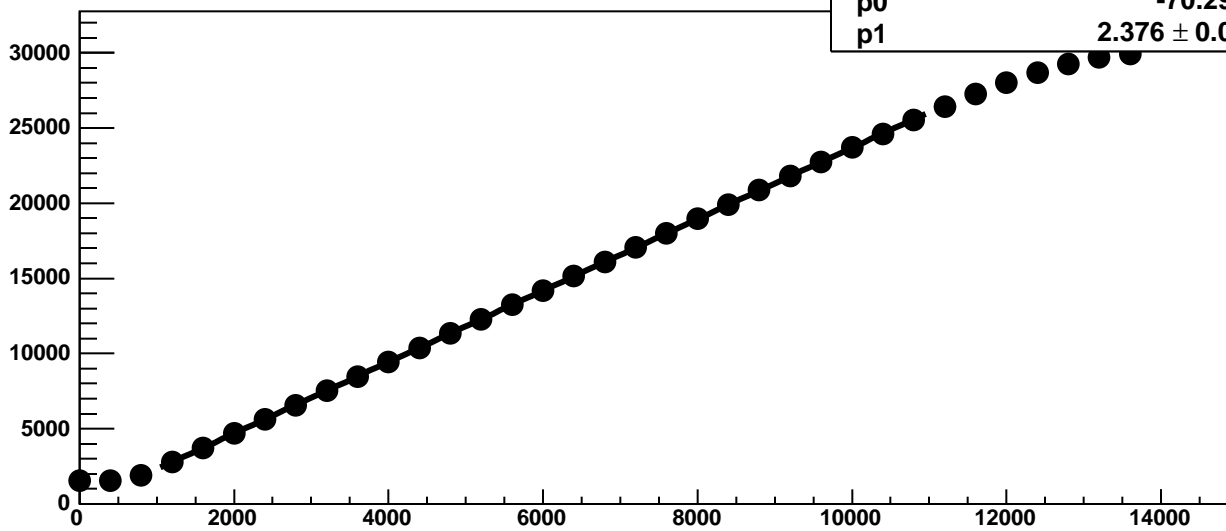
Chip 0, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



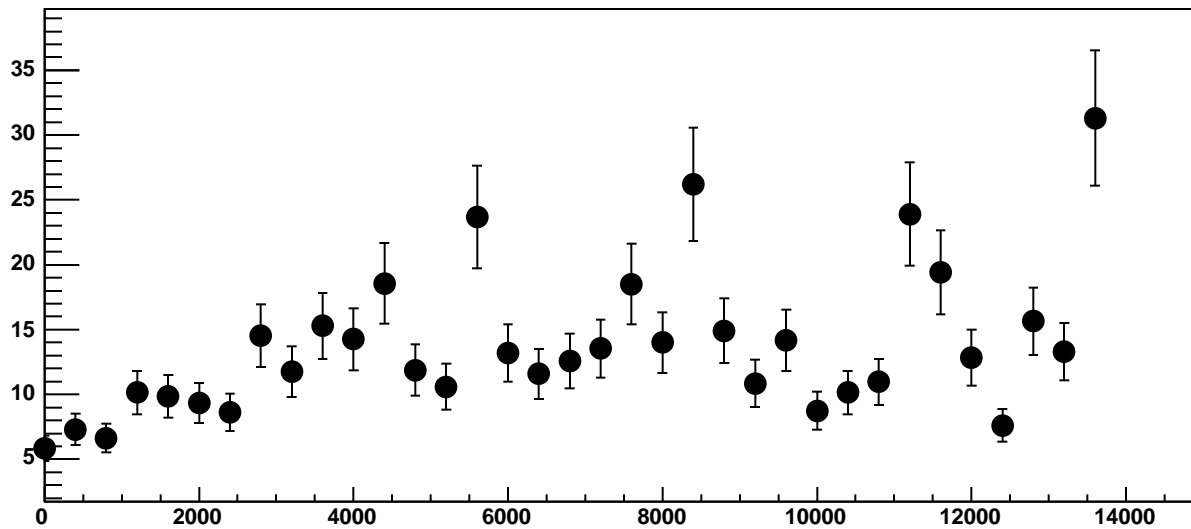
Chip 0, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



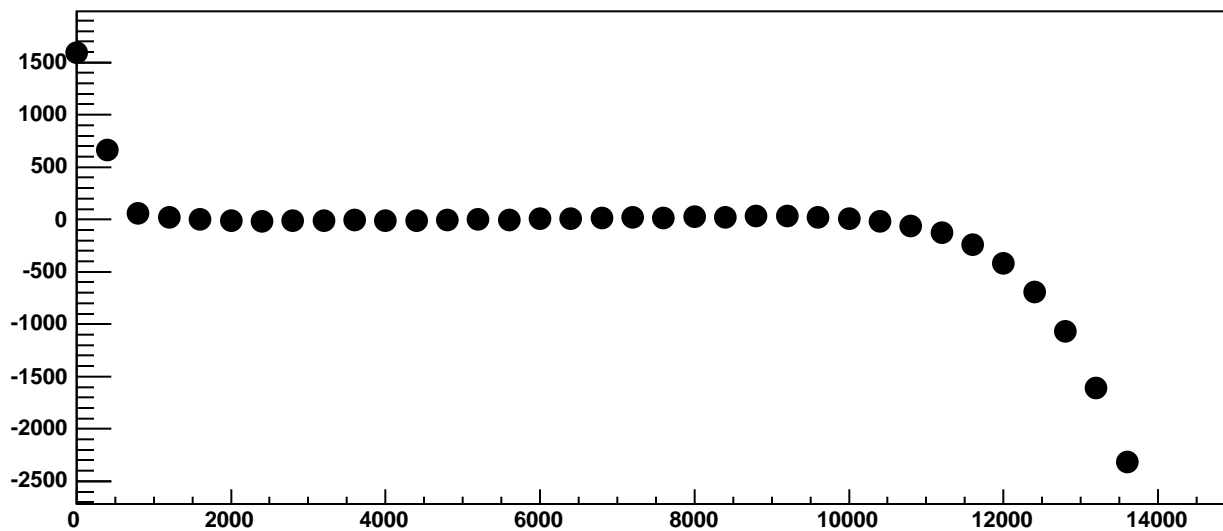
Chip 0, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC



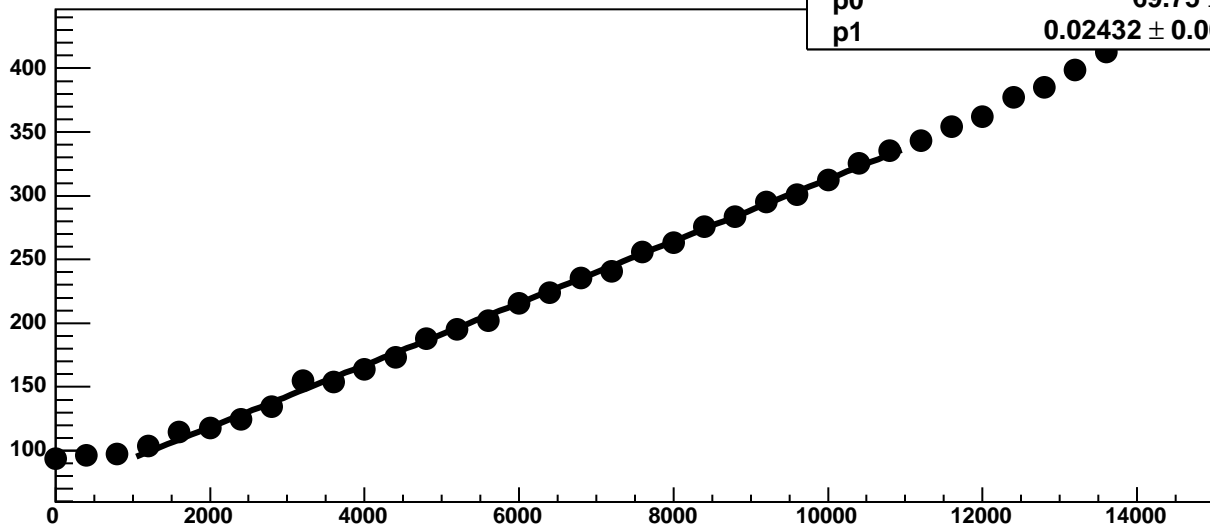
Chip 0, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC

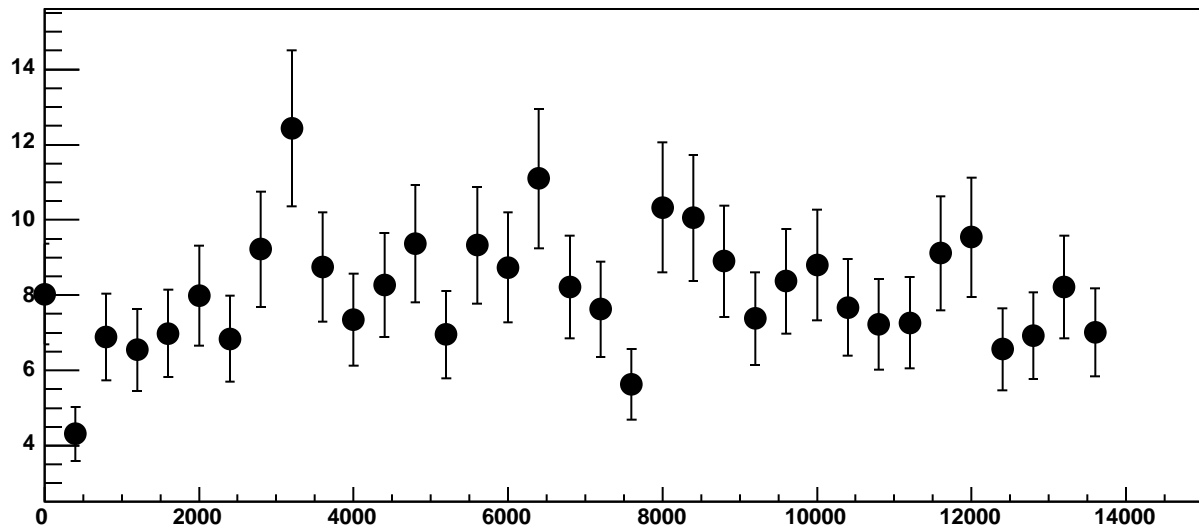


Chip 0, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

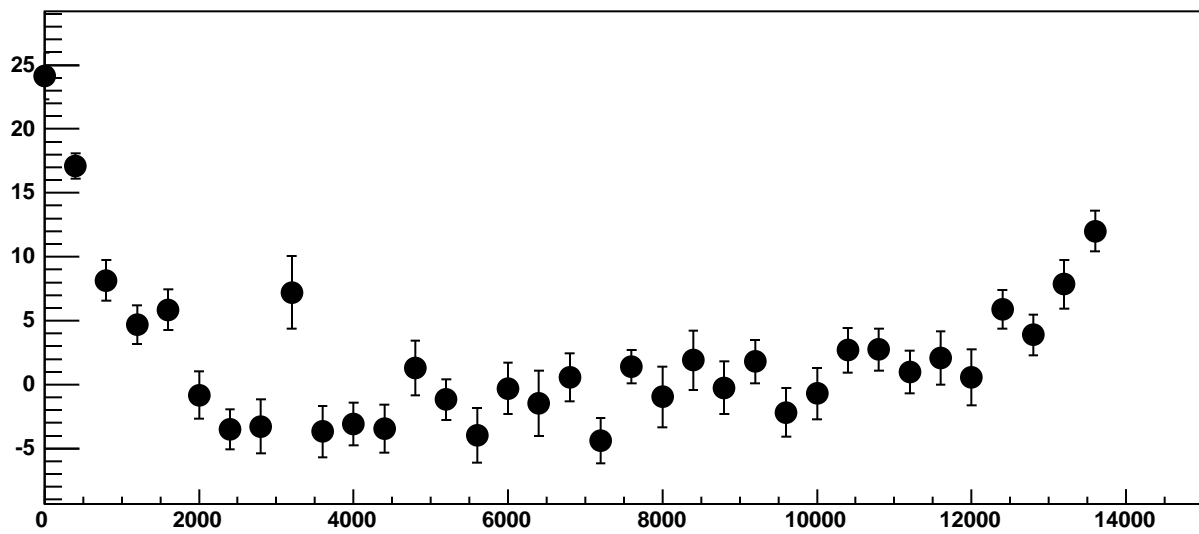


χ^2 / ndf 67.8 / 23
p0 69.75 ± 0.813
p1 0.02432 ± 0.0001224

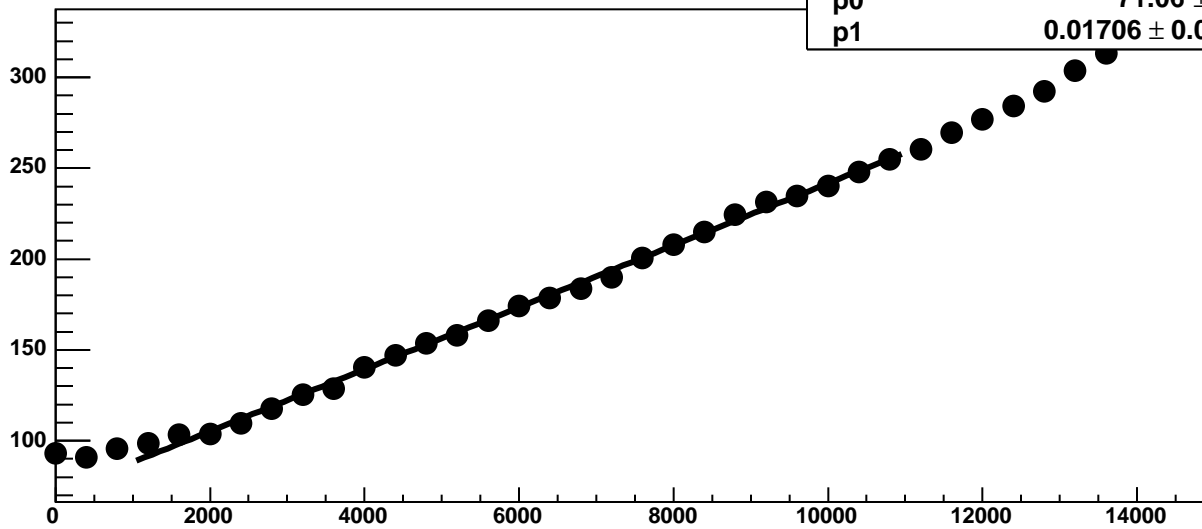
Chip 0, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

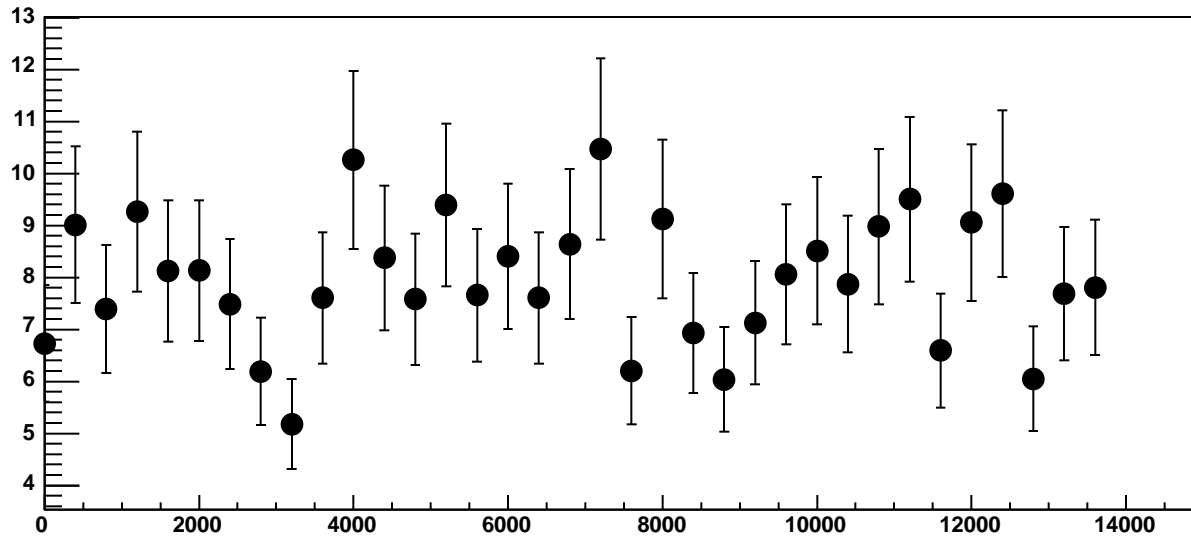


Chip 0, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

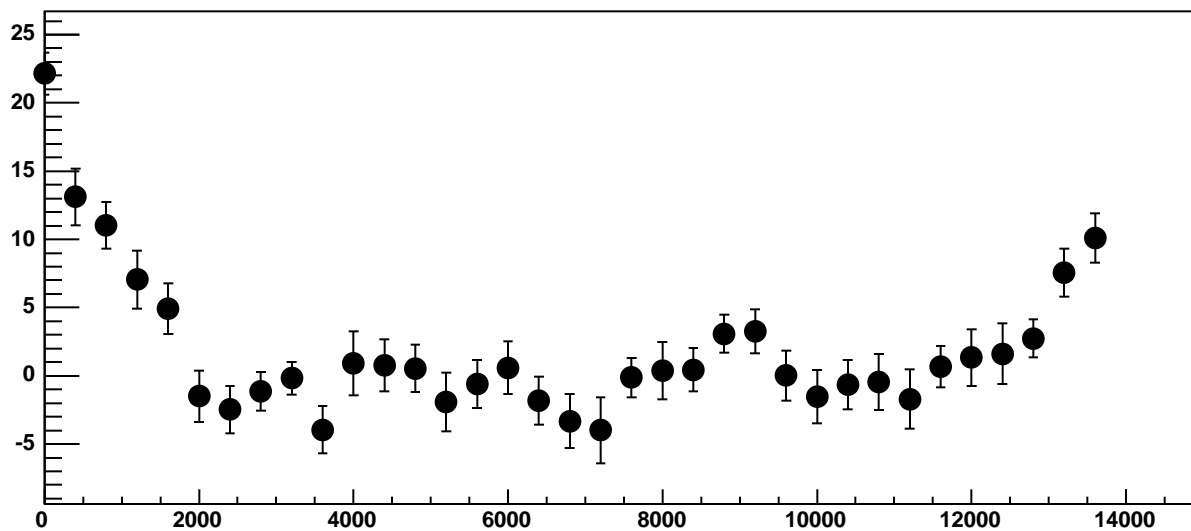


χ^2 / ndf 44.36 / 23
p0 71.06 ± 0.8077
p1 0.01706 ± 0.0001227

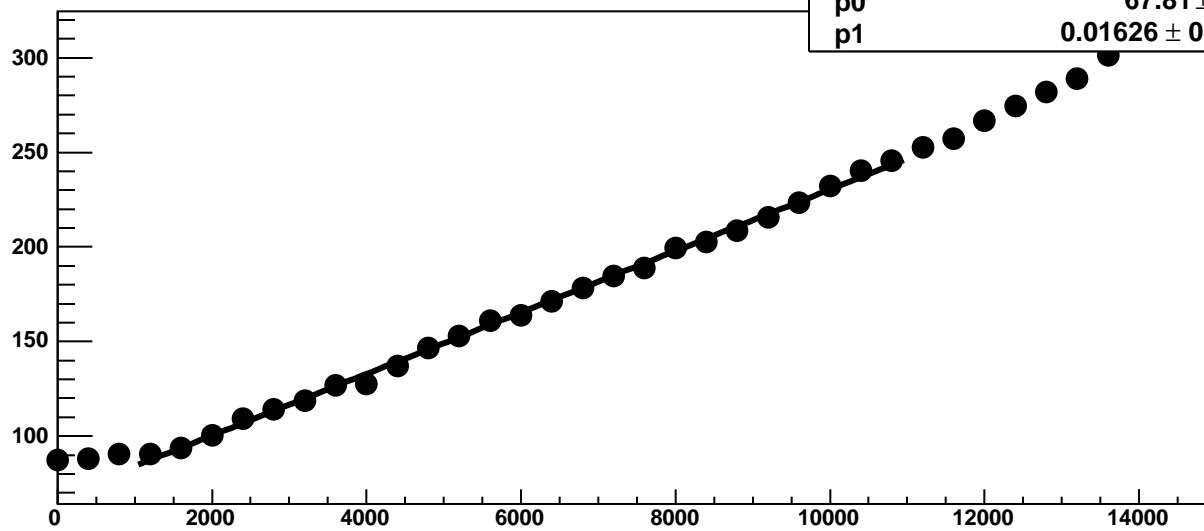
Chip 0, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

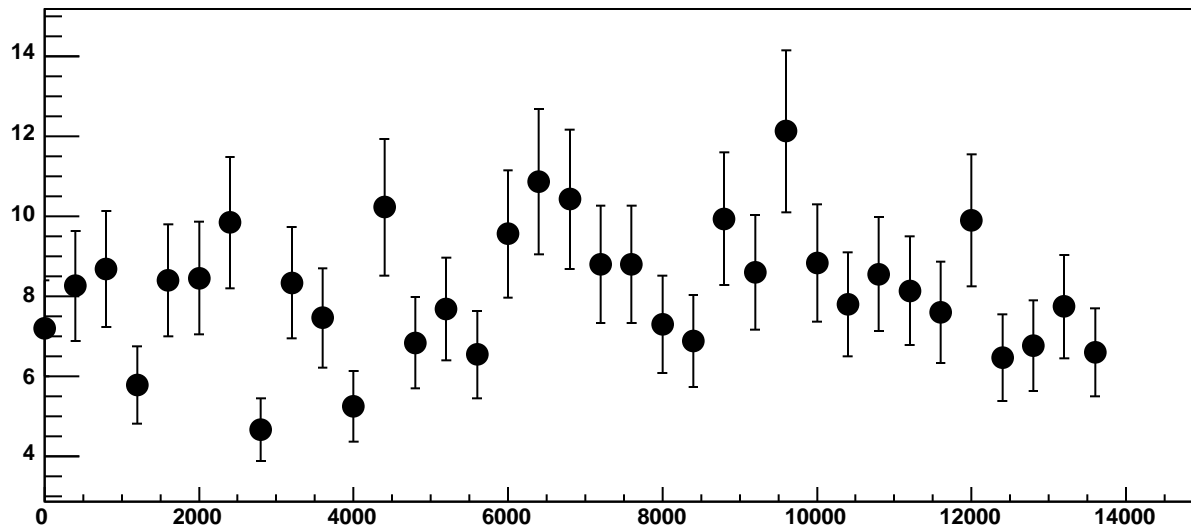


Chip 0, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

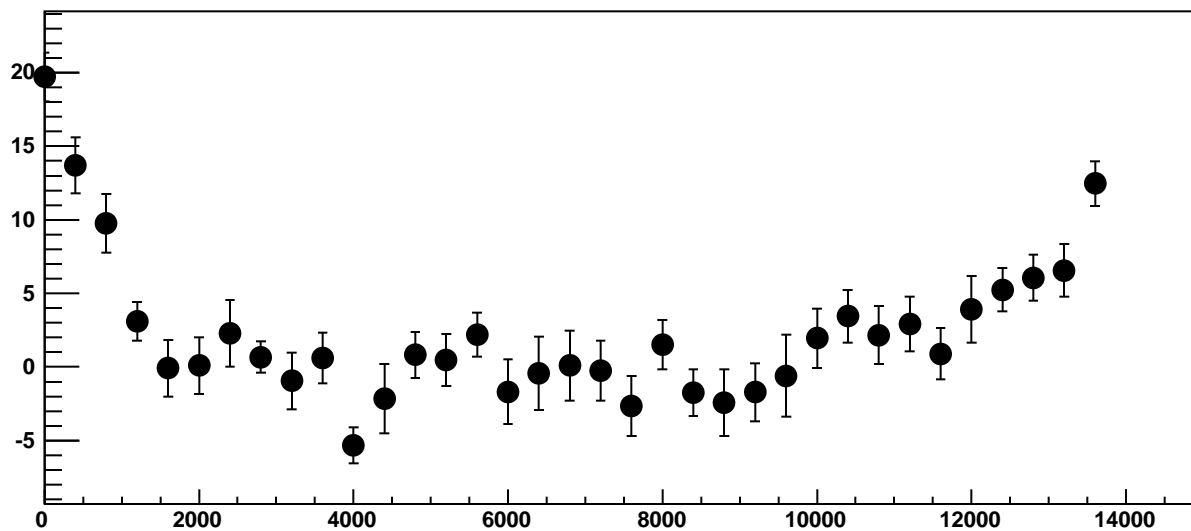


χ^2 / ndf 42.29 / 23
p0 67.81 ± 0.7545
p1 0.01626 ± 0.000123

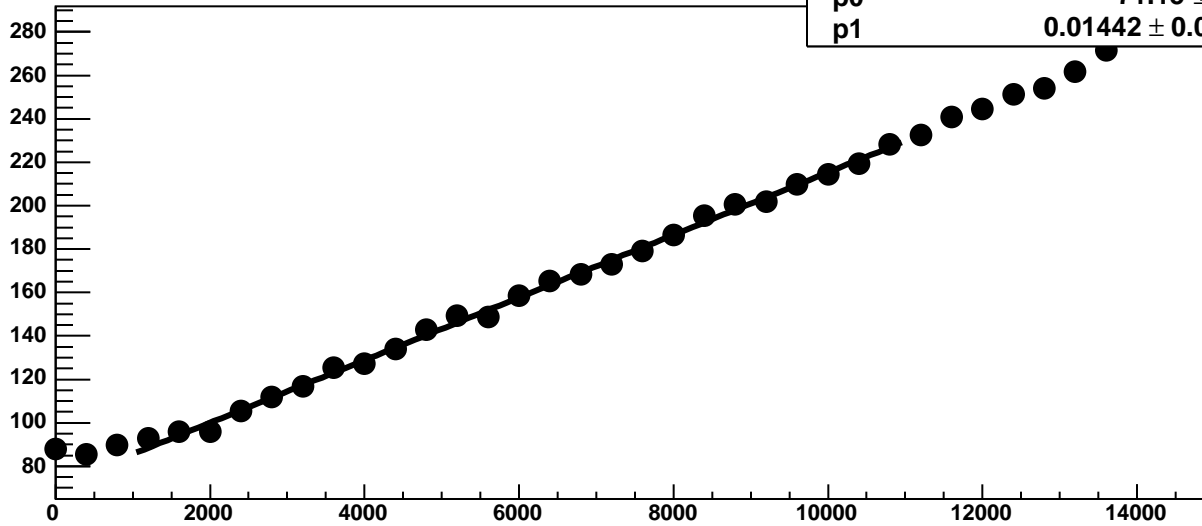
Chip 0, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

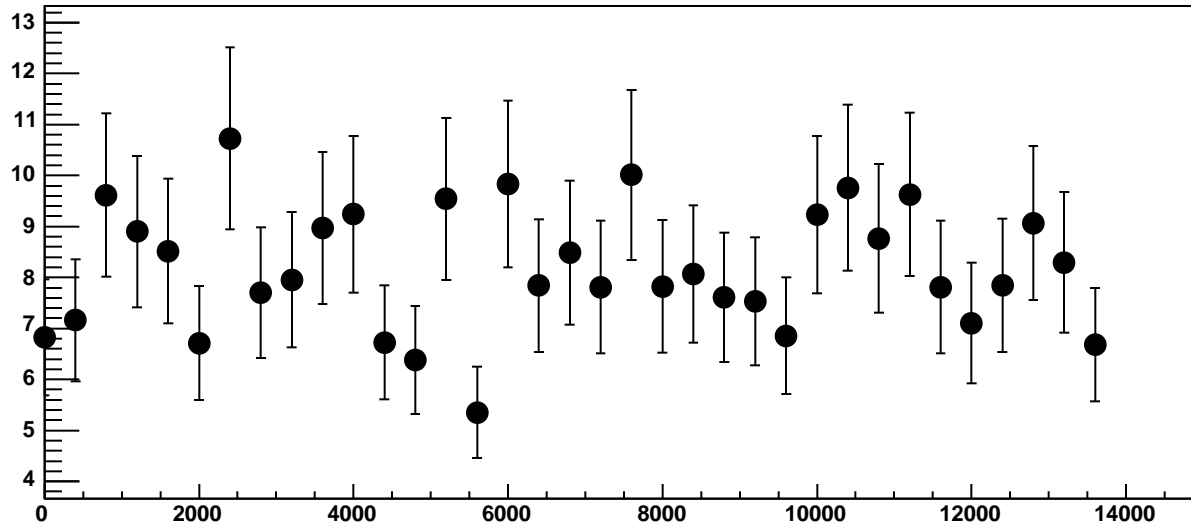


Chip 0, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

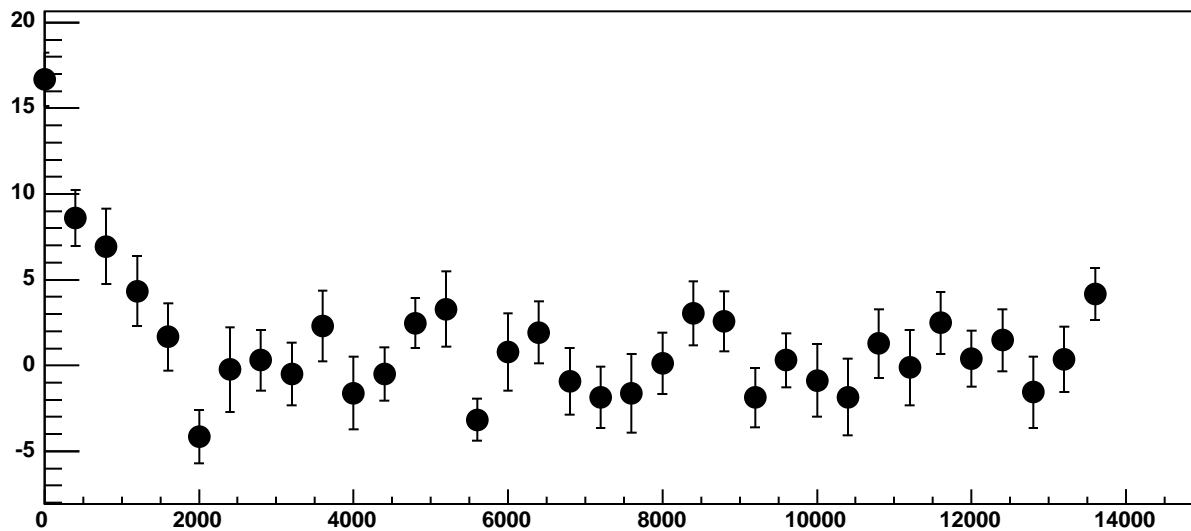


χ^2 / ndf 36.78 / 23
p0 71.15 ± 0.8632
p1 0.01442 ± 0.0001316

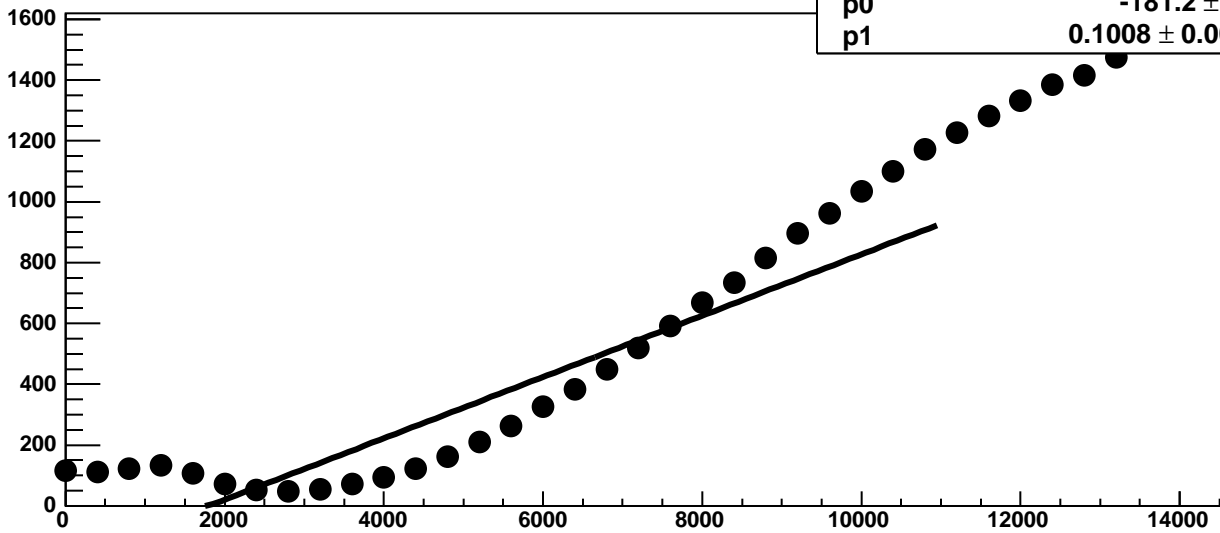
Chip 0, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

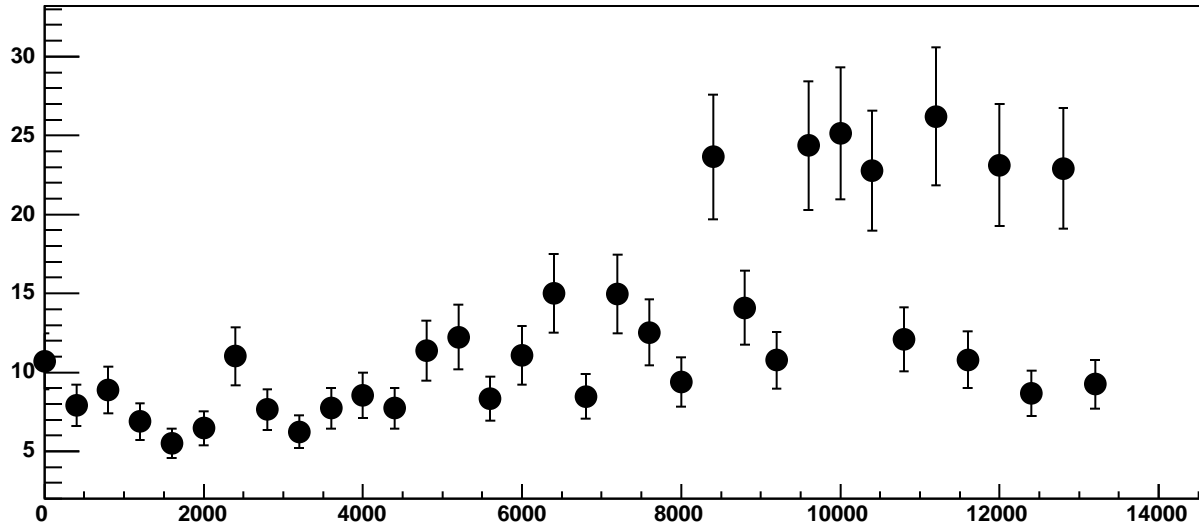


Chip 0, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

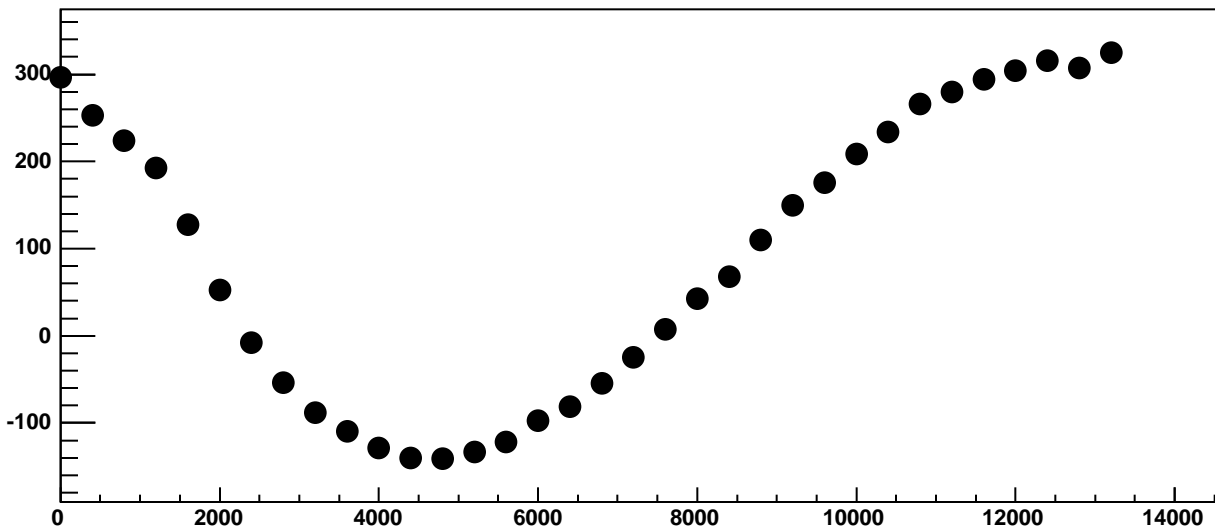


χ^2 / ndf 7.636e+04 / 23
p0 -181.2 ± 0.8438
p1 0.1008 ± 0.0001664

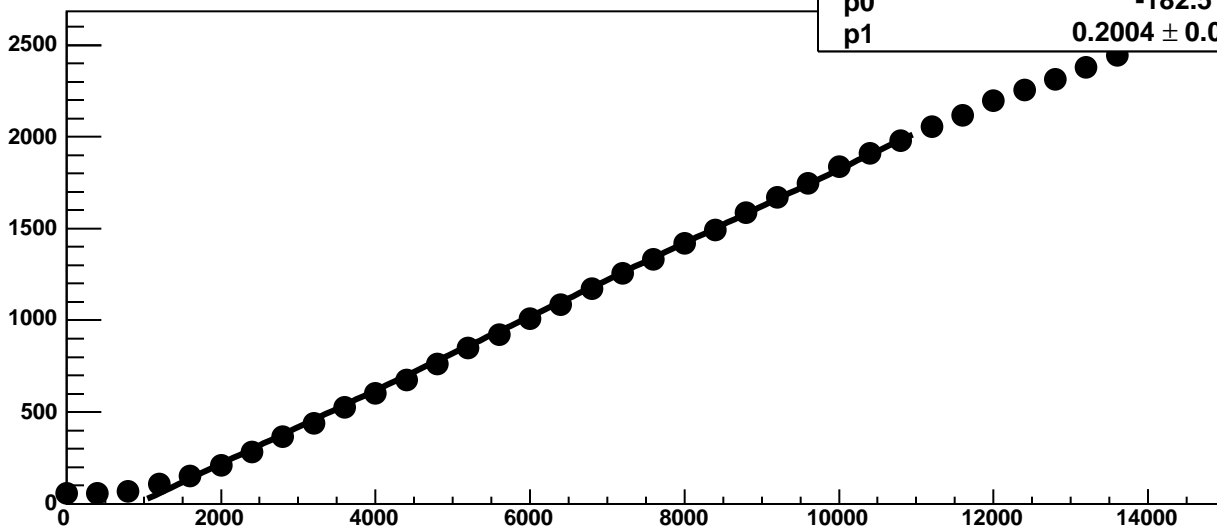
Chip 0, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



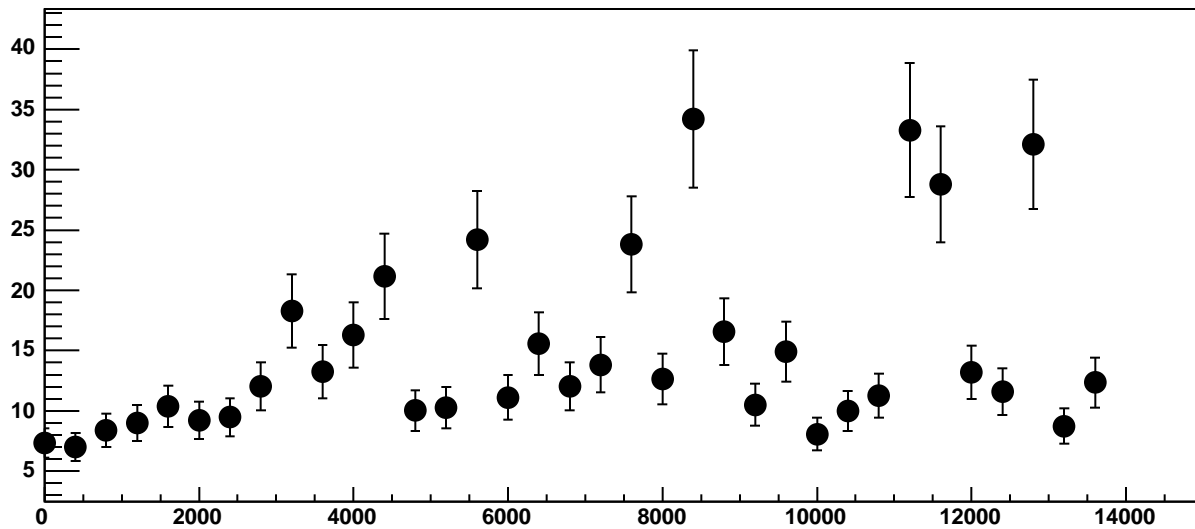
Chip 0, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



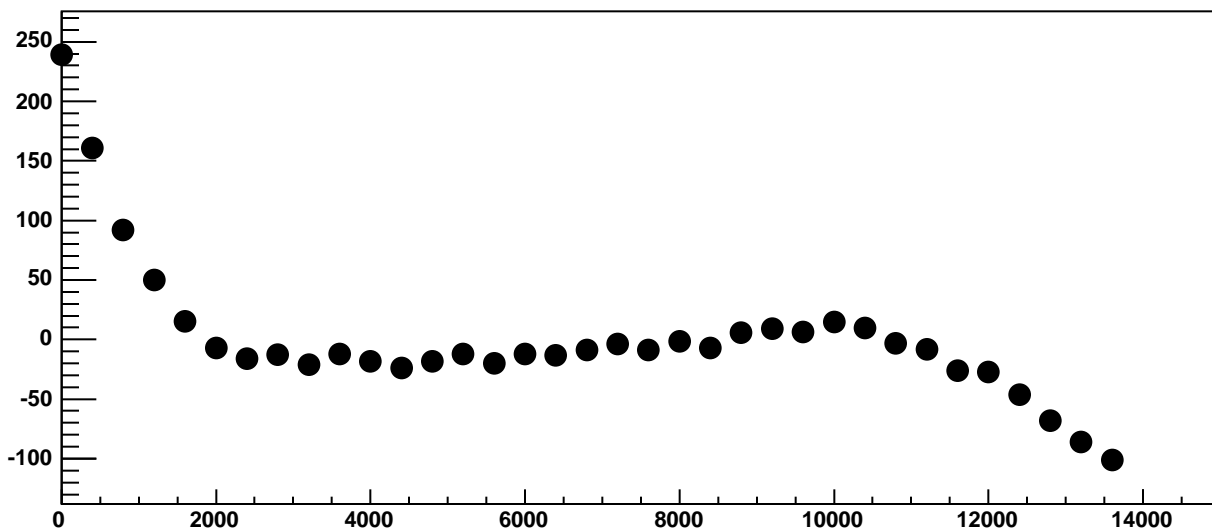
Chip 0, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



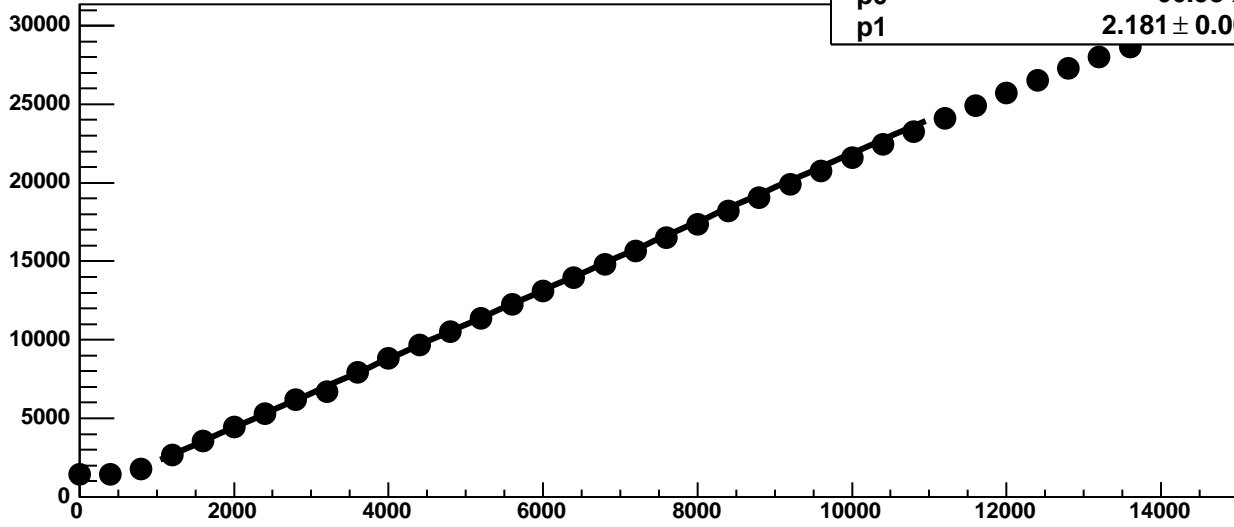
Chip 0, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

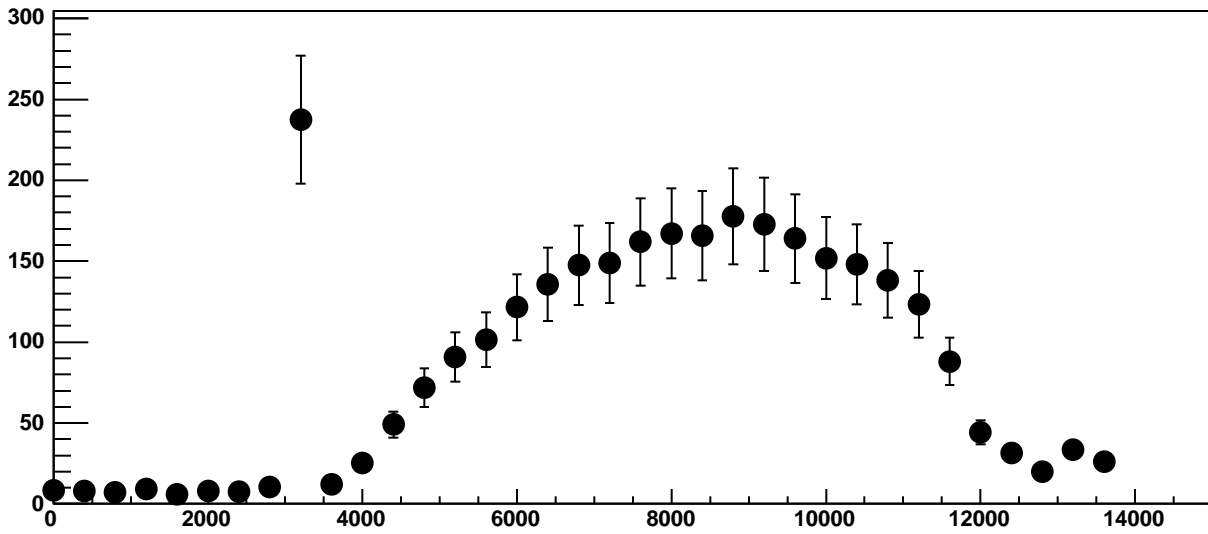


Chip 0, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC

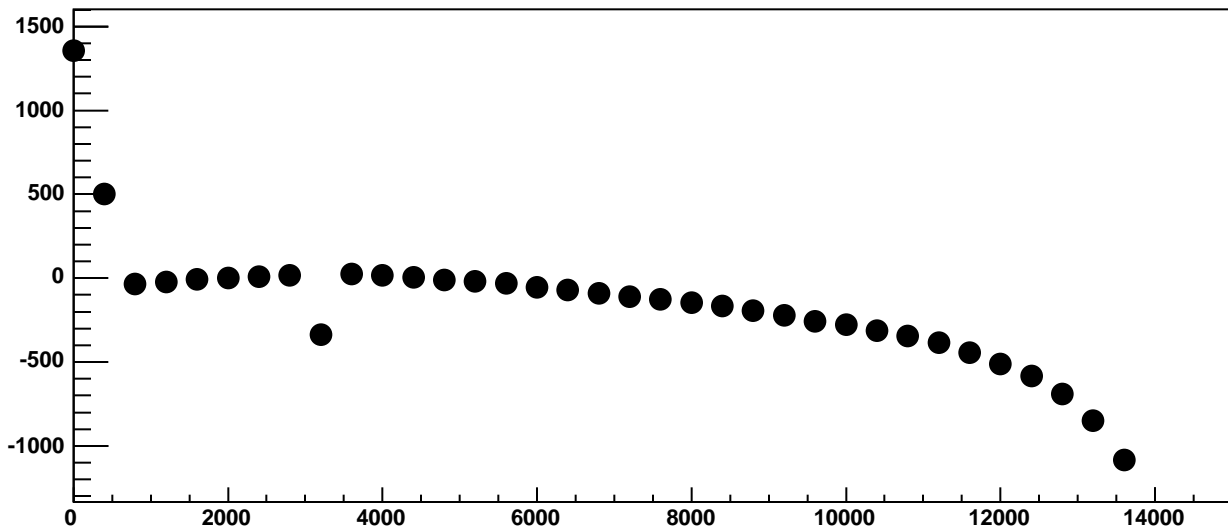


χ^2 / ndf 746.8 / 23
p0 66.98 ± 1.984
p1 2.181 ± 0.0008555

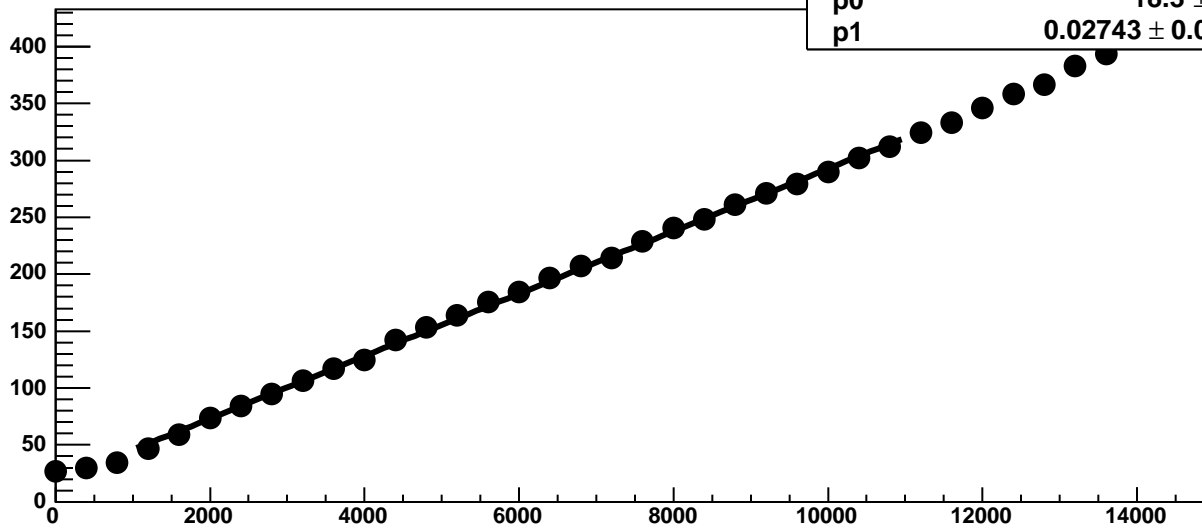
Chip 0, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC

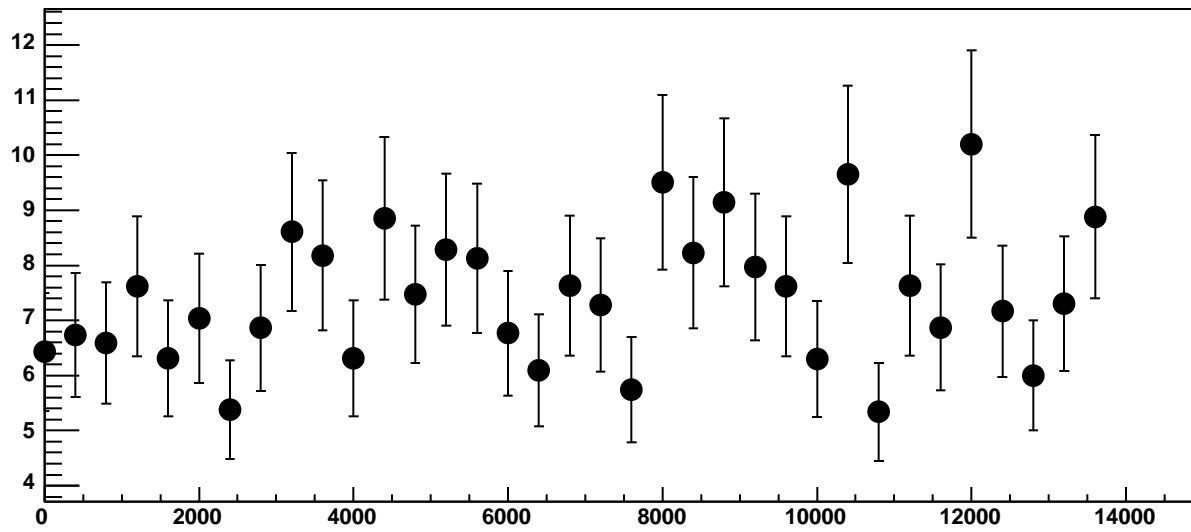


Chip 0, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

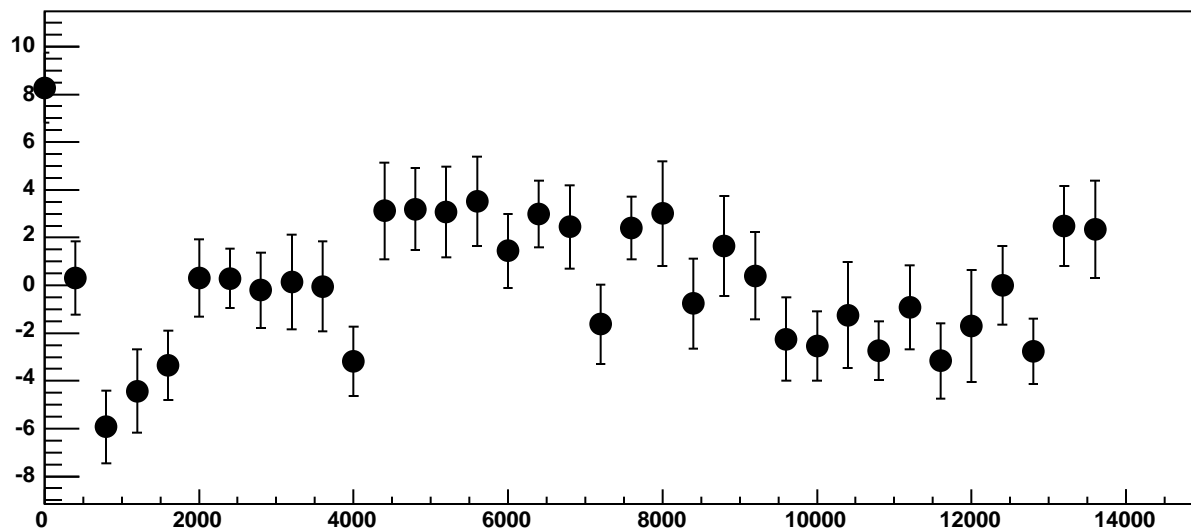


χ^2 / ndf 53.13 / 23
p0 18.3 ± 0.7283
p1 0.02743 ± 0.0001097

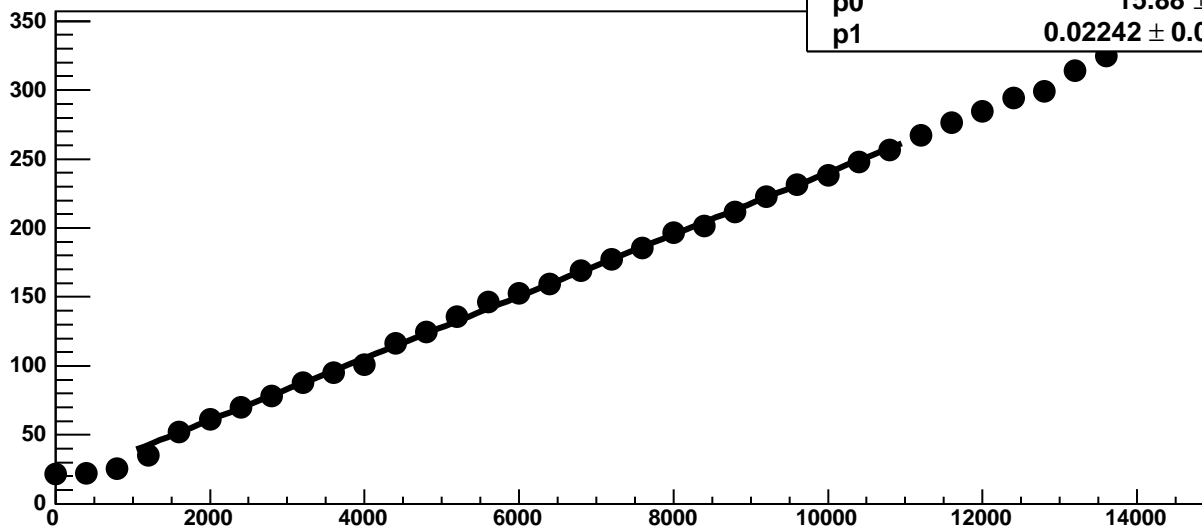
Chip 0, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

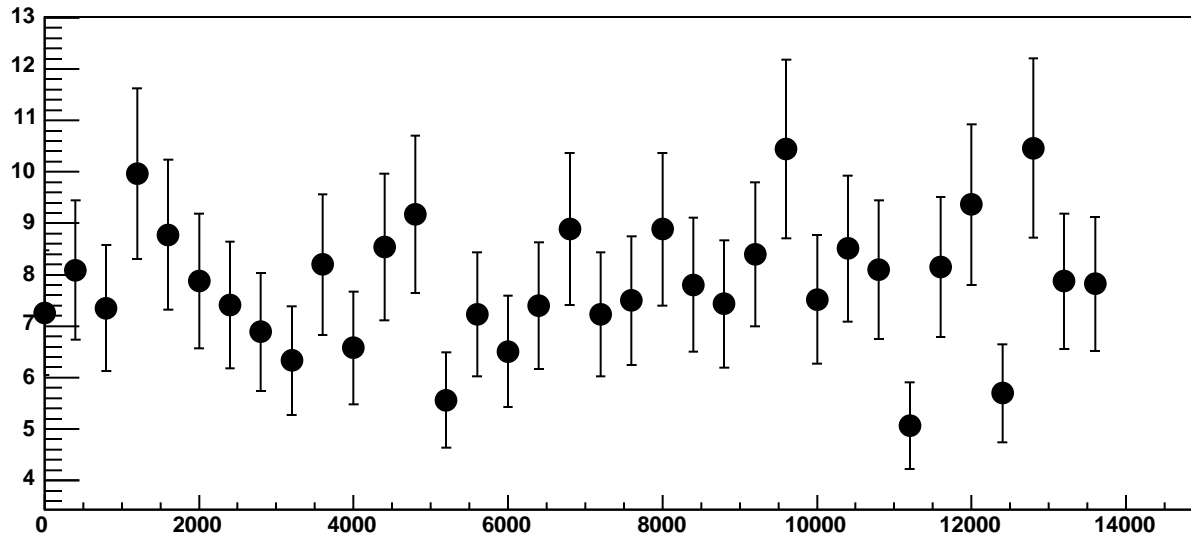


Chip 0, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

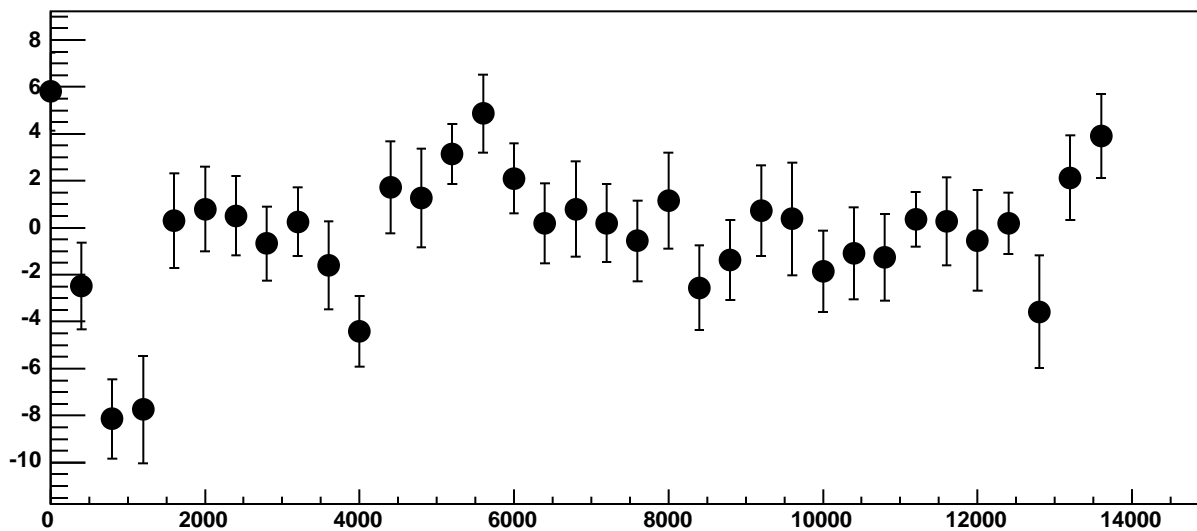


χ^2 / ndf 44.47 / 23
p0 15.88 ± 0.8332
p1 0.02242 ± 0.0001288

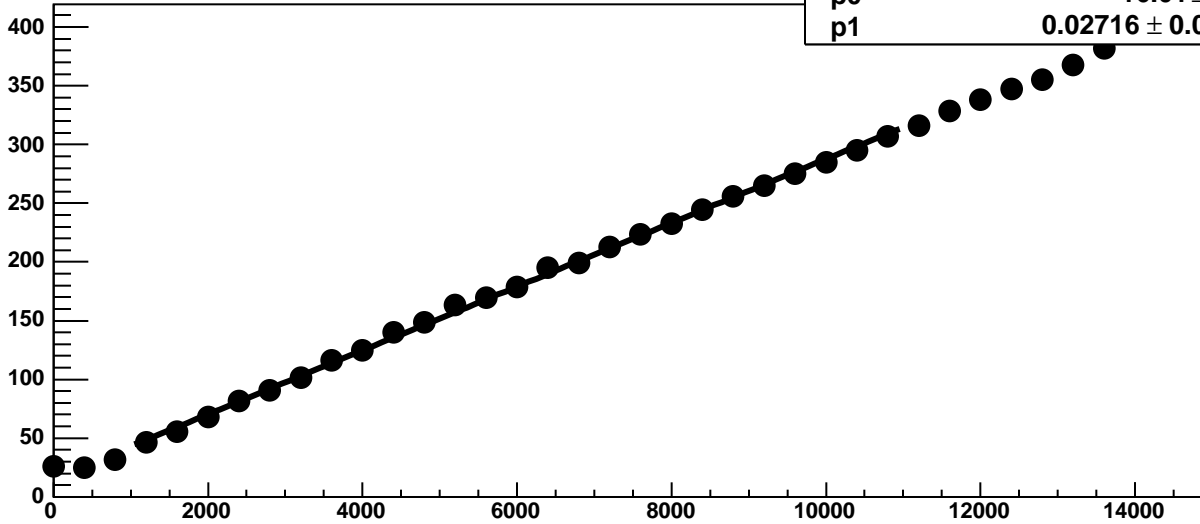
Chip 0, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

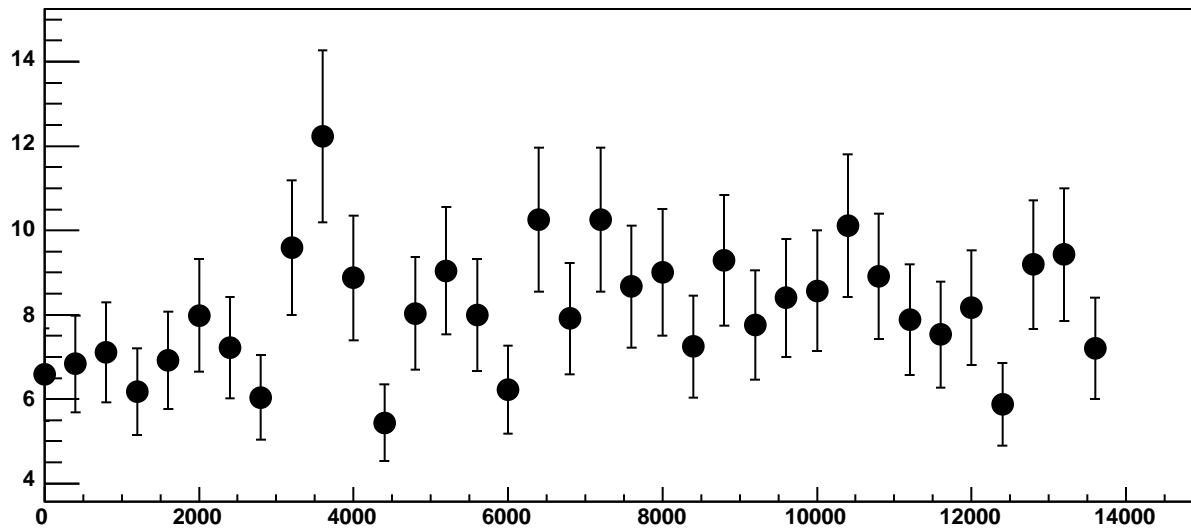


Chip 0, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

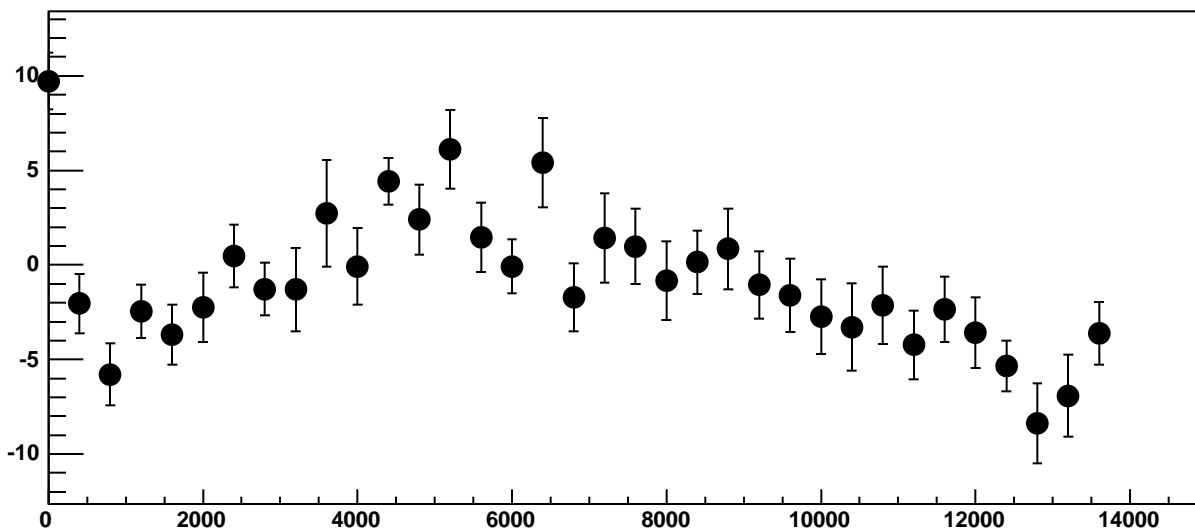


χ^2 / ndf 48.87 / 23
p0 16.01 ± 0.7829
p1 0.02716 ± 0.0001252

Chip 0, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC

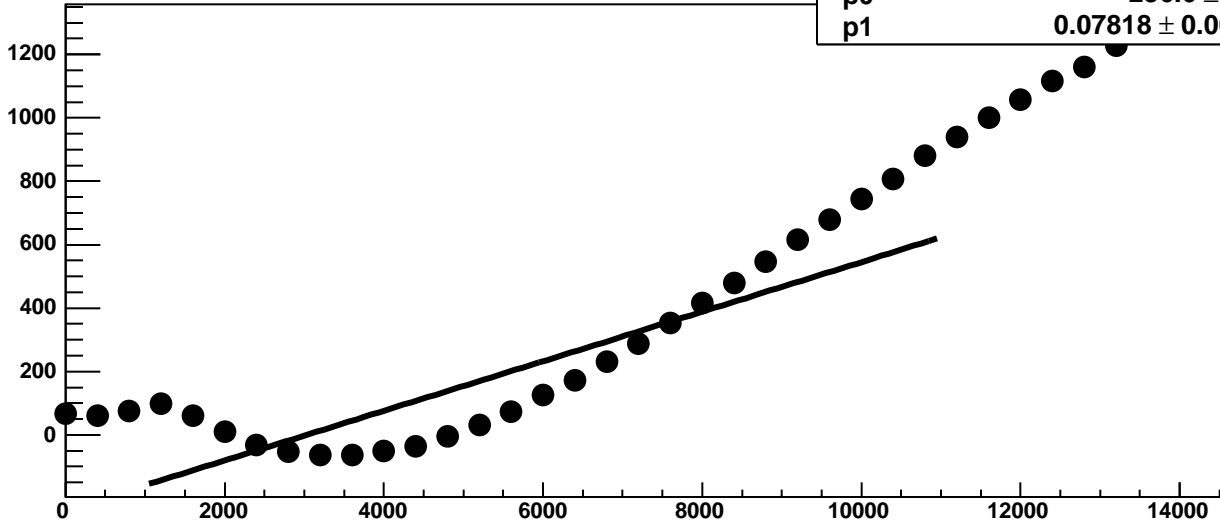


Chip 0, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC

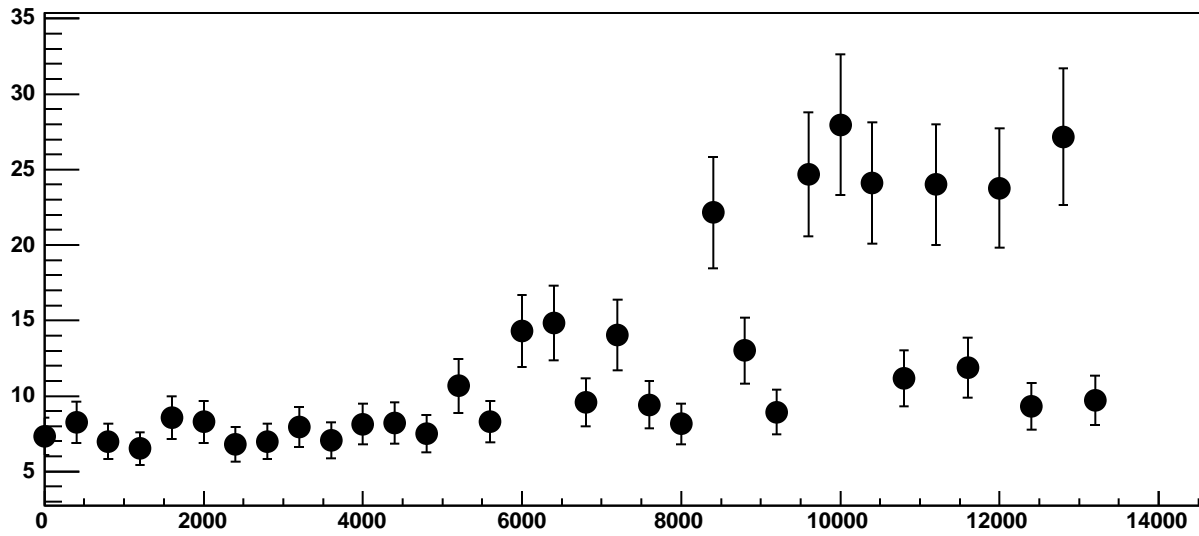


Chip 0, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

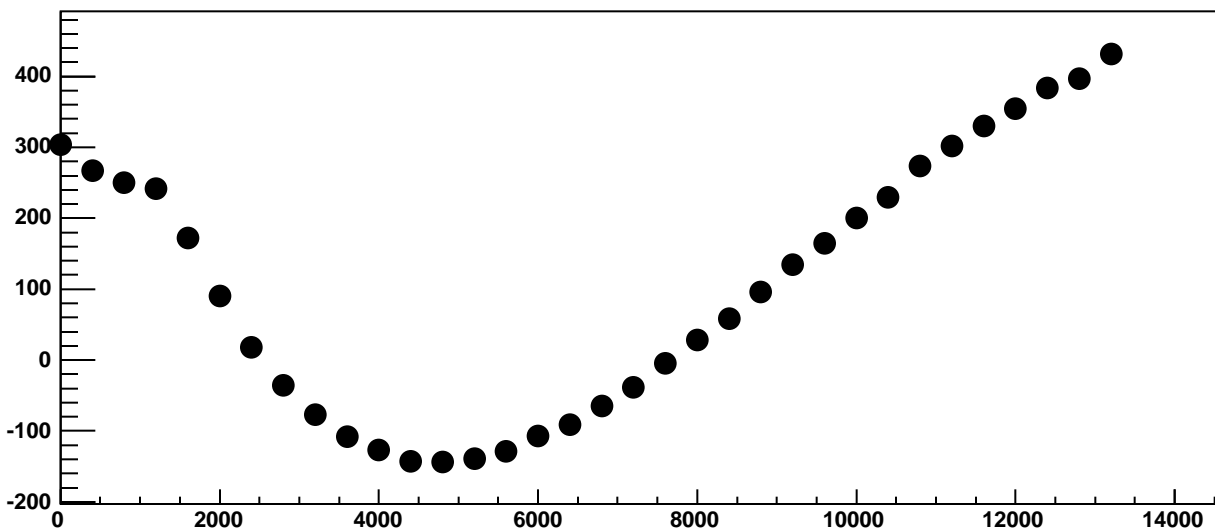
χ^2 / ndf 9.153e+04 / 23
p0 -236.6 ± 0.8804
p1 0.07818 ± 0.0001636



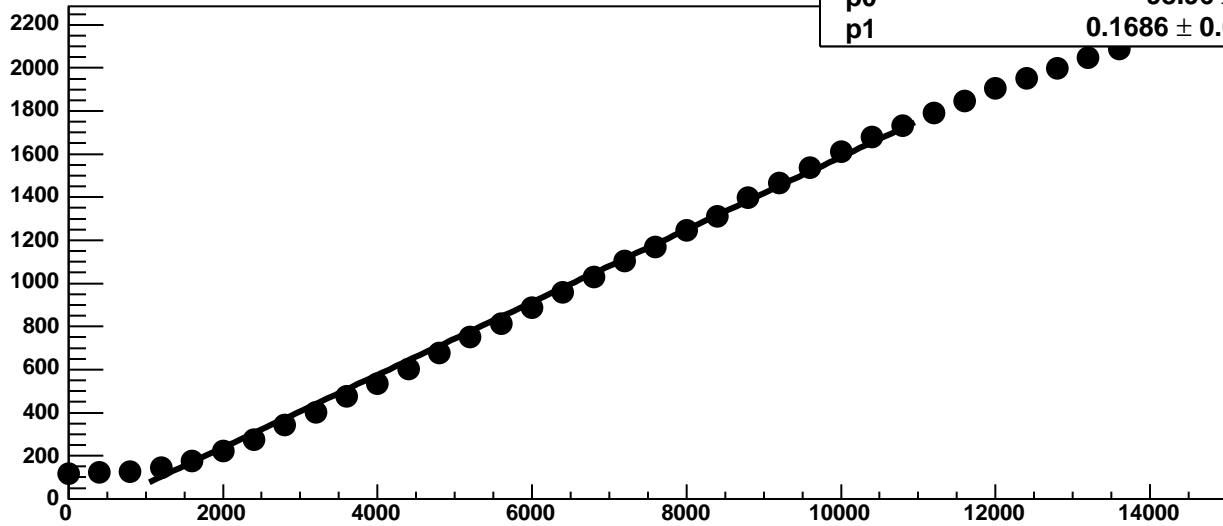
Chip 0, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

2180 / 23

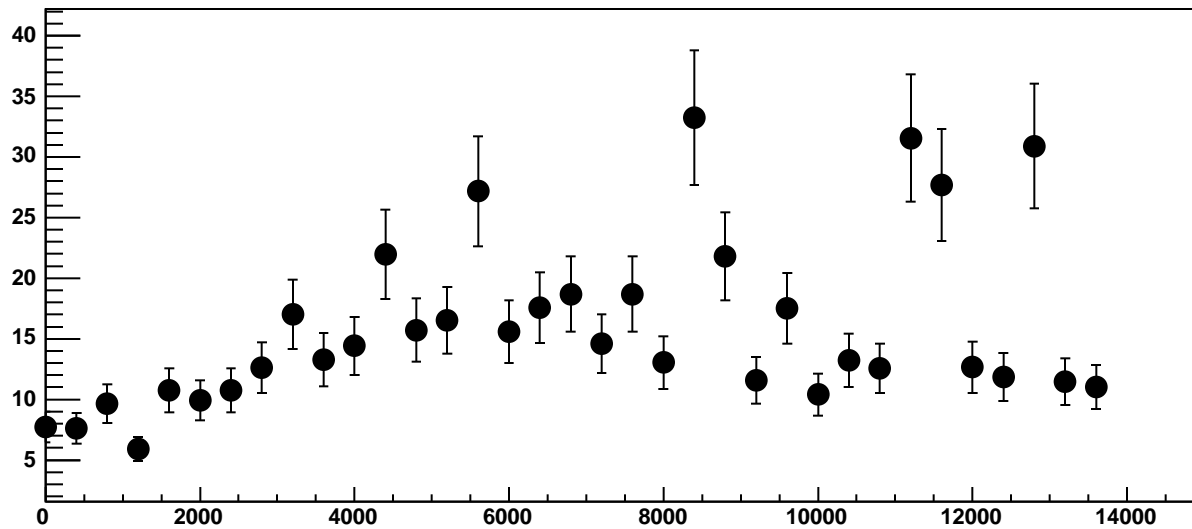
p0

-98.96 ± 1.063

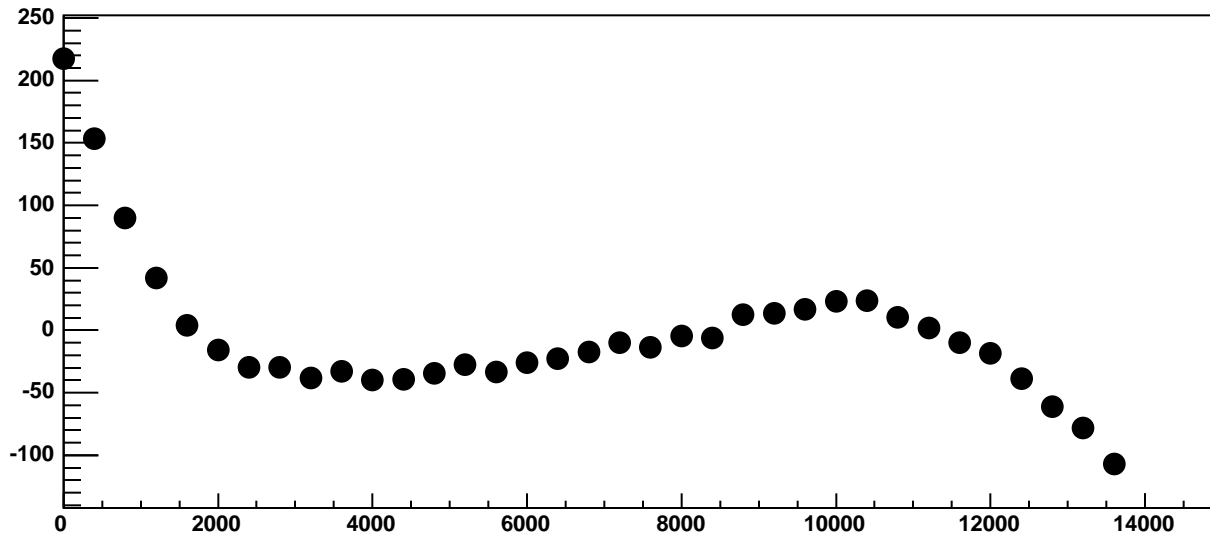
p1

0.1686 ± 0.000176

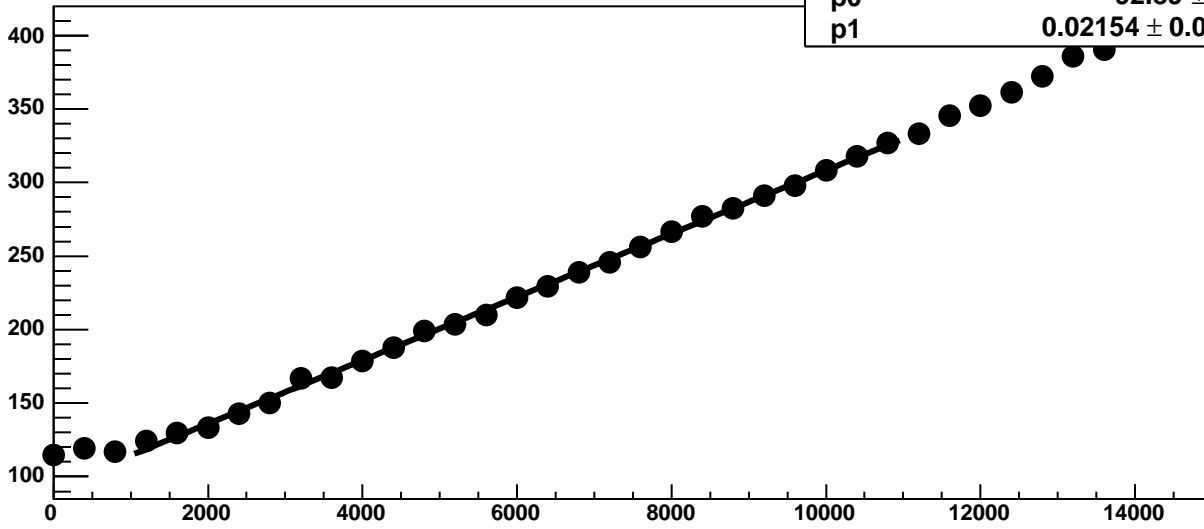
Chip 0, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

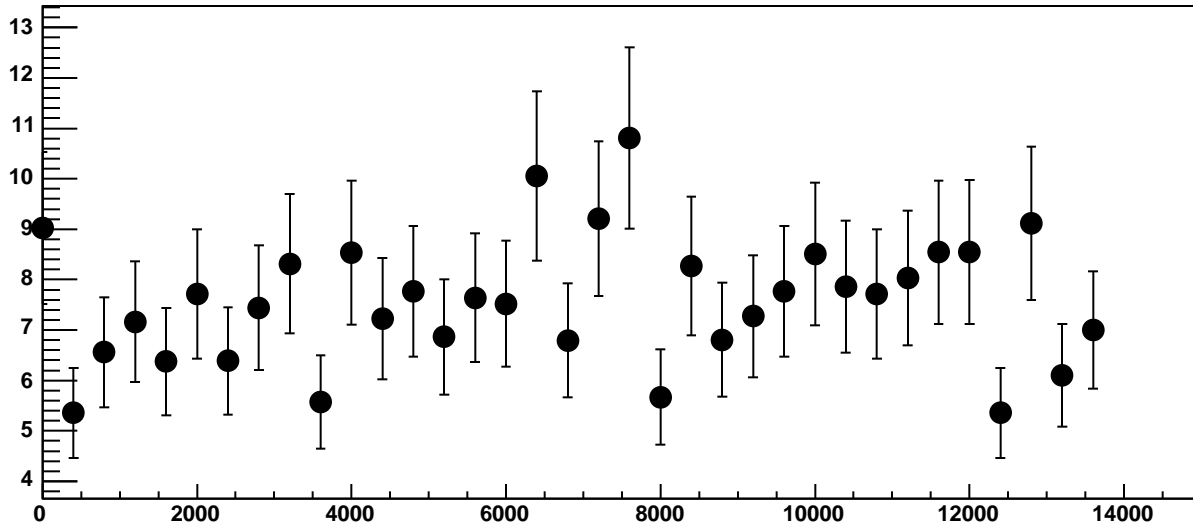


Chip 0, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

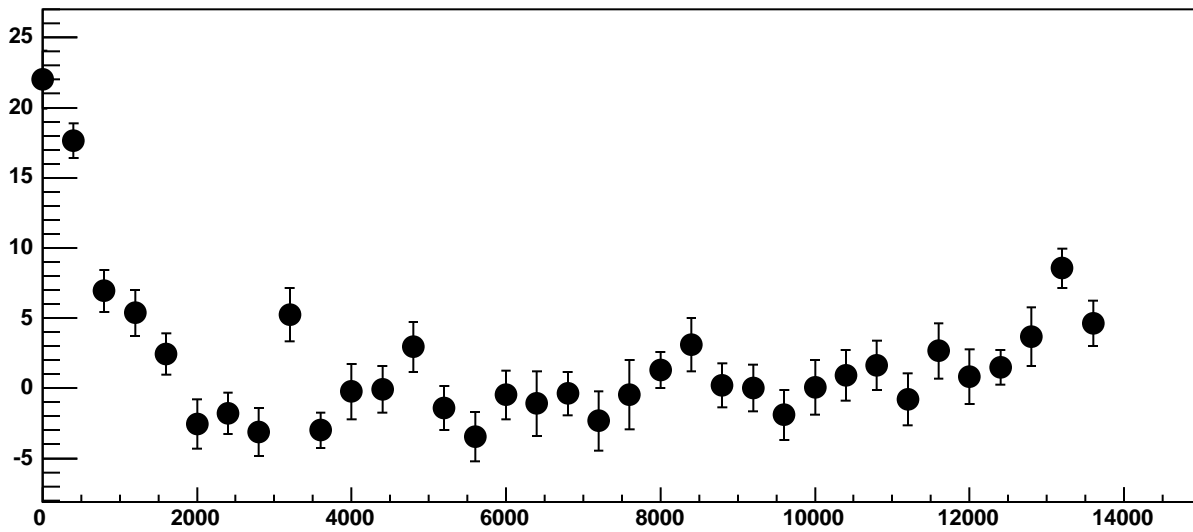


χ^2 / ndf 48.39 / 23
p0 92.89 ± 0.7539
p1 0.02154 ± 0.0001165

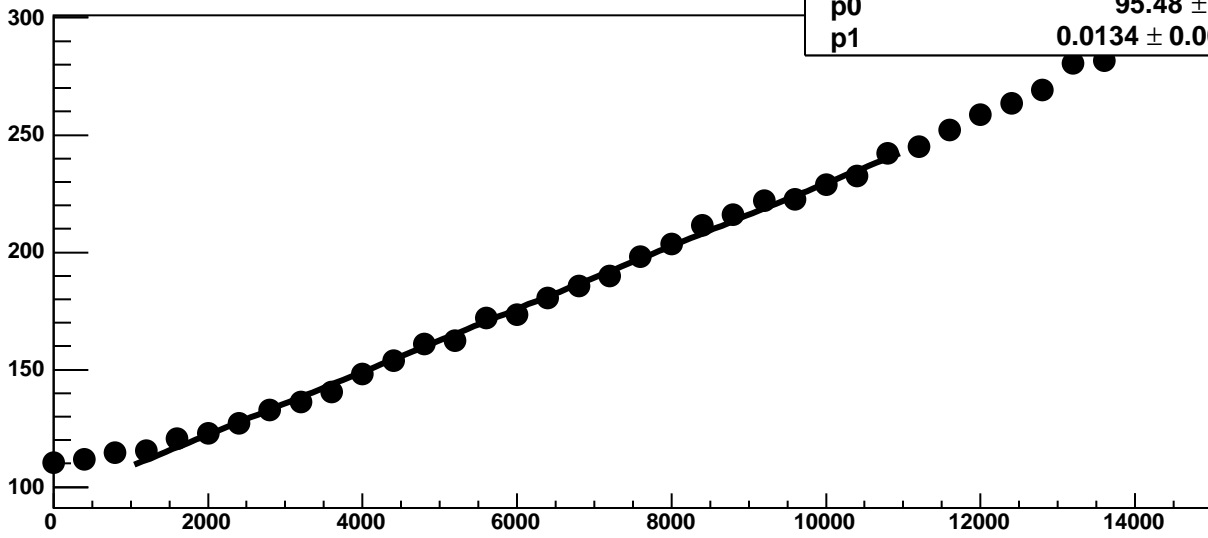
Chip 0, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC

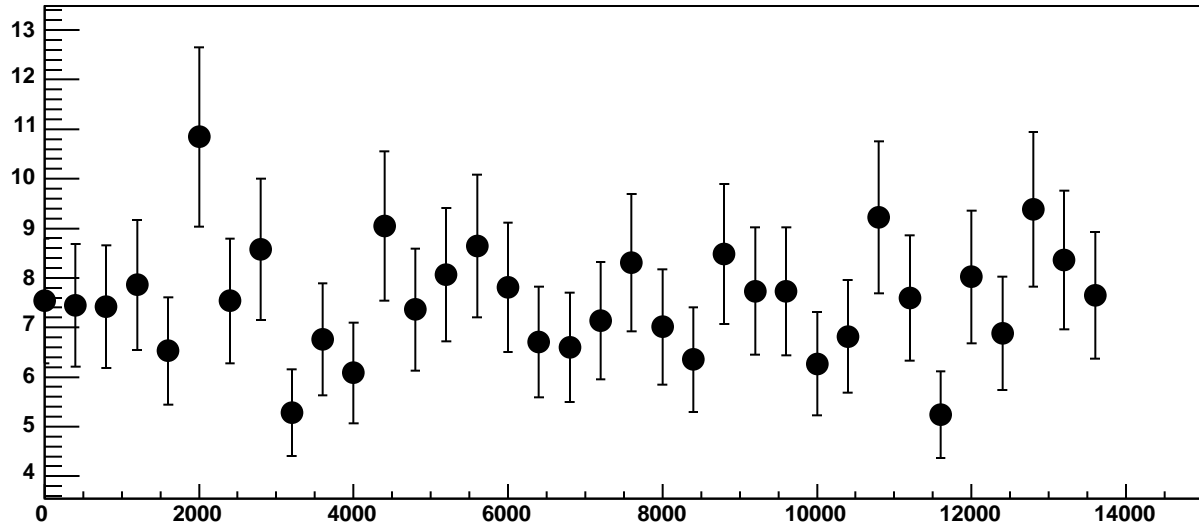


Chip 0, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

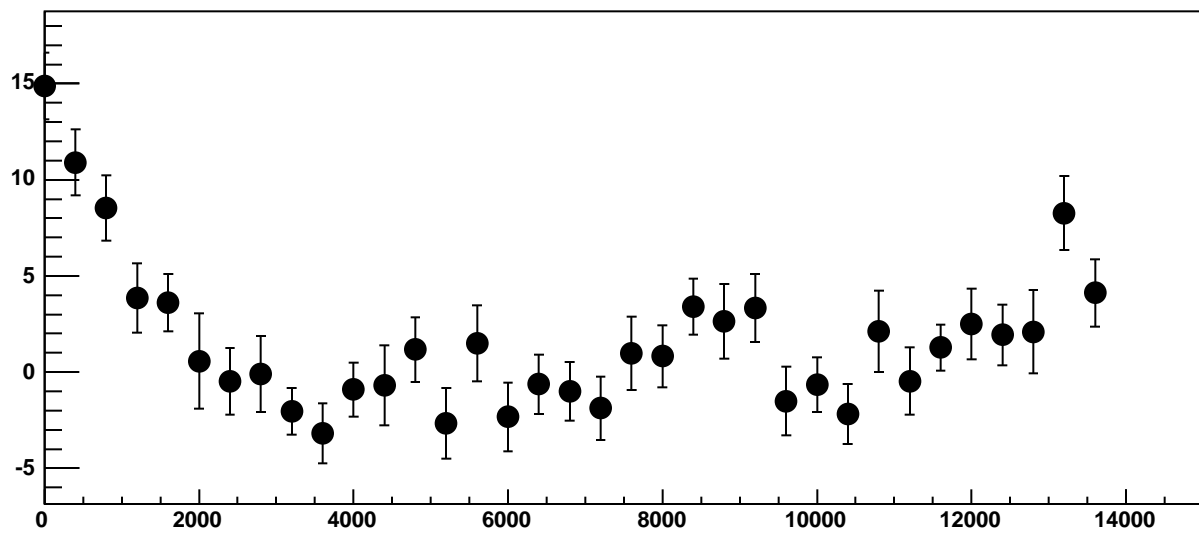


χ^2 / ndf 40.02 / 23
p0 95.48 ± 0.7782
p1 0.0134 ± 0.0001174

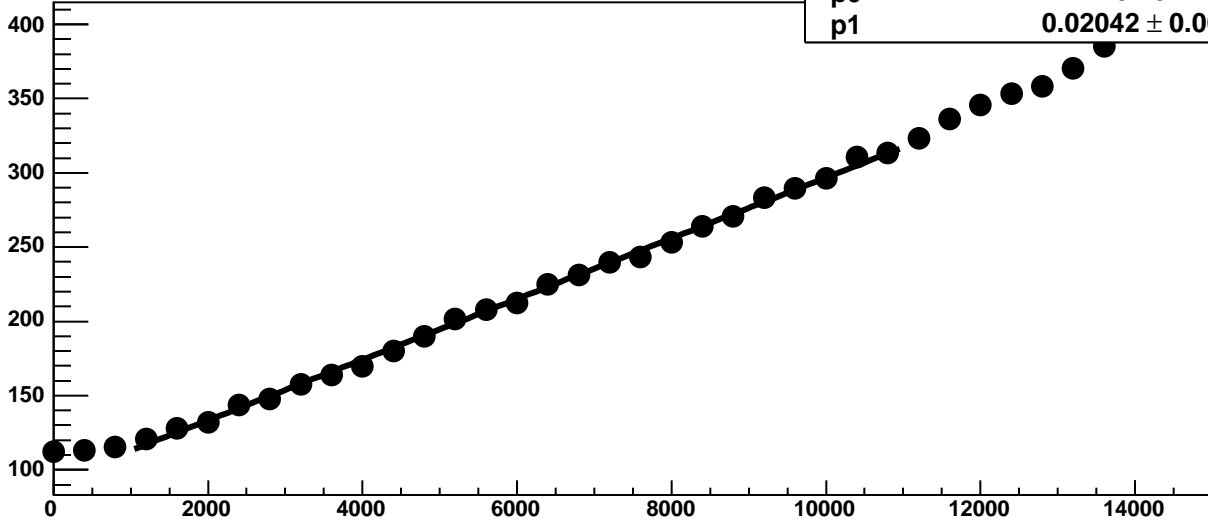
Chip 0, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

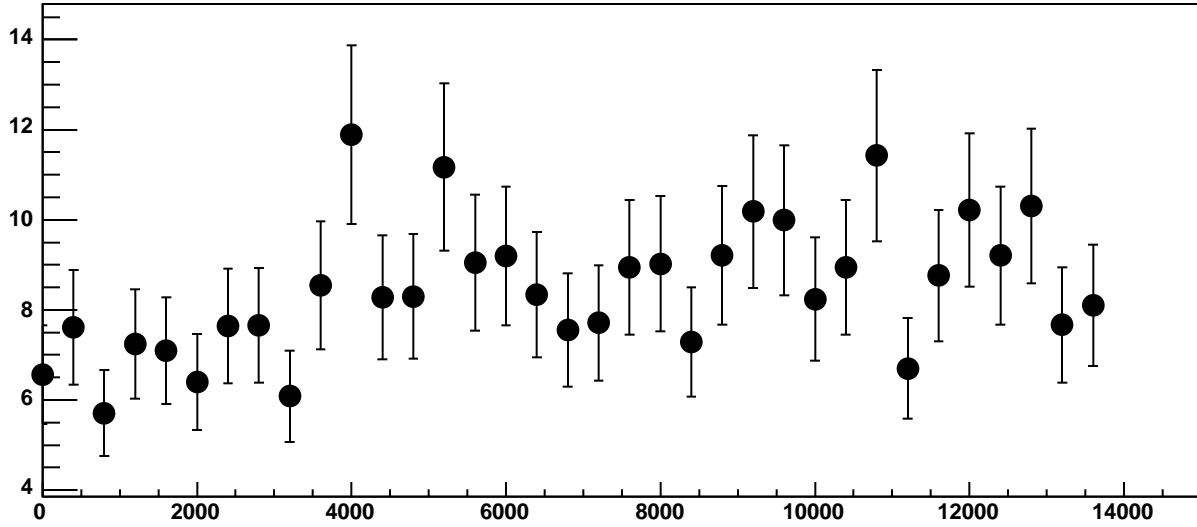


Chip 0, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

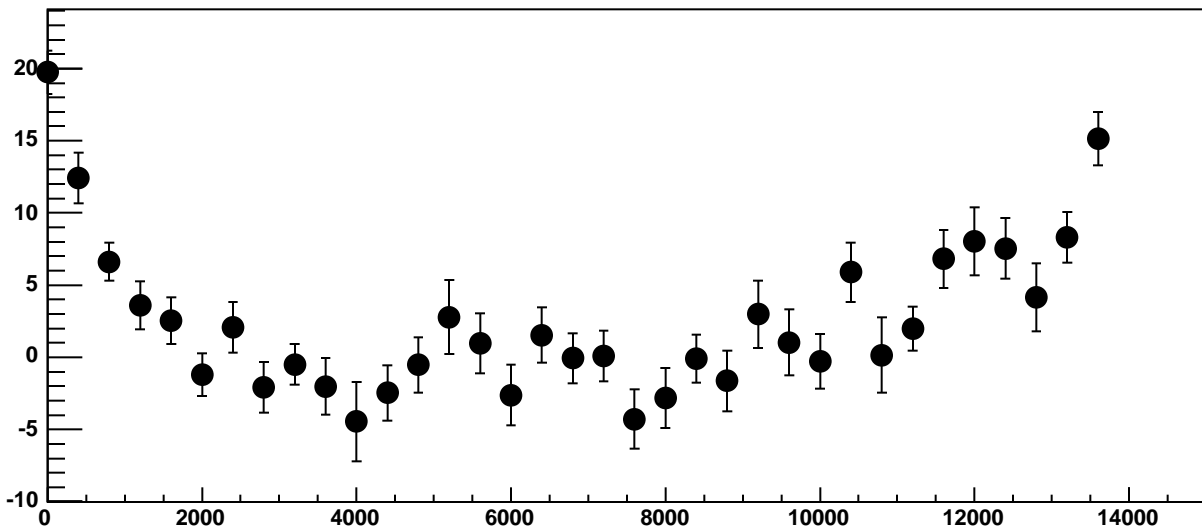


χ^2 / ndf 36.77 / 23
p0 92.57 ± 0.8094
p1 0.02042 ± 0.0001303

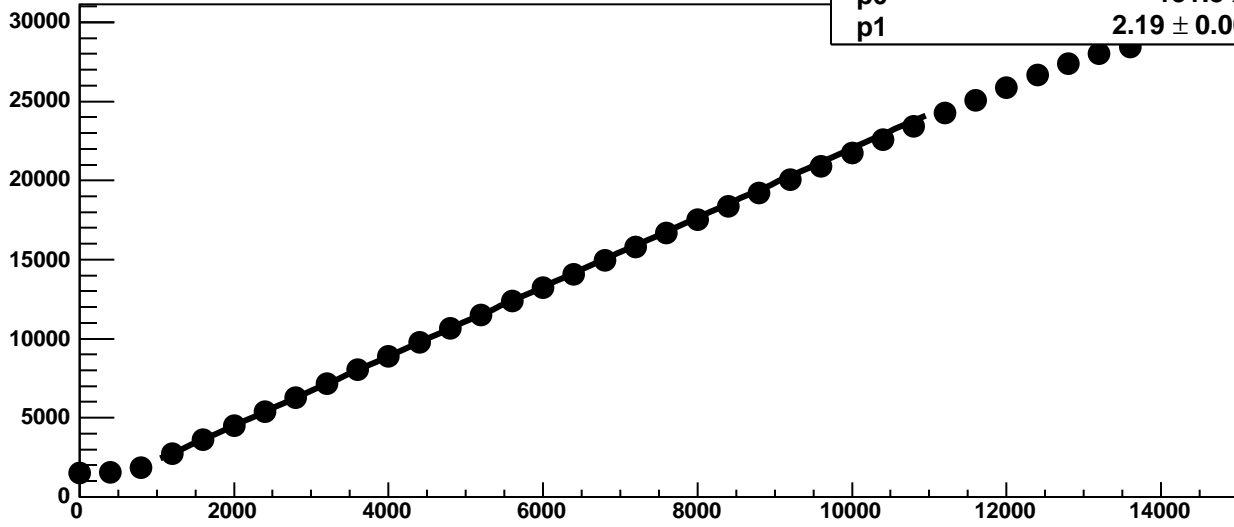
Chip 0, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

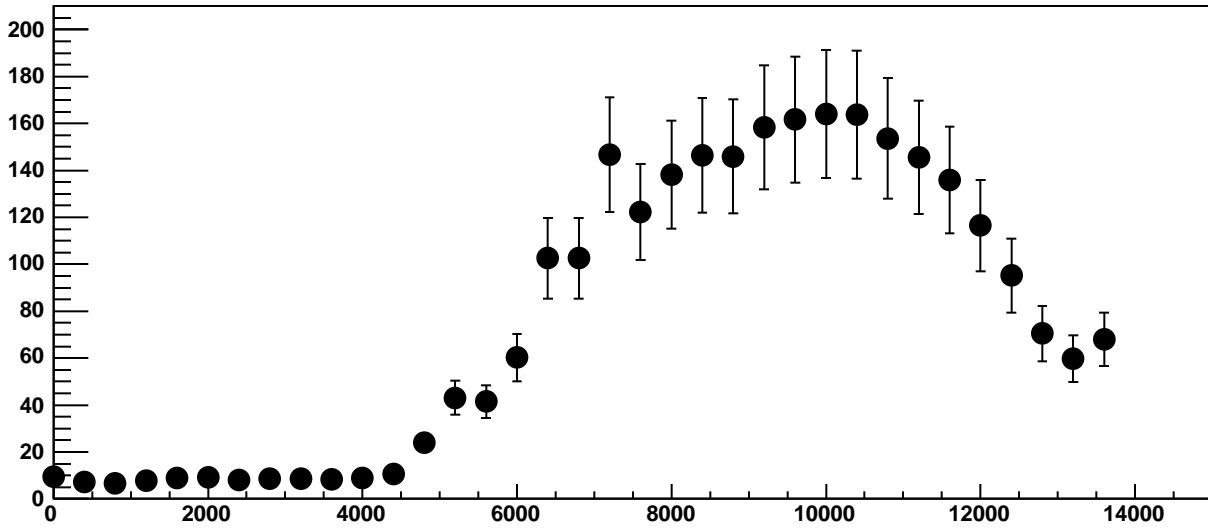


Chip 0, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC

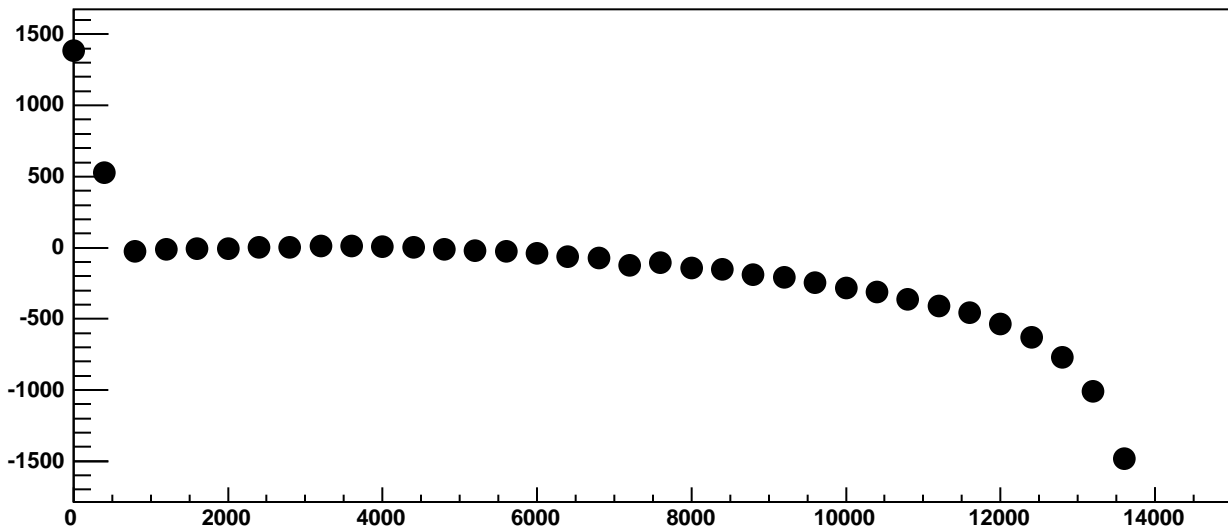


χ^2 / ndf 584.5 / 23
p0 131.8 ± 1.745
p1 2.19 ± 0.0005765

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC

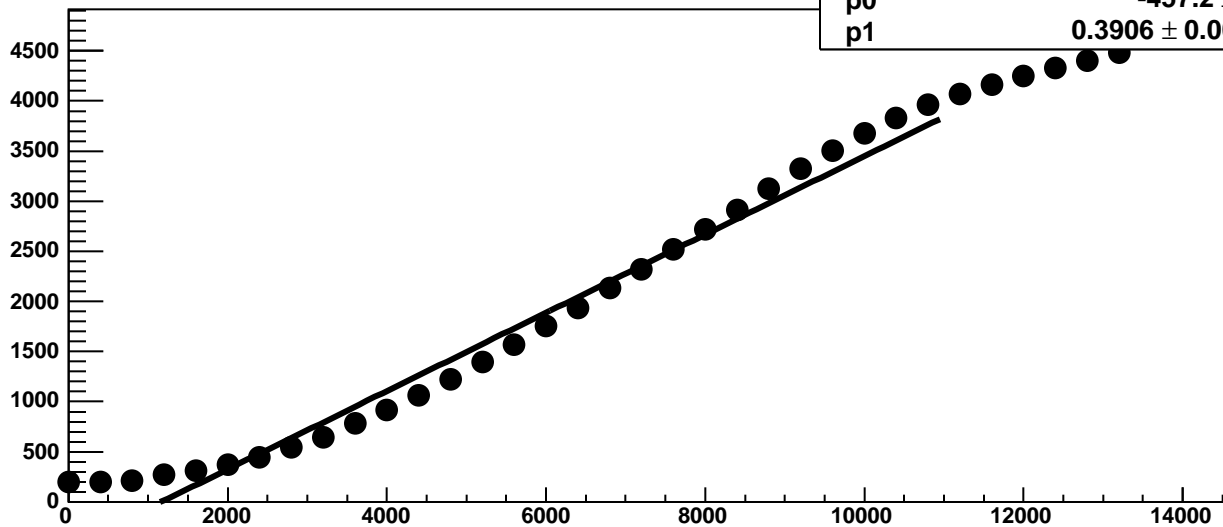


Chip 0, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC

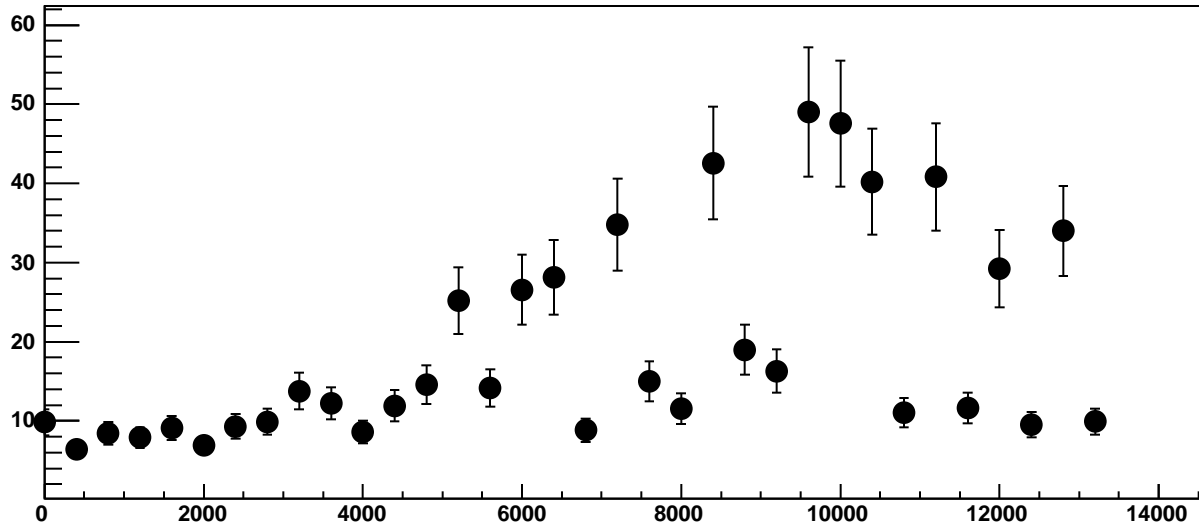


Chip 0, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

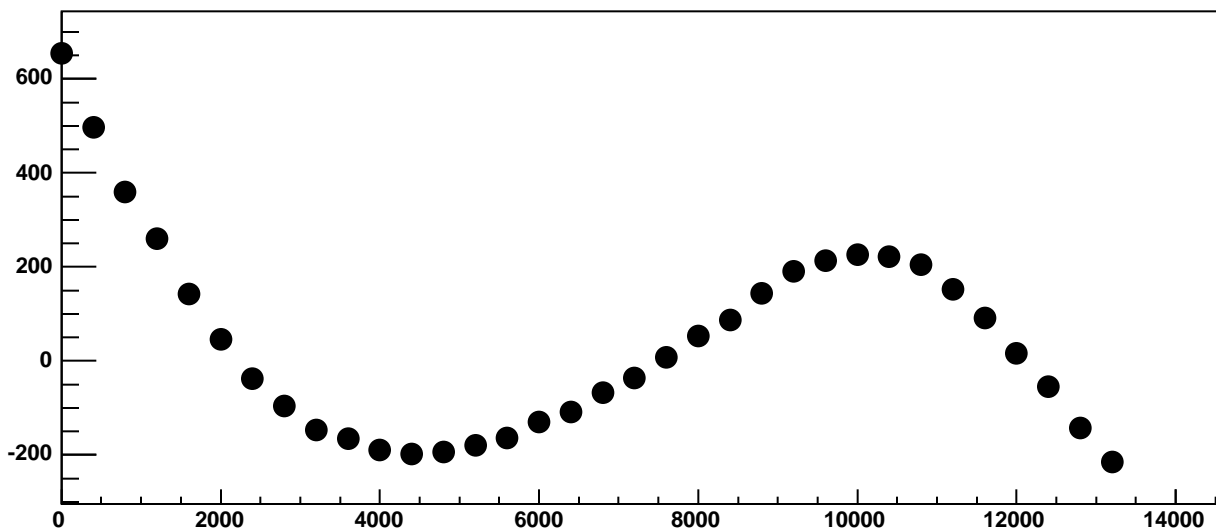
χ^2 / ndf 6.913e+04 / 23
p0 -457.2 ± 1.064
p1 0.3906 ± 0.0002063



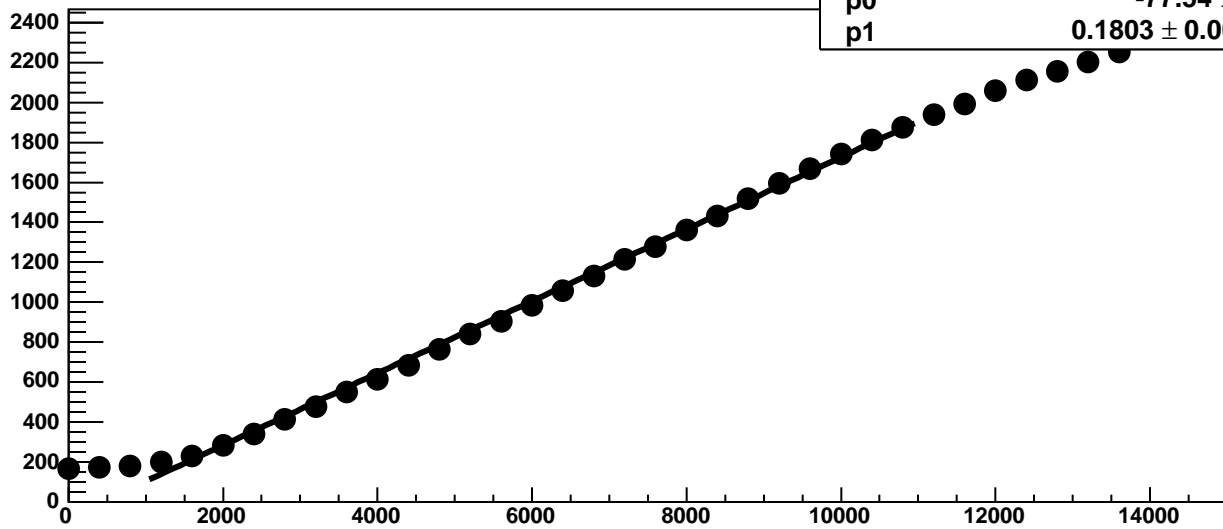
Chip 0, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1307 / 23

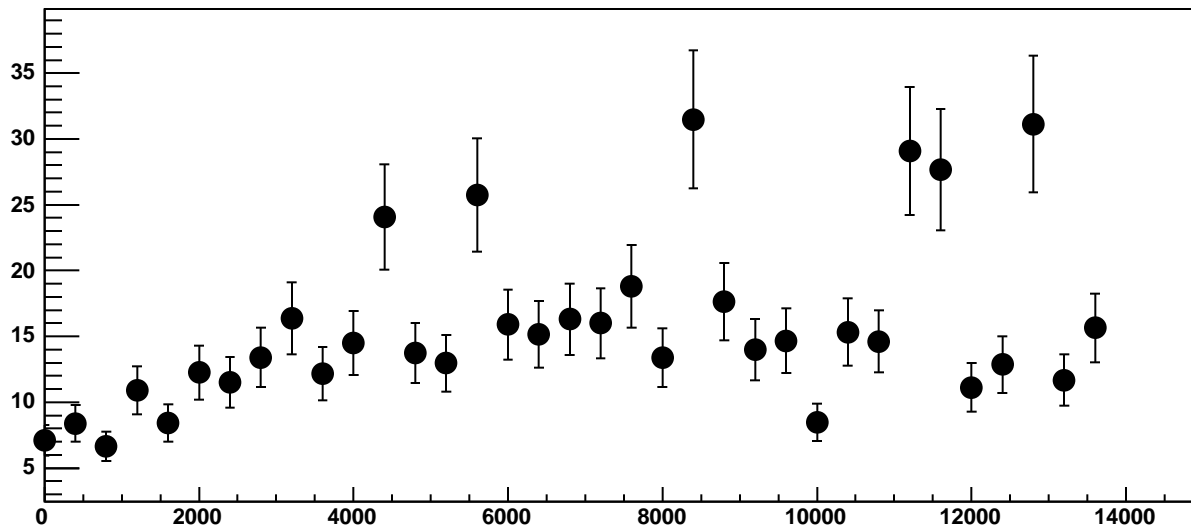
p0

-77.54 ± 1.247

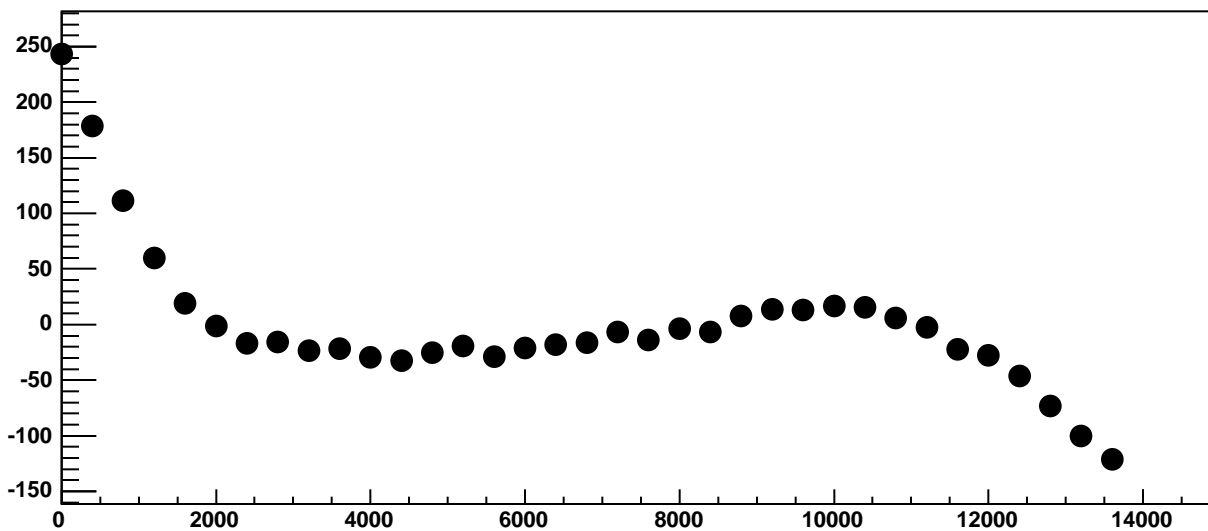
p1

0.1803 ± 0.0001934

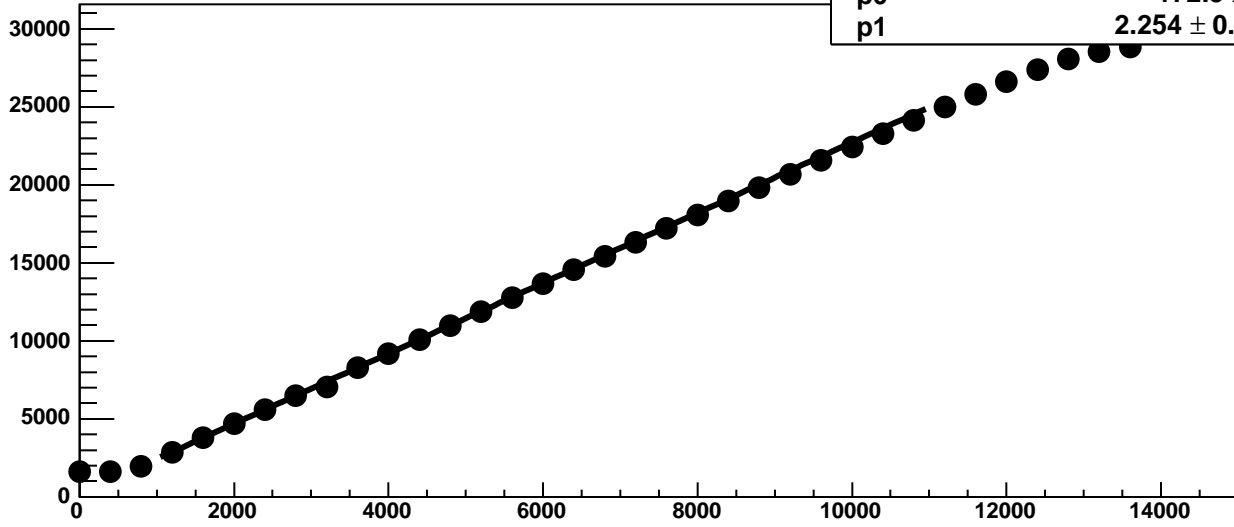
Chip 0, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

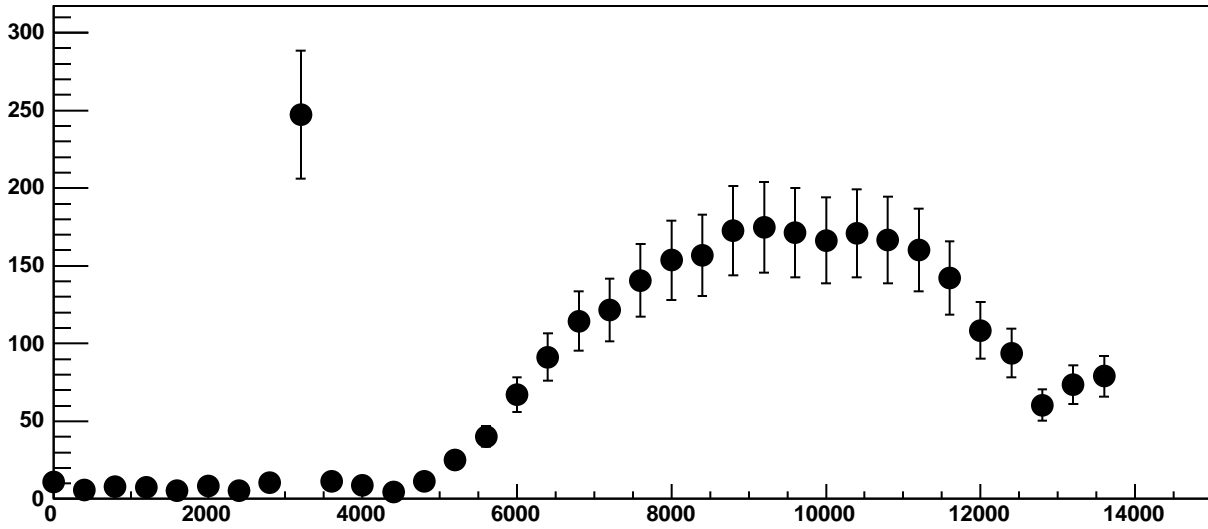


Chip 0, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC

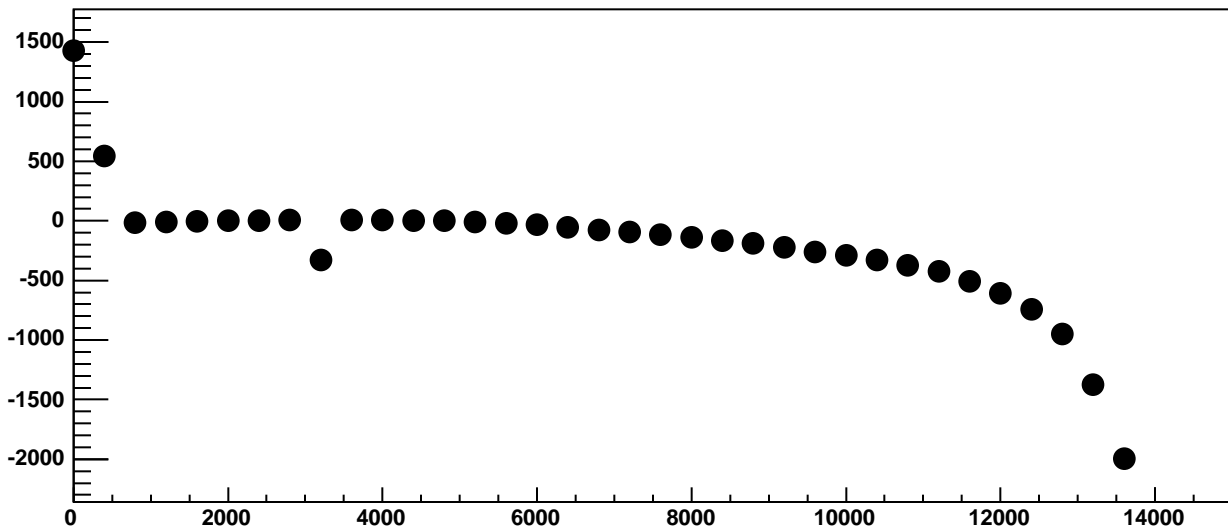


χ^2 / ndf 535.3 / 23
p0 172.5 ± 1.285
p1 2.254 ± 0.000401

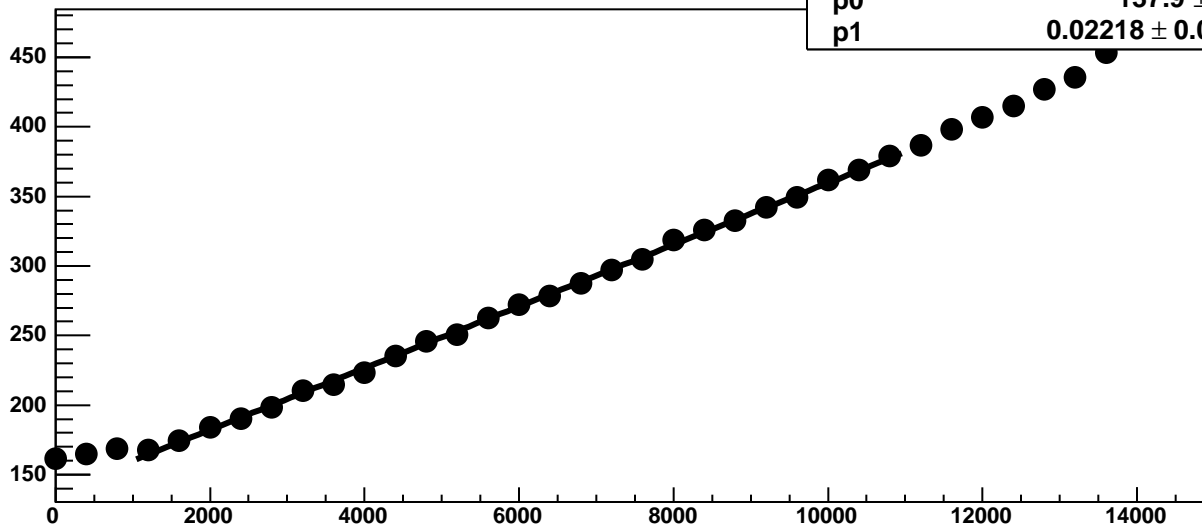
Chip 0, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC

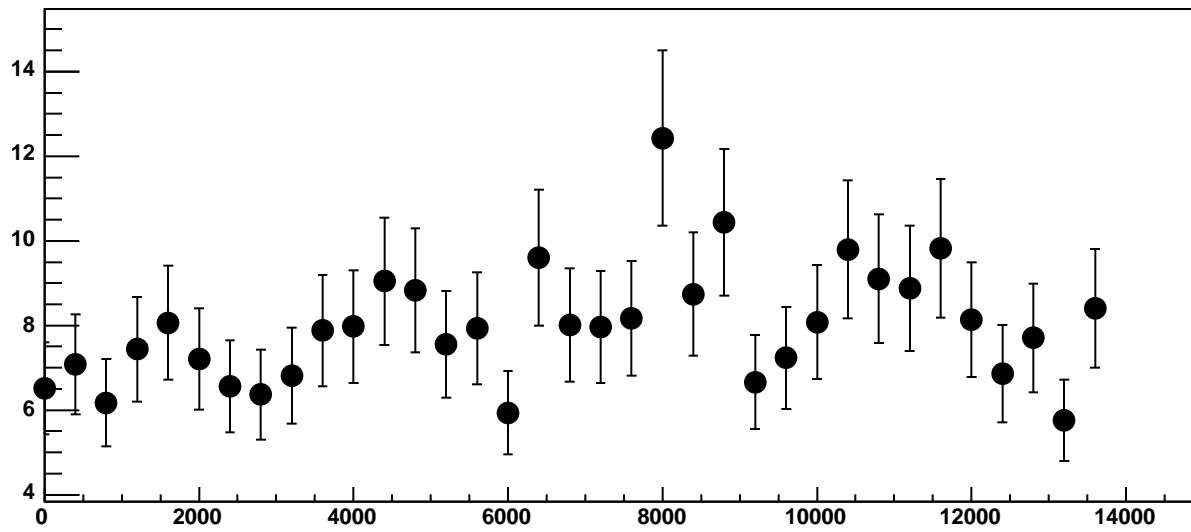


Chip 0, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

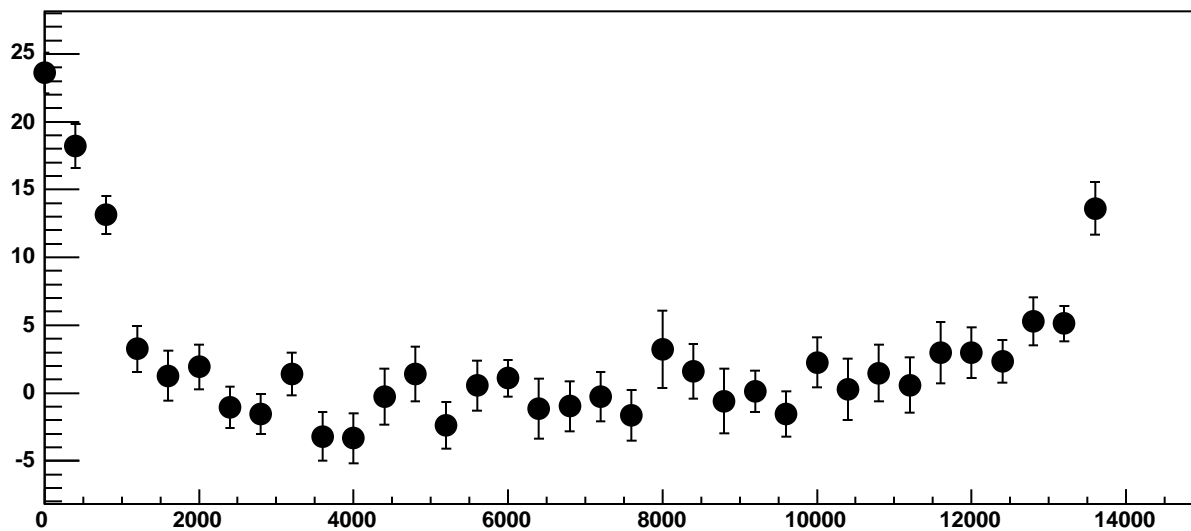


χ^2 / ndf 23.61 / 23
p0 137.9 ± 0.7929
p1 0.02218 ± 0.0001251

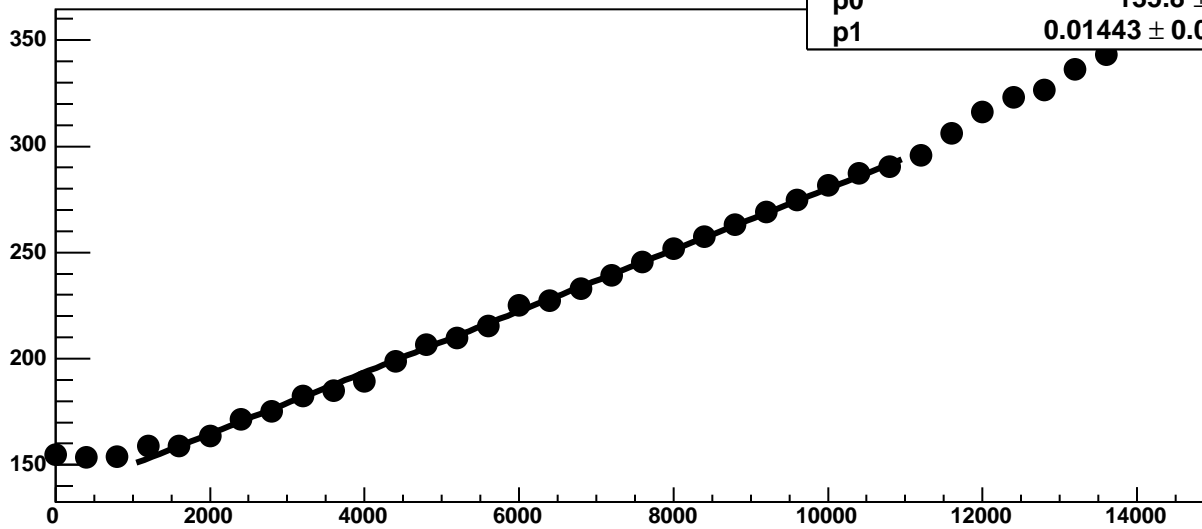
Chip 0, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

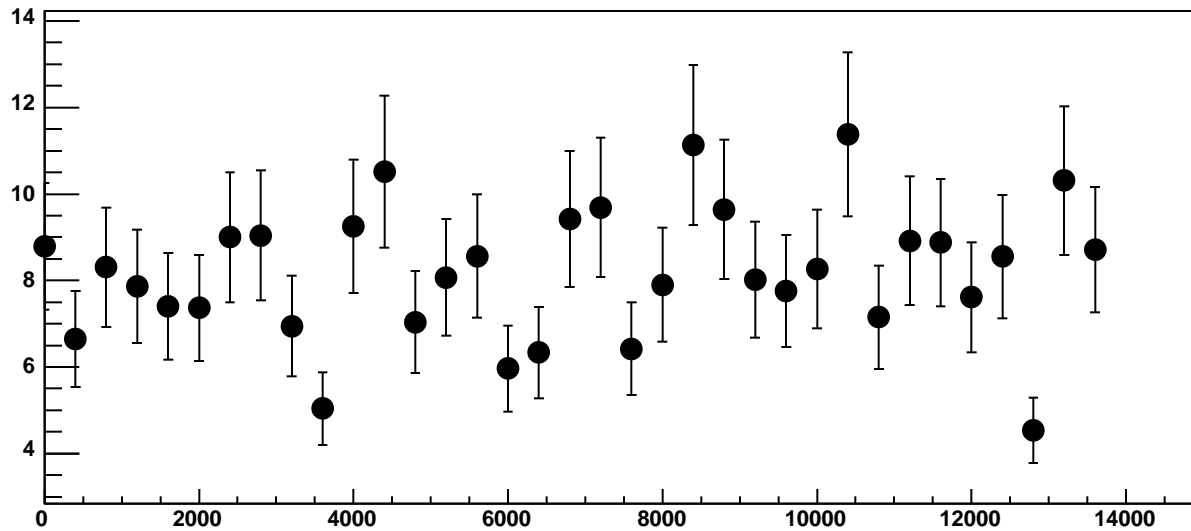


Chip 0, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

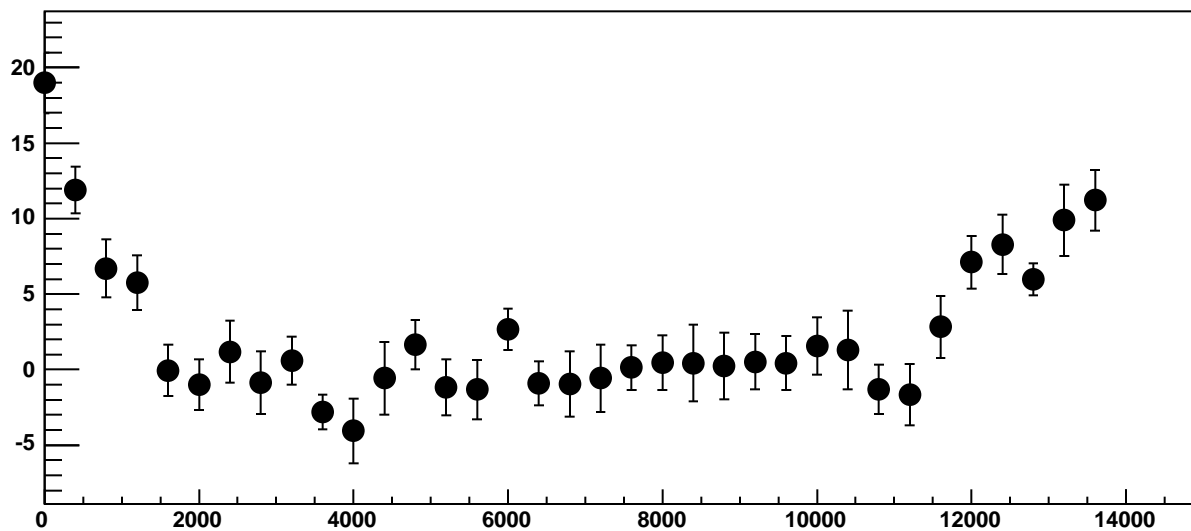


χ^2 / ndf 28.96 / 23
p0 135.8 ± 0.8124
p1 0.01443 ± 0.0001268

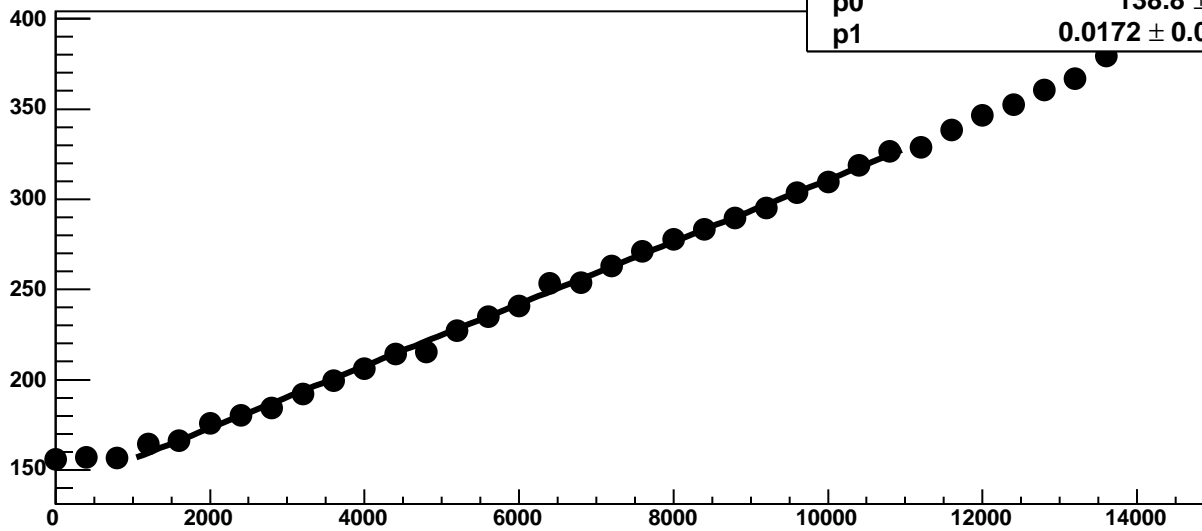
Chip 0, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

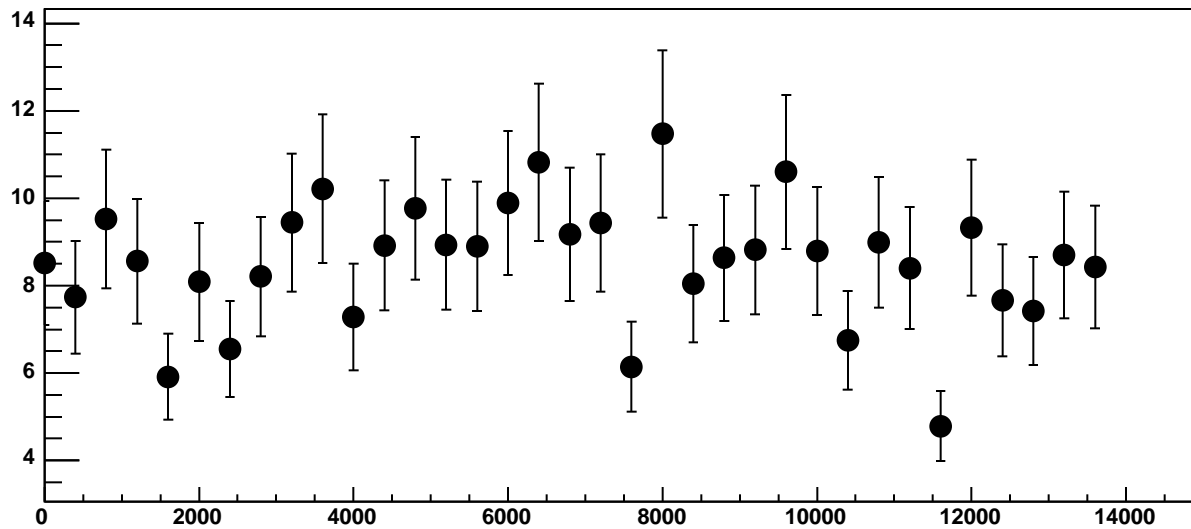


Chip 0, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

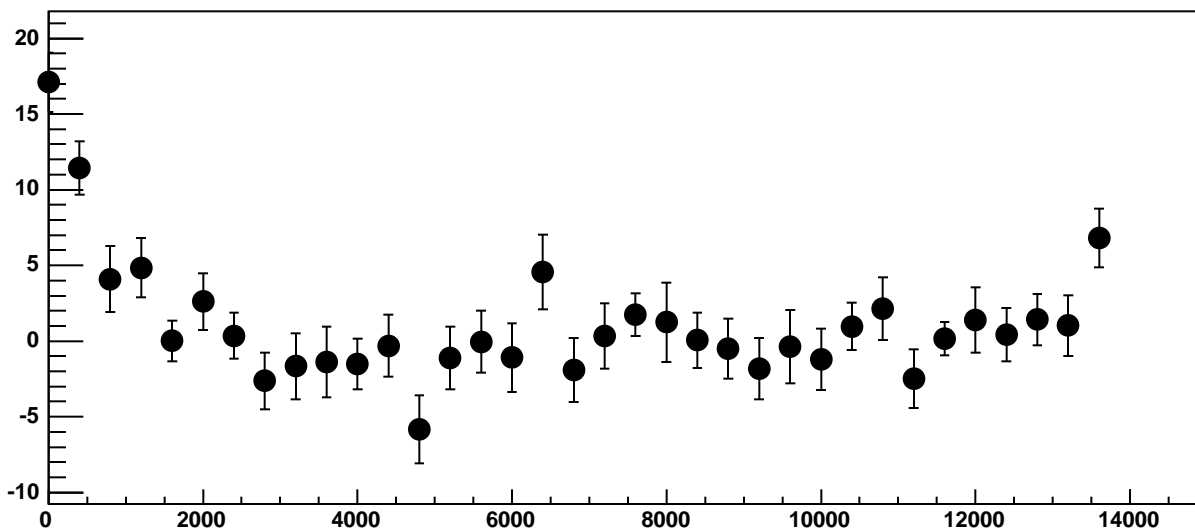


χ^2 / ndf 27.84 / 23
p0 138.8 ± 0.8226
p1 0.0172 ± 0.0001262

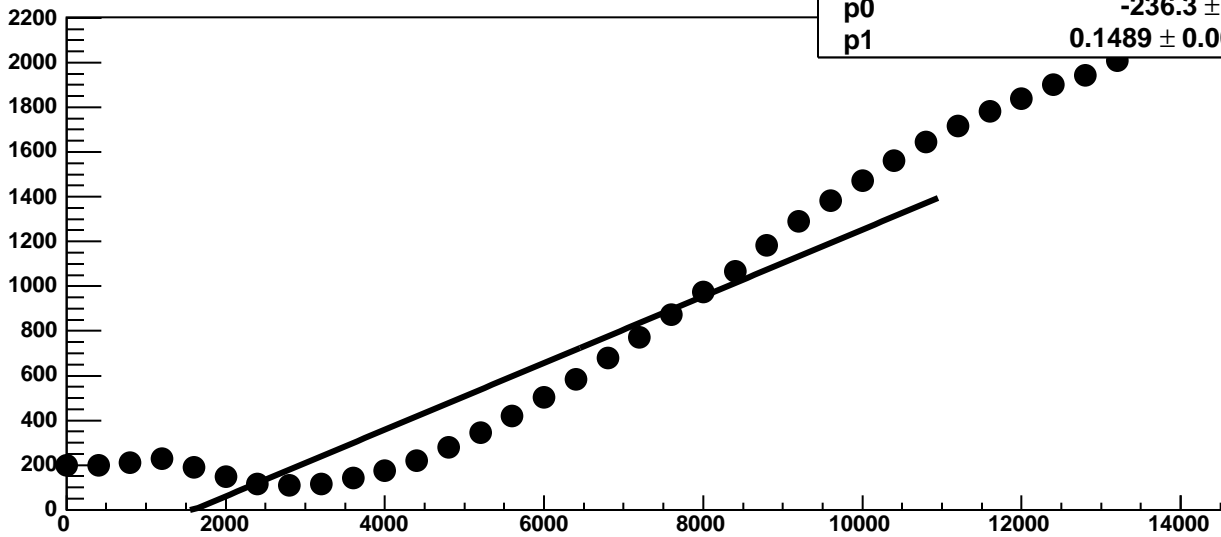
Chip 0, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



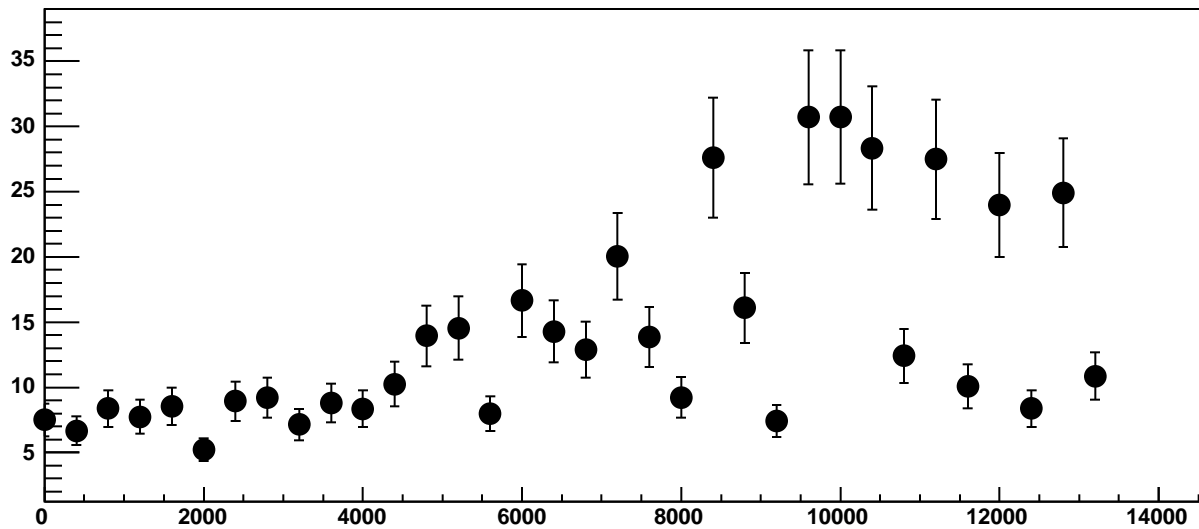
Chip 0, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



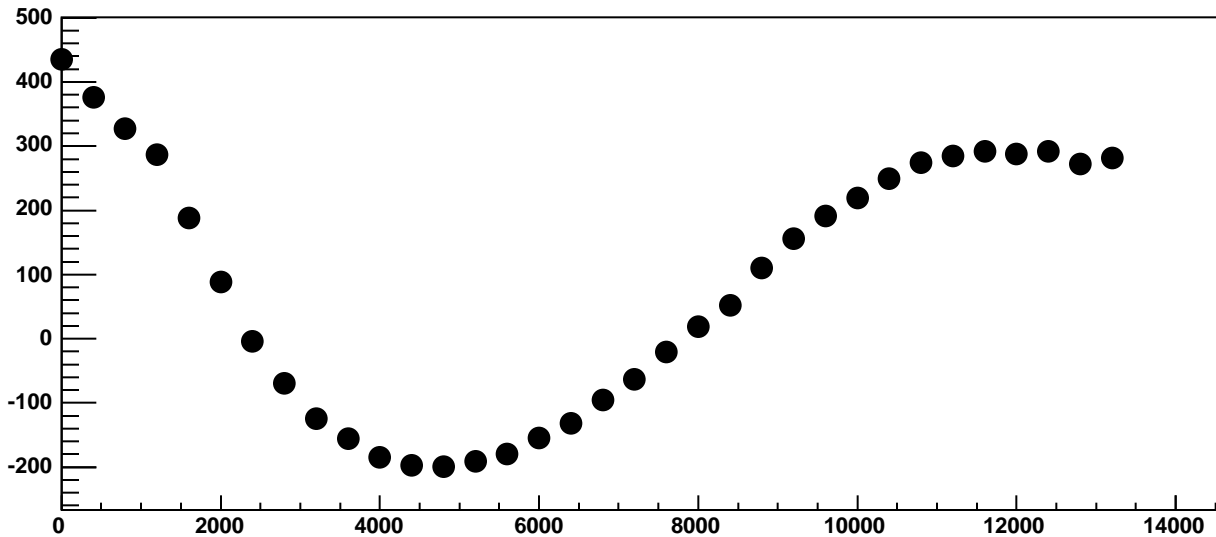
Chip 0, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



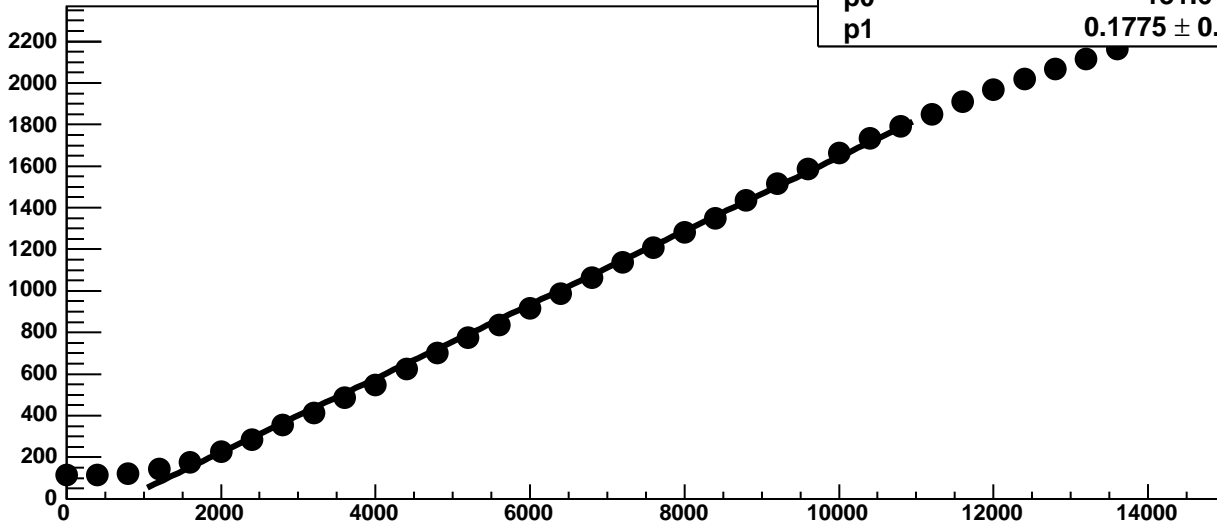
Chip 0, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1243 / 23

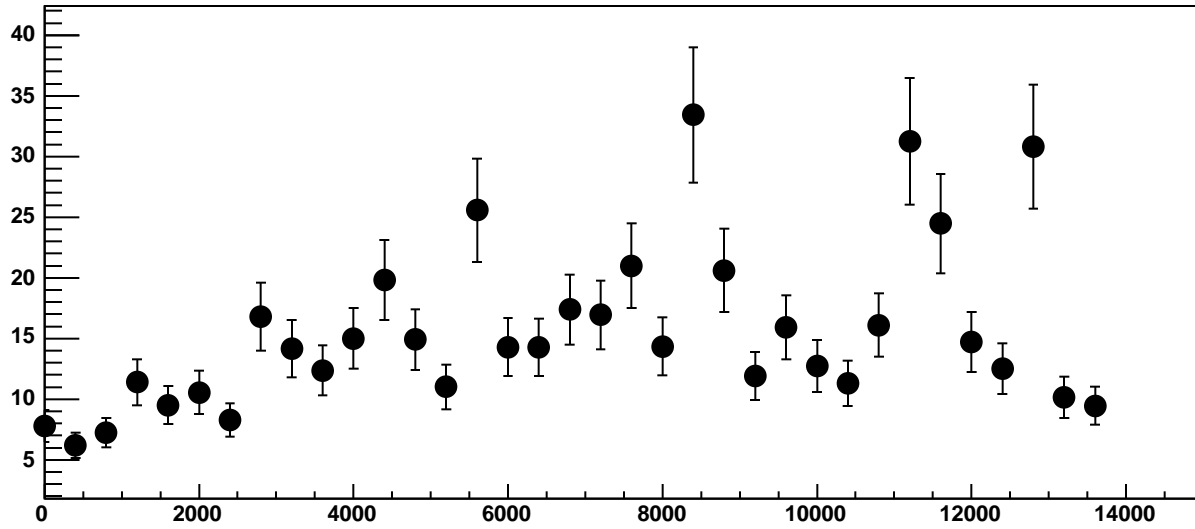
p0

-131.6 ± 1.226

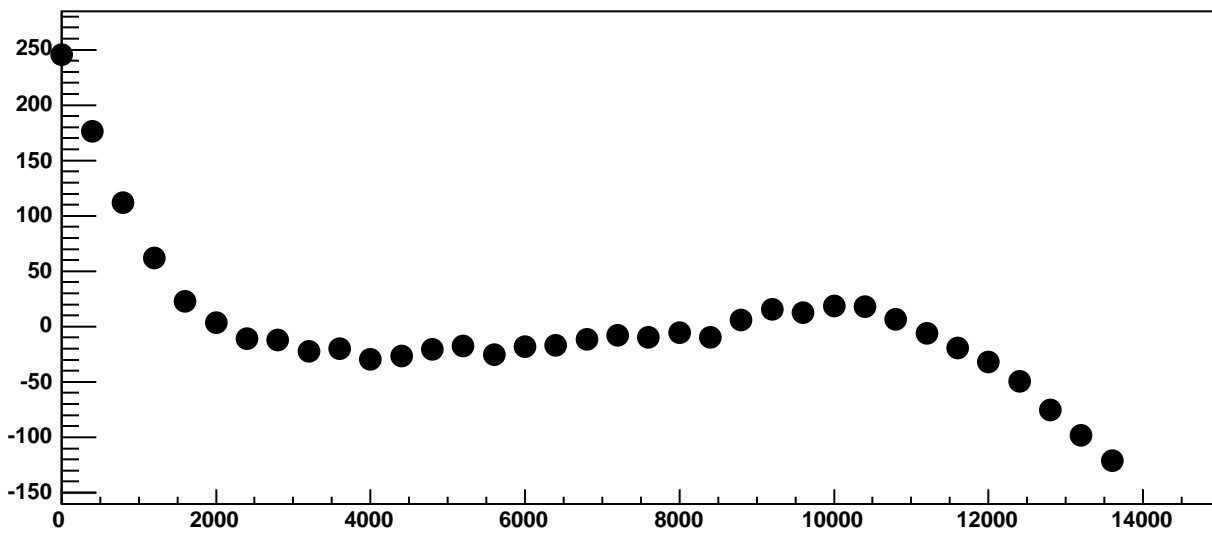
p1

0.1775 ± 0.000199

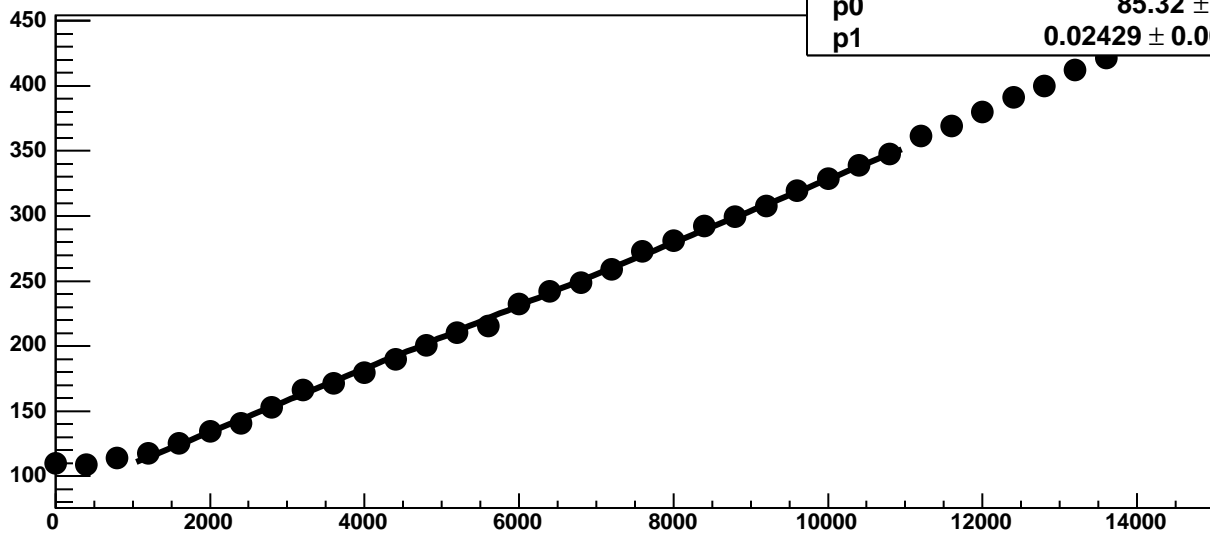
Chip 0, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

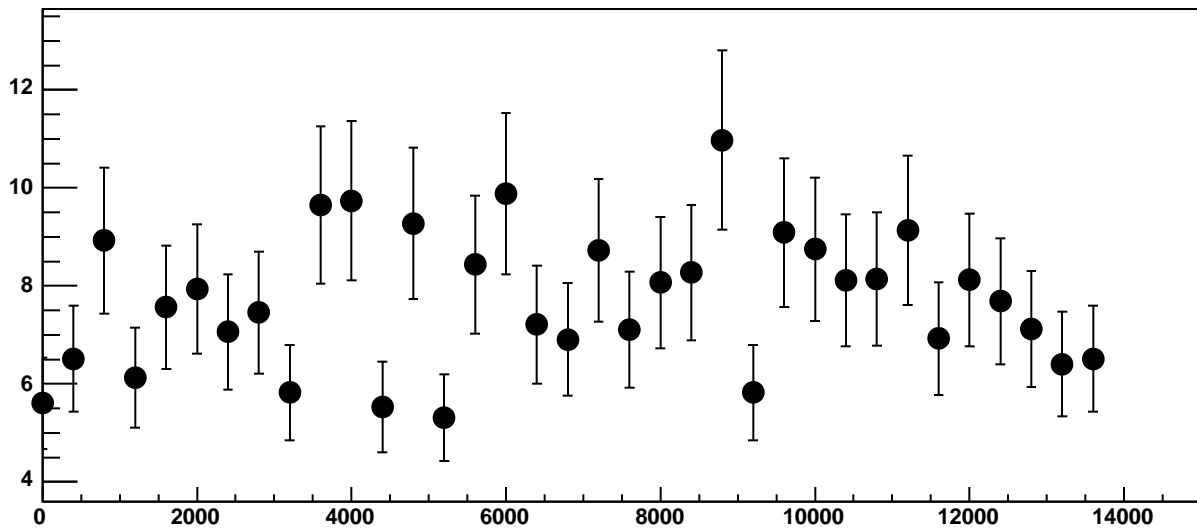


Chip 0, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

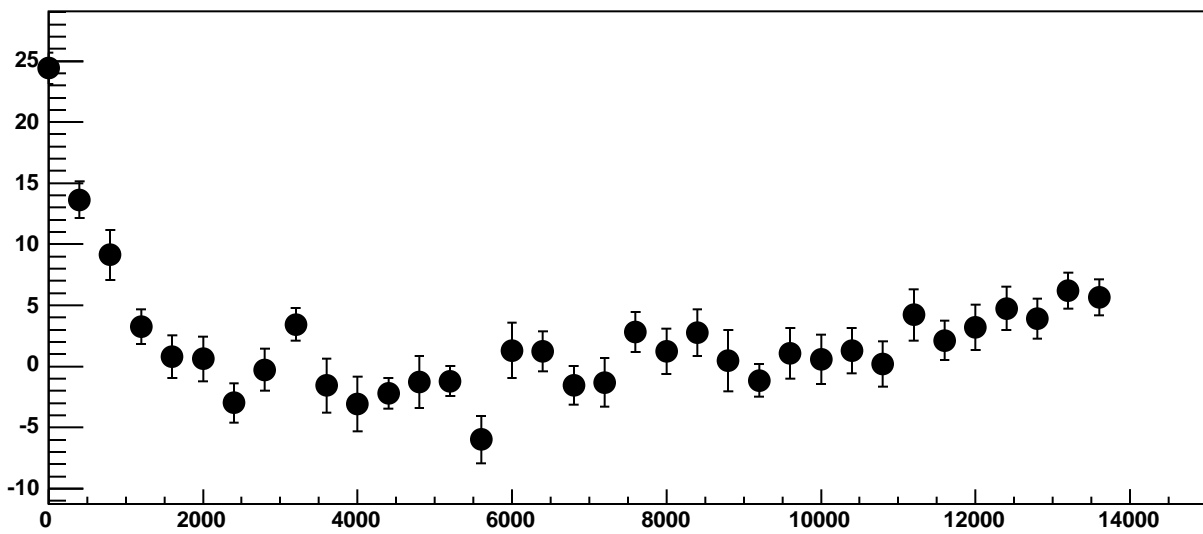


χ^2 / ndf 41.38 / 23
p0 85.32 ± 0.7627
p1 0.02429 ± 0.0001197

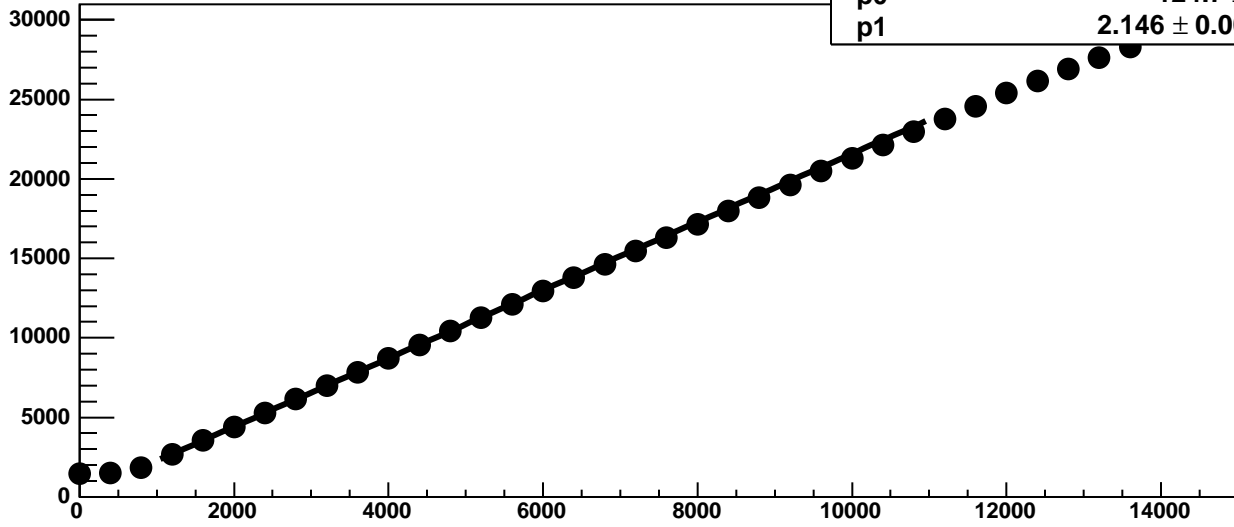
Chip 0, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

527 / 23

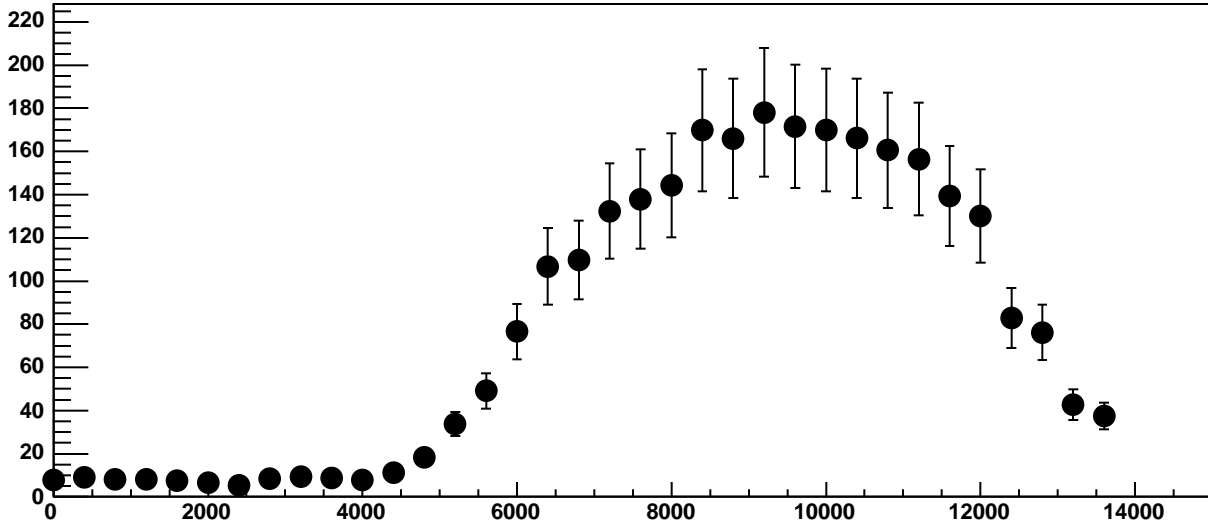
p0

124.7 ± 1.585

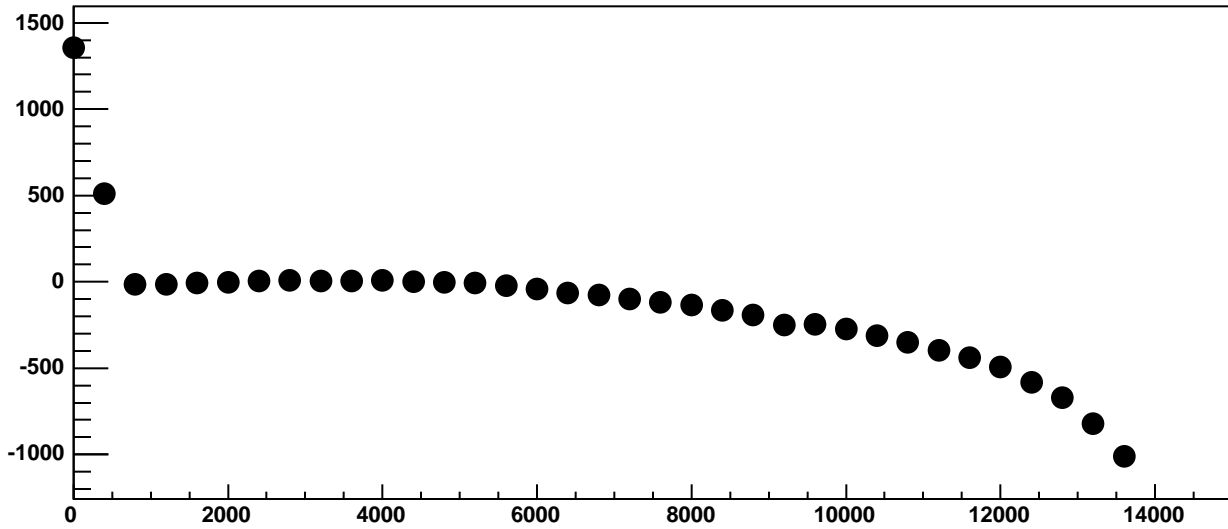
p1

2.146 ± 0.0005473

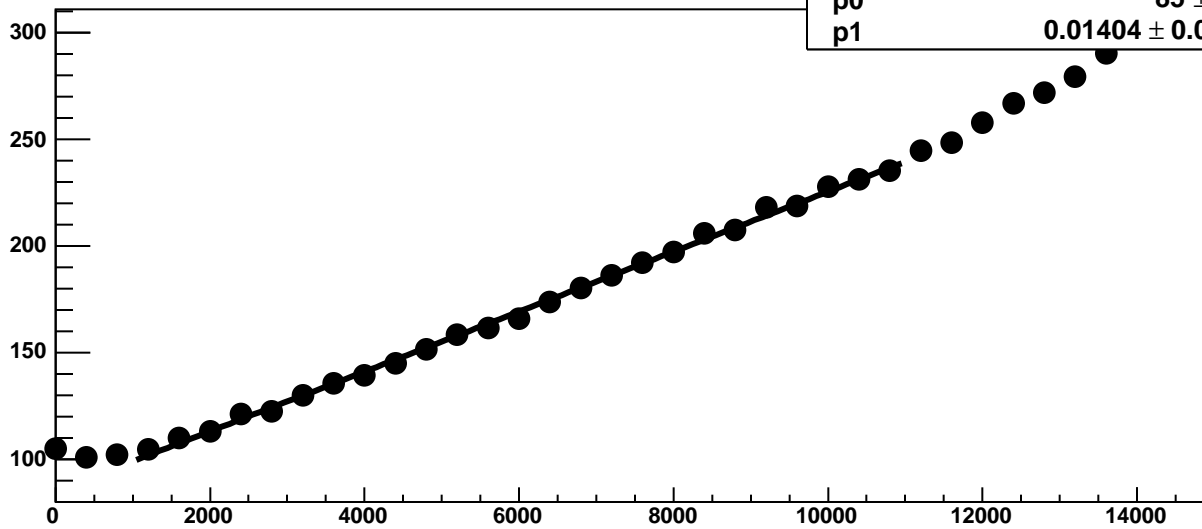
Chip 0, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

23.02 / 23

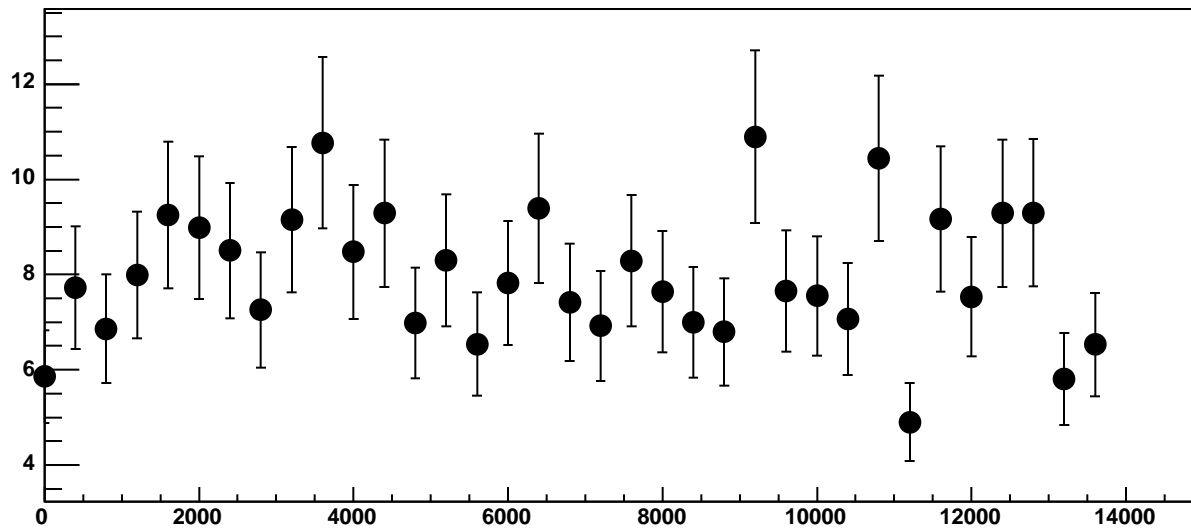
p0

85 ± 0.8919

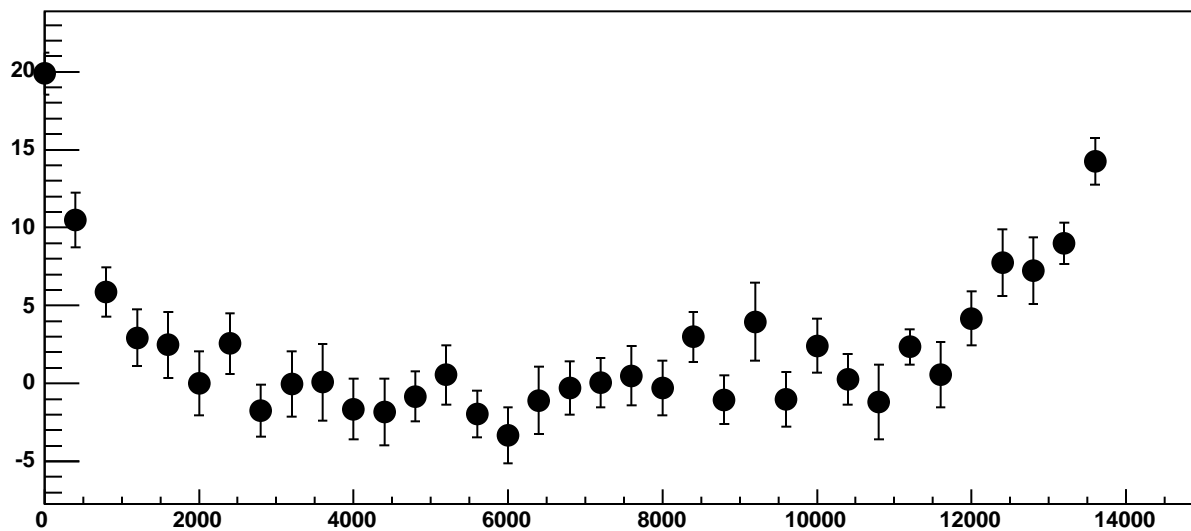
p1

0.01404 ± 0.0001316

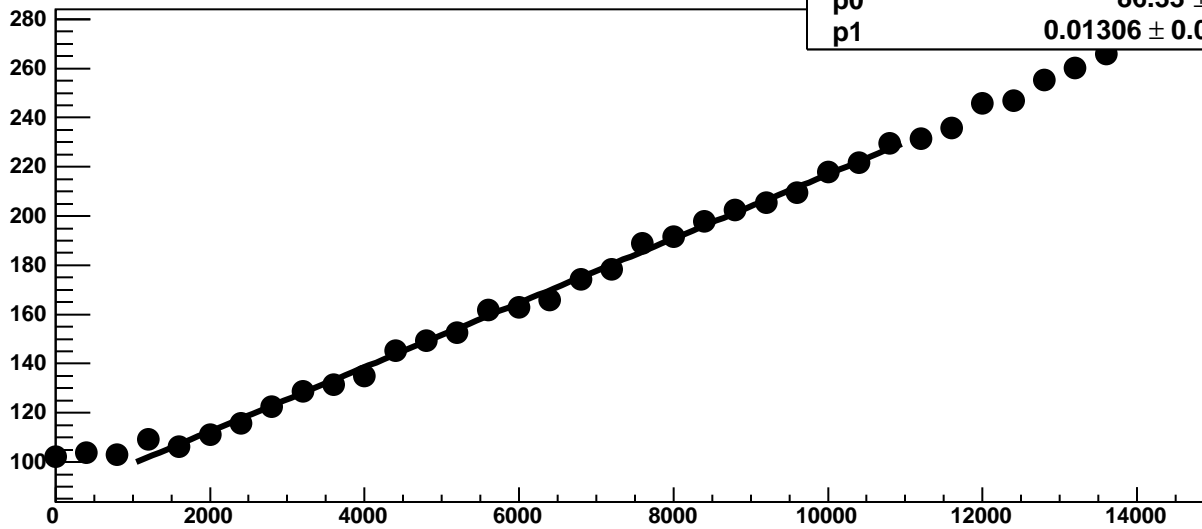
Chip 0, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

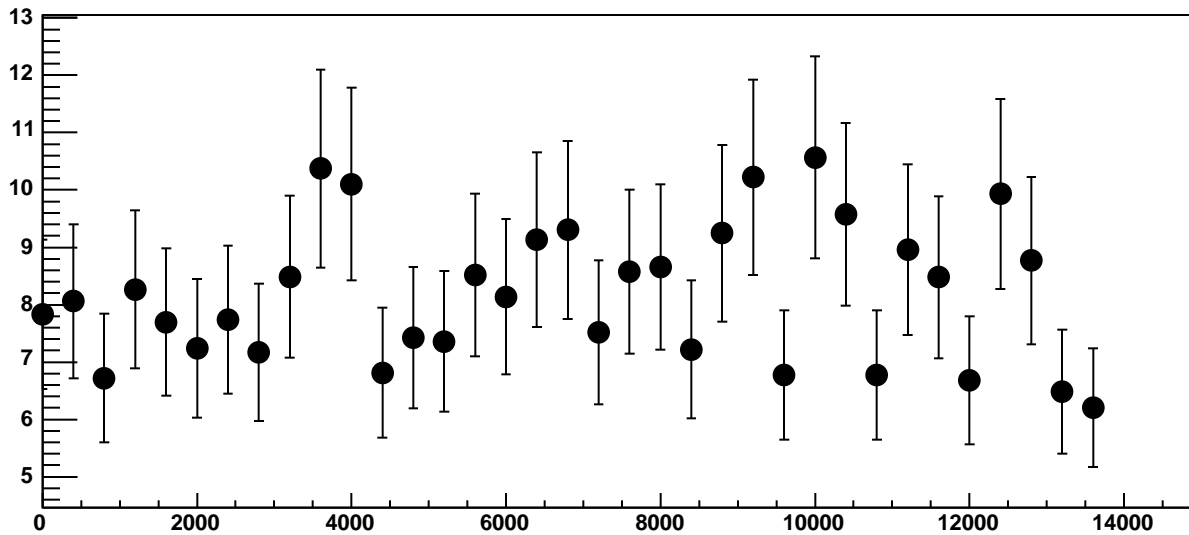


Chip 0, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

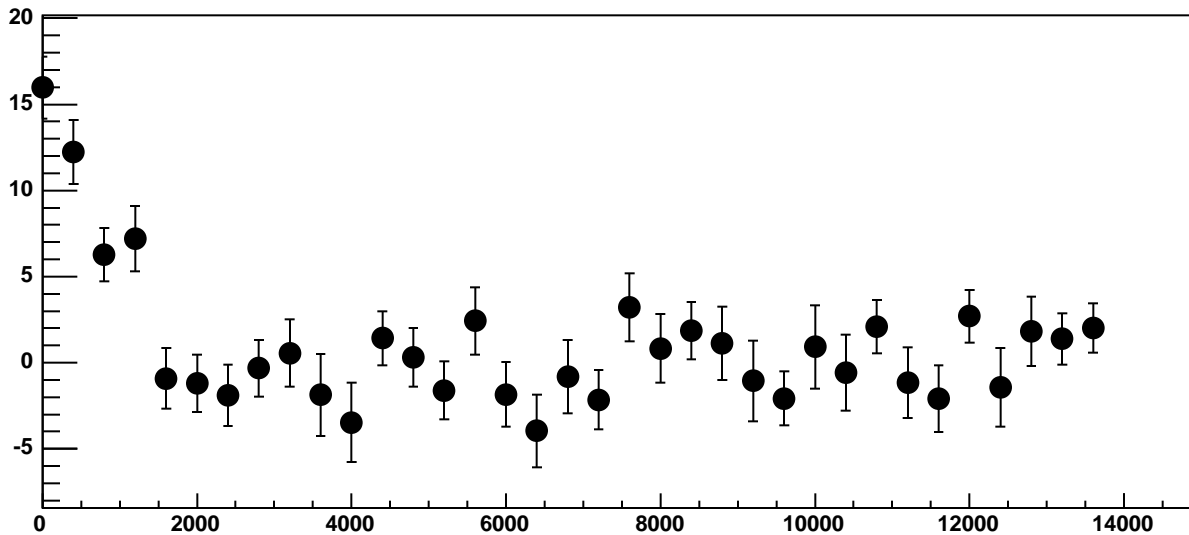


χ^2 / ndf 37.28 / 23
p0 86.33 ± 0.8385
p1 0.01306 ± 0.0001275

Chip 0, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

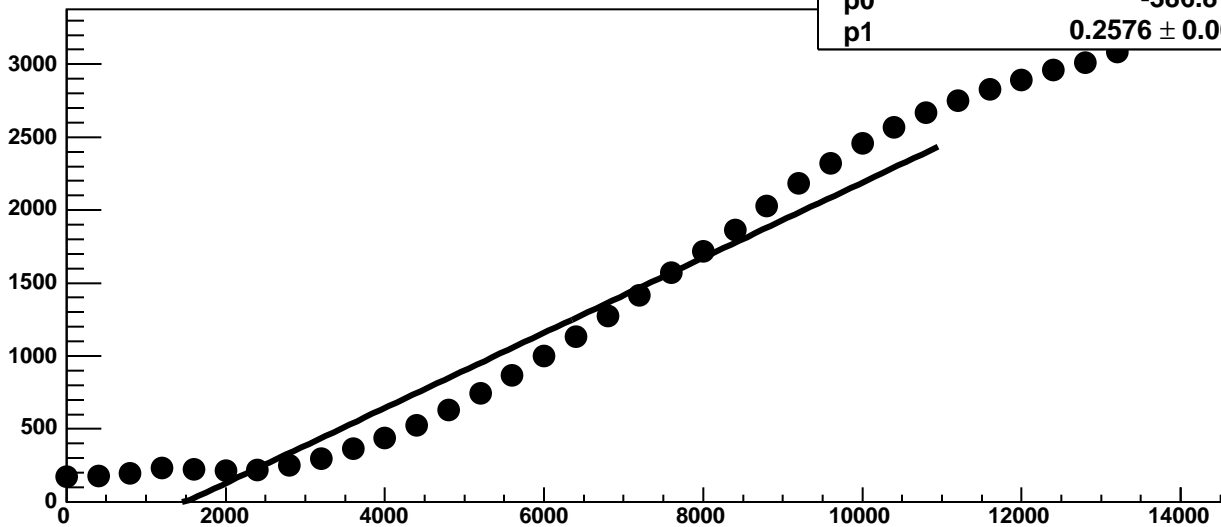


Chip 0, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

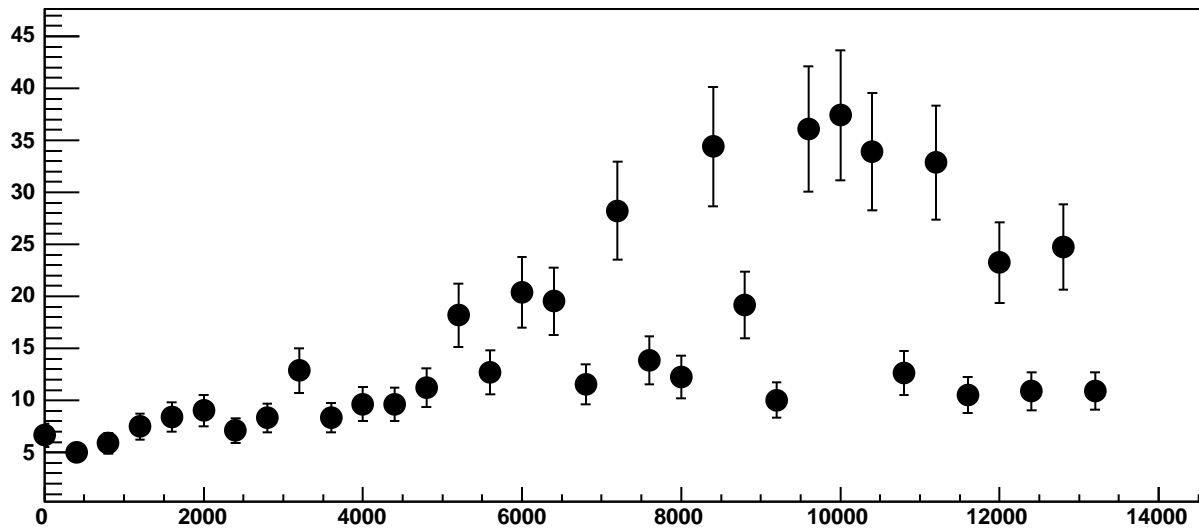


Chip 0, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC

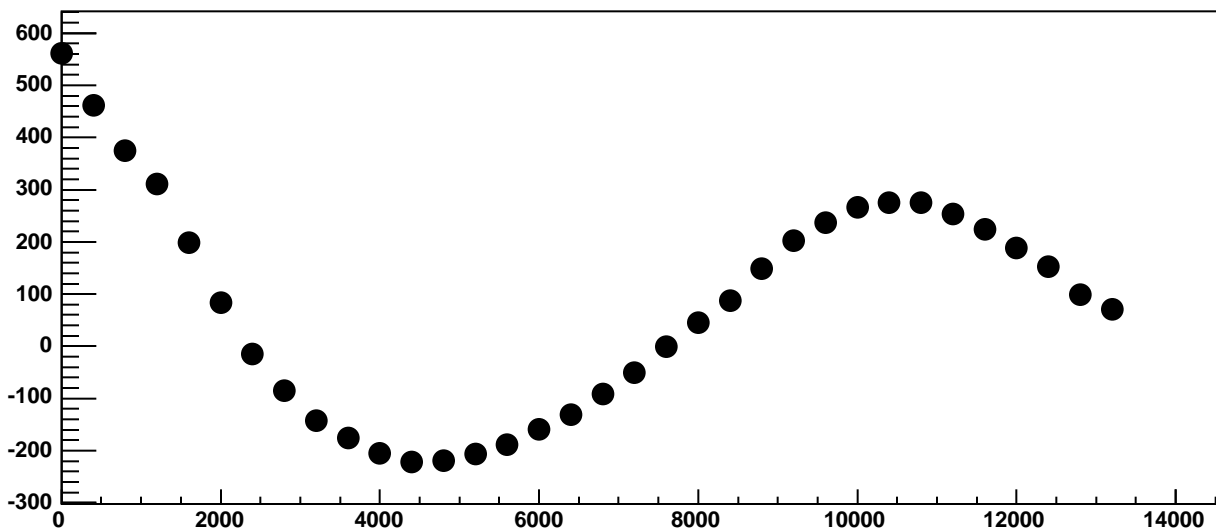
χ^2 / ndf 1.146e+05 / 23
p0 -386.8 ± 1.011
p1 0.2576 ± 0.0001978



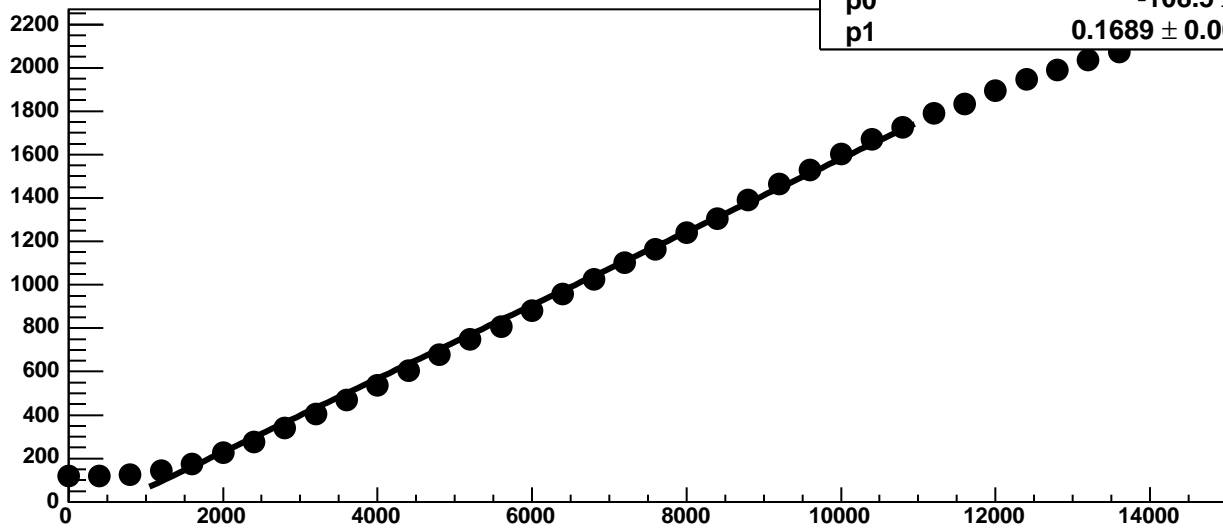
Chip 0, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



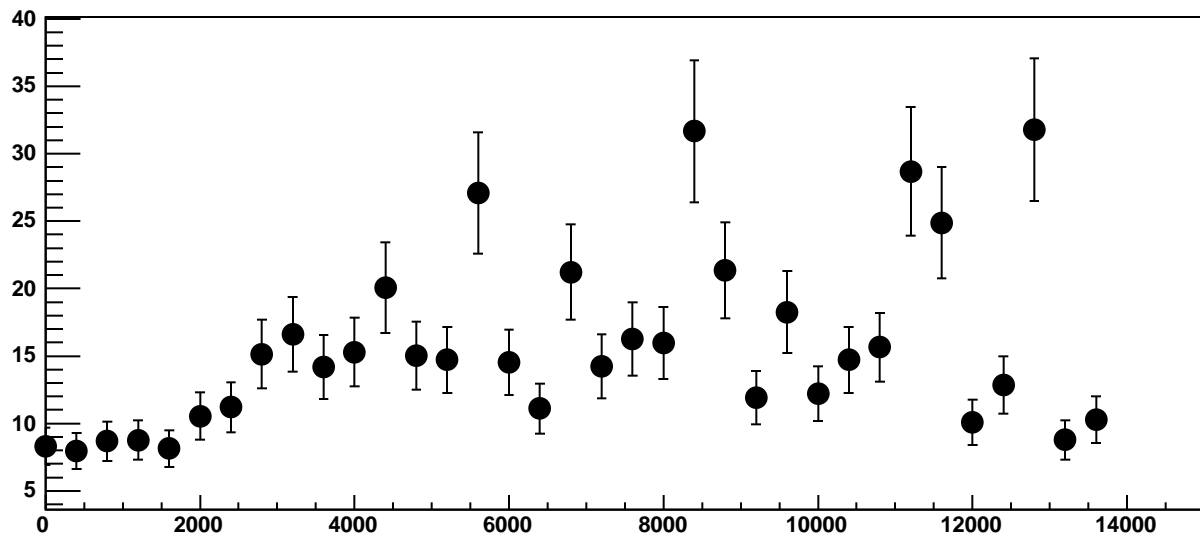
Chip 0, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC



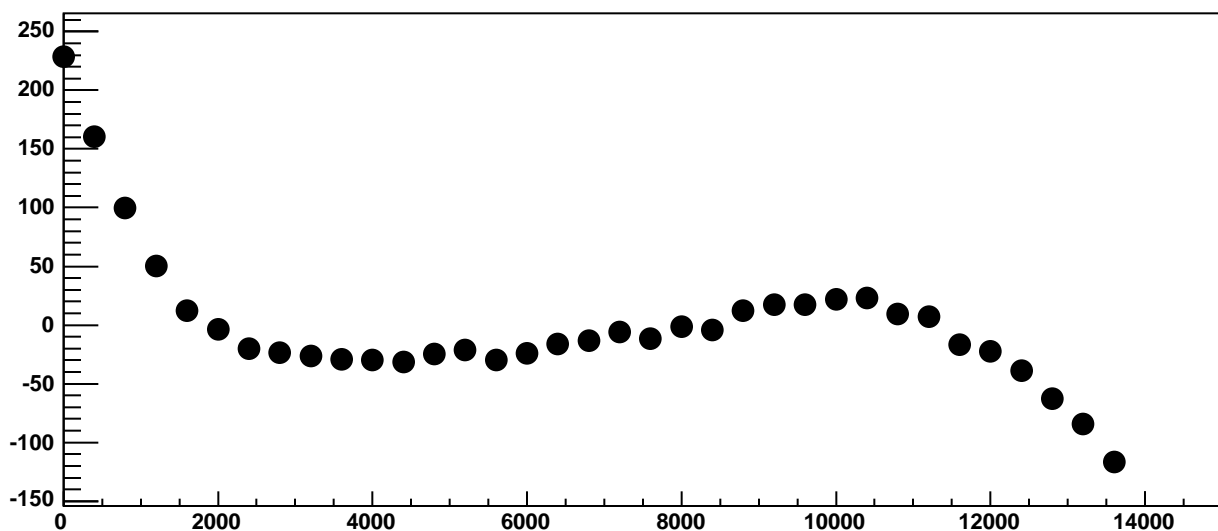
Chip 0, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



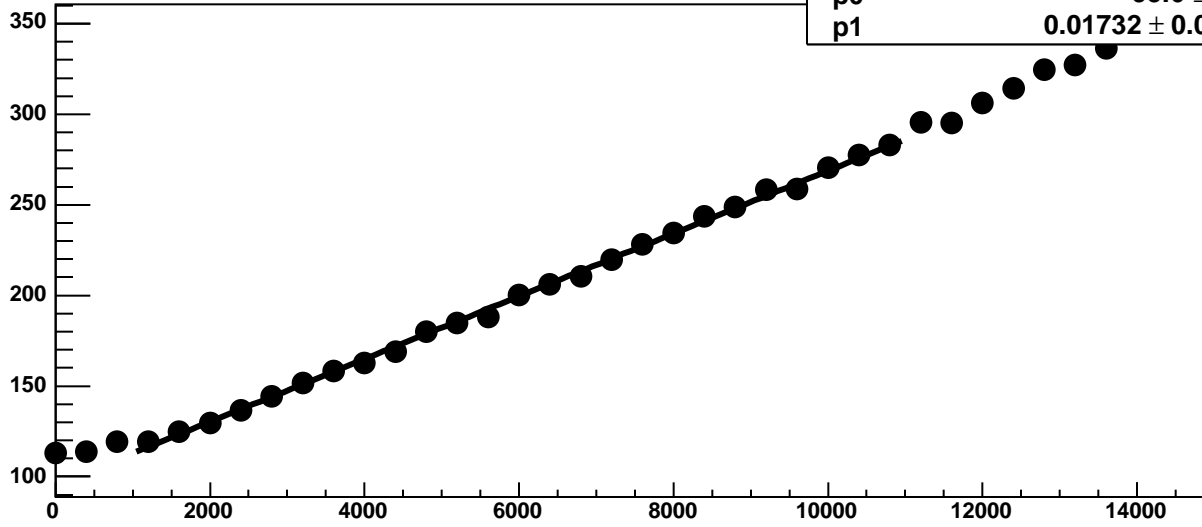
Chip 0, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

33.19 / 23

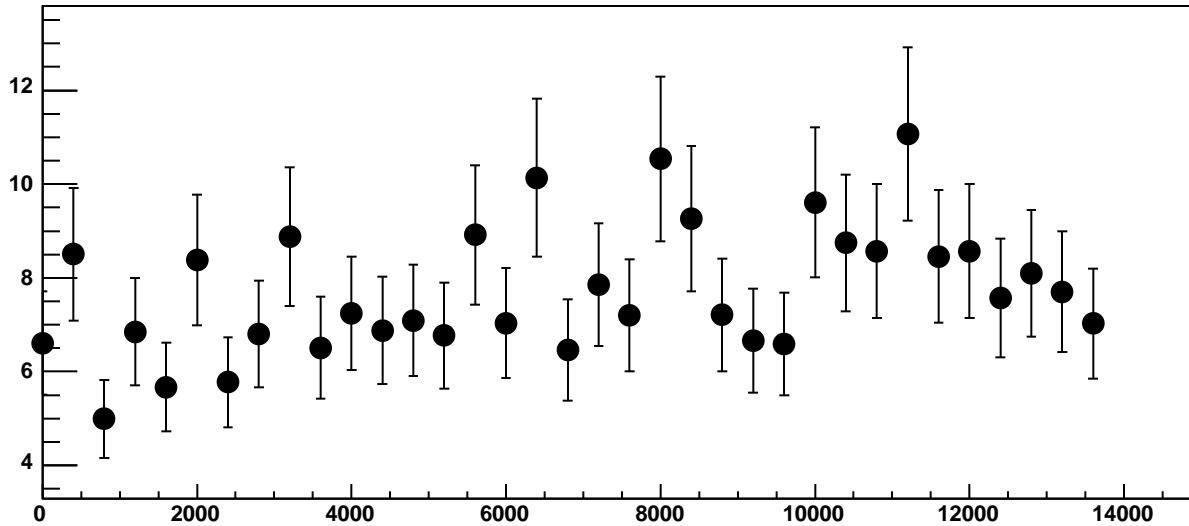
p0

95.6 ± 0.7336

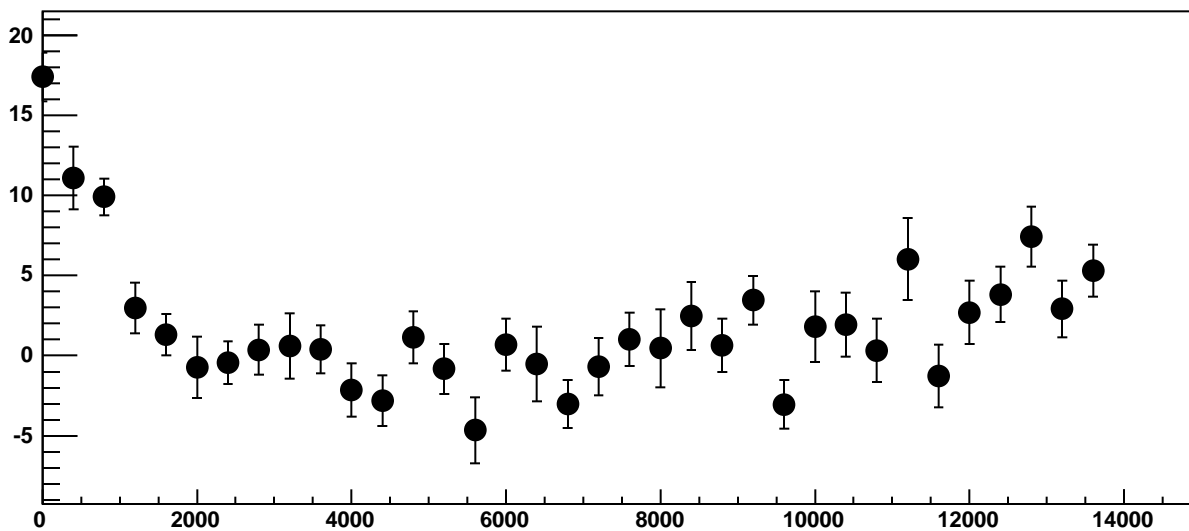
p1

0.01732 ± 0.0001165

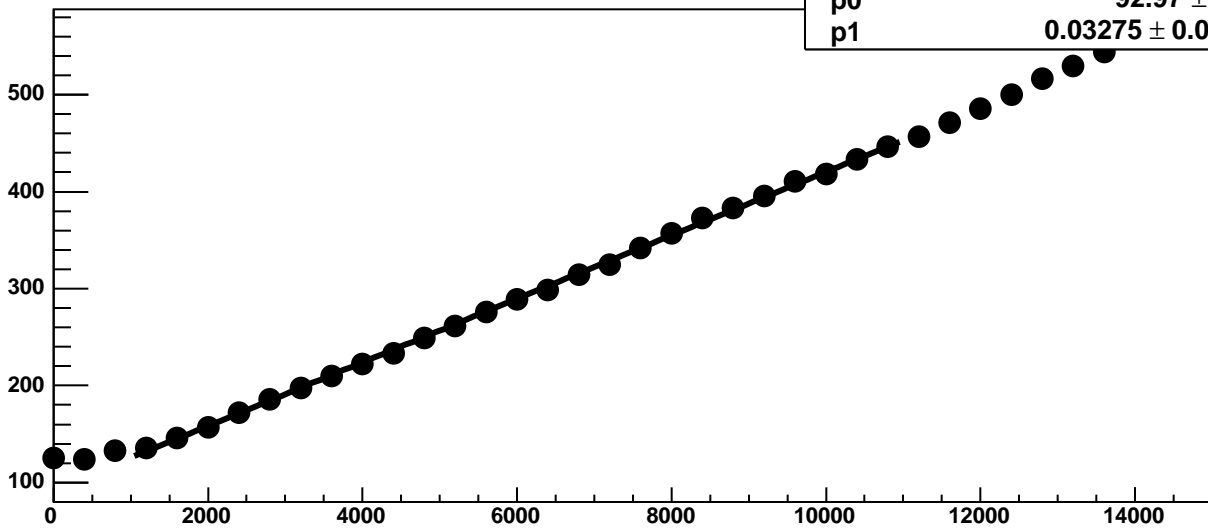
Chip 0, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

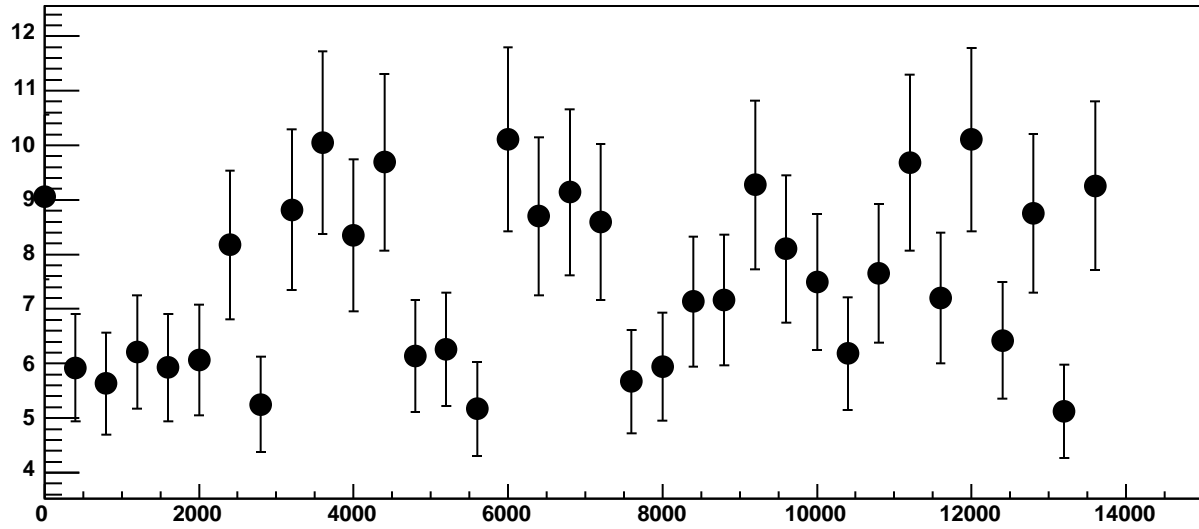


Chip 0, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC

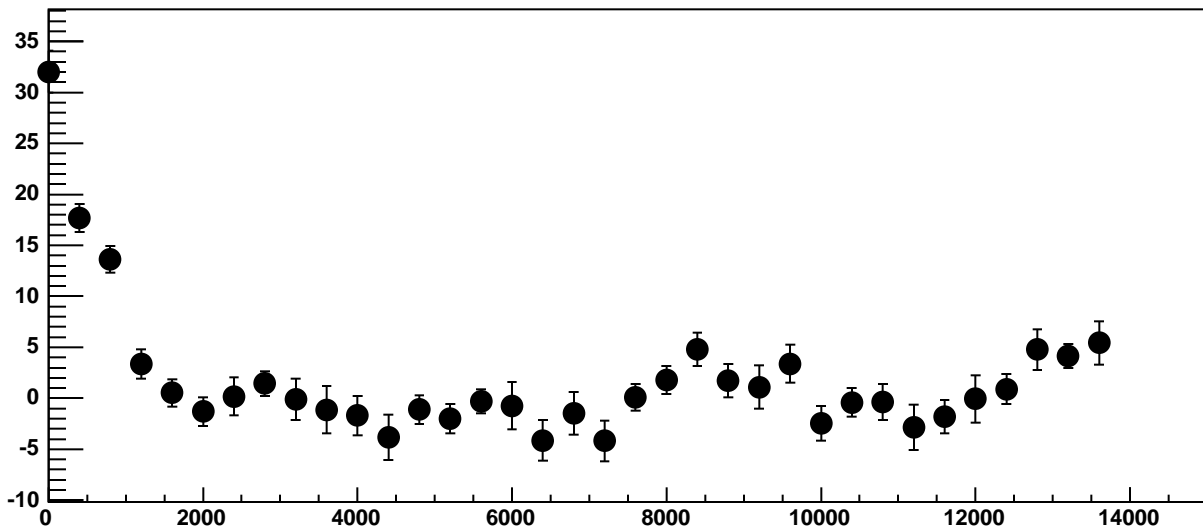


χ^2 / ndf 41.1 / 23
p0 92.97 ± 0.7103
p1 0.03275 ± 0.0001091

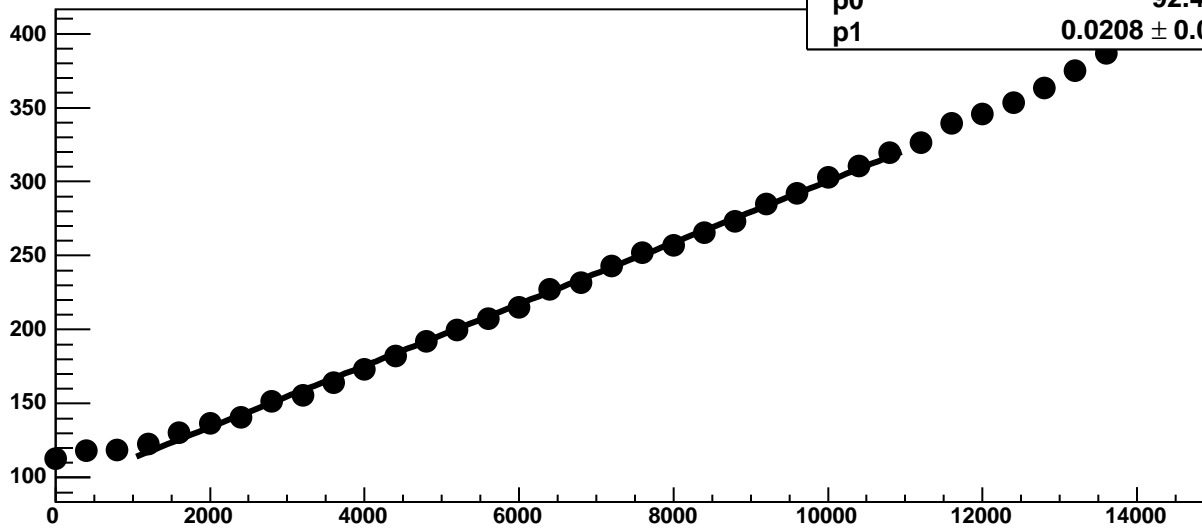
Chip 0, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

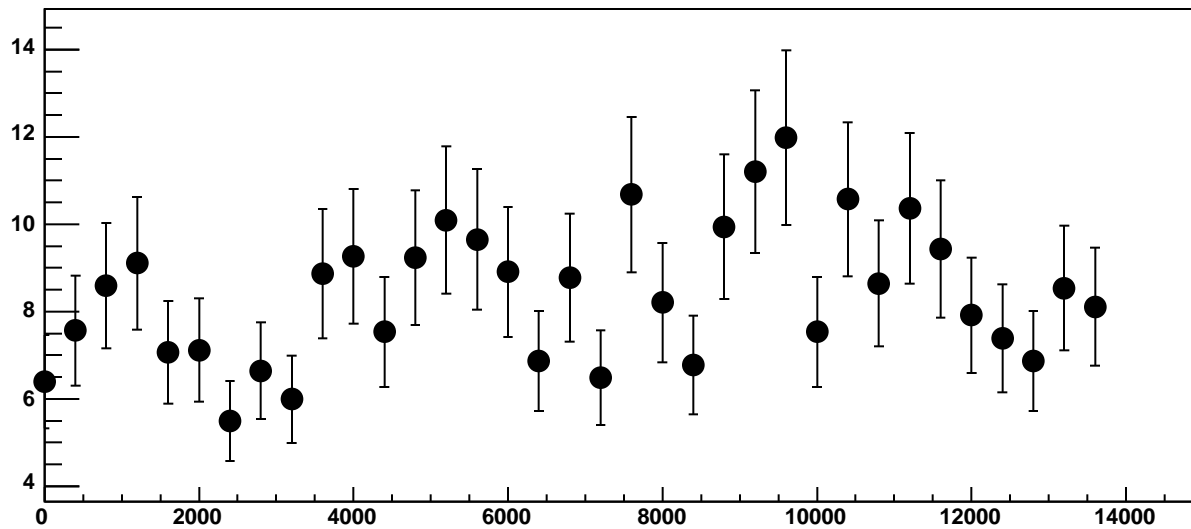


Chip 0, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

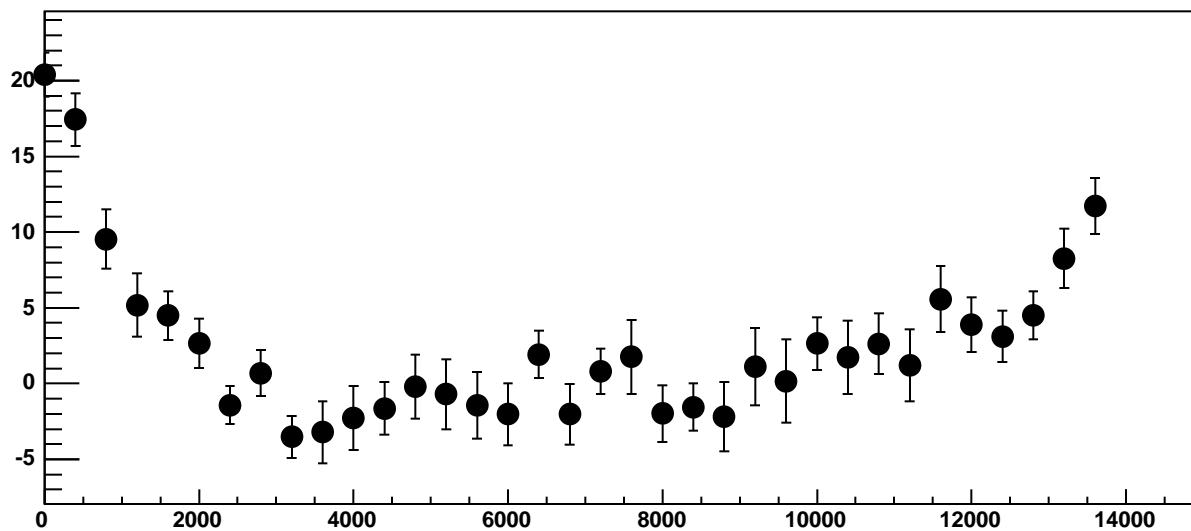


χ^2 / ndf 41.63 / 23
p0 92.4 ± 0.791
p1 0.0208 ± 0.0001281

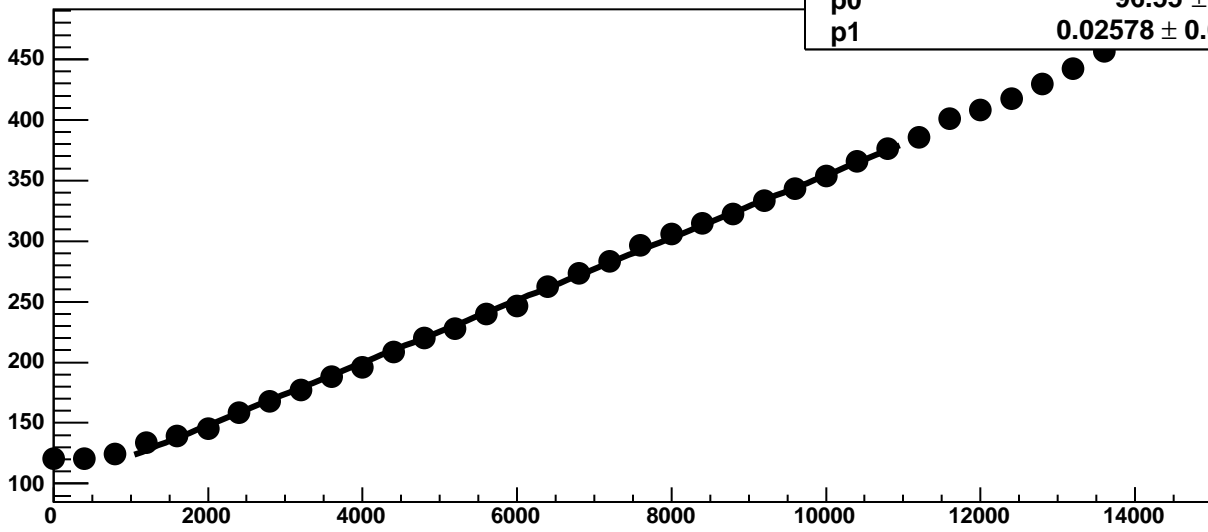
Chip 0, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

49.57 / 23

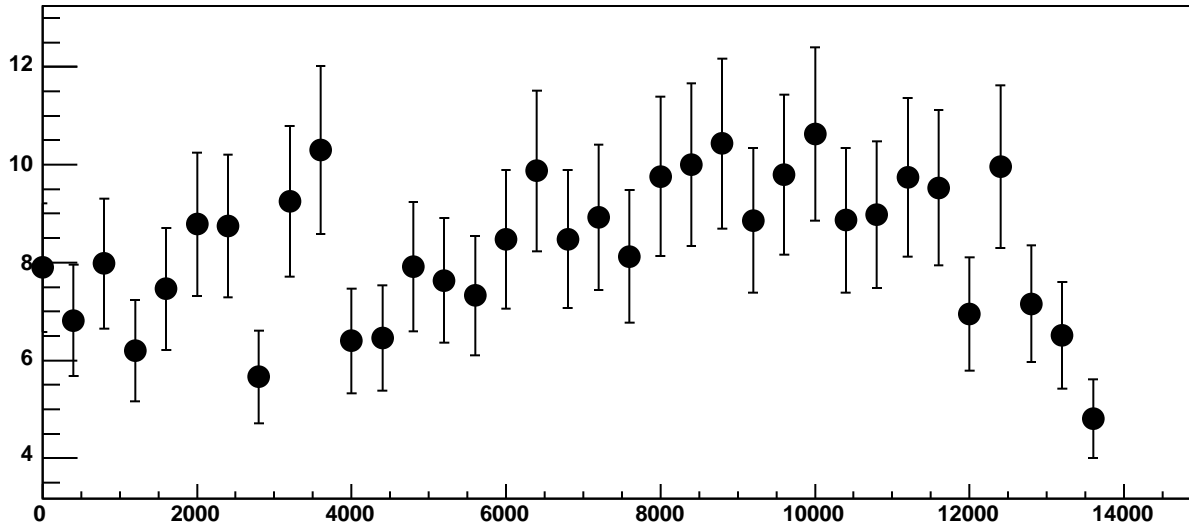
p0

96.55 ± 0.8035

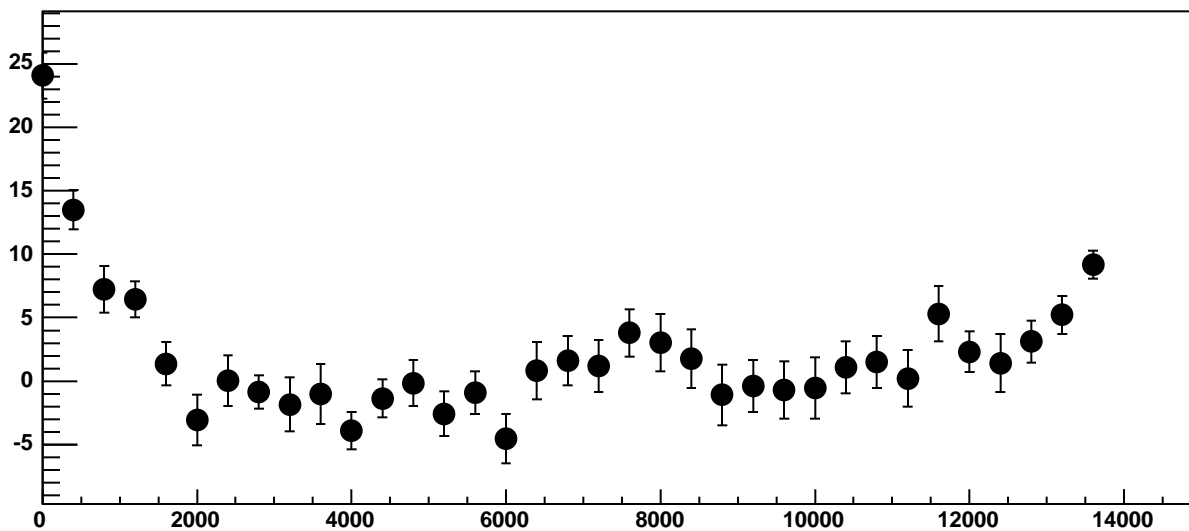
p1

0.02578 ± 0.000132

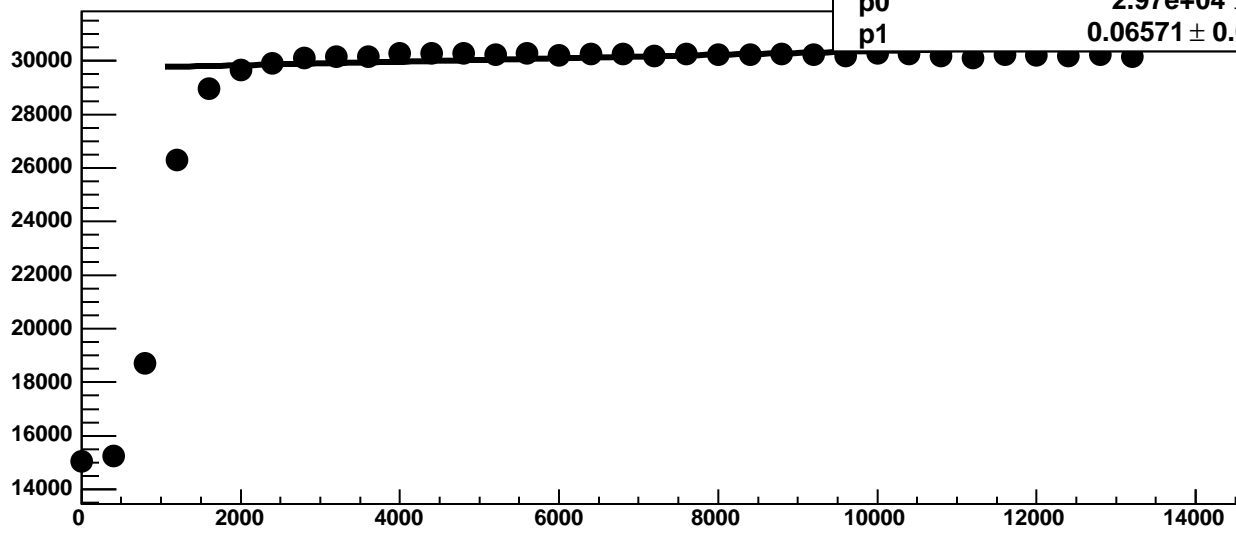
Chip 0, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



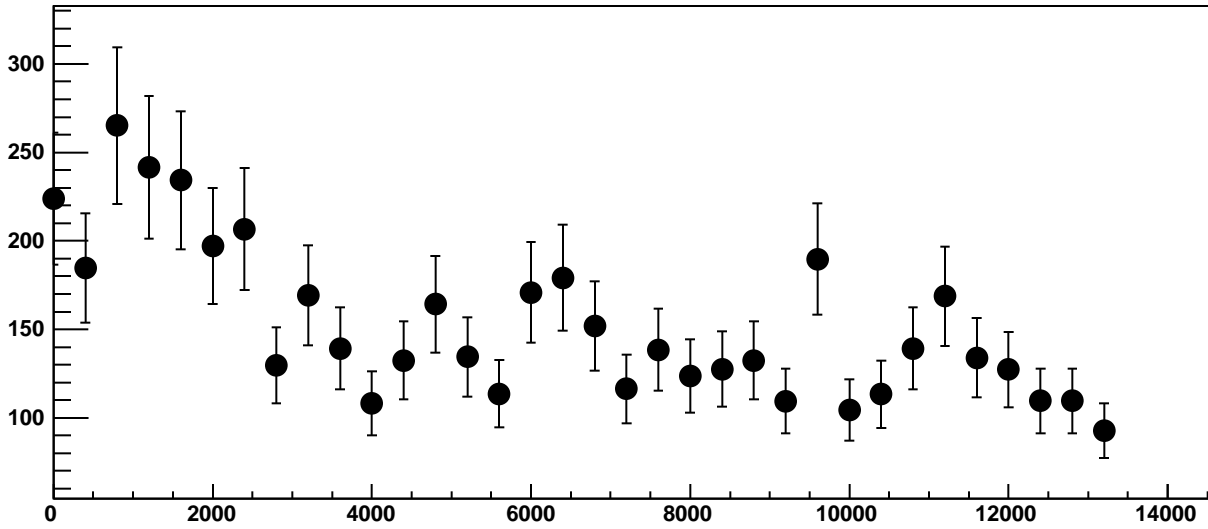
Chip 0, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



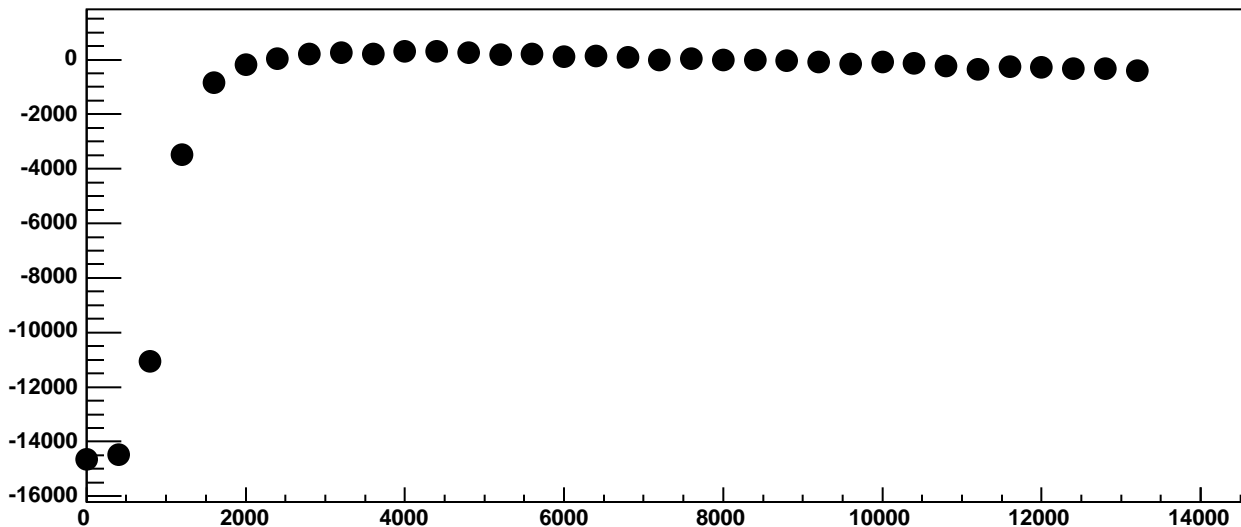
Chip 0, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC



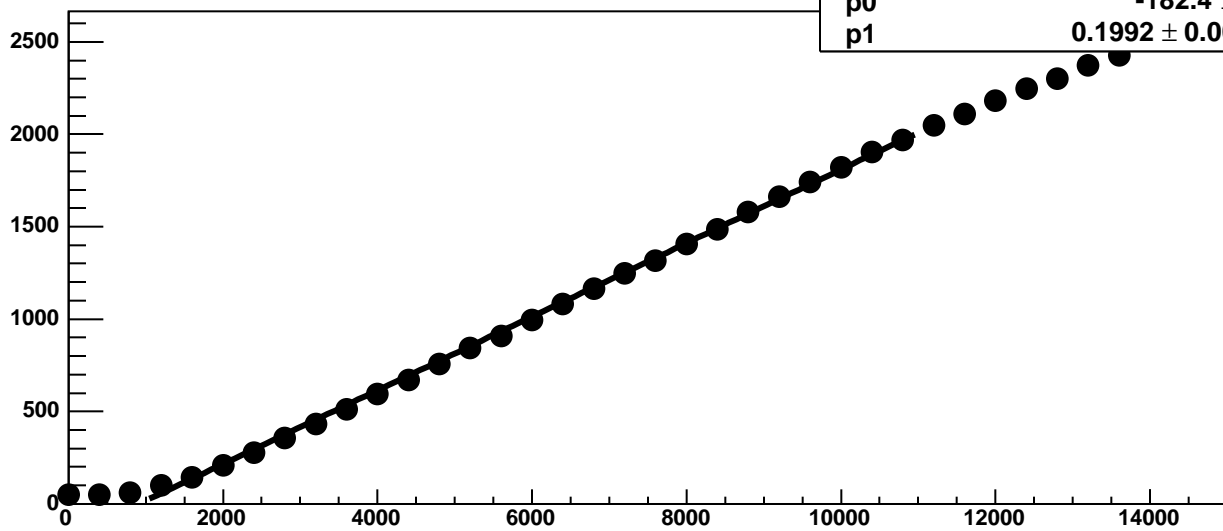
Chip 0, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1175 / 23

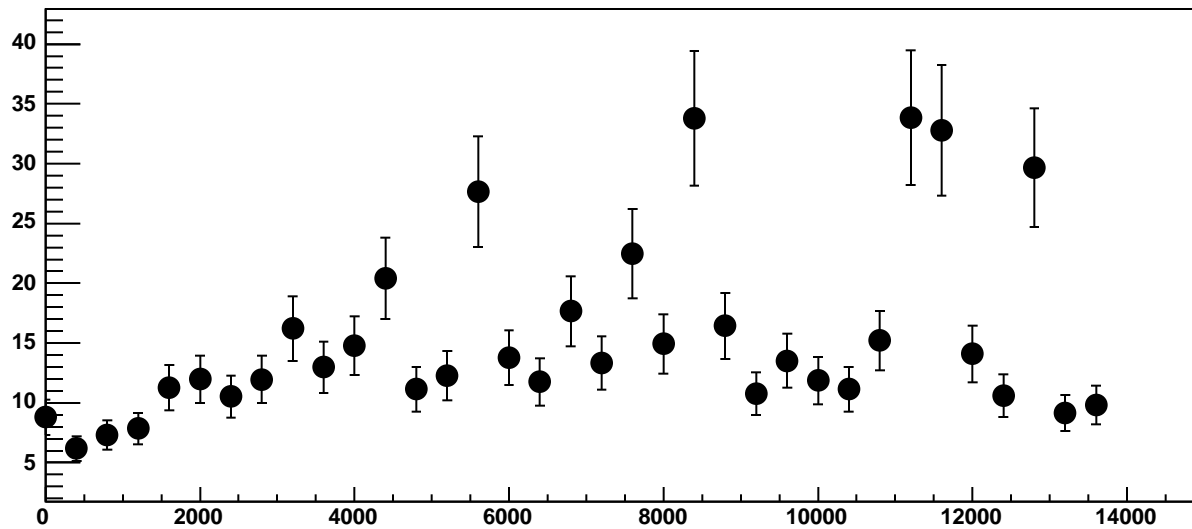
p0

-182.4 ± 1.189

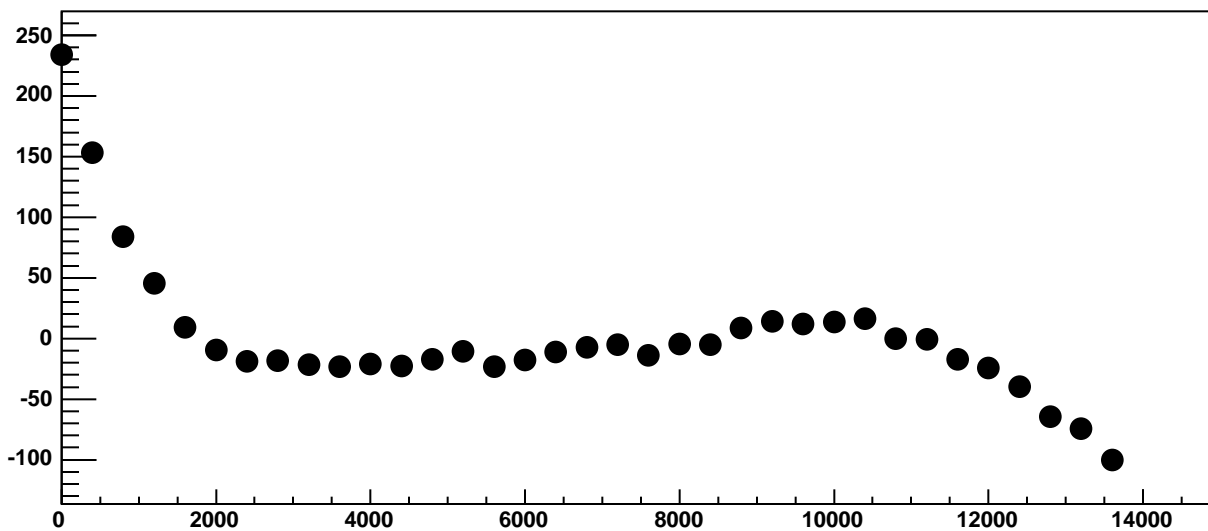
p1

0.1992 ± 0.0001879

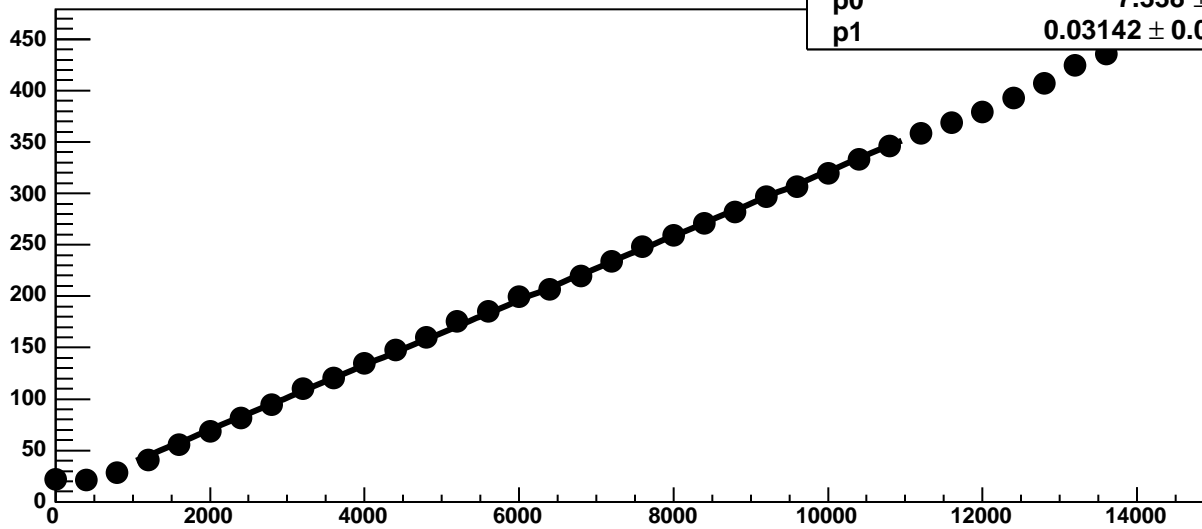
Chip 0, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

35.13 / 23

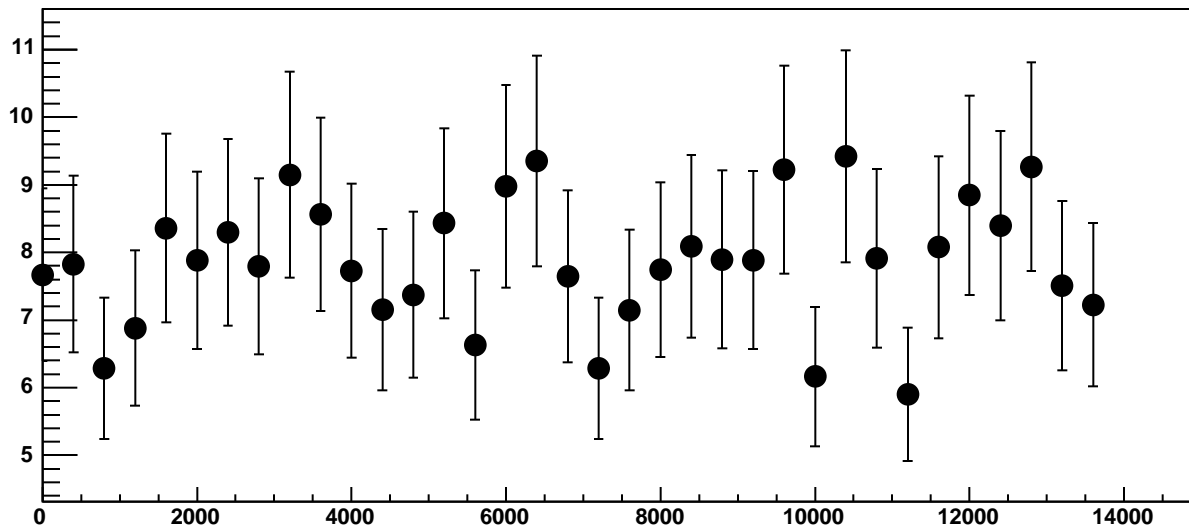
p0

7.338 ± 0.8298

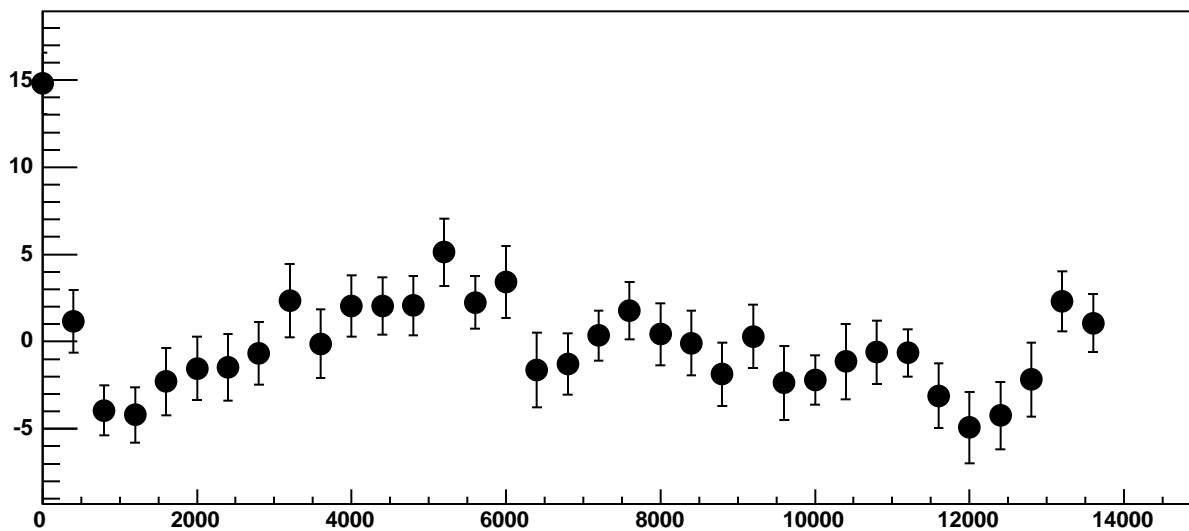
p1

0.03142 ± 0.0001243

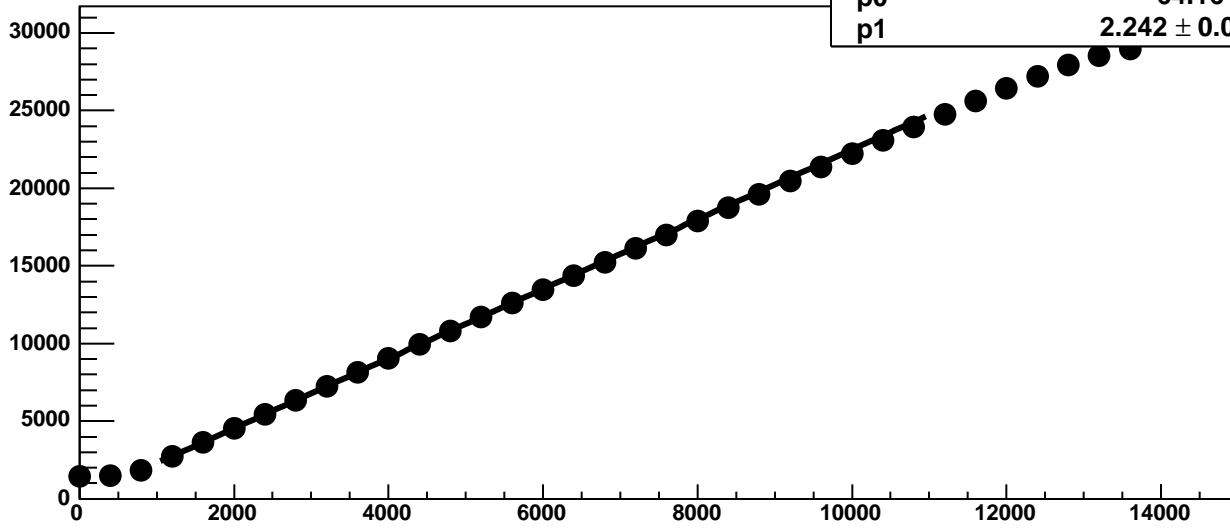
Chip 0, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

552.6 / 23

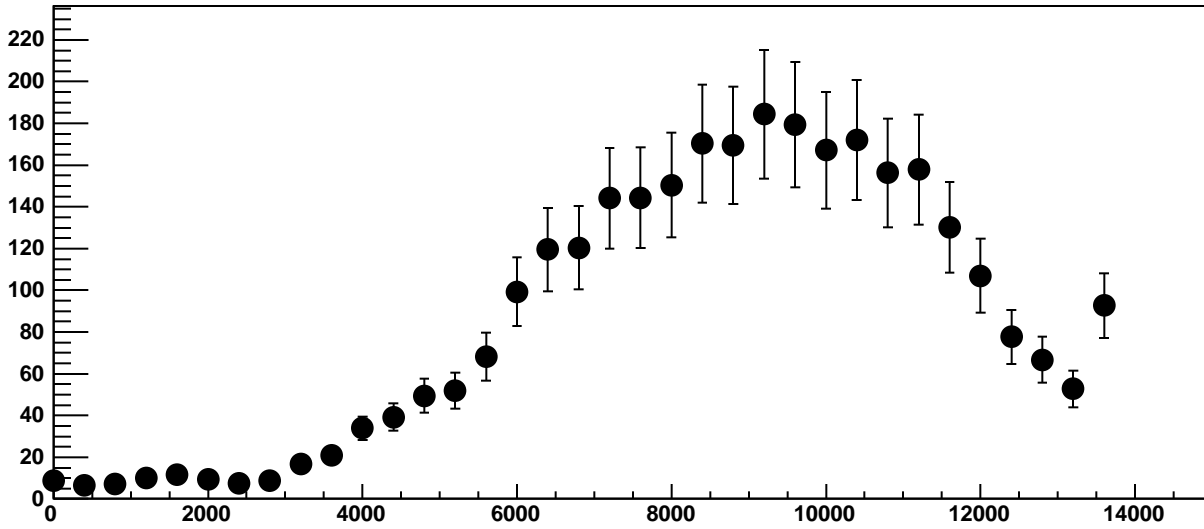
p0

64.16 ± 2.377

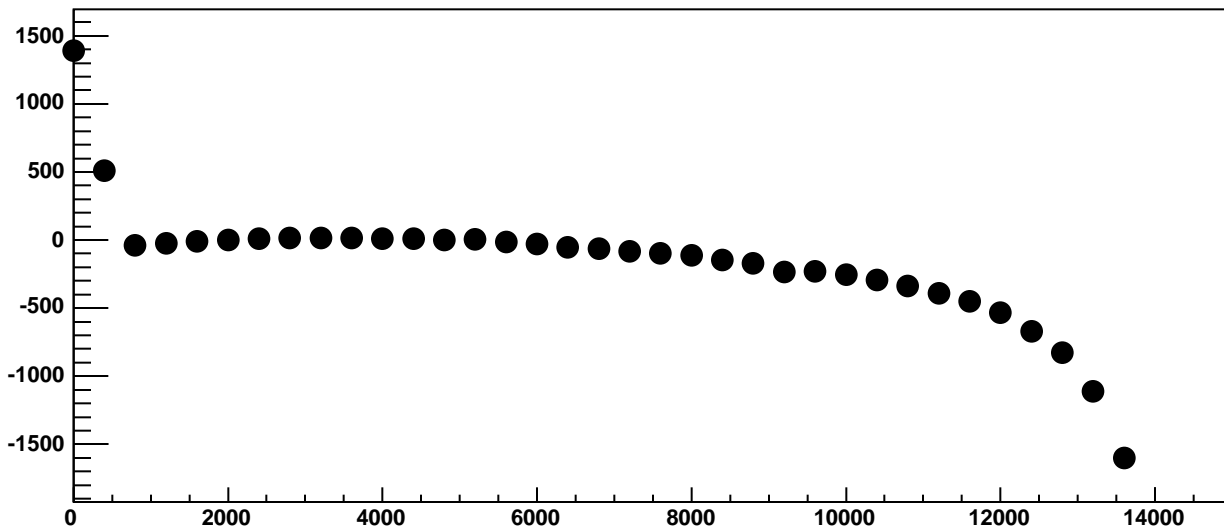
p1

2.242 ± 0.0009331

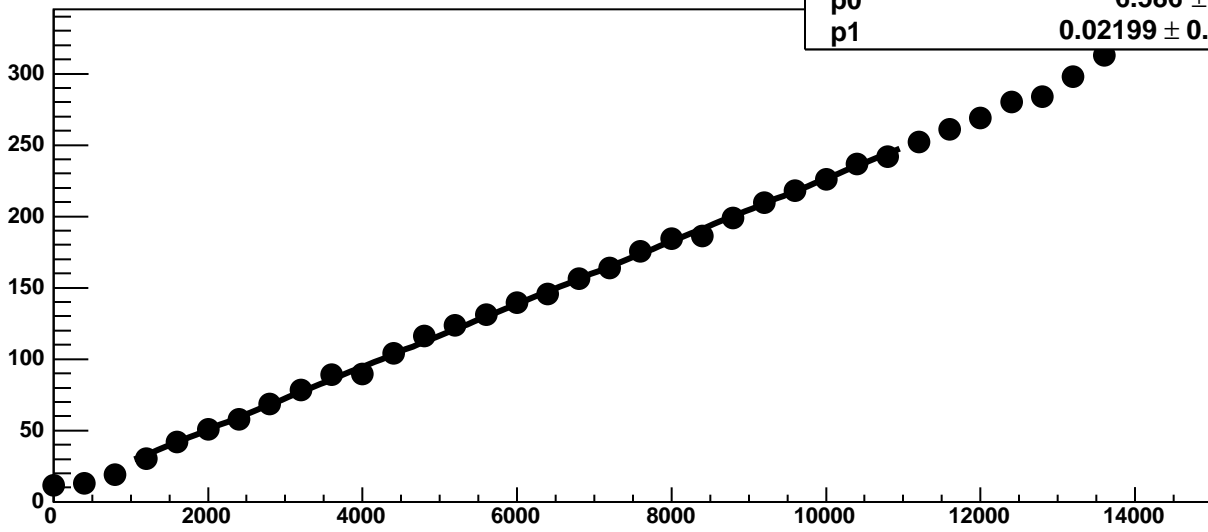
Chip 0, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC

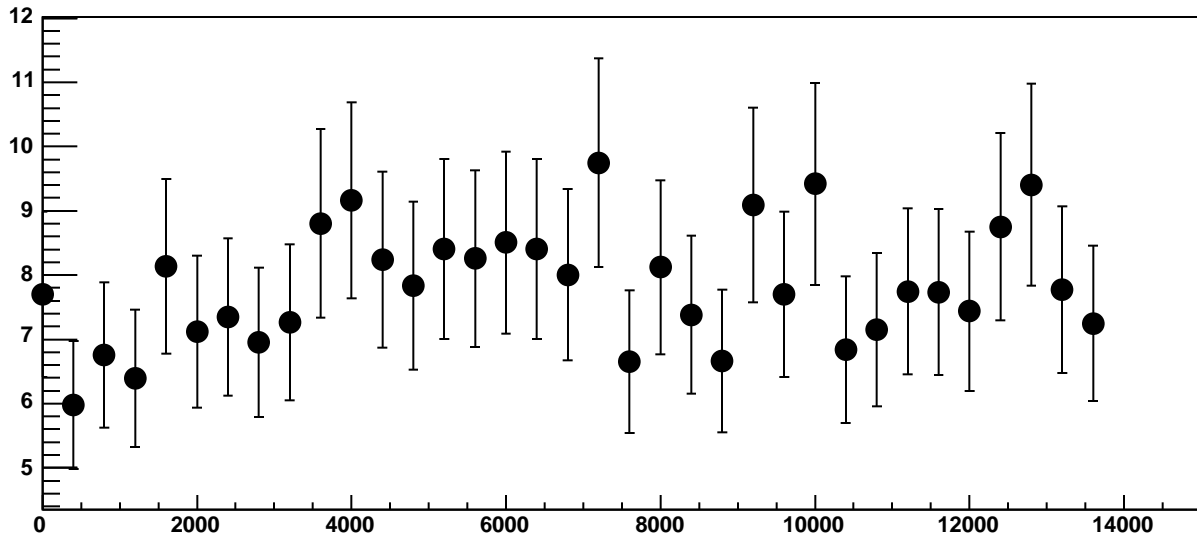


Chip 0, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

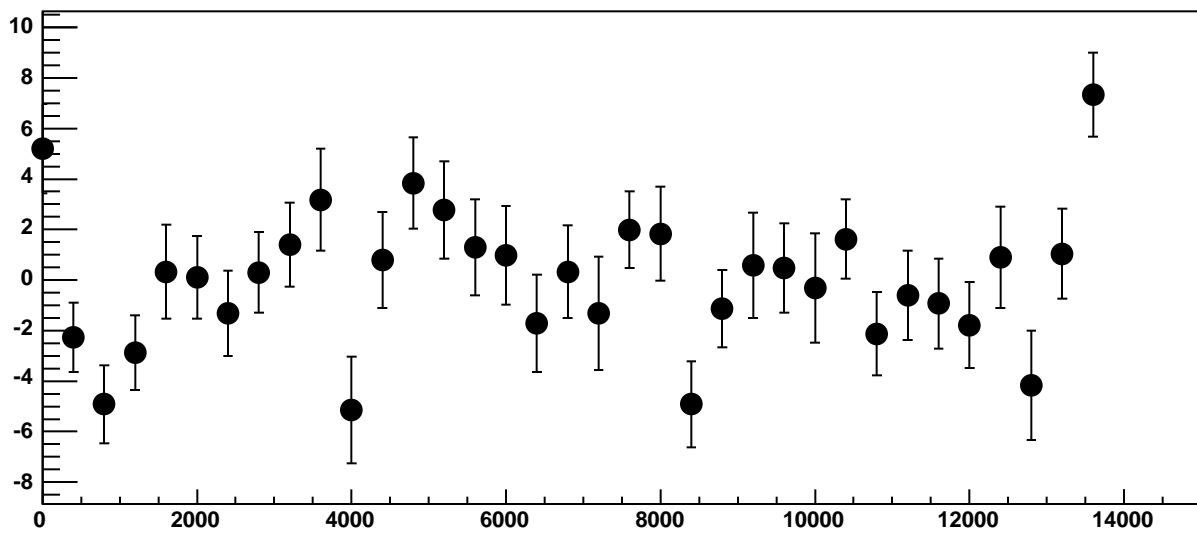


χ^2 / ndf 36.98 / 23
p0 6.586 ± 0.7855
p1 0.02199 ± 0.000118

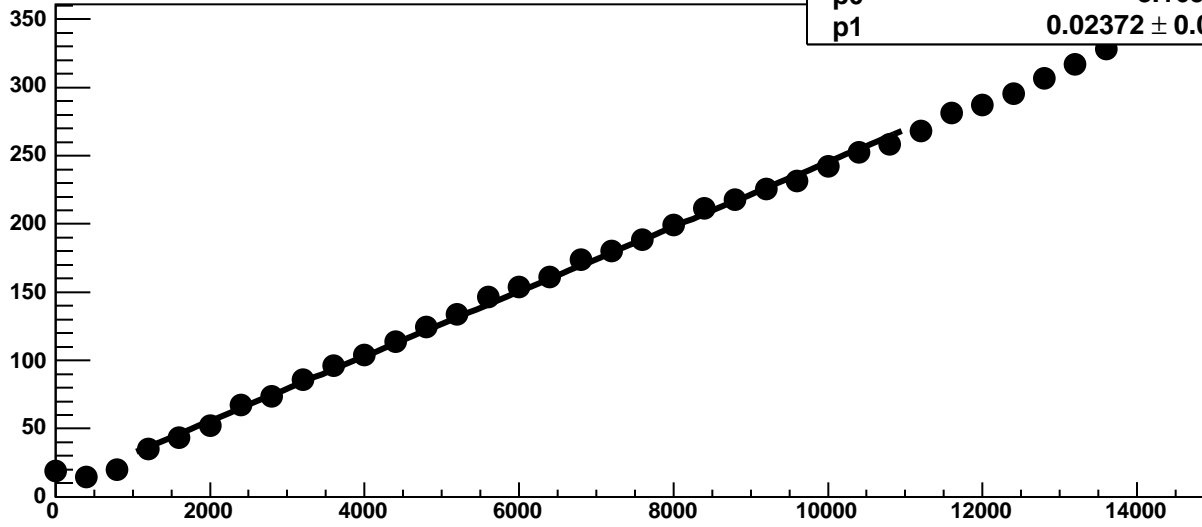
Chip 0, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

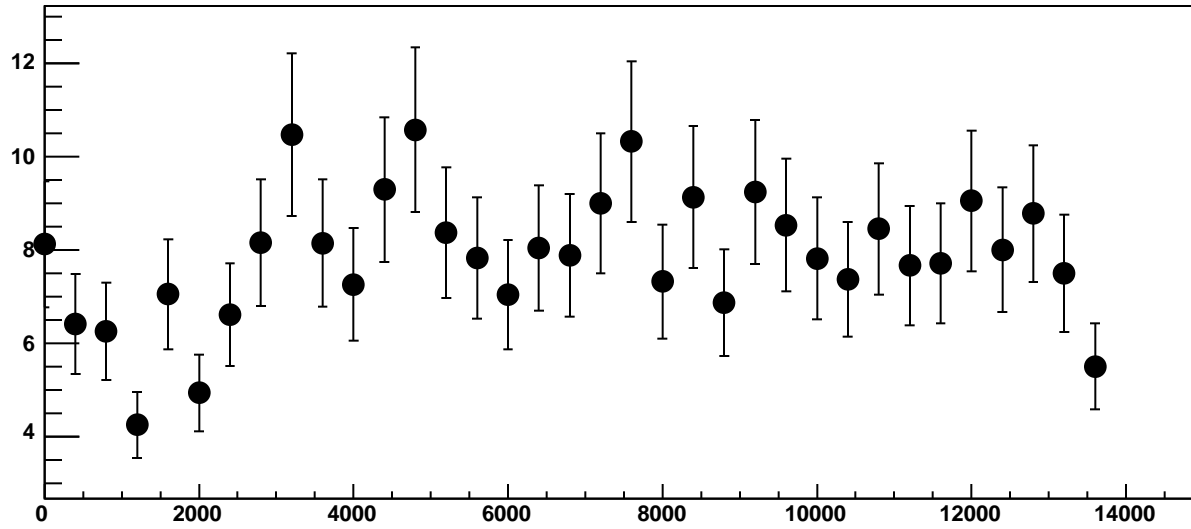


Chip 0, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

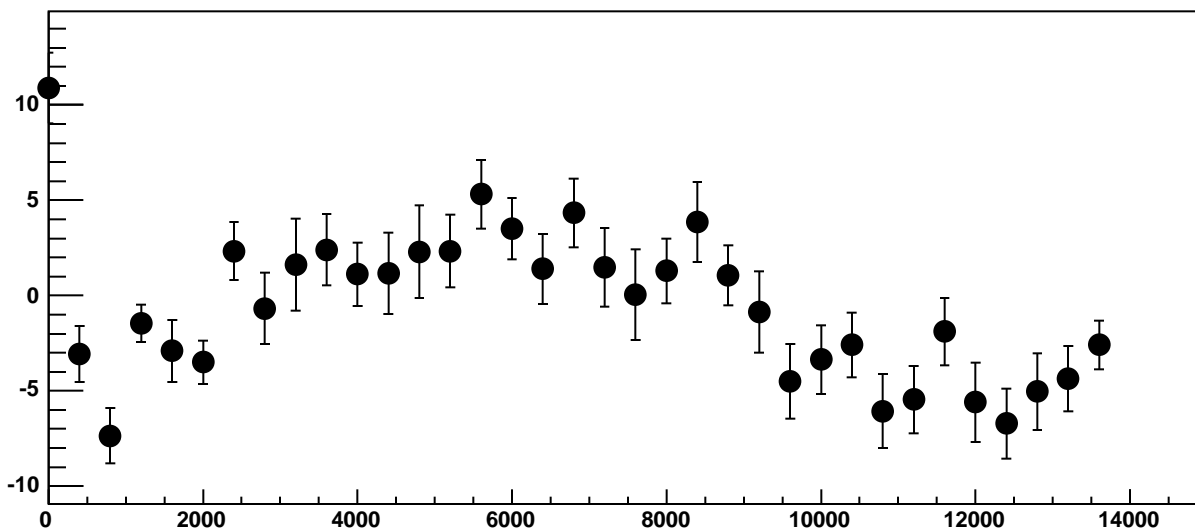


χ^2 / ndf 68.49 / 23
p0 8.165 ± 0.671
p1 0.02372 ± 0.0001091

Chip 0, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC

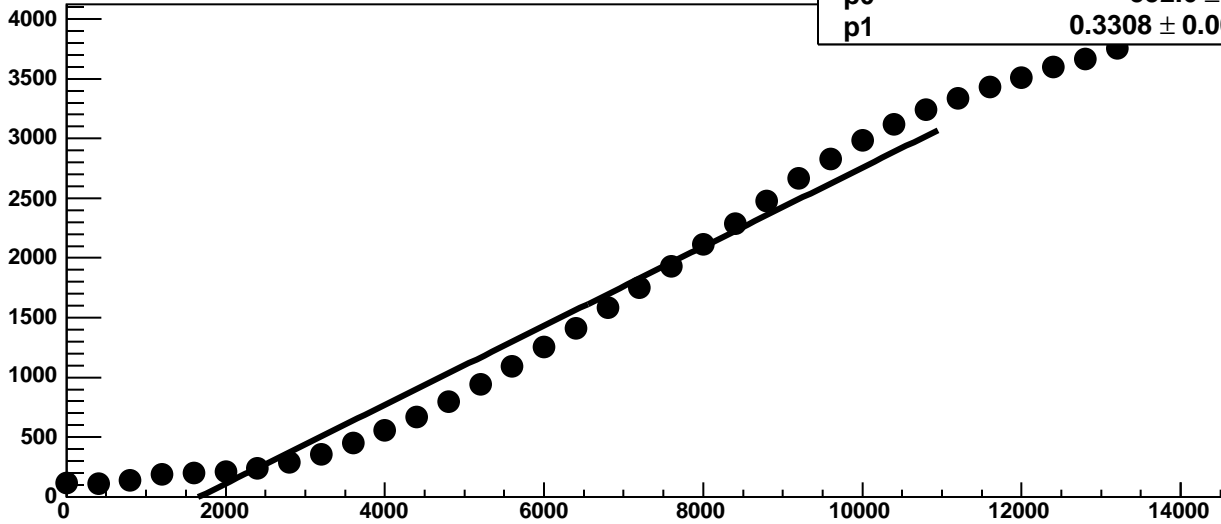


Chip 0, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

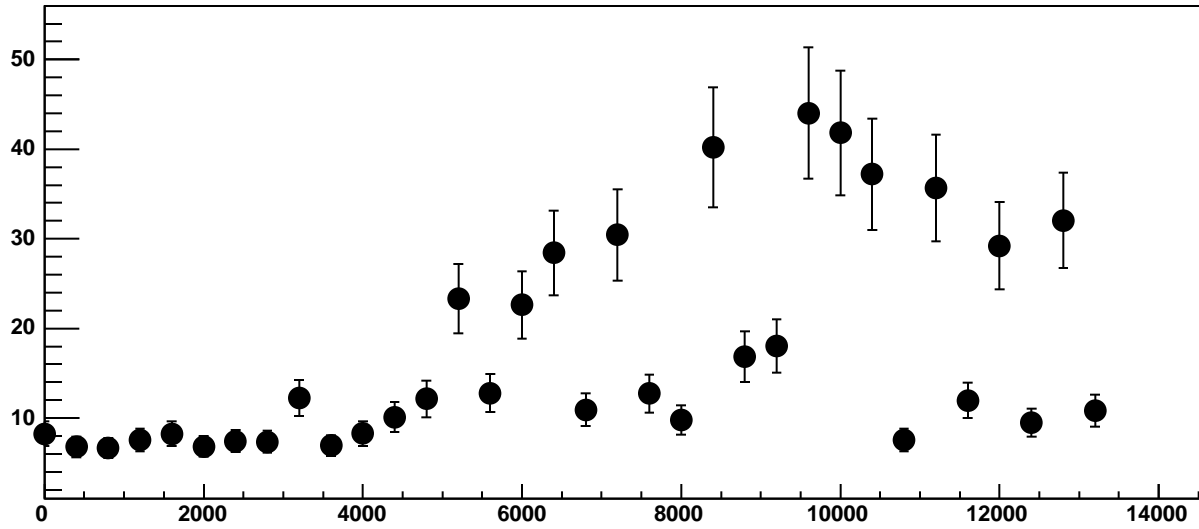


Chip 0, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC

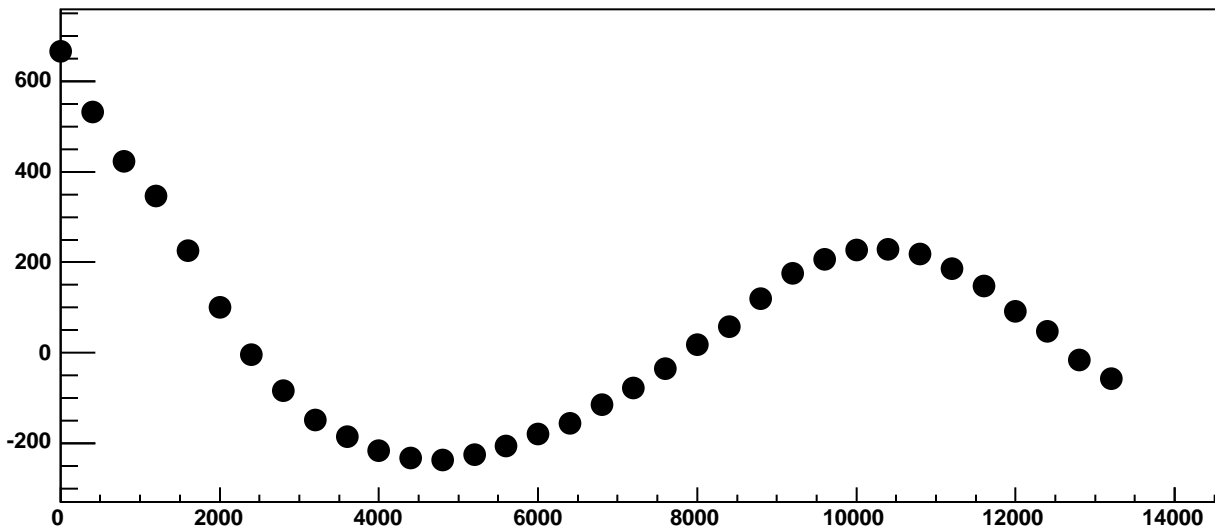
χ^2 / ndf 1.384e+05 / 23
p0 -552.6 ± 0.9135
p1 0.3308 ± 0.0001725



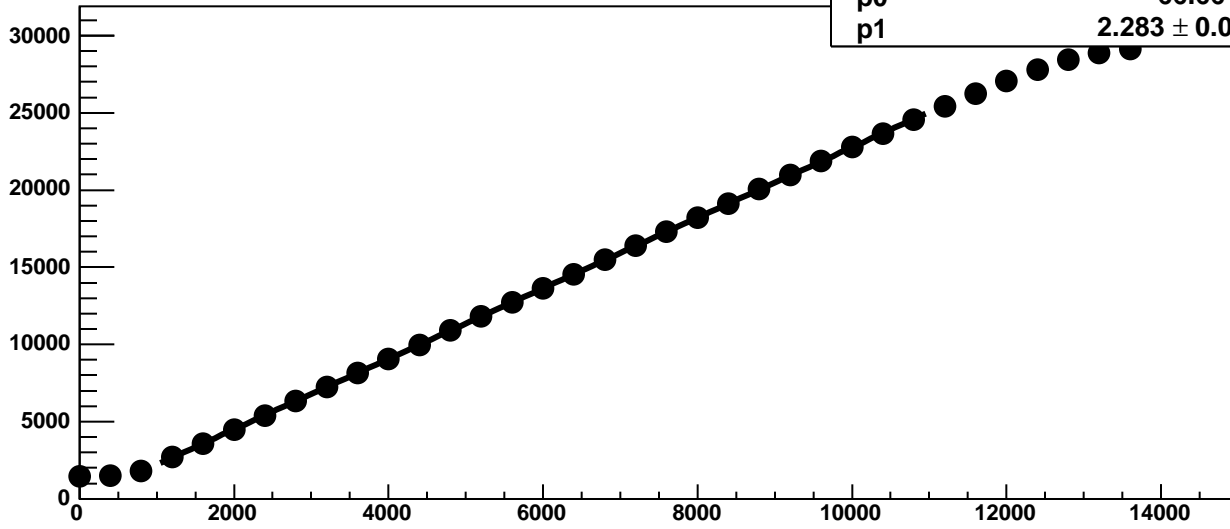
Chip 0, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



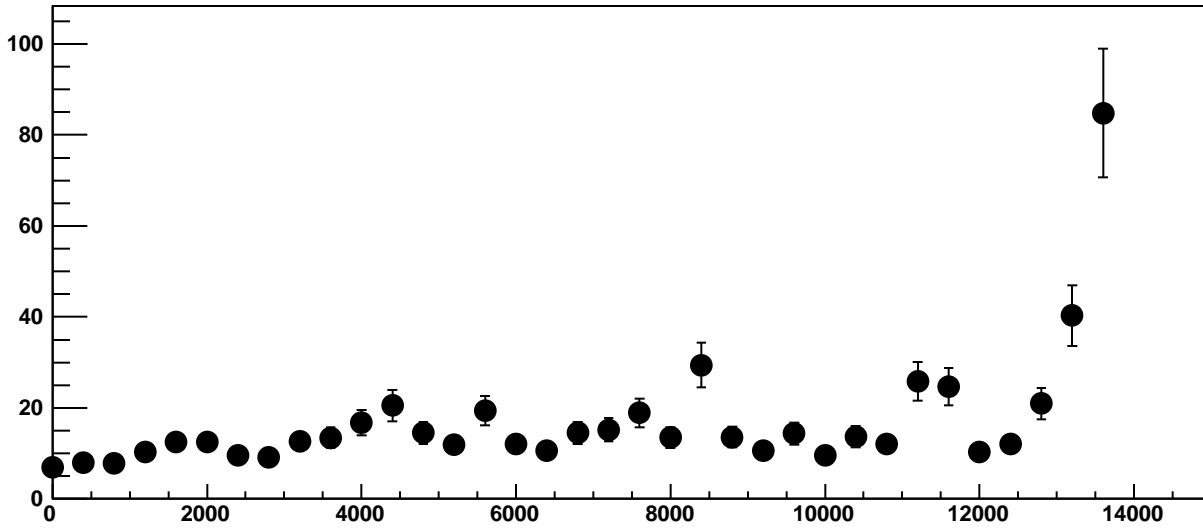
Chip 0, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



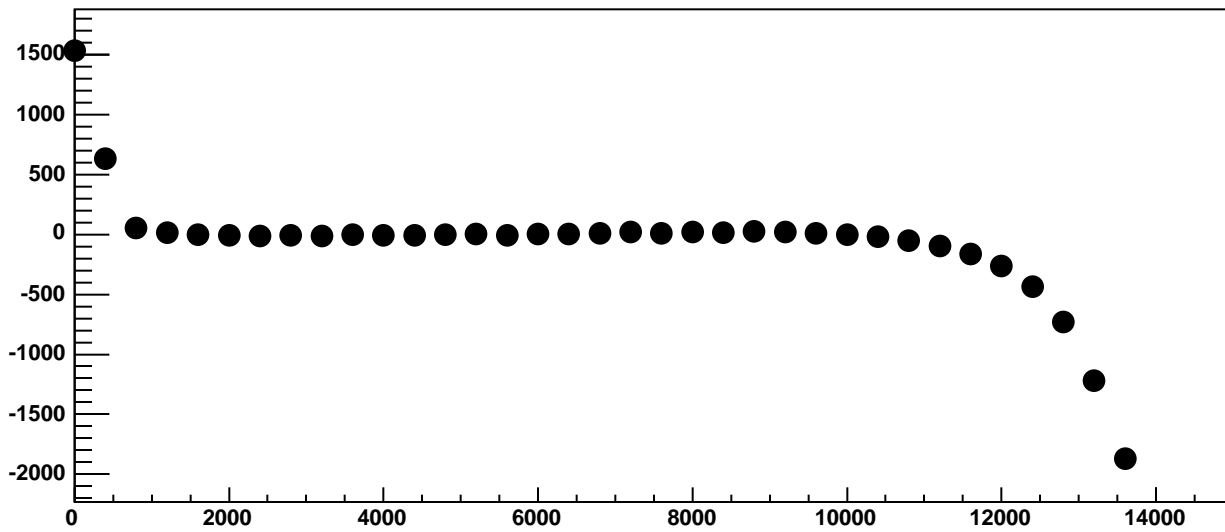
Chip 0, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



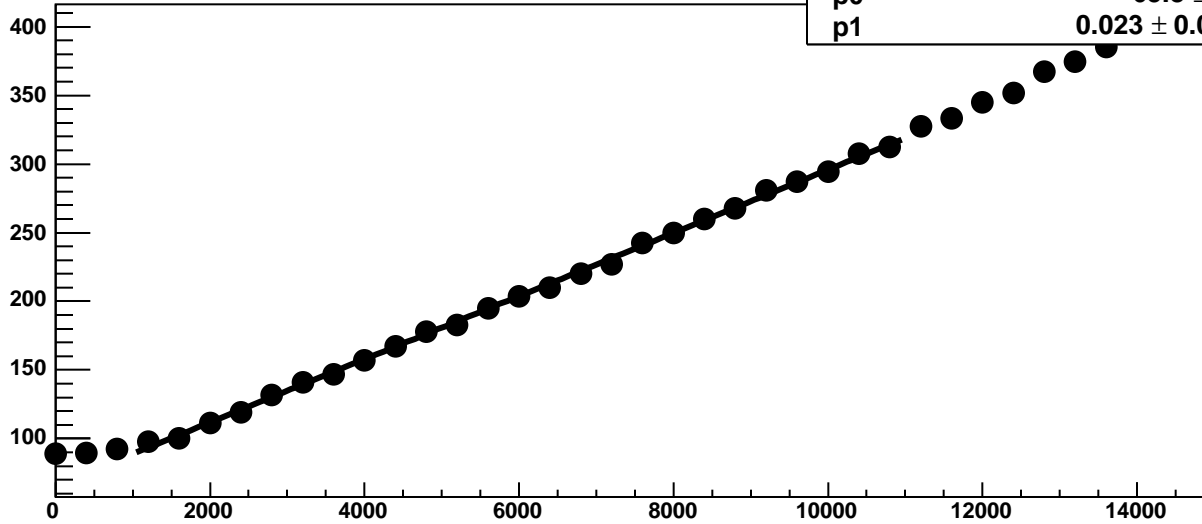
Chip 0, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



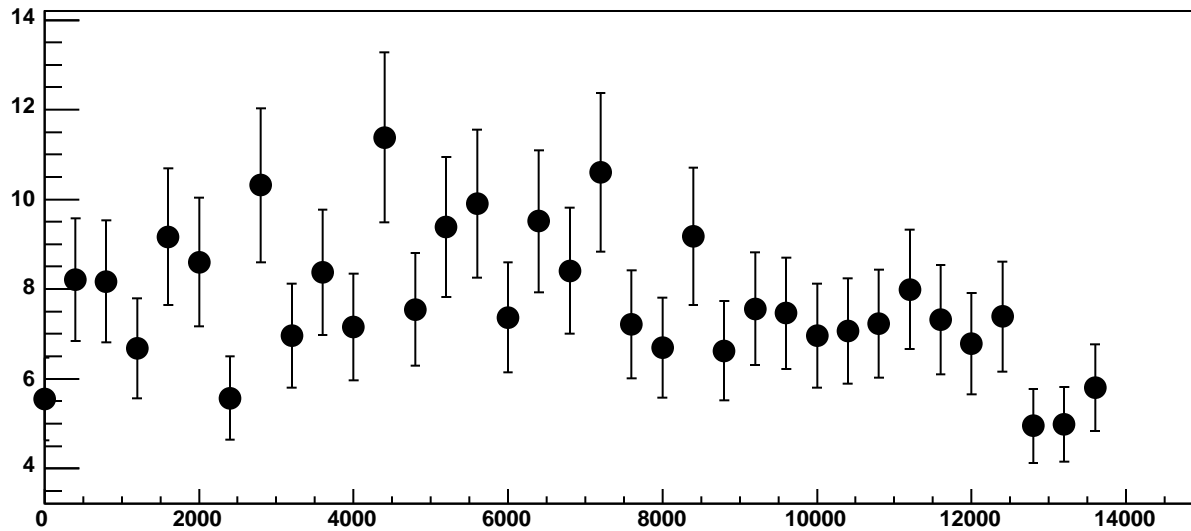
Chip 0, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC



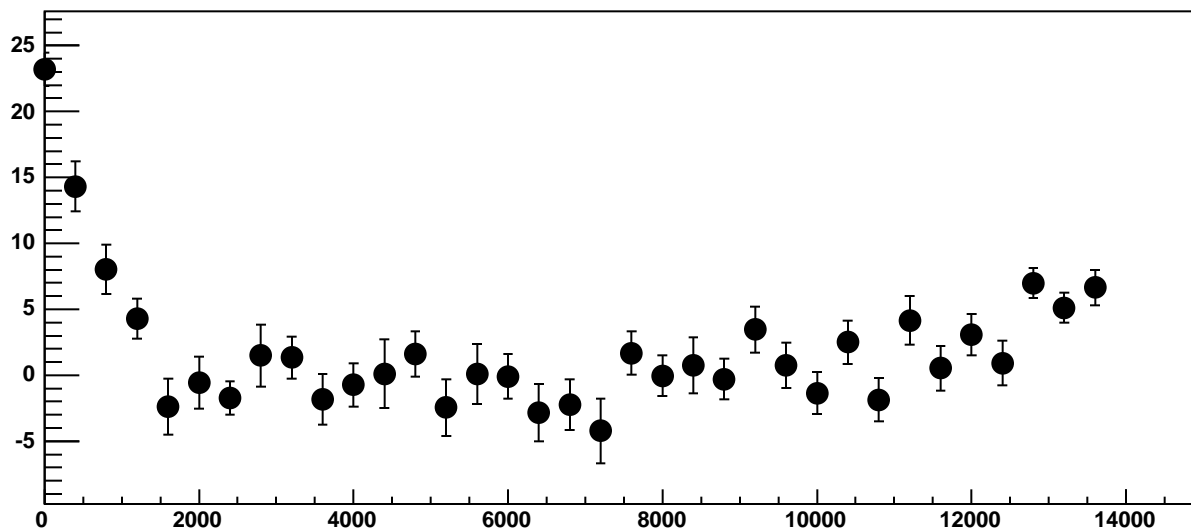
Chip 0, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



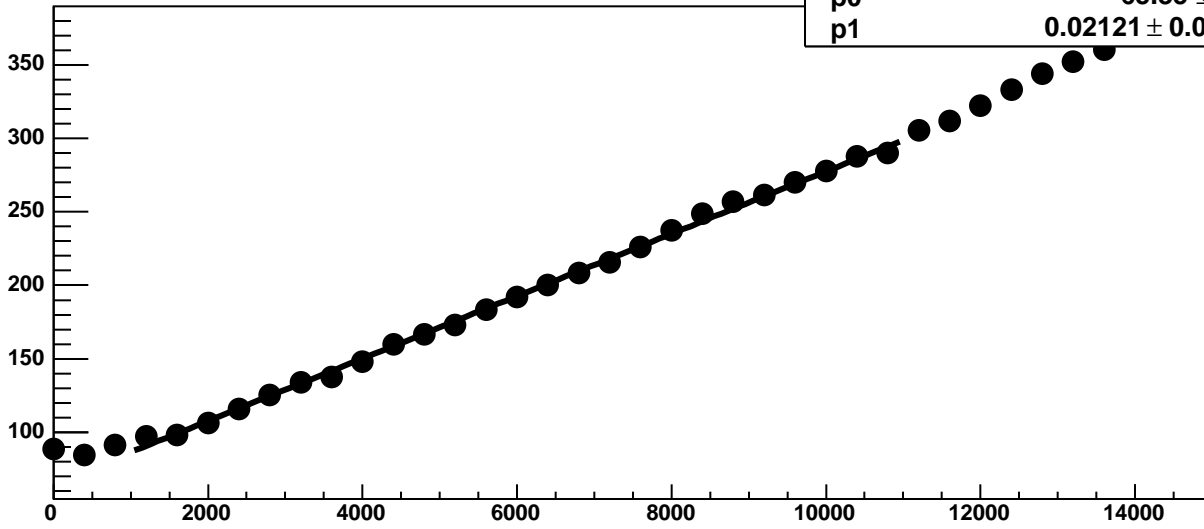
Chip 0, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

43.14 / 23

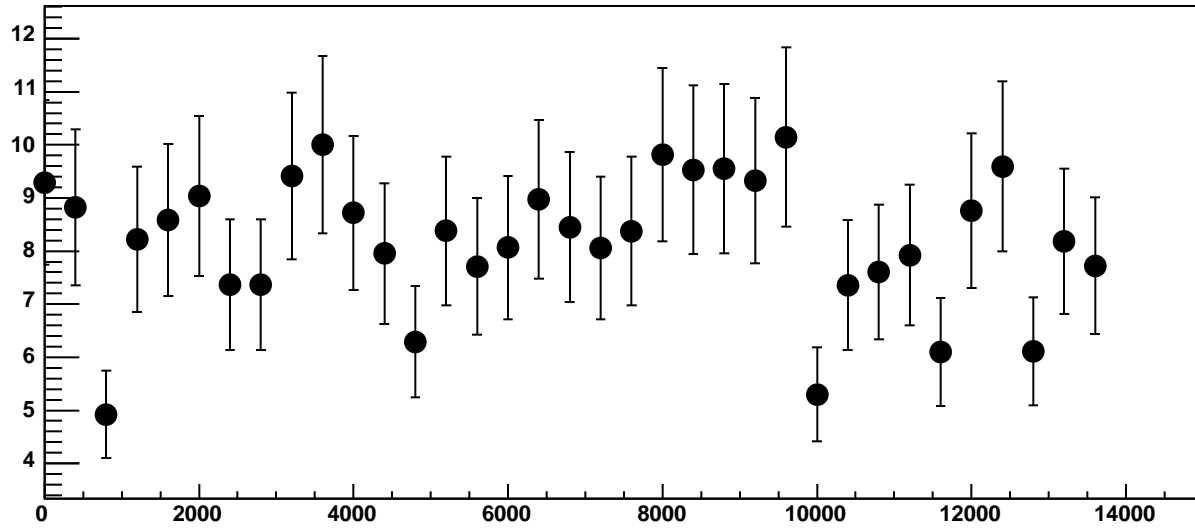
p0

65.35 ± 0.8561

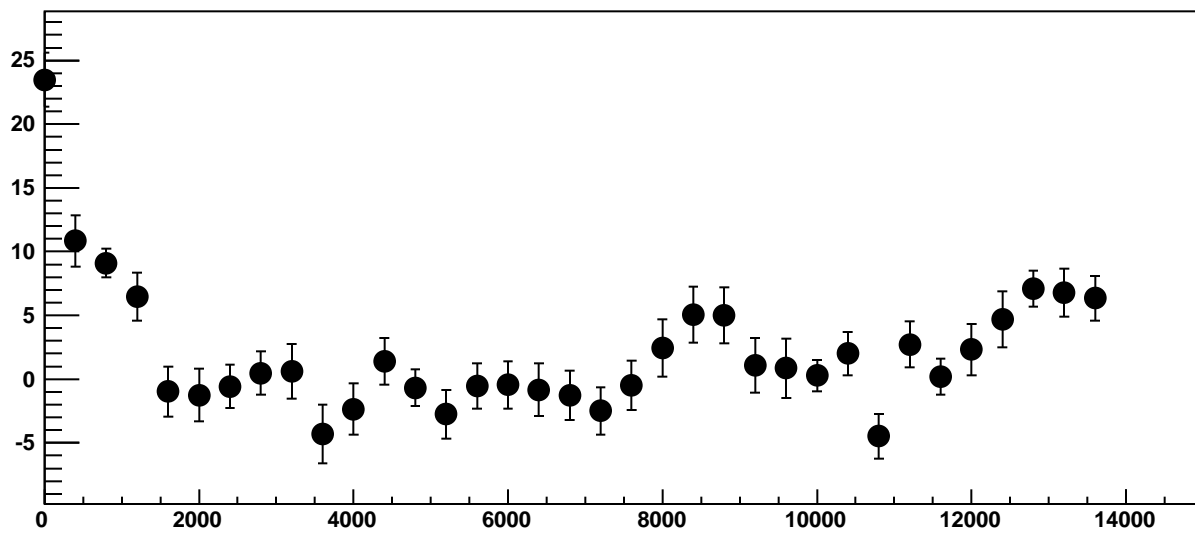
p1

0.02121 ± 0.0001256

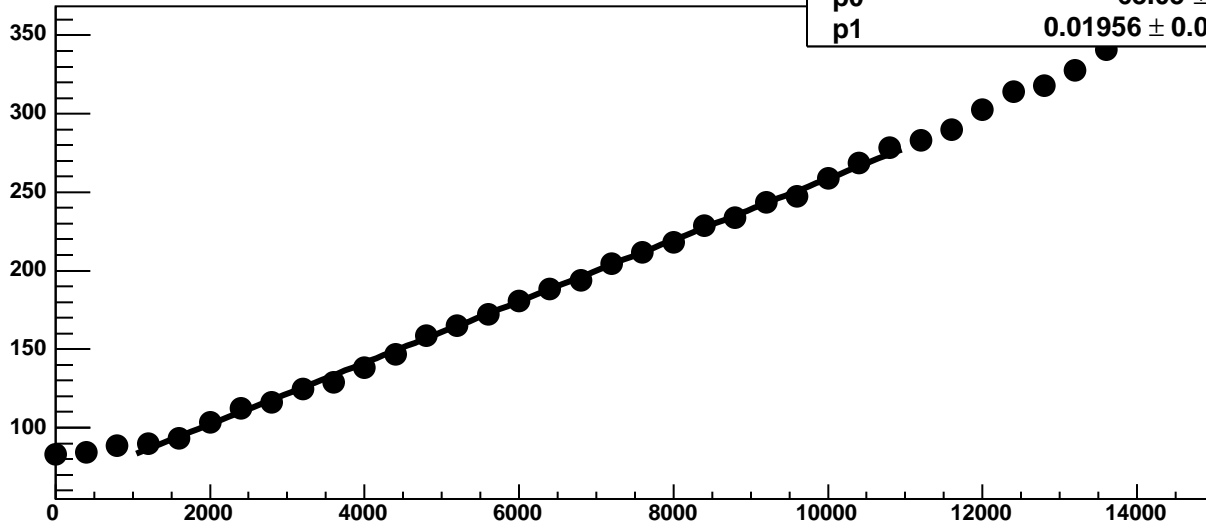
Chip 0, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

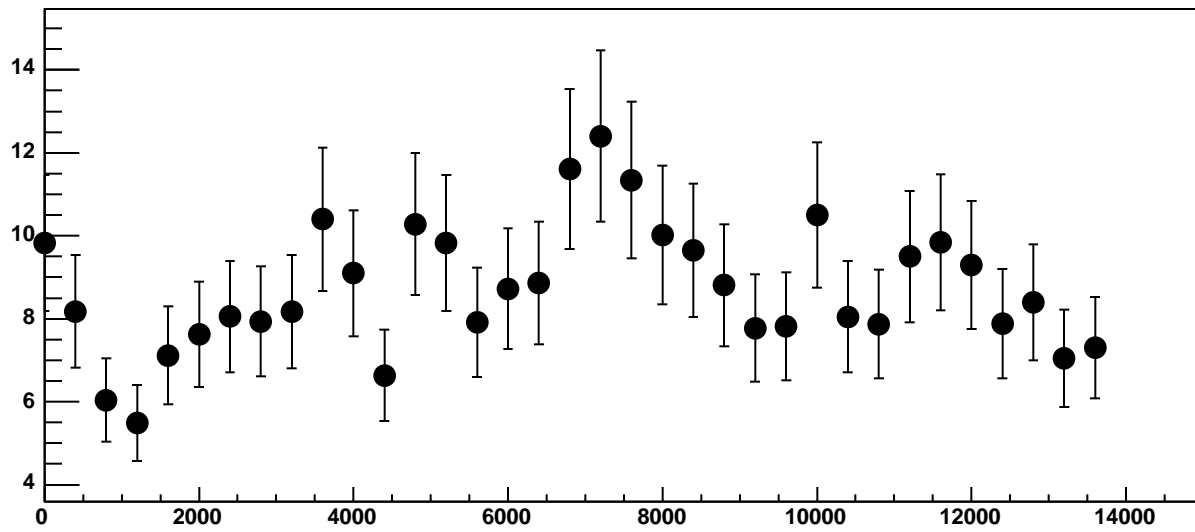


Chip 0, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

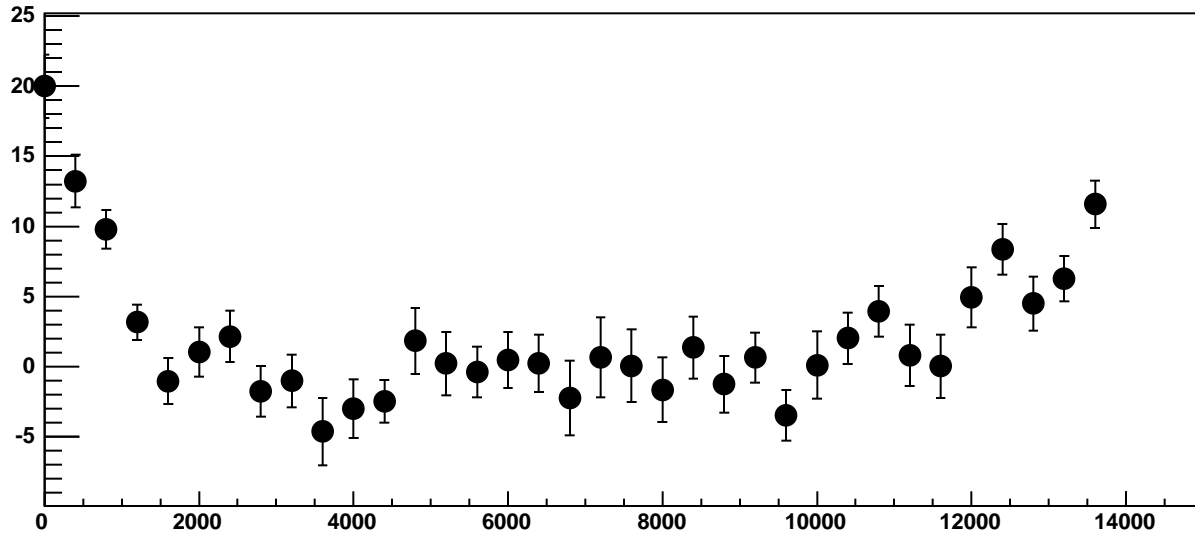


χ^2 / ndf 30.8 / 23
p0 63.05 ± 0.7857
p1 0.01956 ± 0.0001236

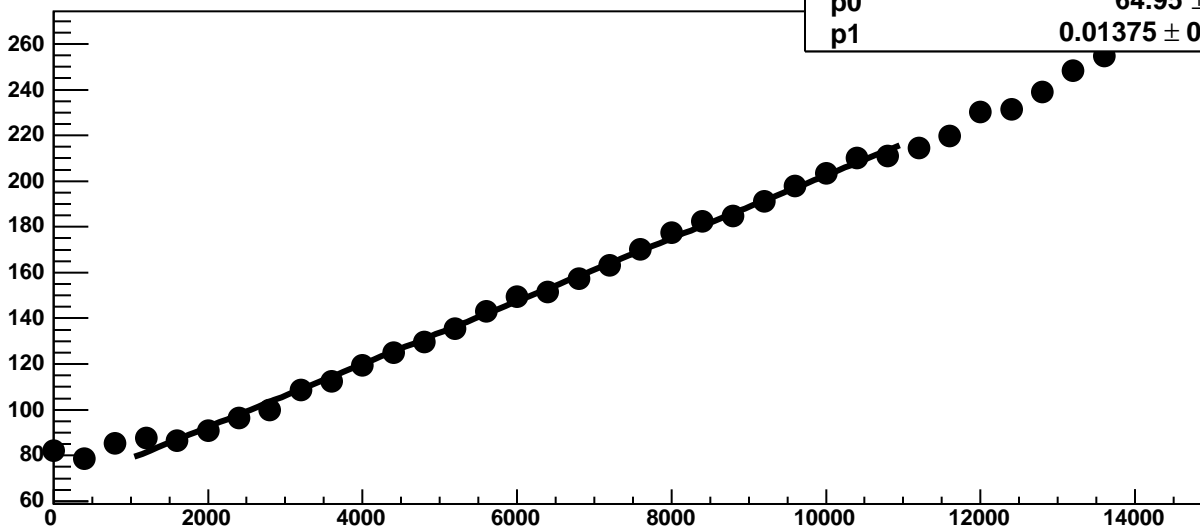
Chip 0, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

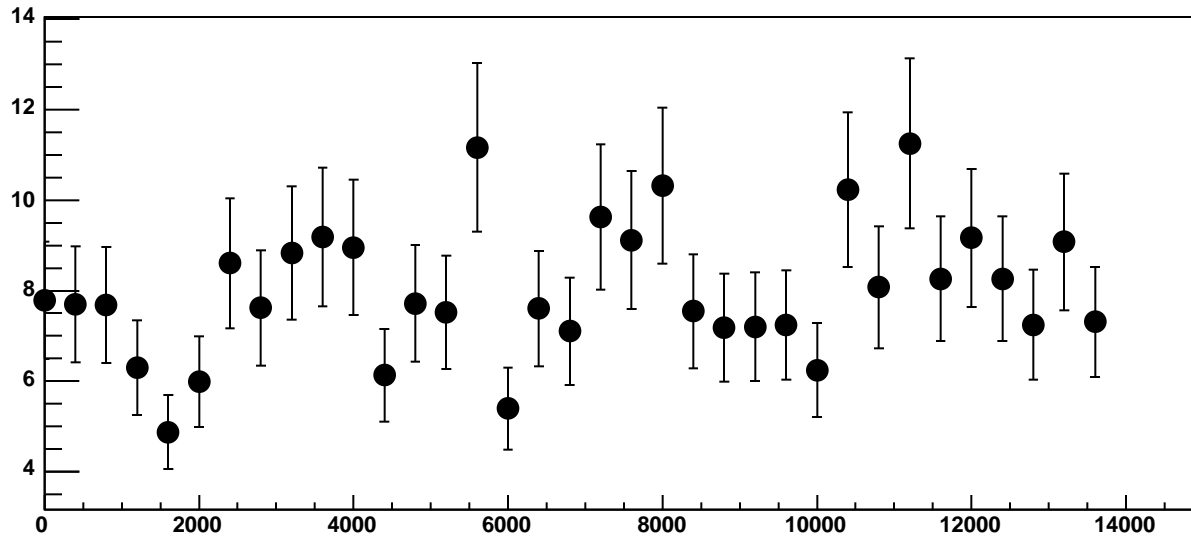


Chip 0, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

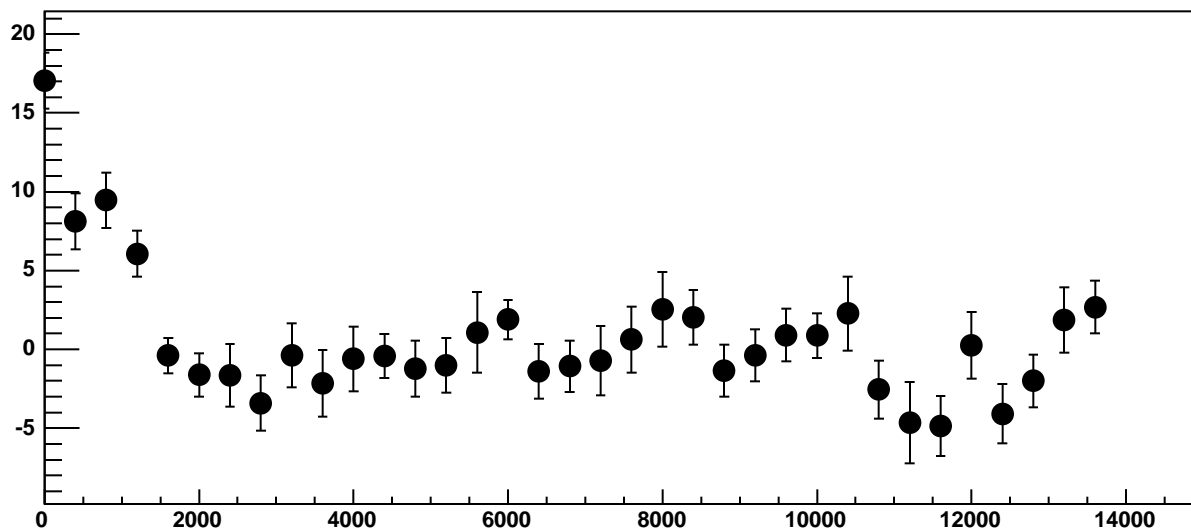


χ^2 / ndf 36.24 / 23
p0 64.95 ± 0.7152
p1 0.01375 ± 0.000112

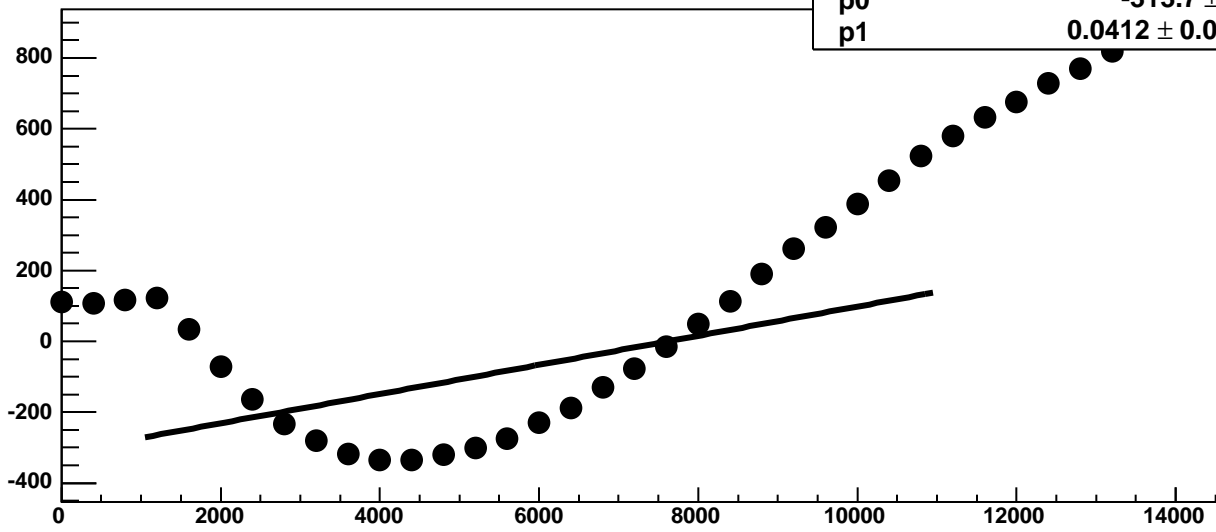
Chip 0, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

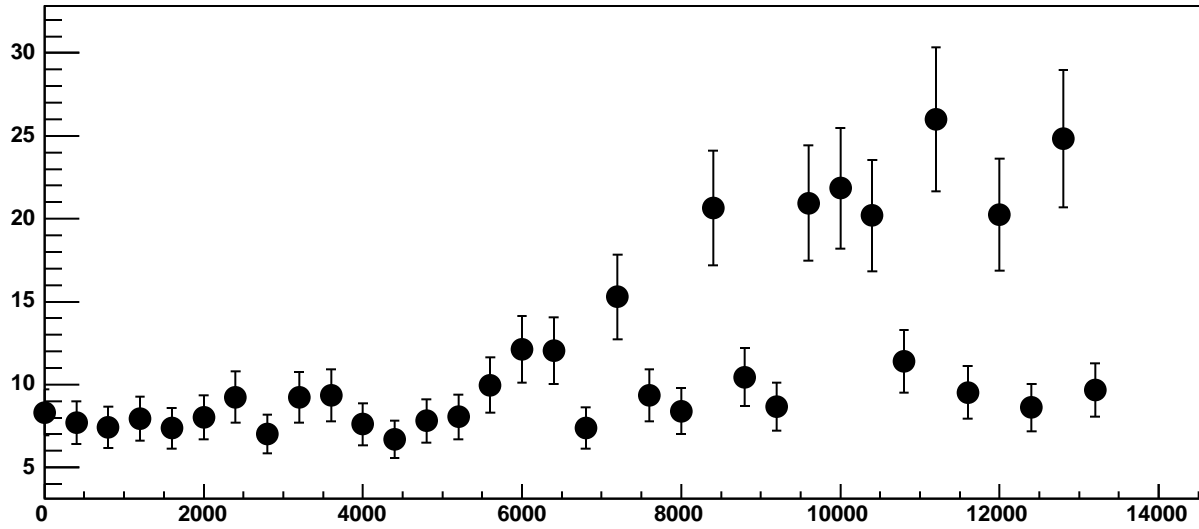


Chip 0, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

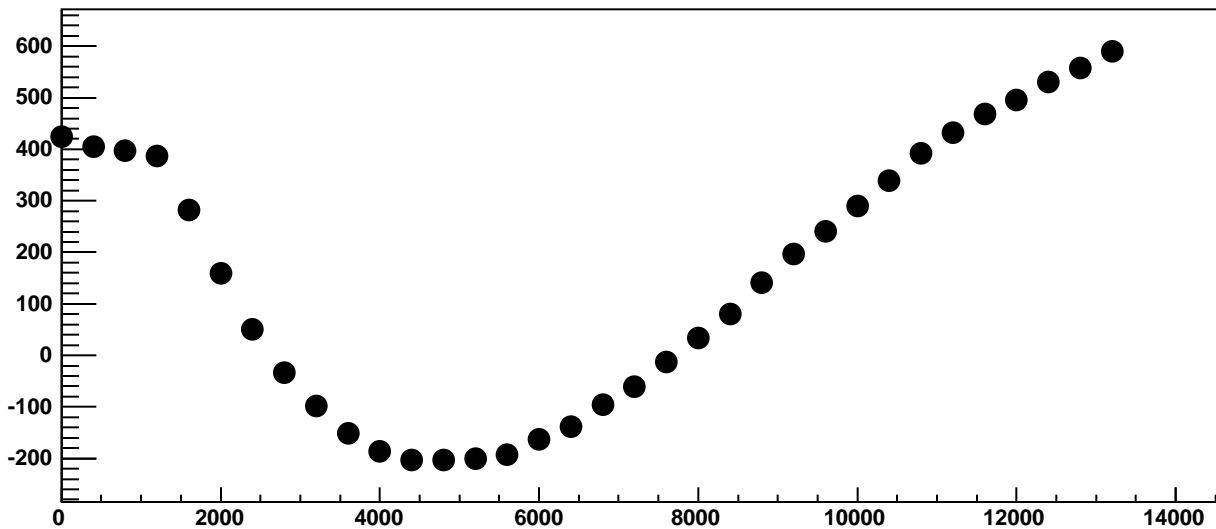


χ^2 / ndf 2.06e+05 / 23
p0 -313.7 ± 0.9221
p1 0.0412 ± 0.0001631

Chip 0, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

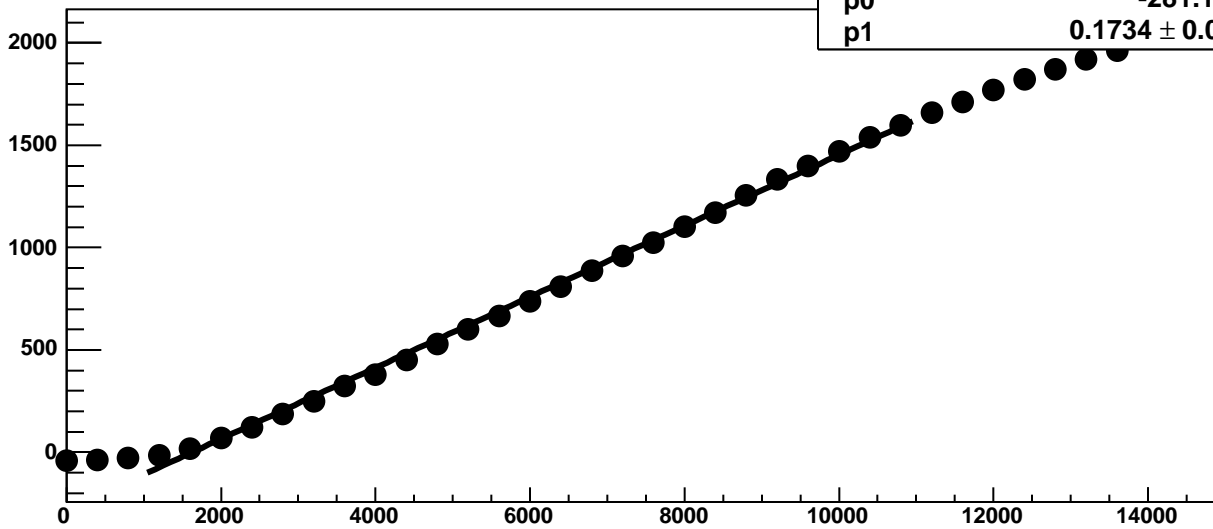


Chip 0, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

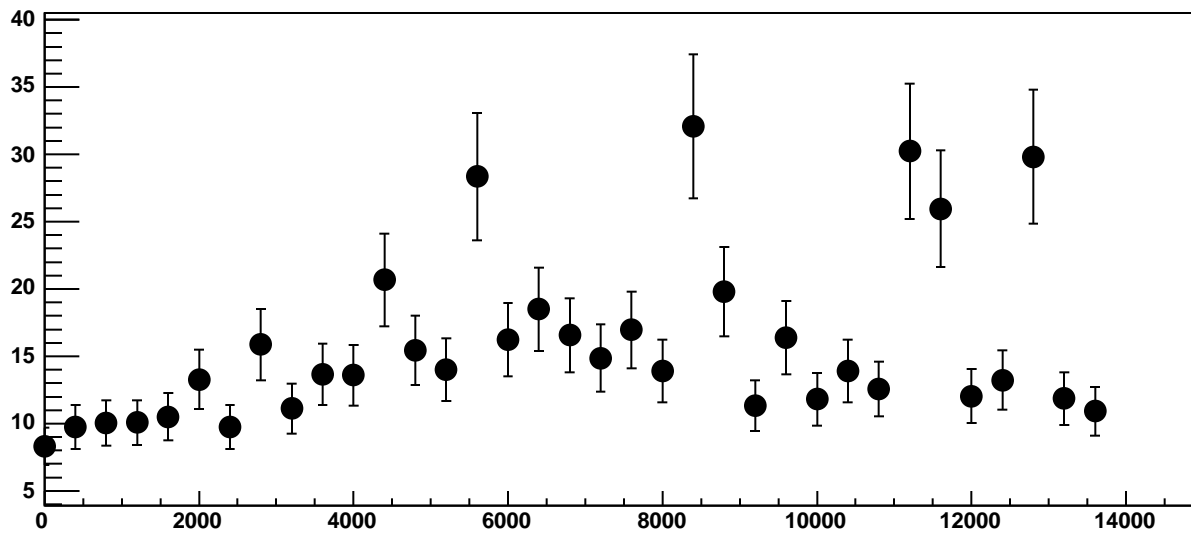


Chip 0, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC

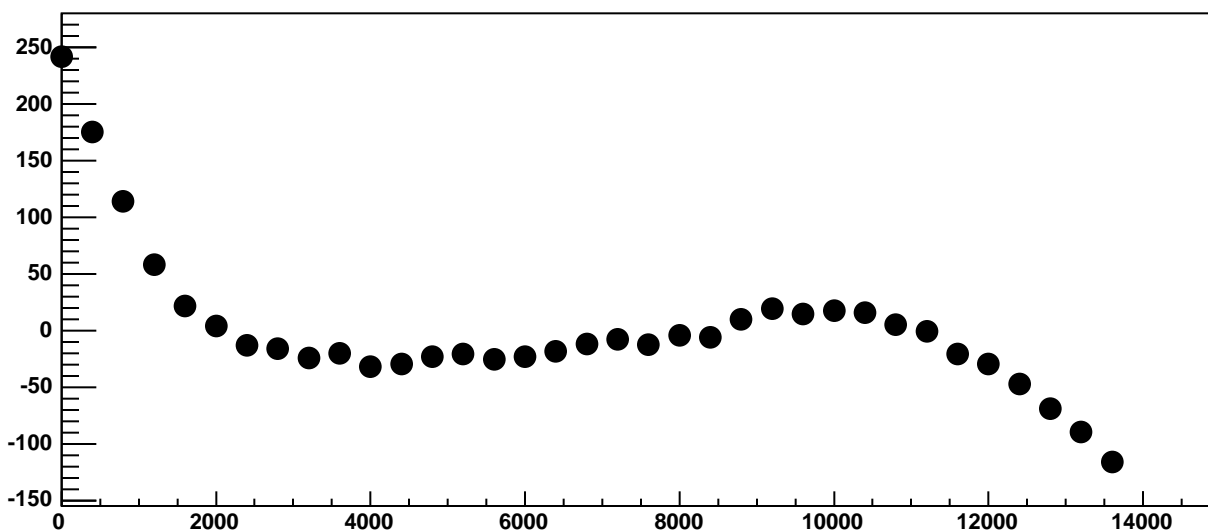
χ^2 / ndf 1370 / 23
p0 -281.1 ± 1.268
p1 0.1734 ± 0.0001988



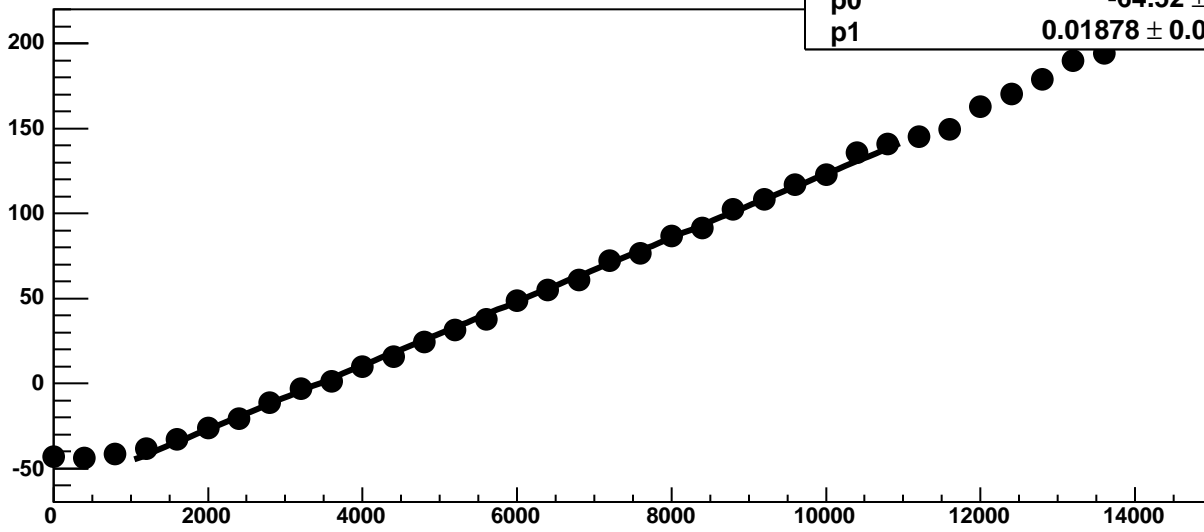
Chip 0, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC

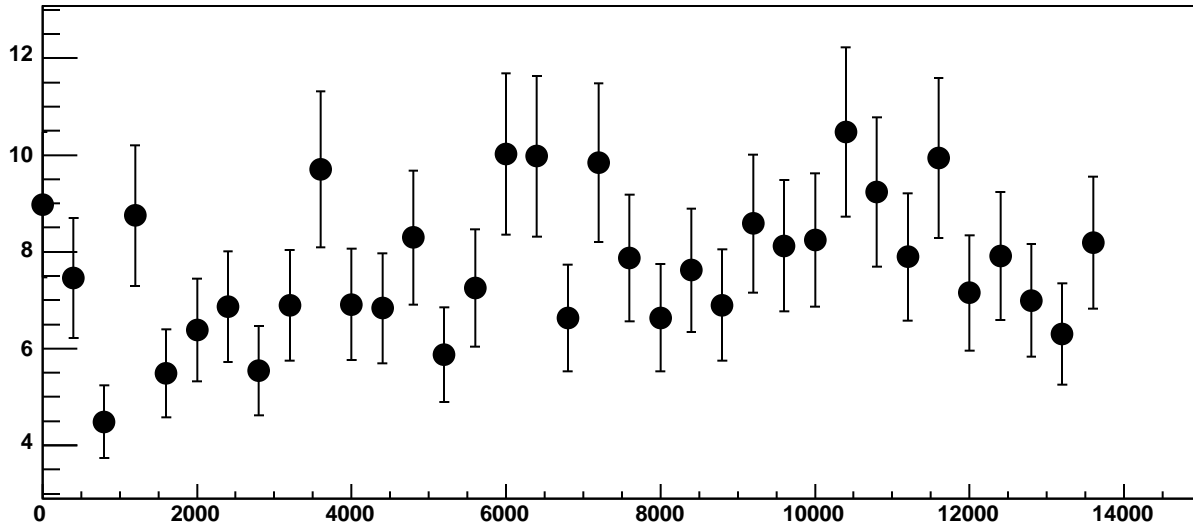


Chip 0, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

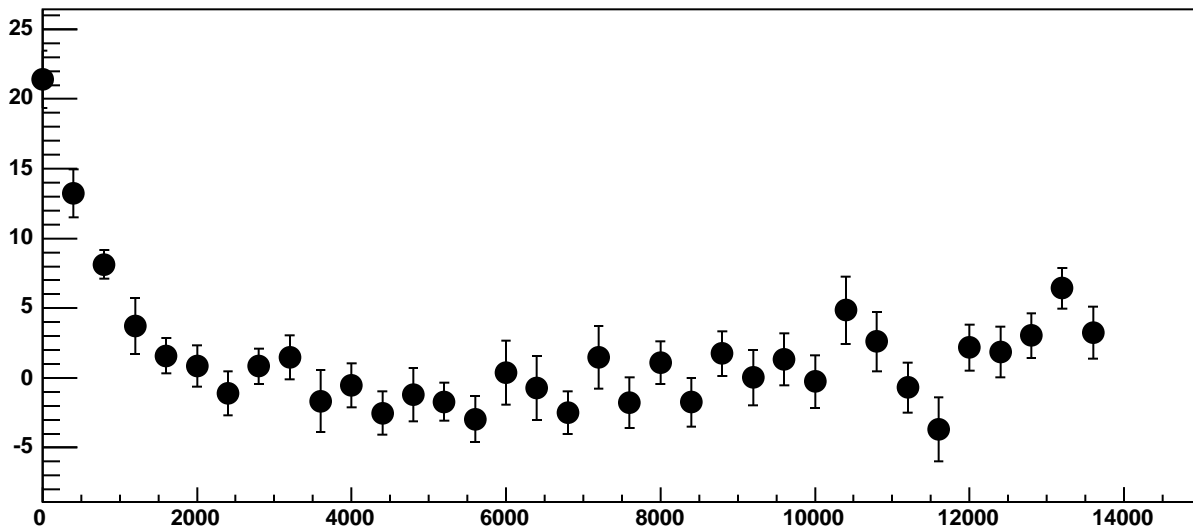


χ^2 / ndf 28.66 / 23
p0 -64.52 ± 0.7386
p1 0.01878 ± 0.0001197

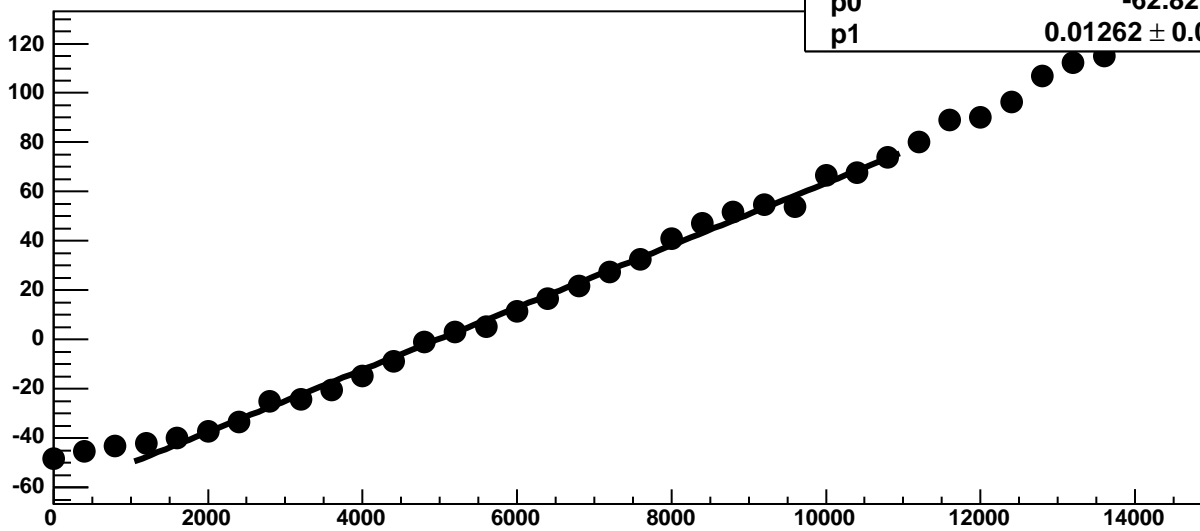
Chip 0, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



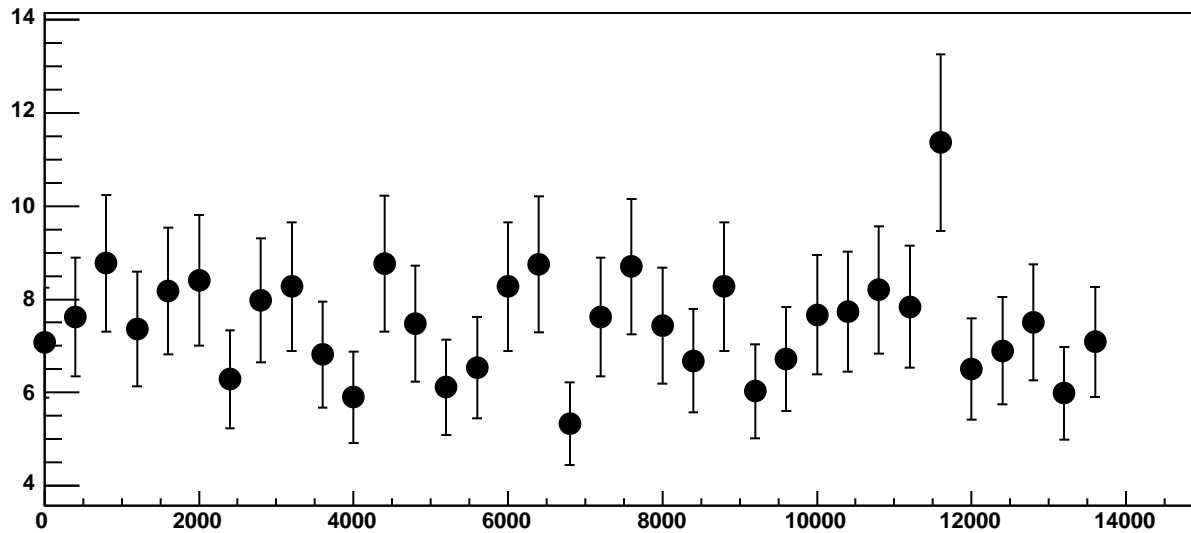
Chip 0, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



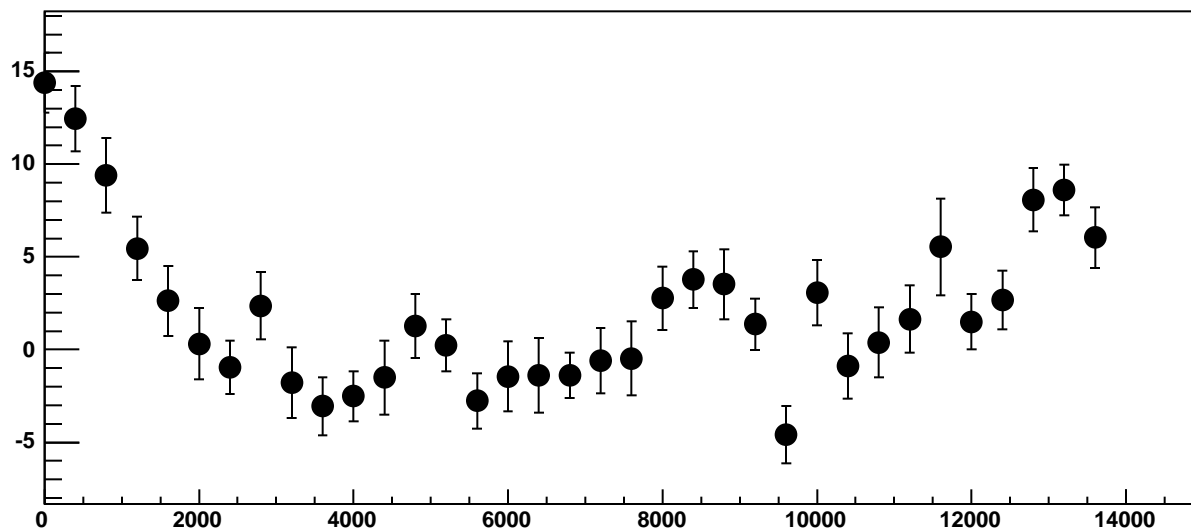
Chip 0, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



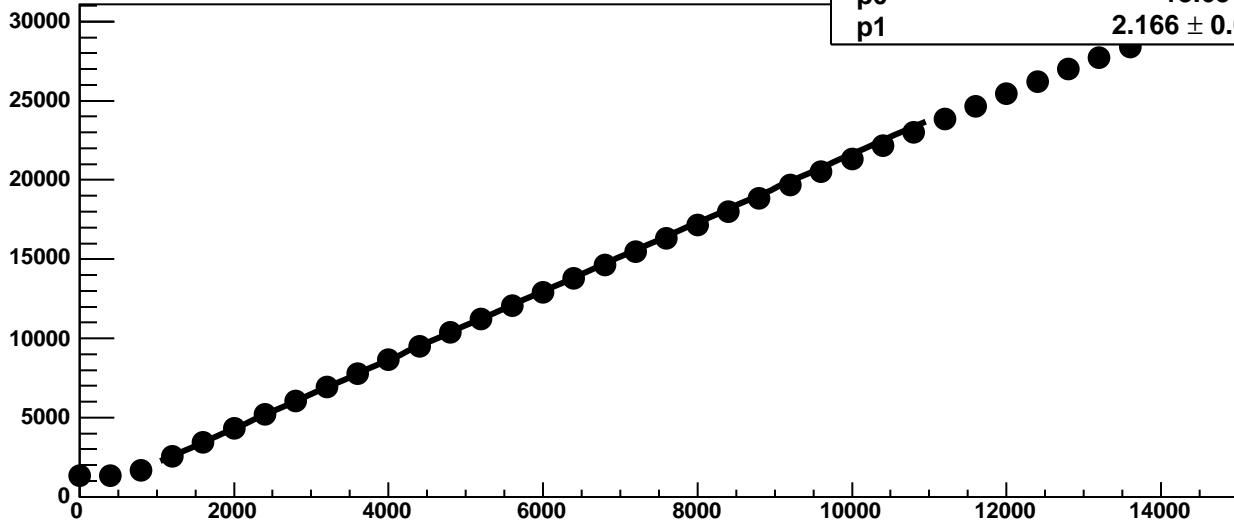
Chip 0, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



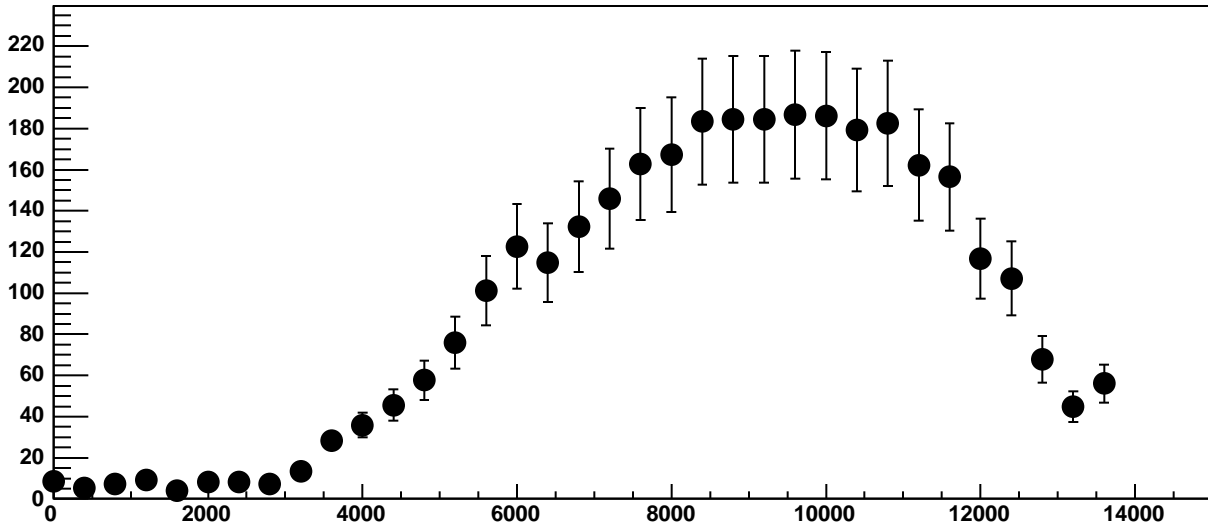
Chip 0, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



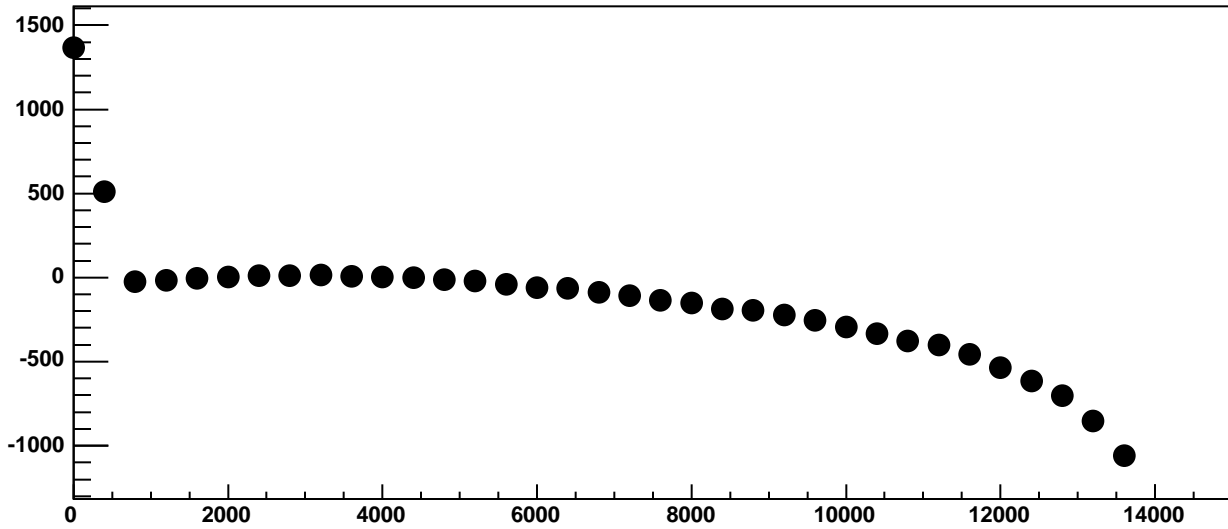
Chip 0, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC



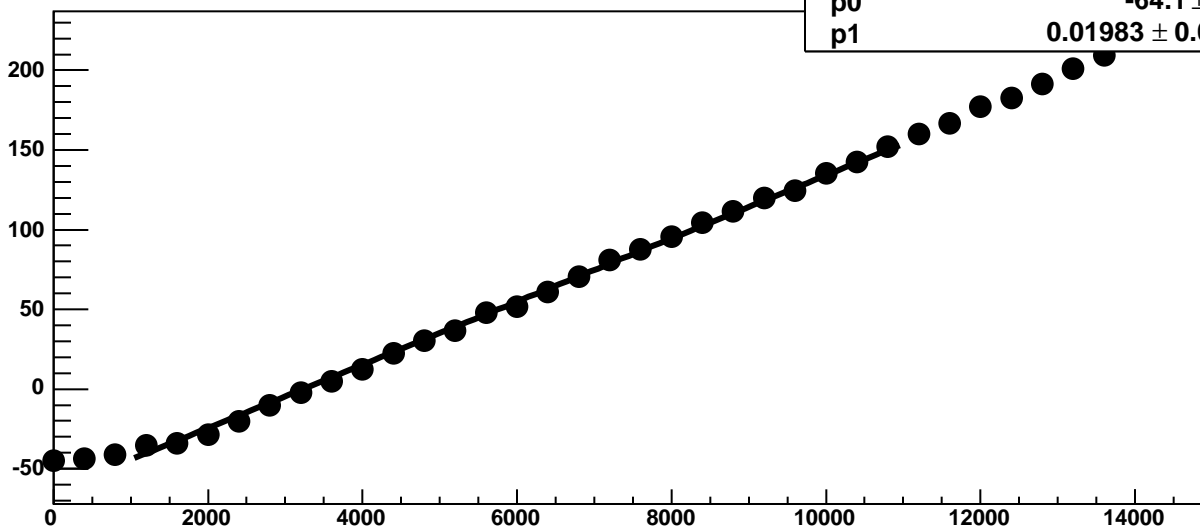
Chip 0, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC

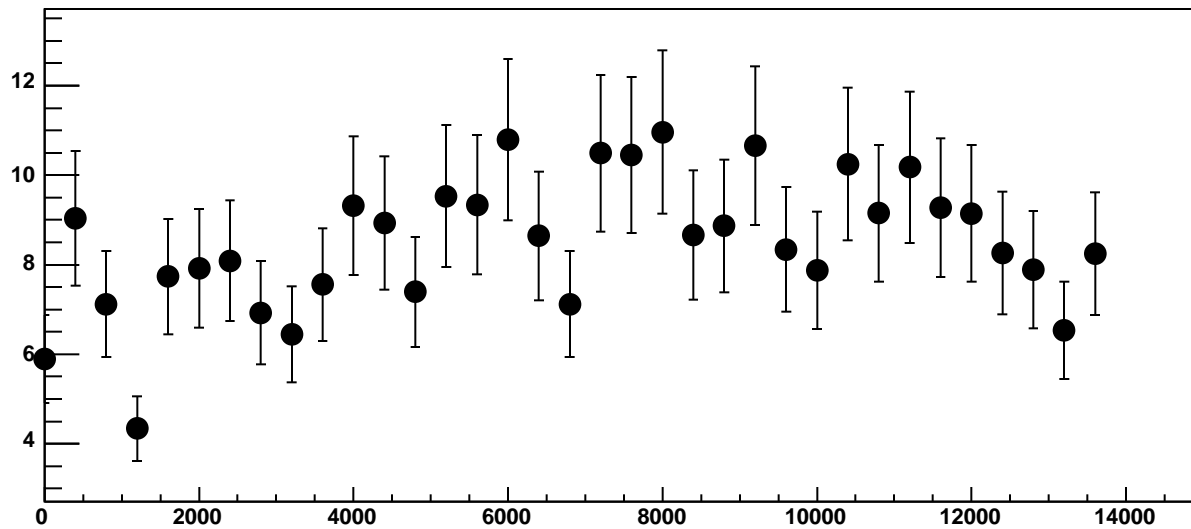


Chip 0, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

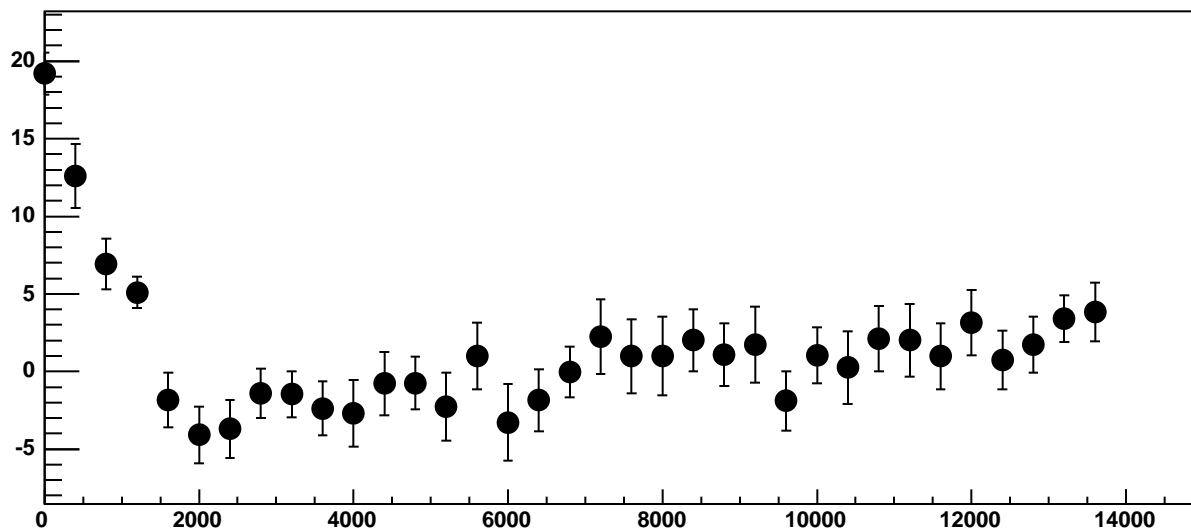


χ^2 / ndf 51.12 / 23
p0 -64.1 ± 0.7249
p1 0.01983 ± 0.0001211

Chip 0, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

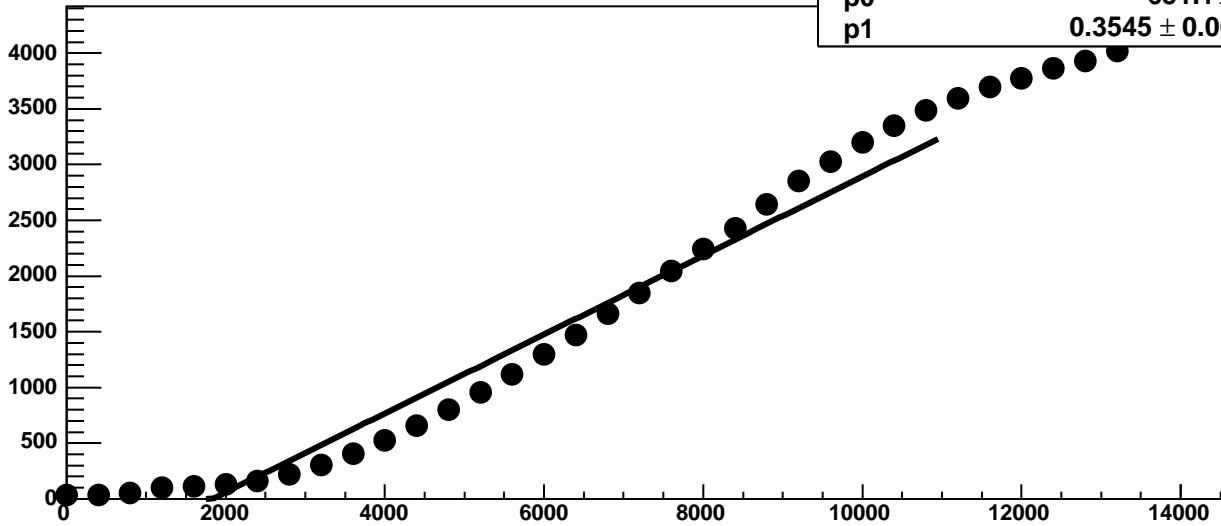


Chip 0, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

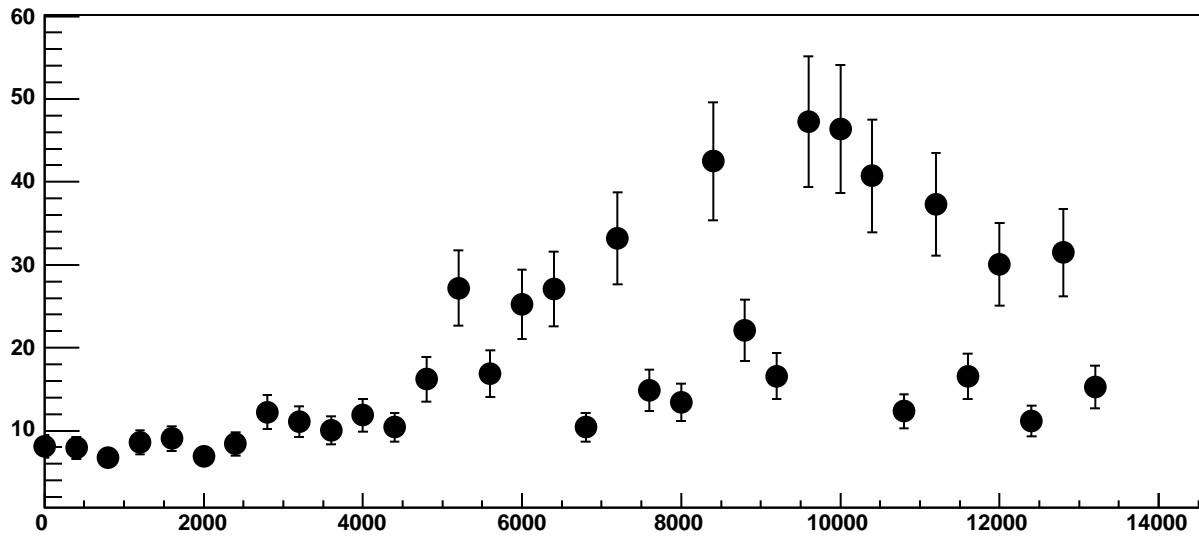


Chip 0, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

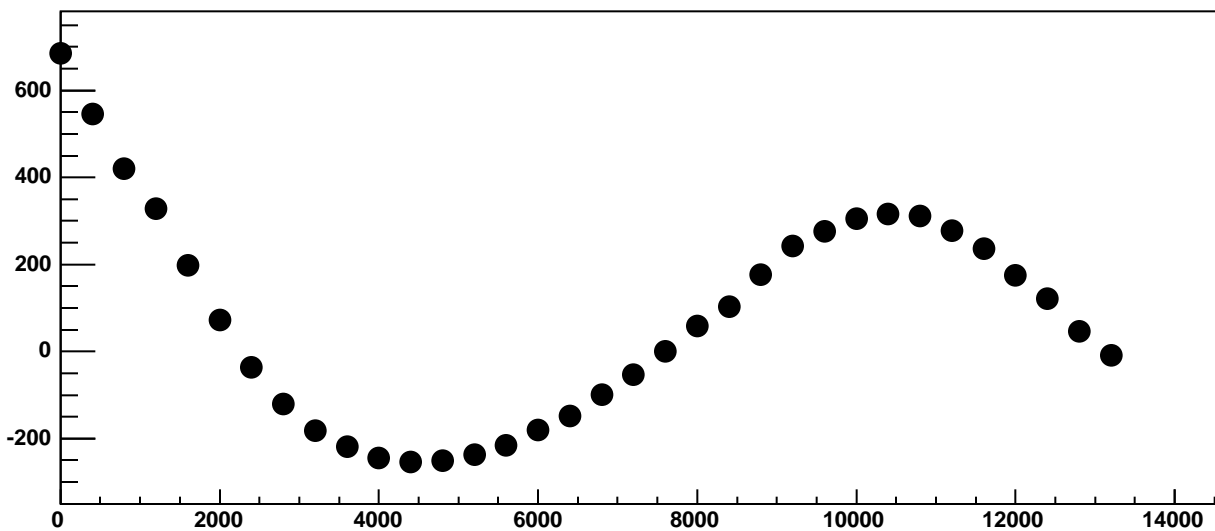
χ^2 / ndf 1.075e+05 / 23
p0 -651.1 ± 1.093
p1 0.3545 ± 0.0002206



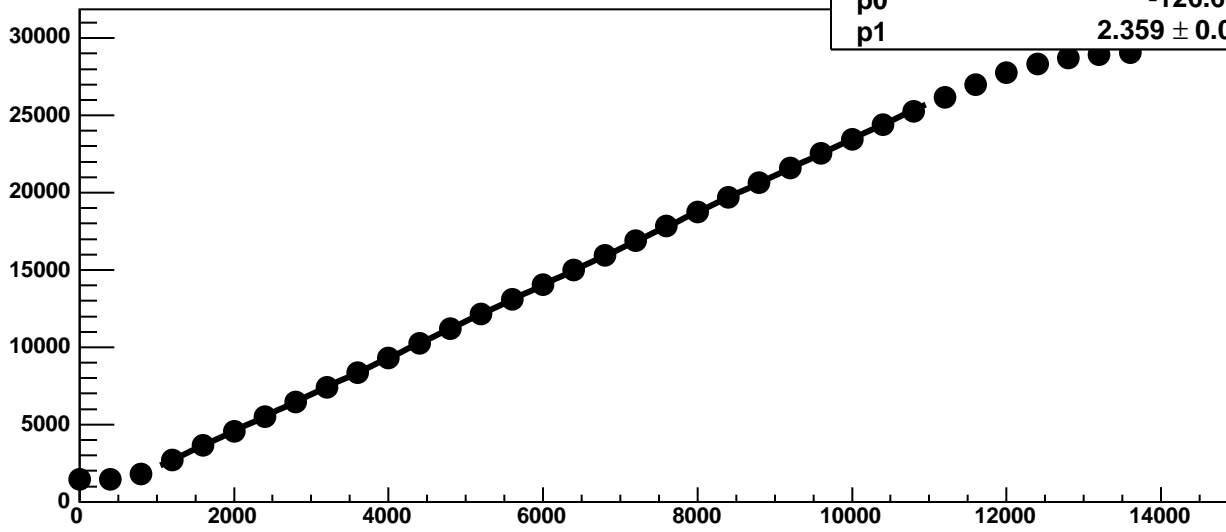
Chip 0, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



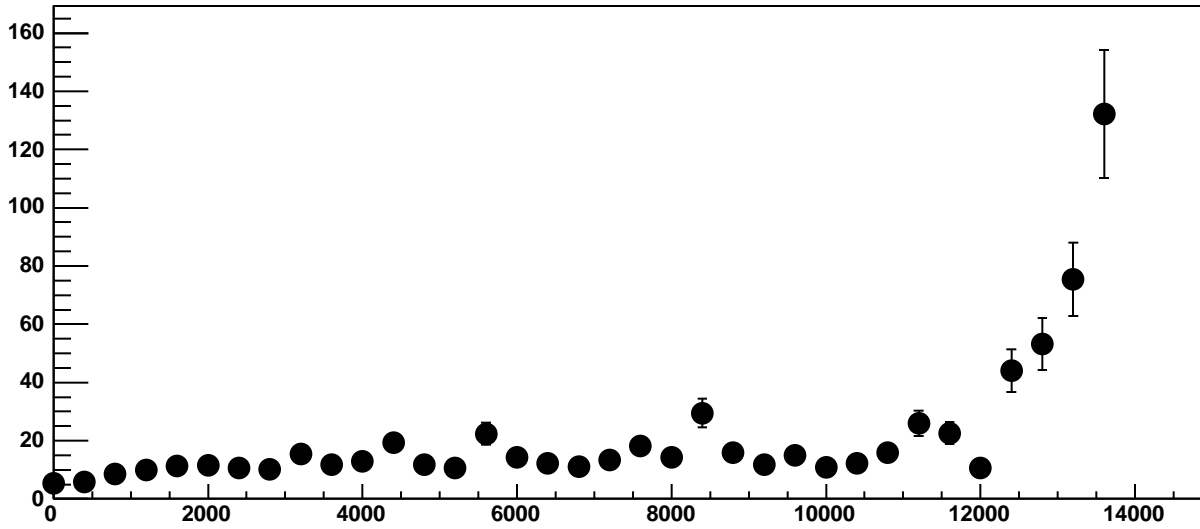
Chip 0, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



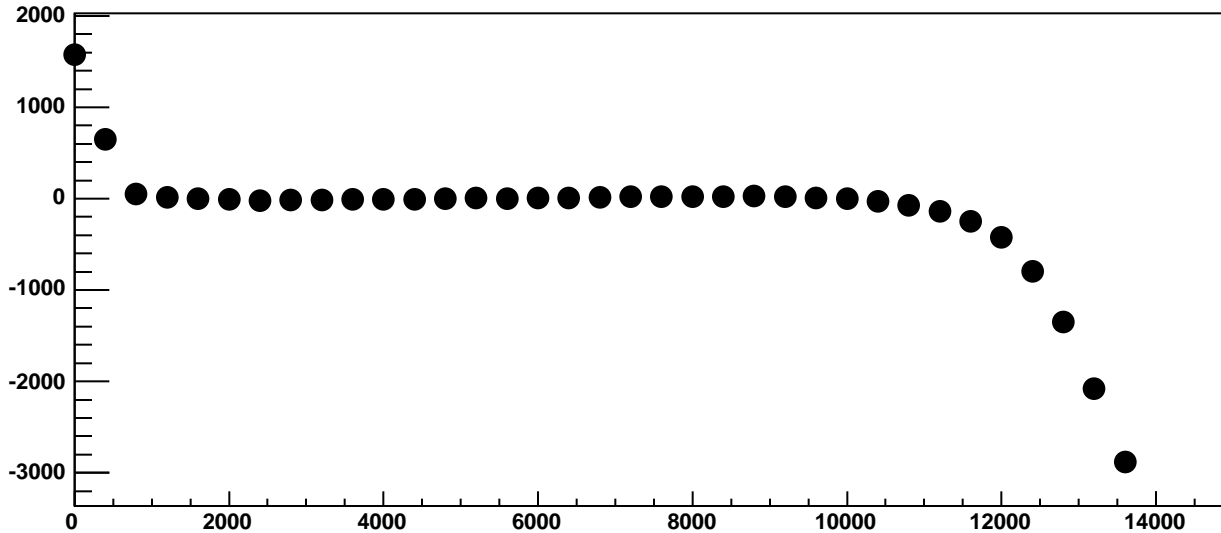
Chip 0, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC



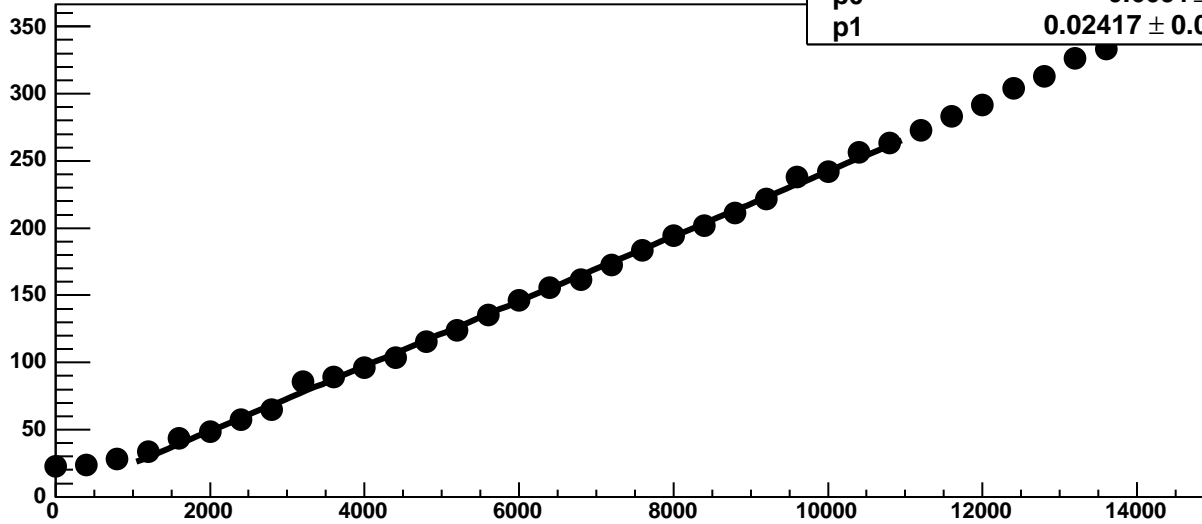
Chip 0, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

54.74 / 23

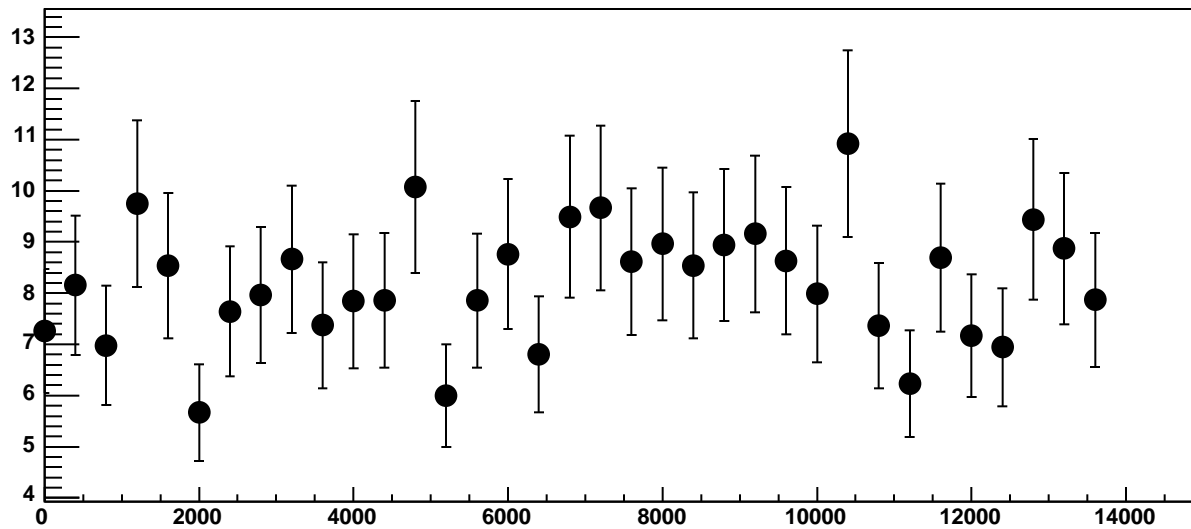
p0

0.6091 ± 0.8288

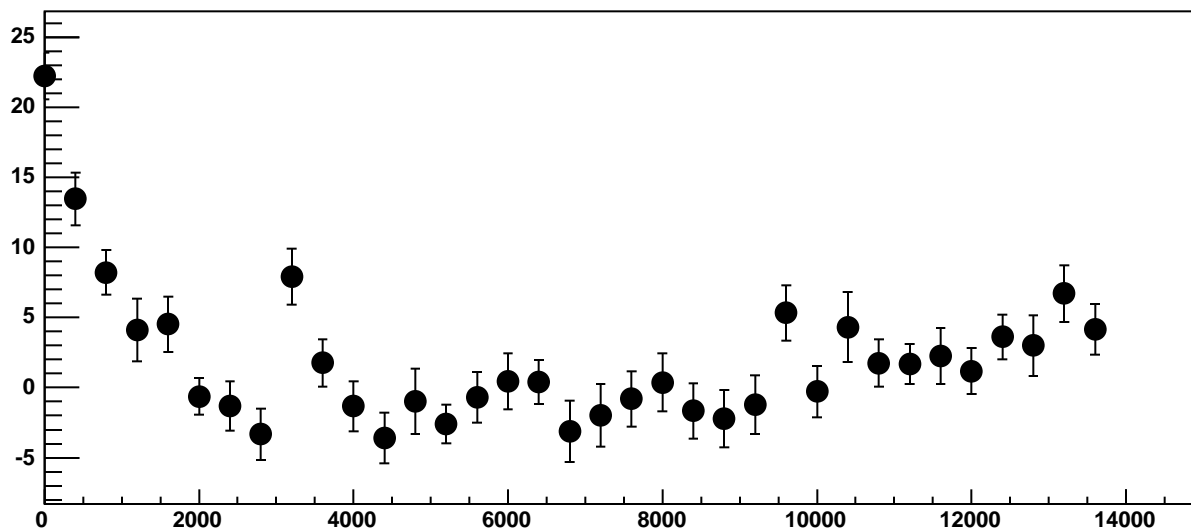
p1

0.02417 ± 0.0001297

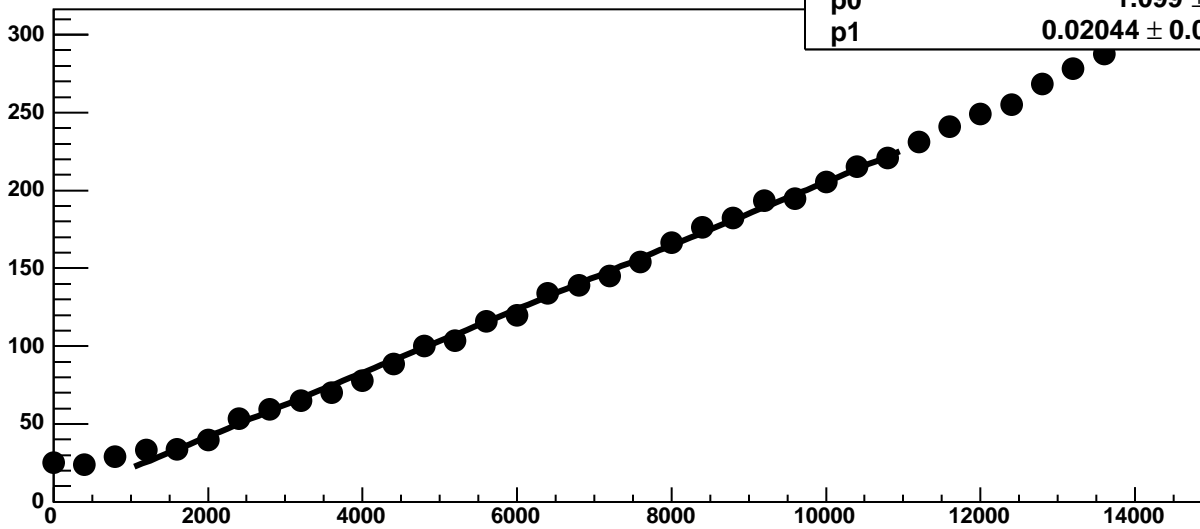
Chip 0, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



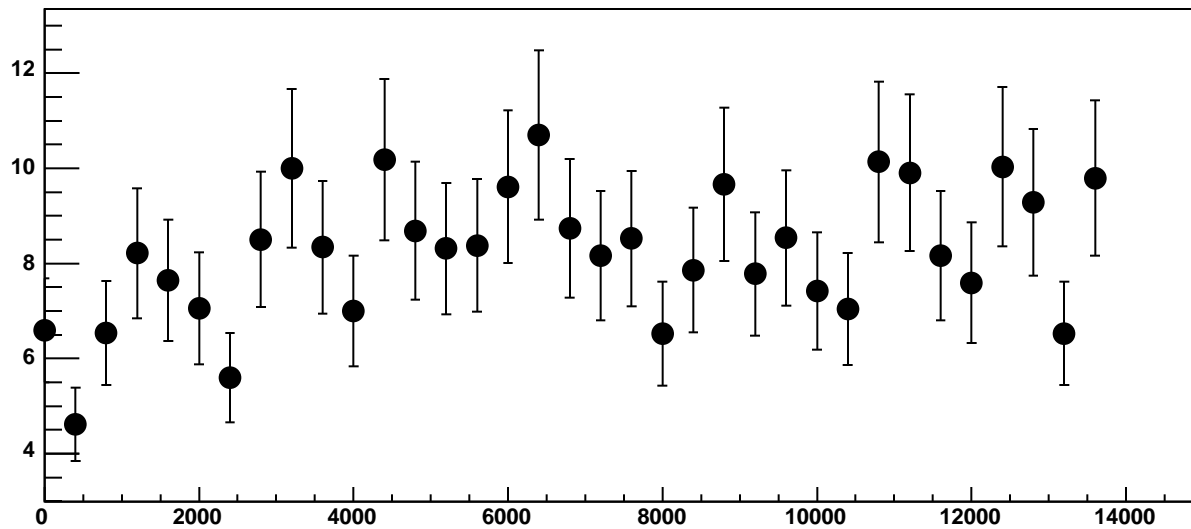
Chip 0, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



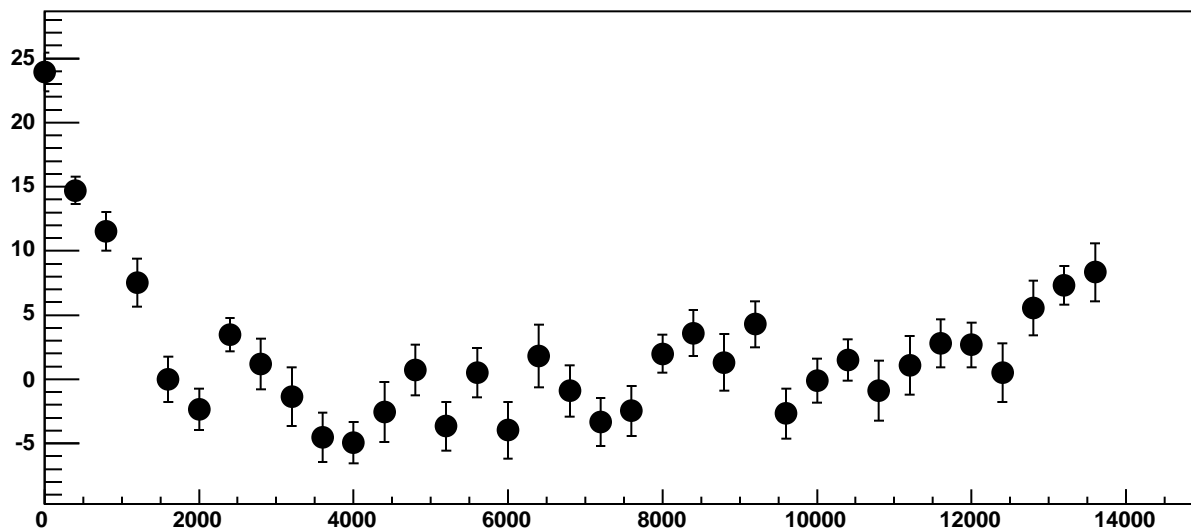
Chip 0, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



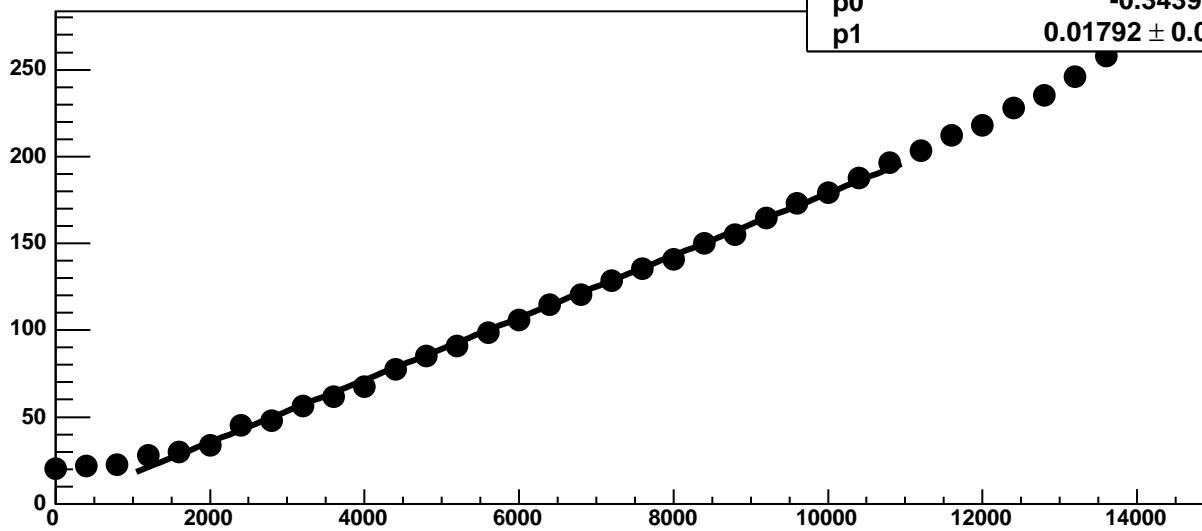
Chip 0, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

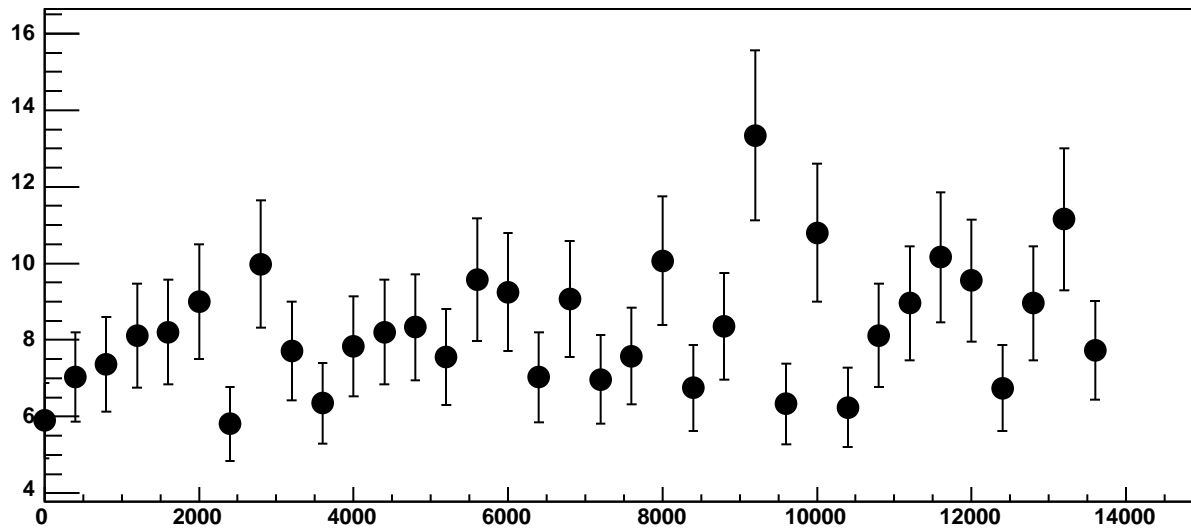


Chip 0, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

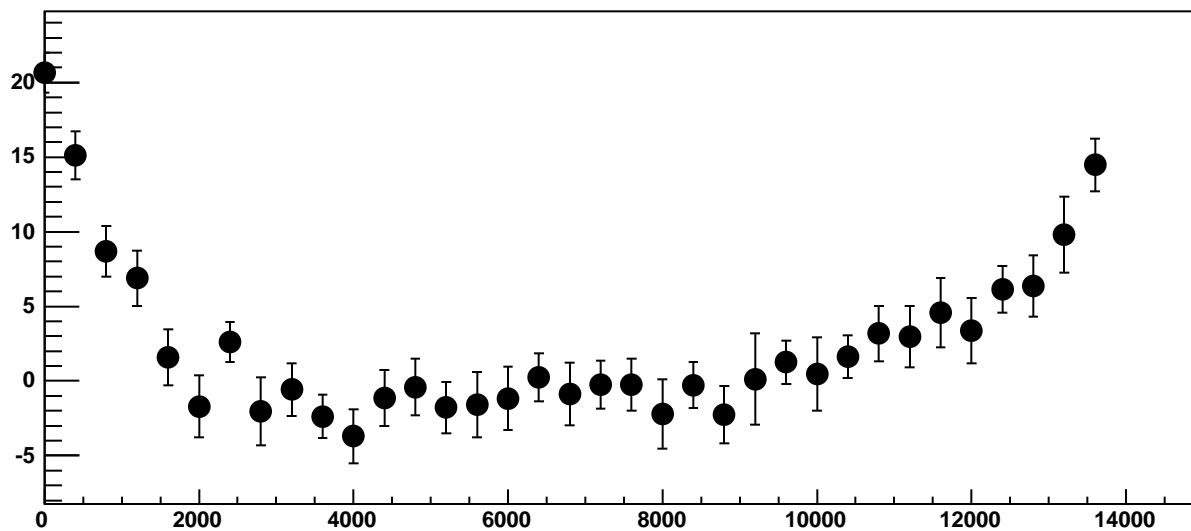


χ^2 / ndf 36.55 / 23
p0 -0.3439 ± 0.819
p1 0.01792 ± 0.0001233

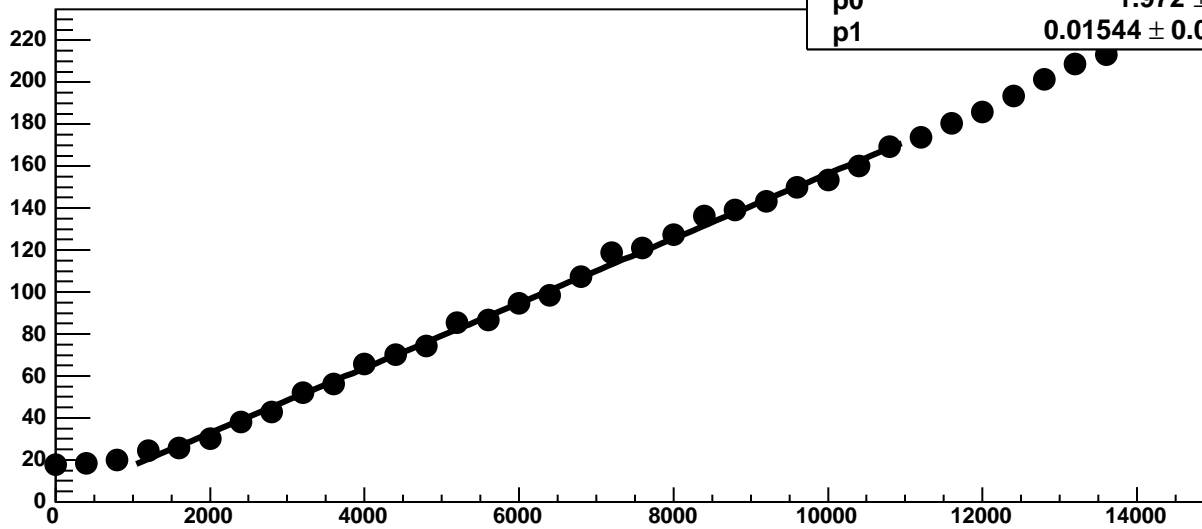
Chip 0, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

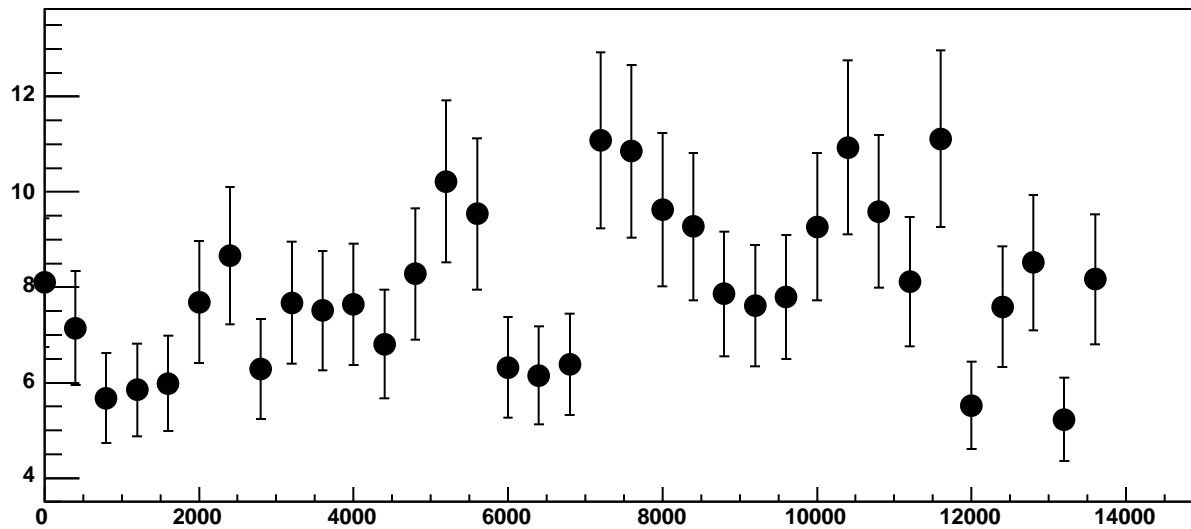


Chip 0, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

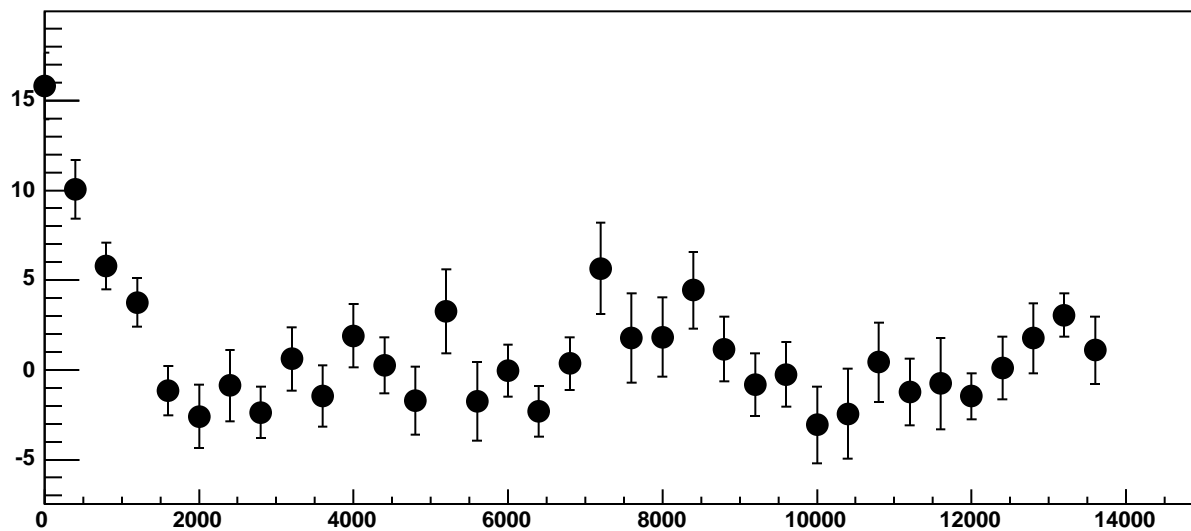


χ^2 / ndf 35.95 / 23
p0 1.972 ± 0.7573
p1 0.01544 ± 0.0001238

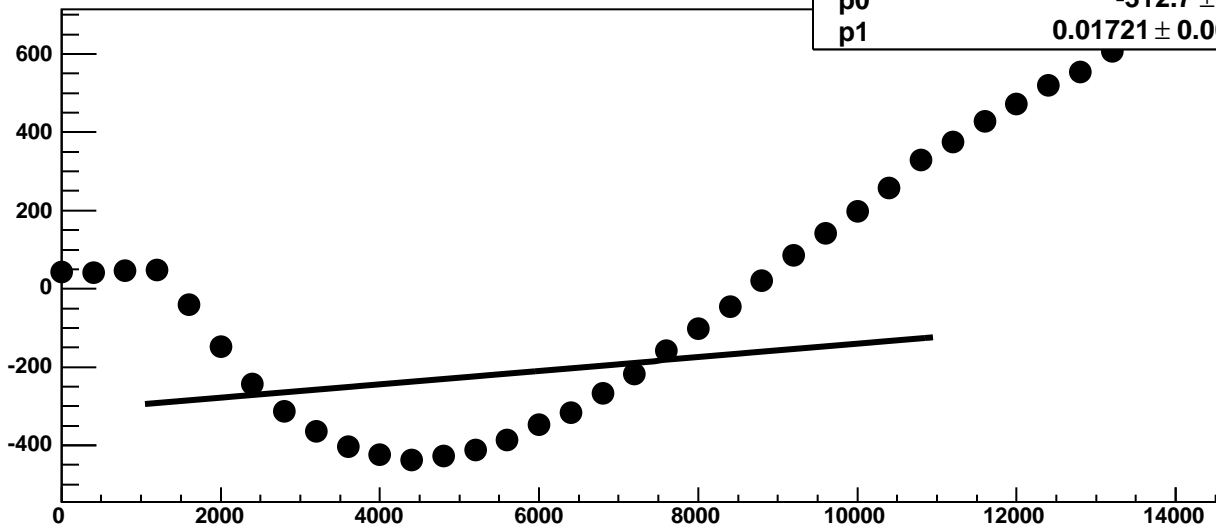
Chip 0, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



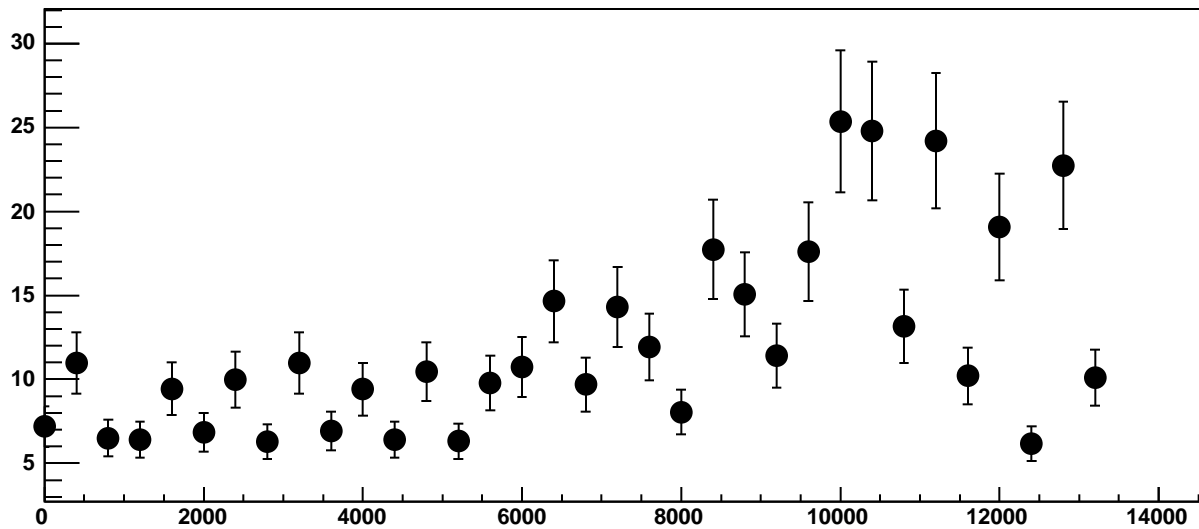
Chip 0, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



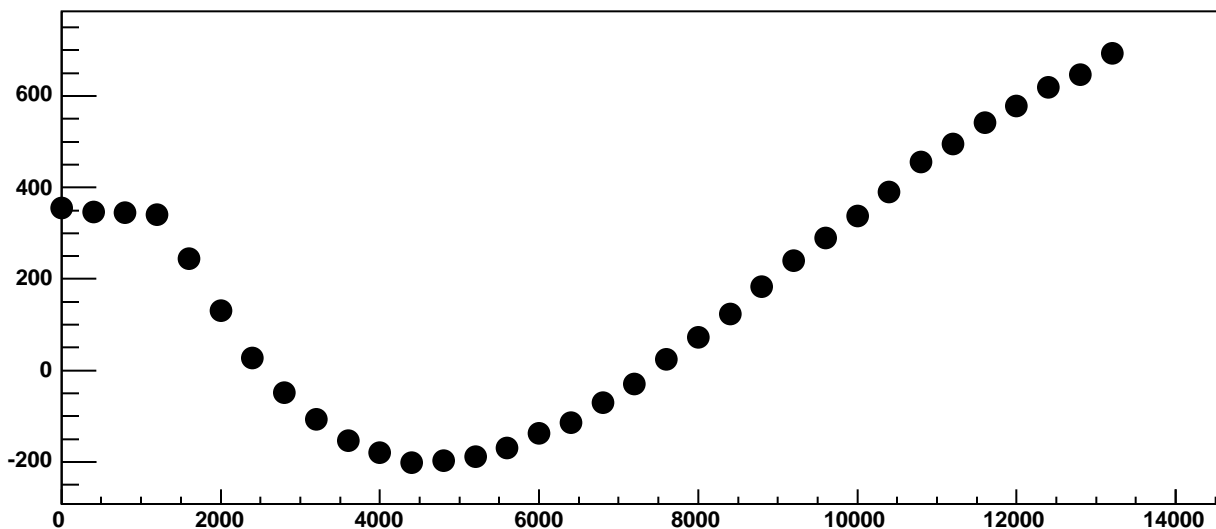
Chip 0, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



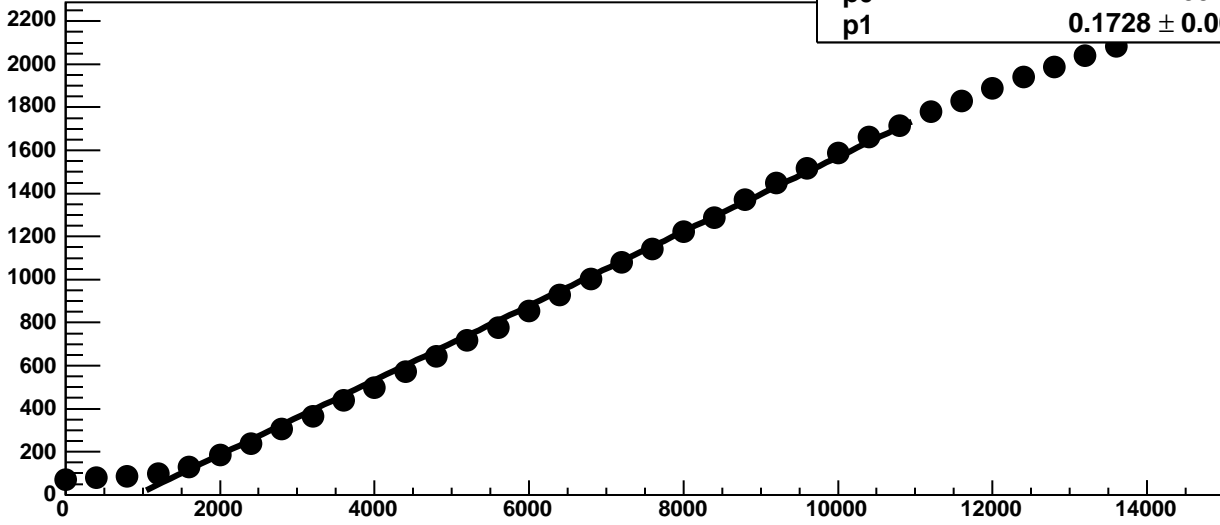
Chip 0, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1485 / 23

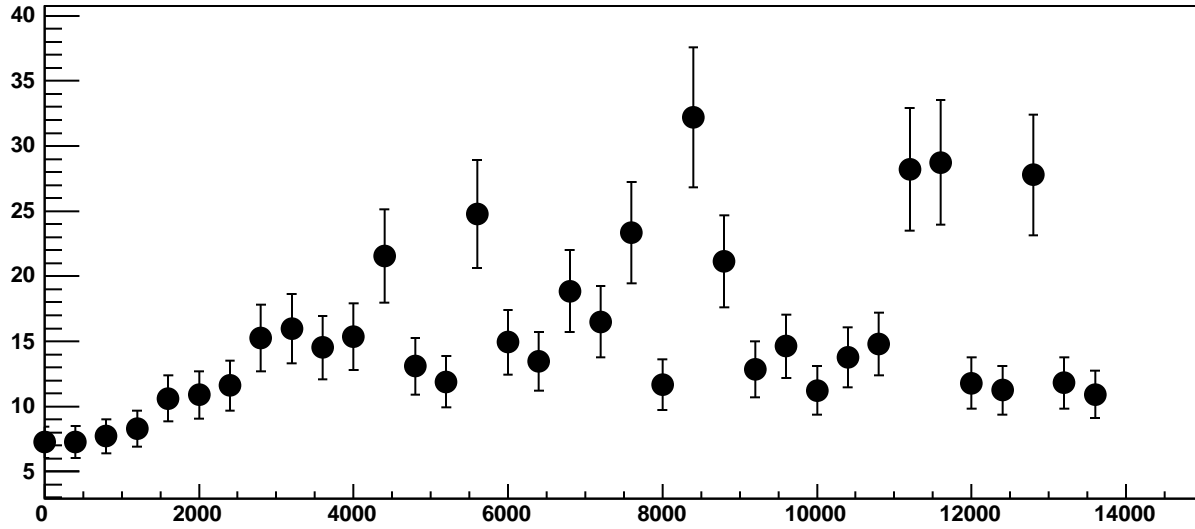
p0

-159 ± 1.235

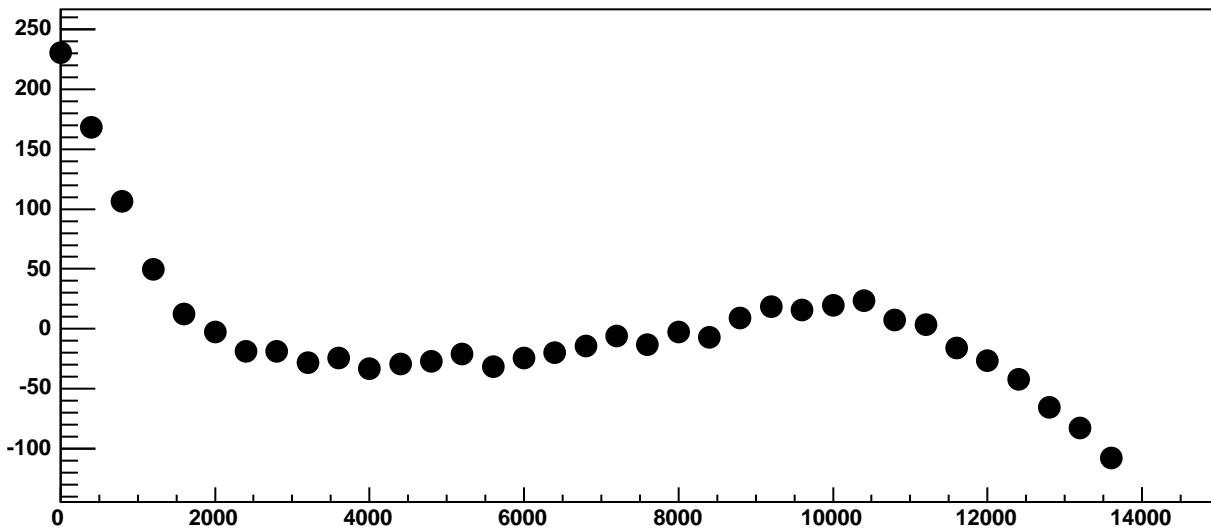
p1

0.1728 ± 0.0001972

Chip 0, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

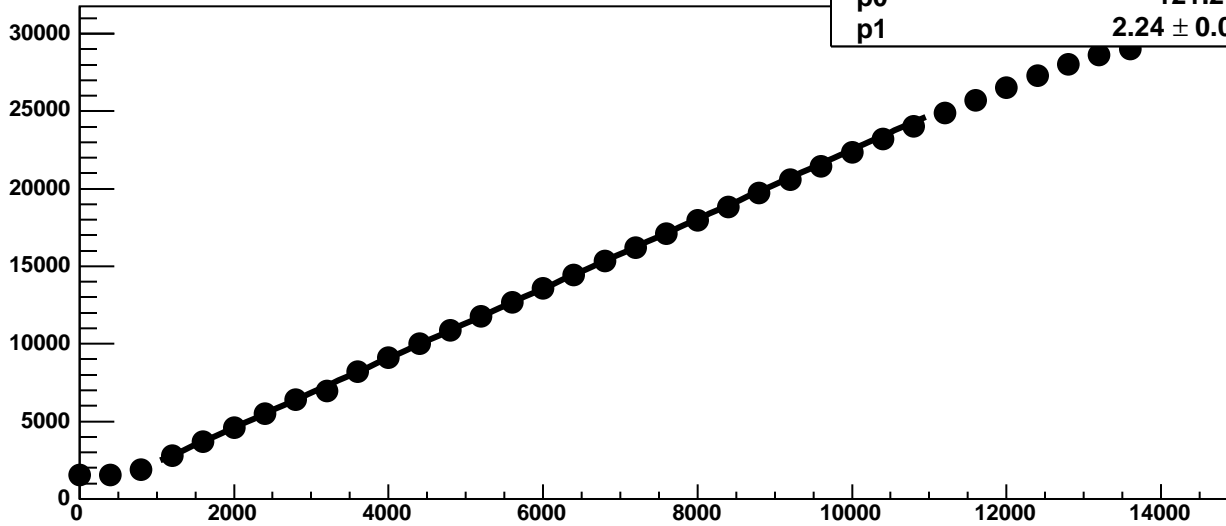


Chip 0, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

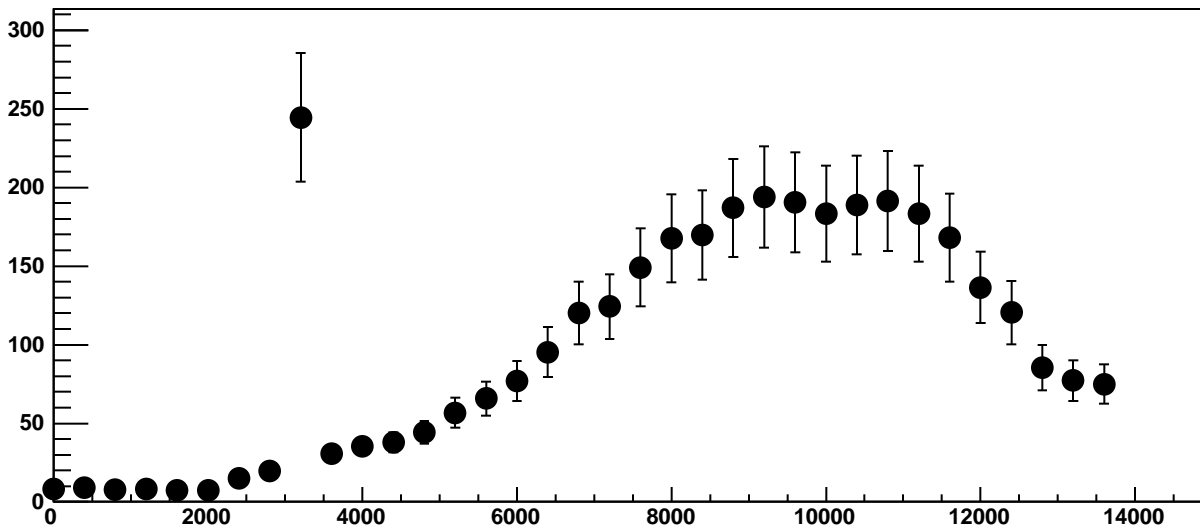


Chip 0, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC

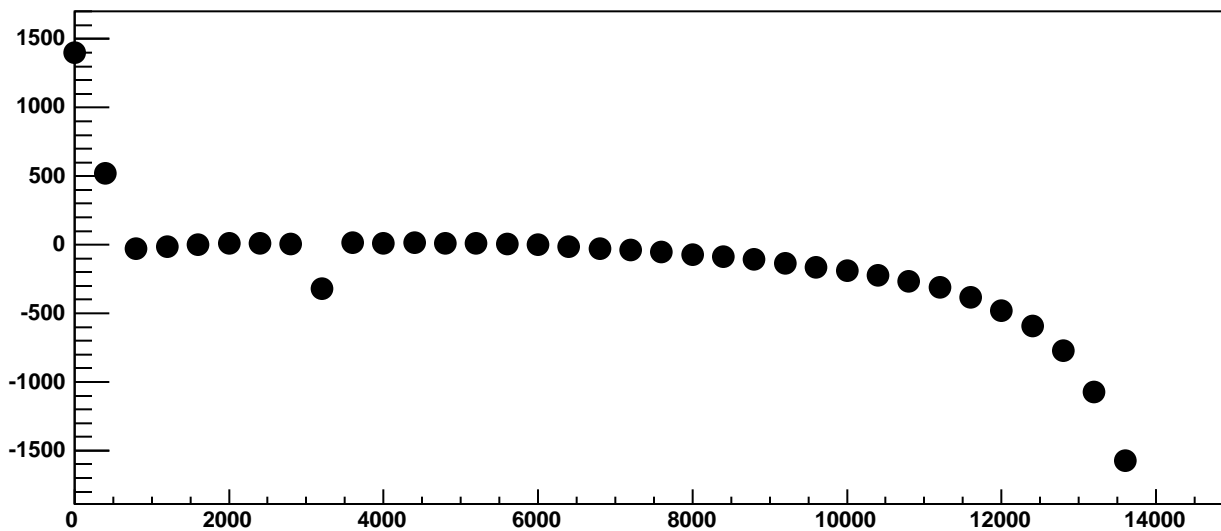
χ^2 / ndf 299.7 / 23
p0 121.2 ± 2.077
p1 2.24 ± 0.0009568



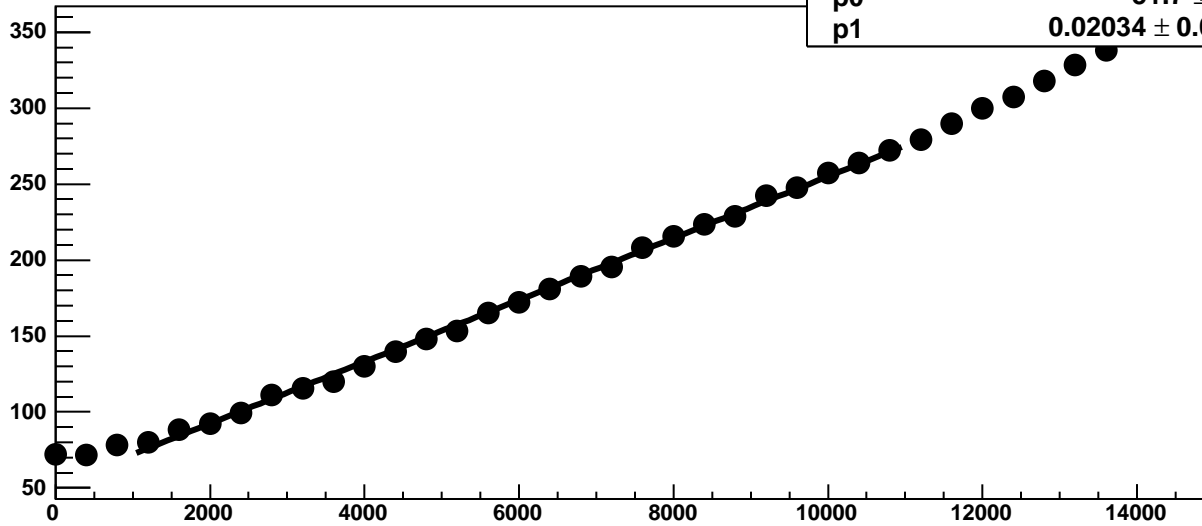
Chip 0, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

47.56 / 23

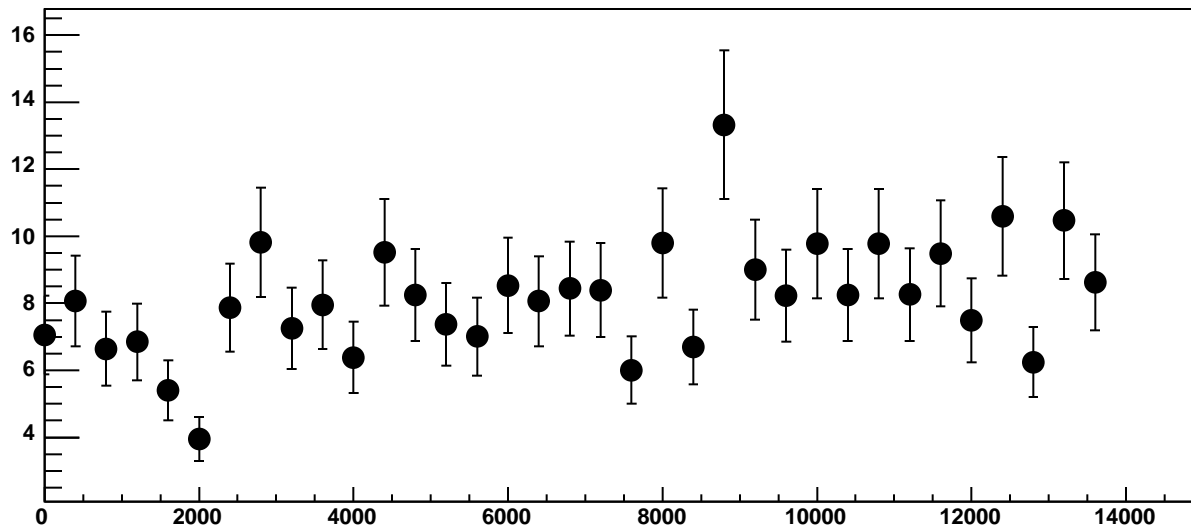
p0

51.7 ± 0.6892

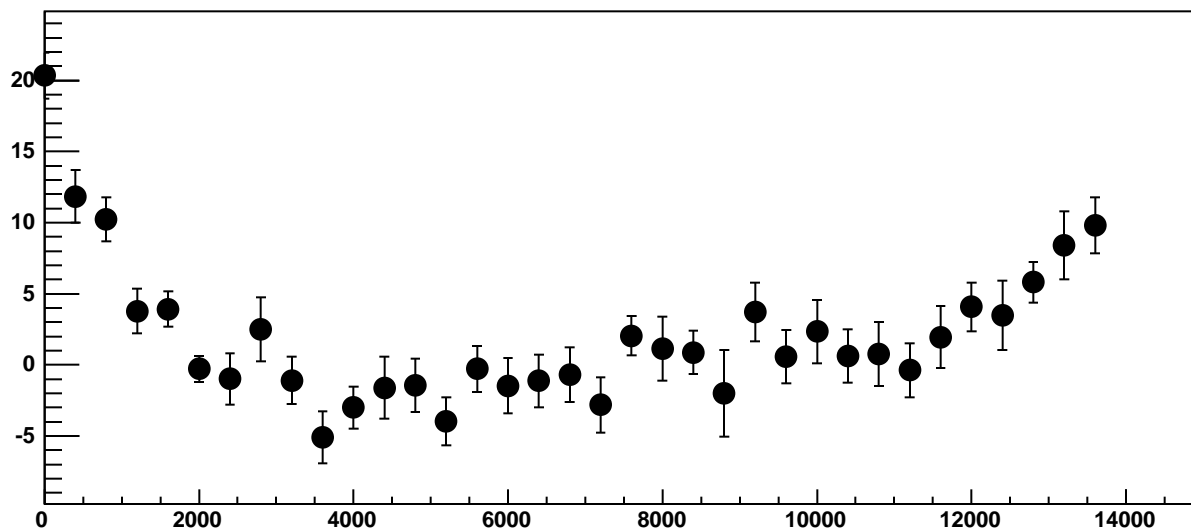
p1

0.02034 ± 0.0001161

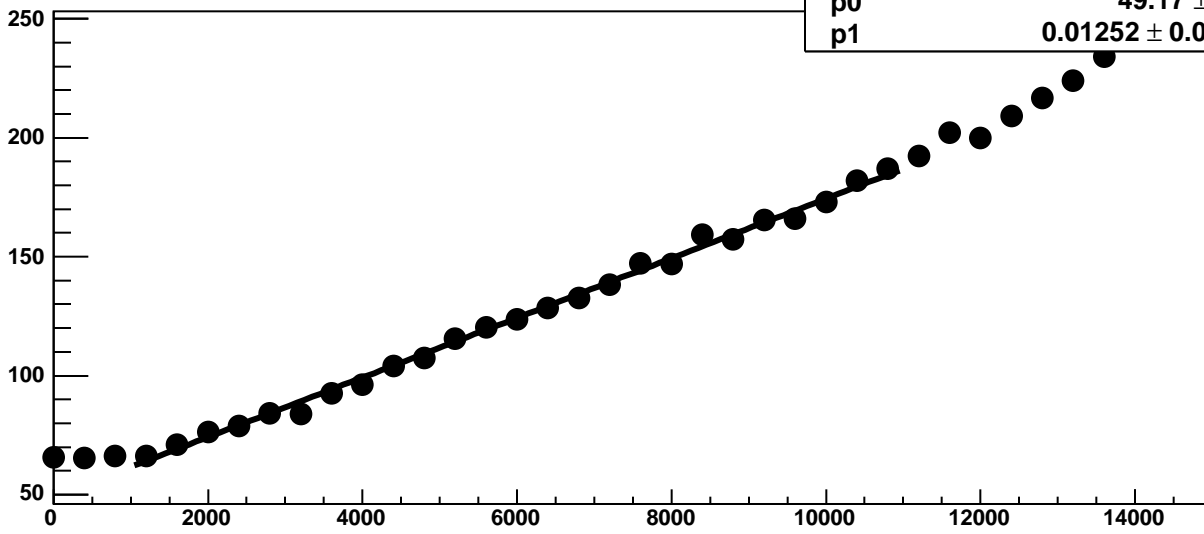
Chip 0, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

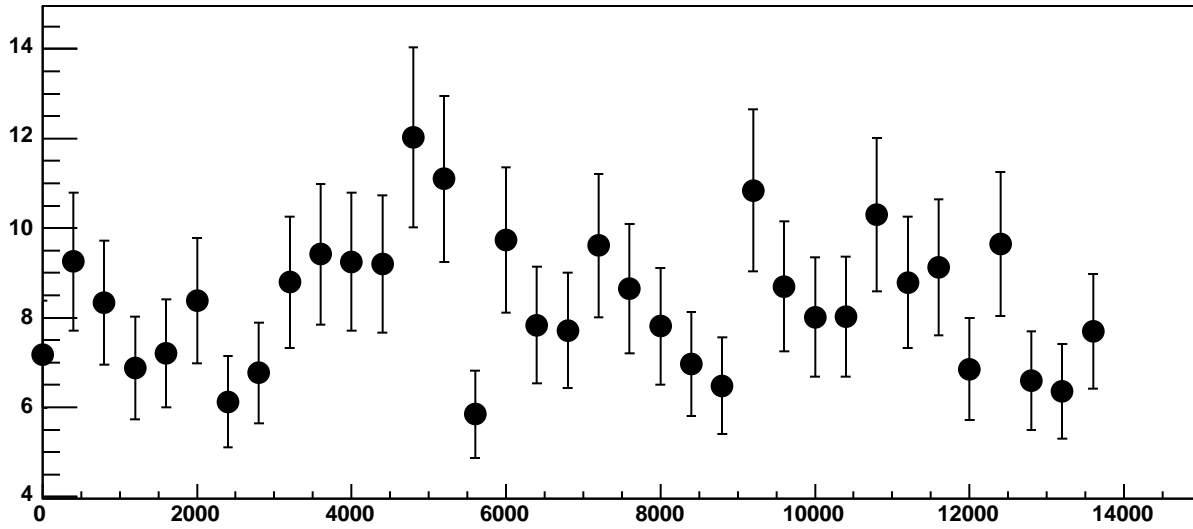


Chip 0, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

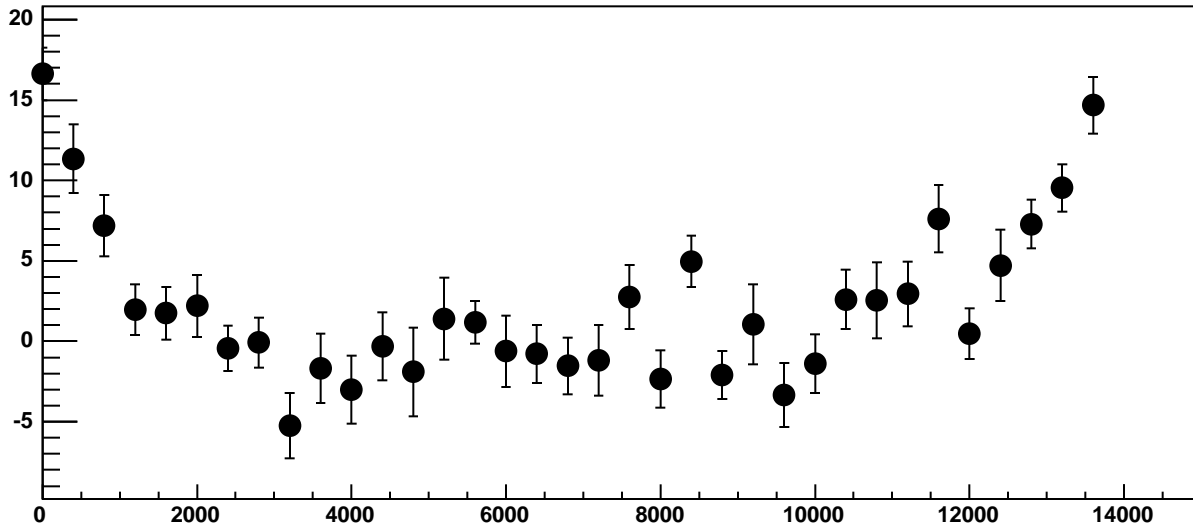


χ^2 / ndf 38.25 / 23
p0 49.17 ± 0.8117
p1 0.01252 ± 0.0001252

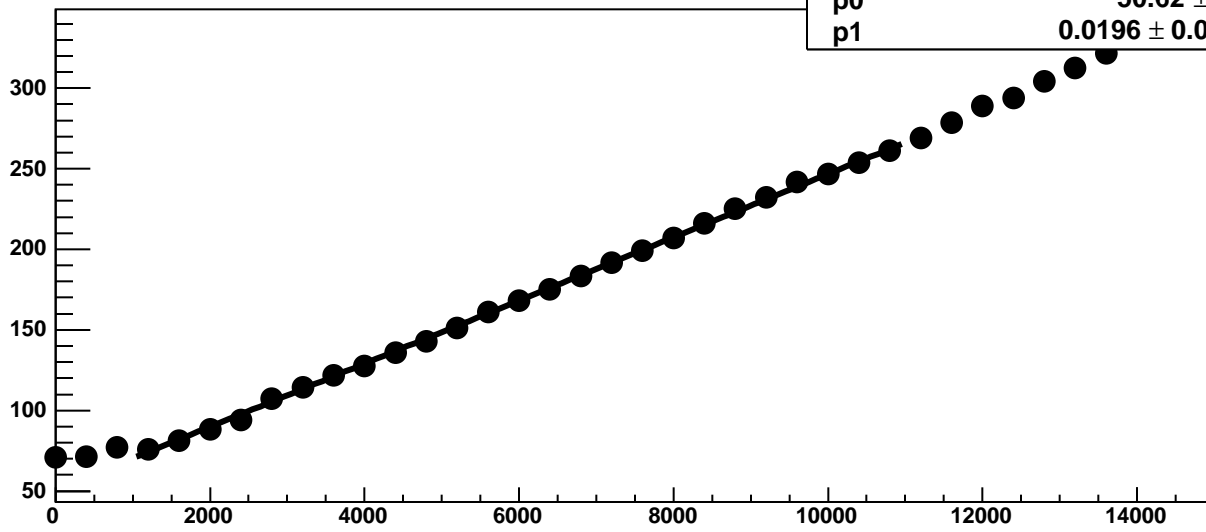
Chip 0, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

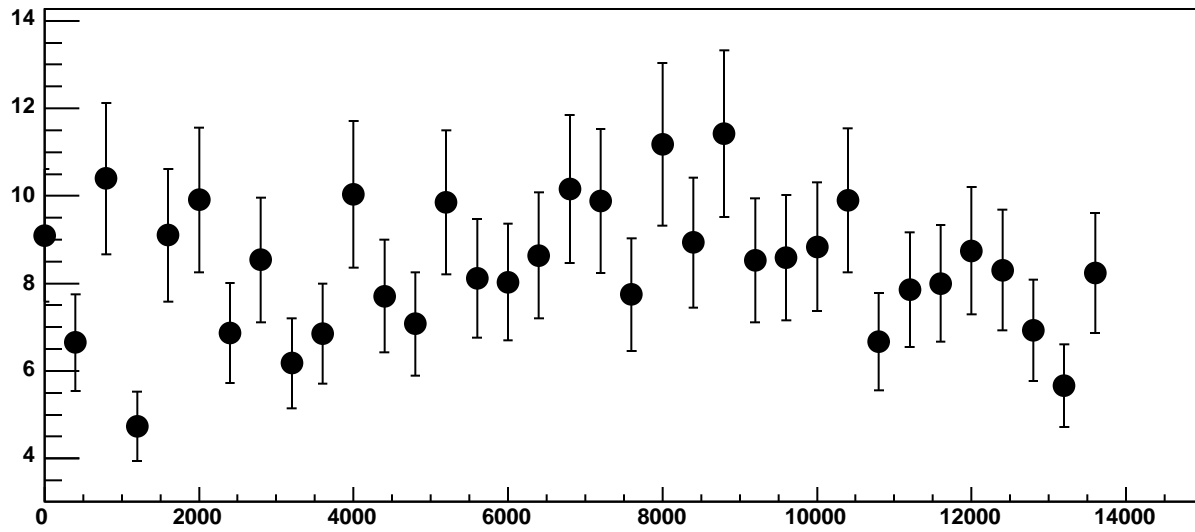


Chip 0, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC

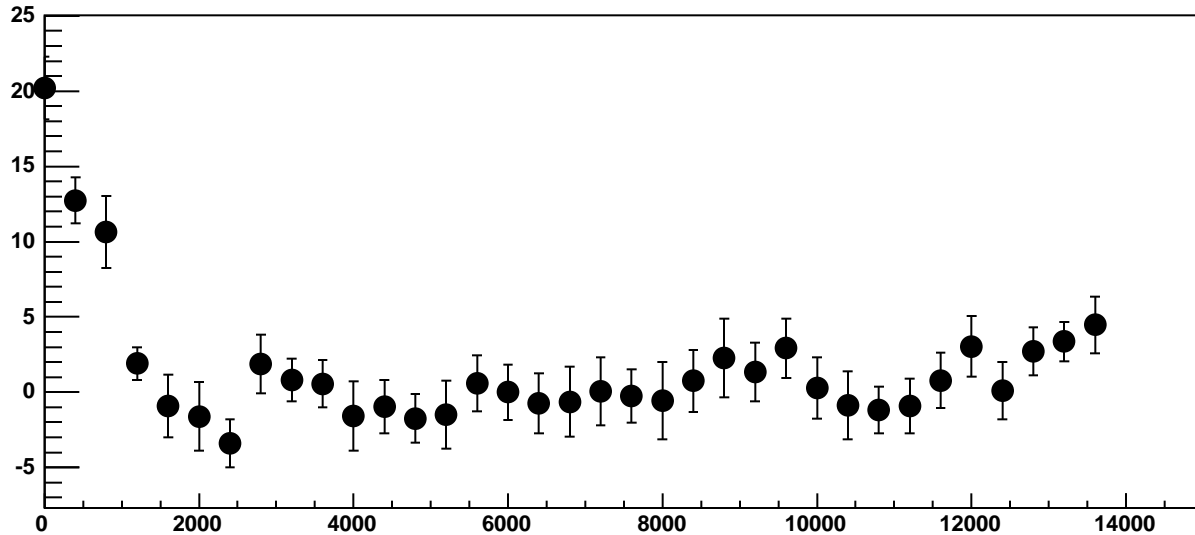


χ^2 / ndf 16.87 / 23
p0 50.62 ± 0.7503
p1 0.0196 ± 0.0001204

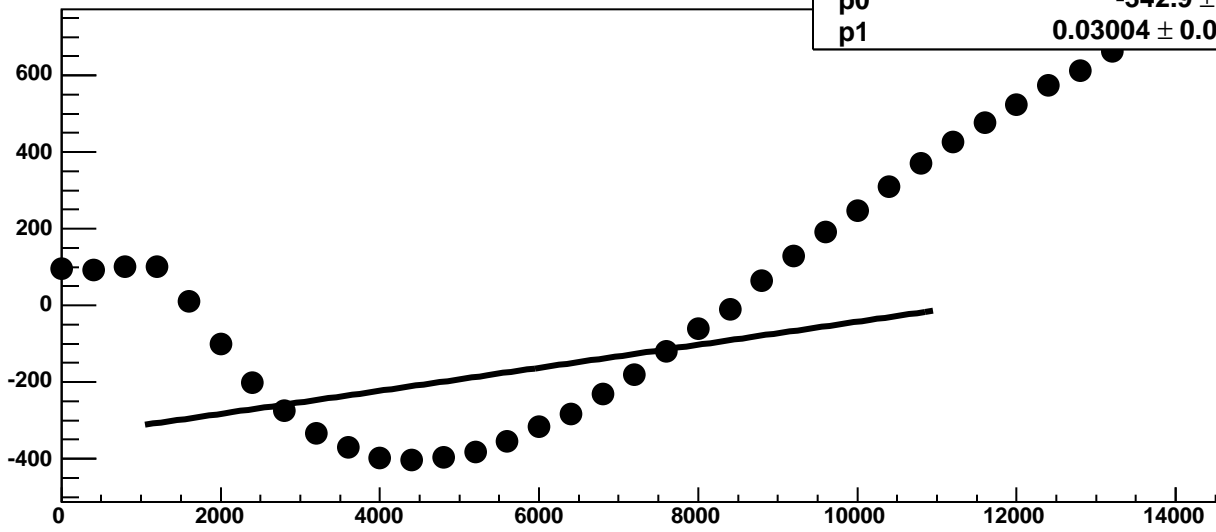
Chip 0, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



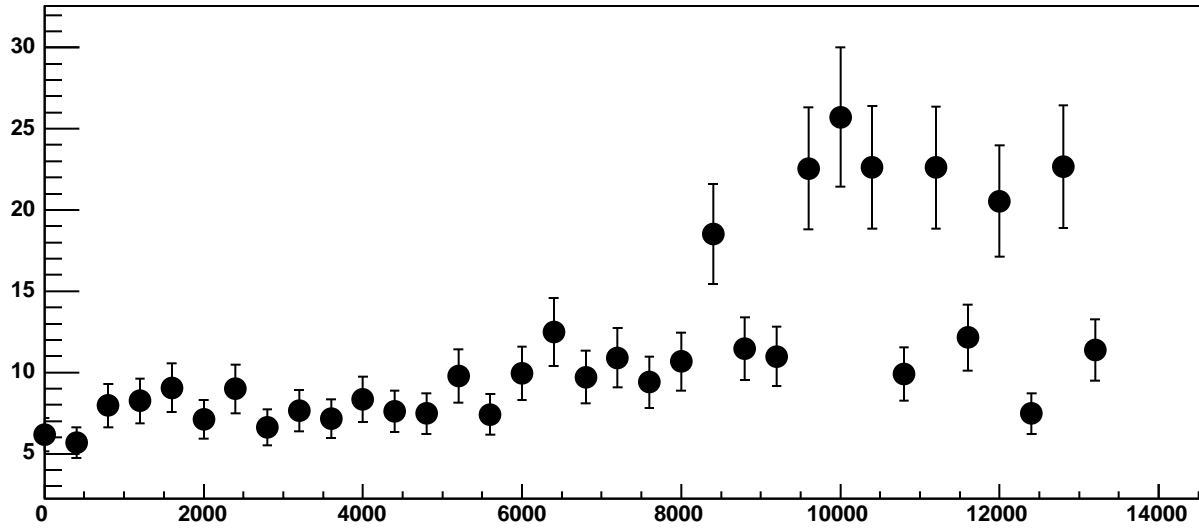
Chip 0, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC



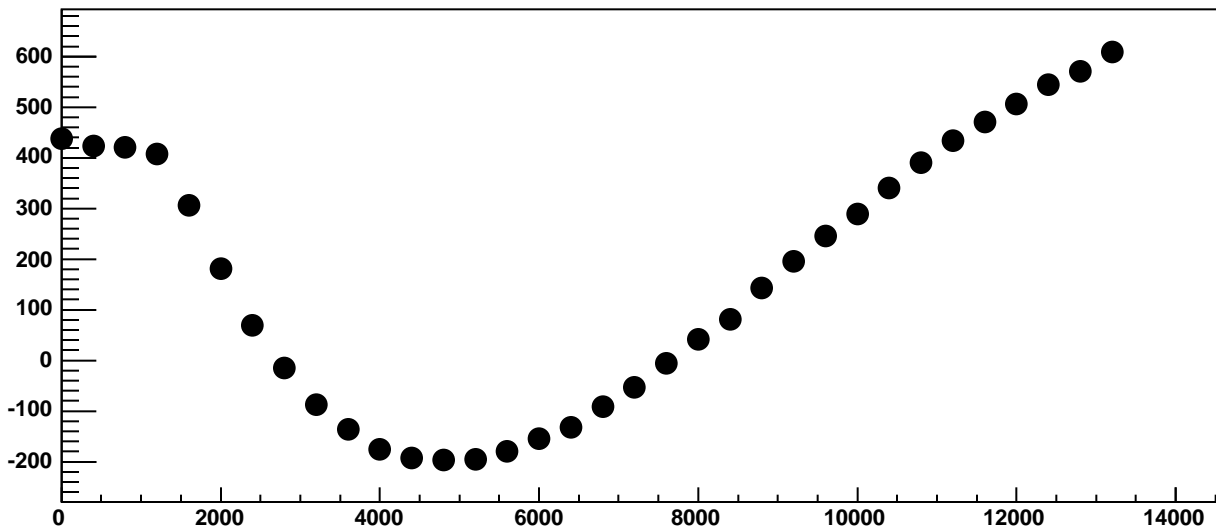
Chip 0, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC



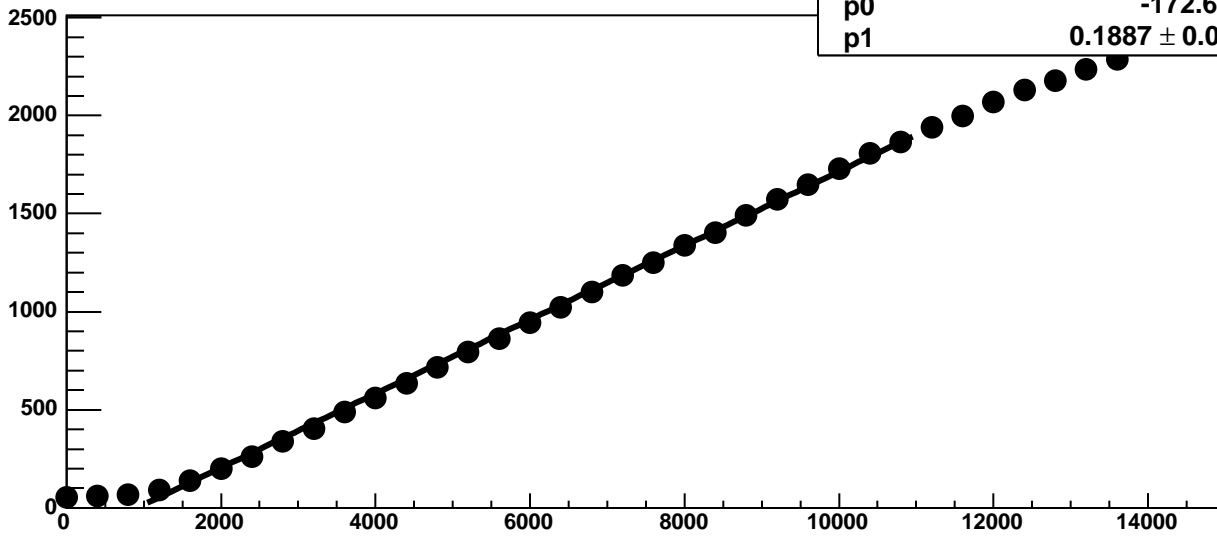
Chip 0, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

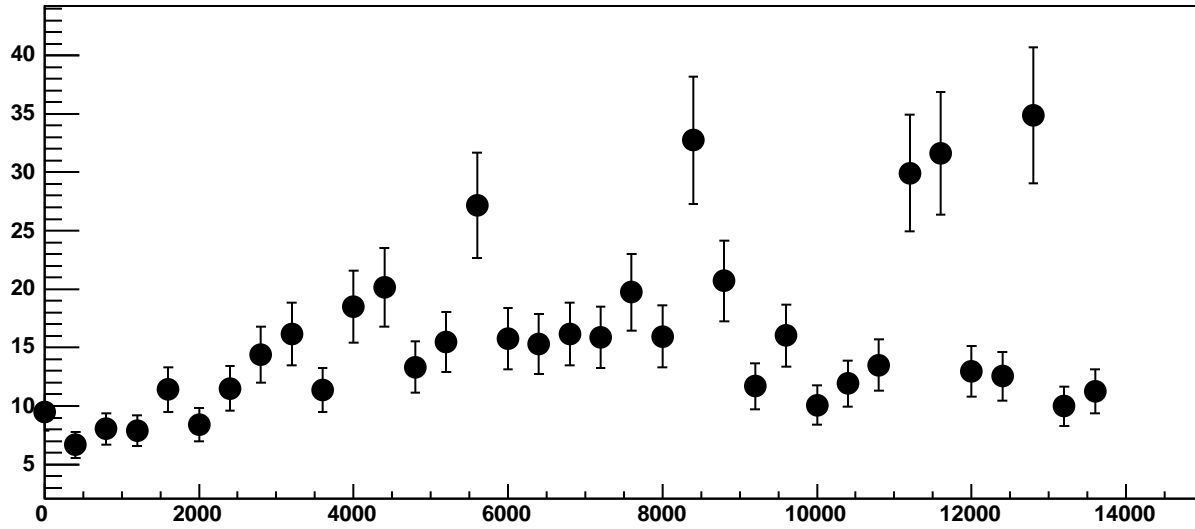


Chip 0, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

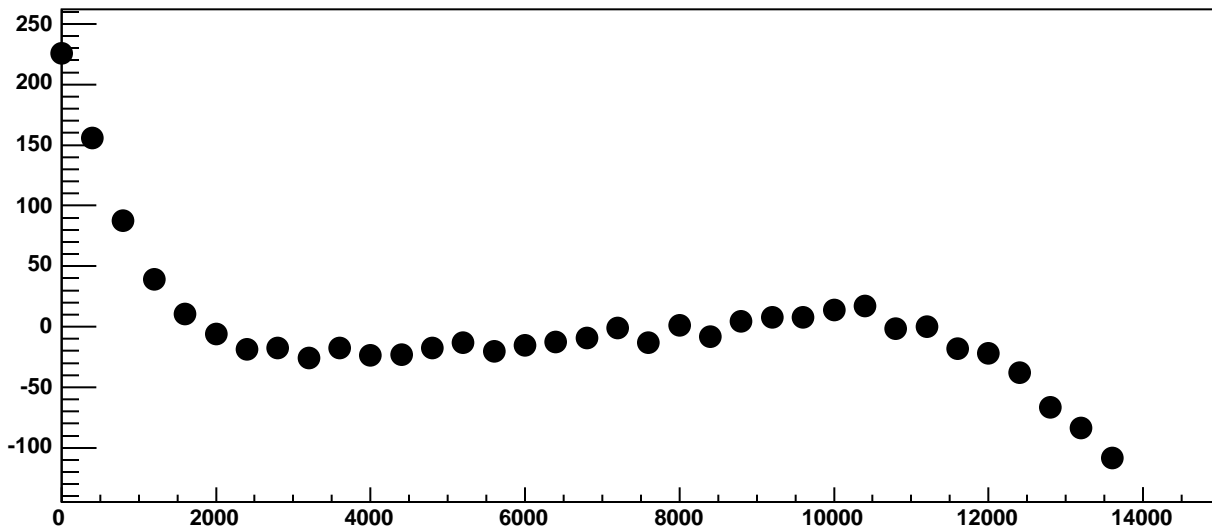


χ^2 / ndf 906.3 / 23
p0 -172.6 ± 1.161
p1 0.1887 ± 0.0001849

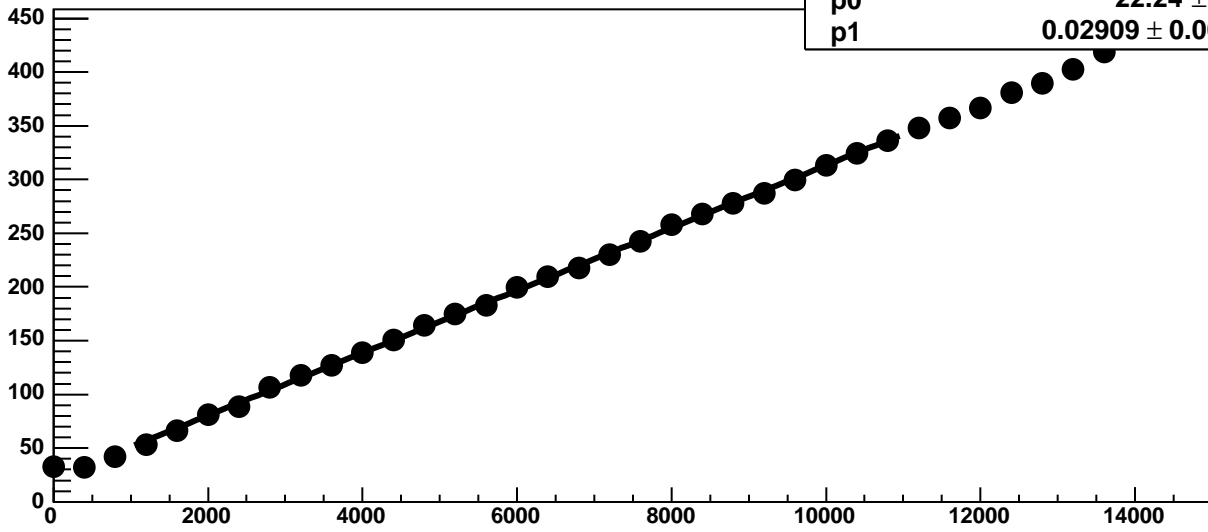
Chip 0, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

33.1 / 23

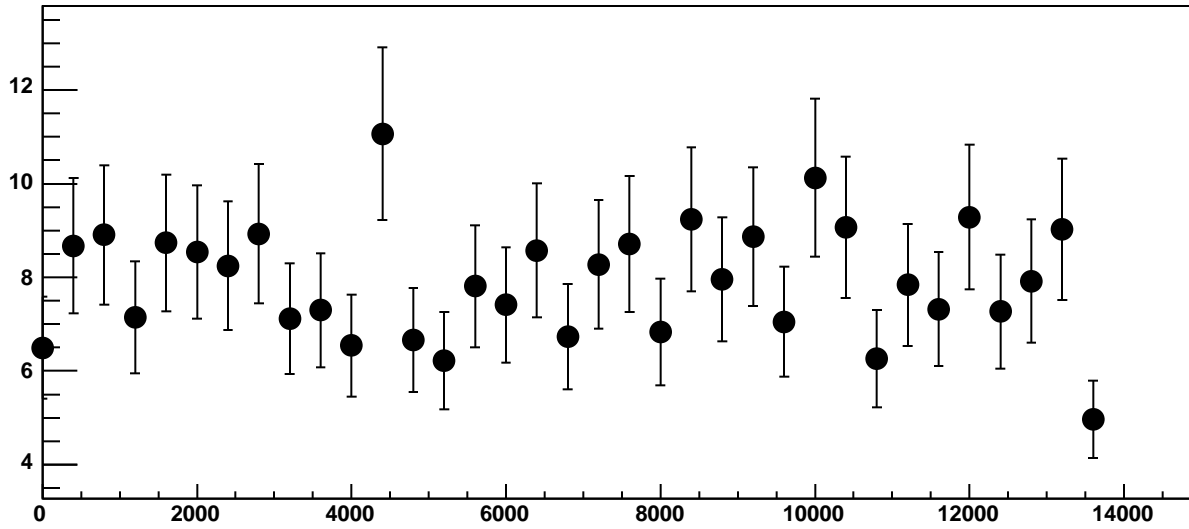
p0

22.24 ± 0.8249

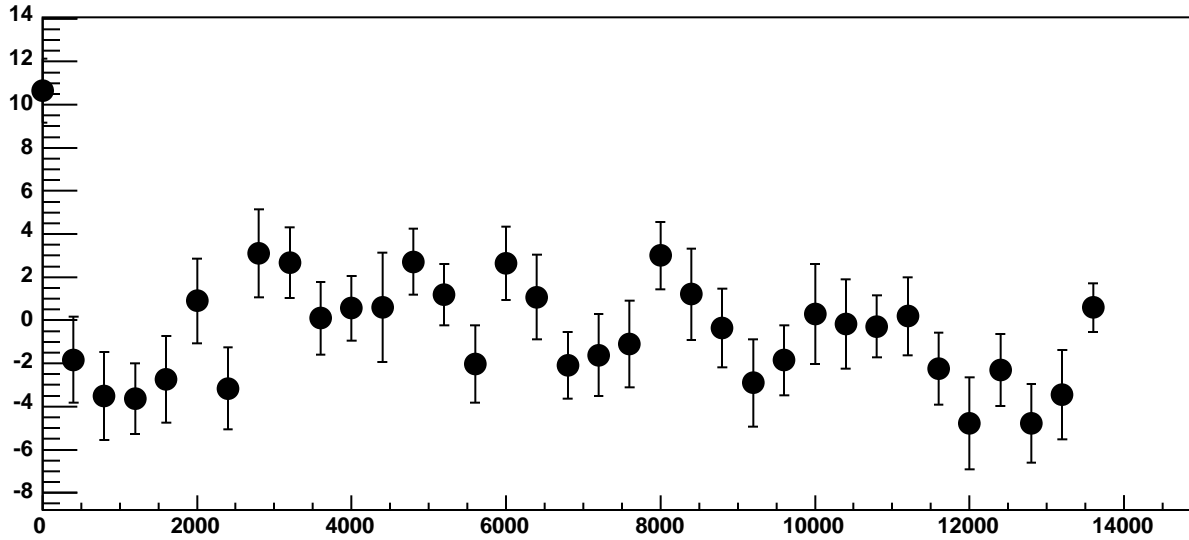
p1

0.02909 ± 0.0001246

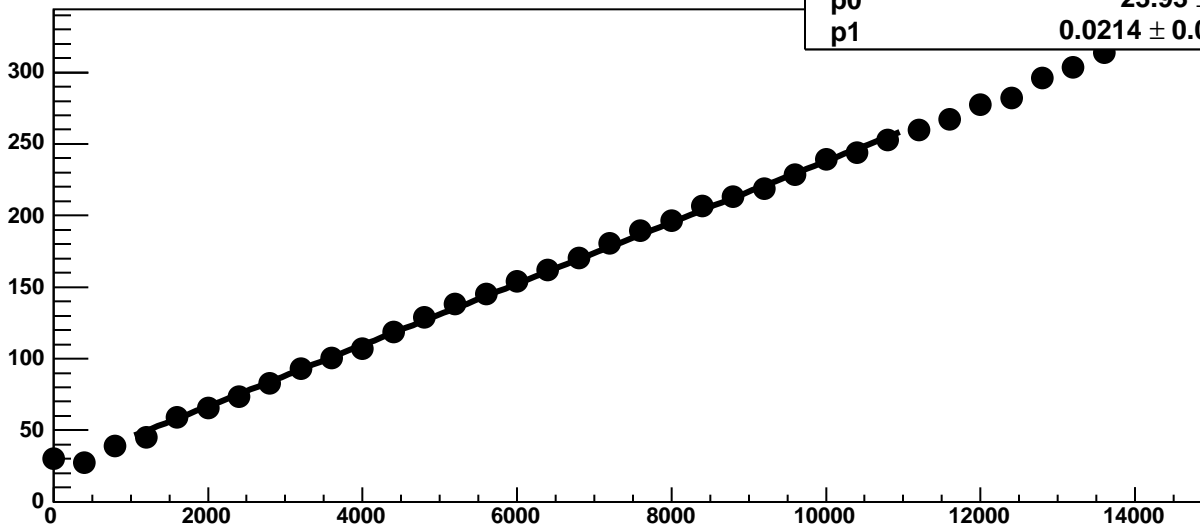
Chip 0, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC

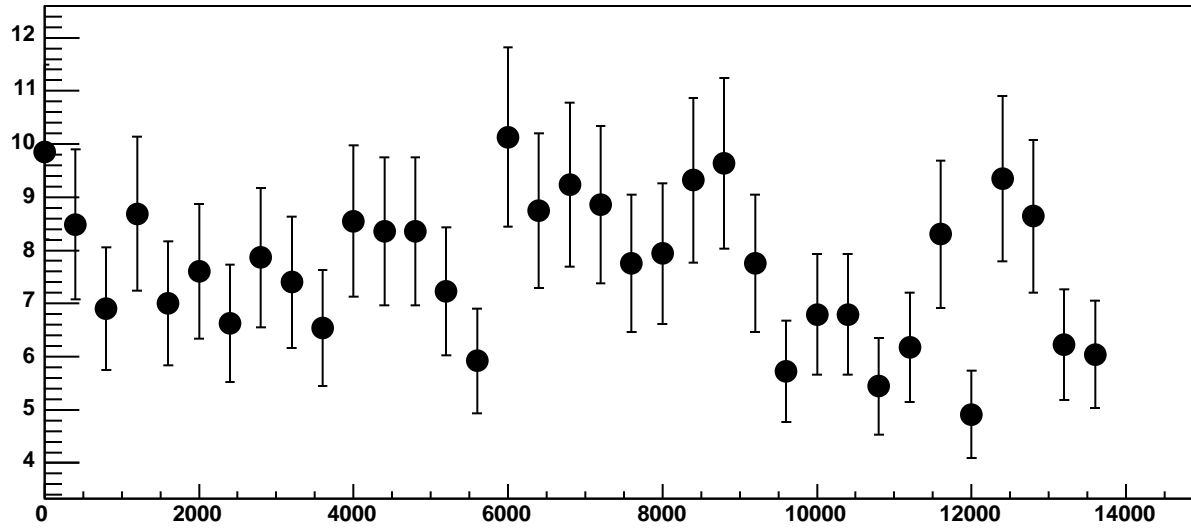


Chip 0, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

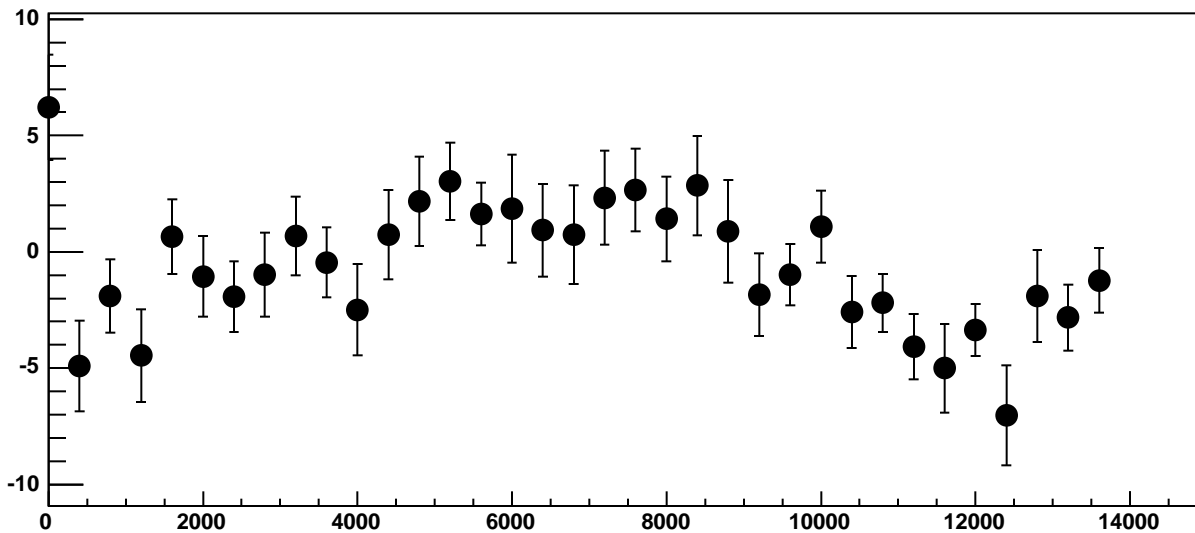


χ^2 / ndf 30.56 / 23
p0 23.93 ± 0.7741
p1 0.0214 ± 0.0001117

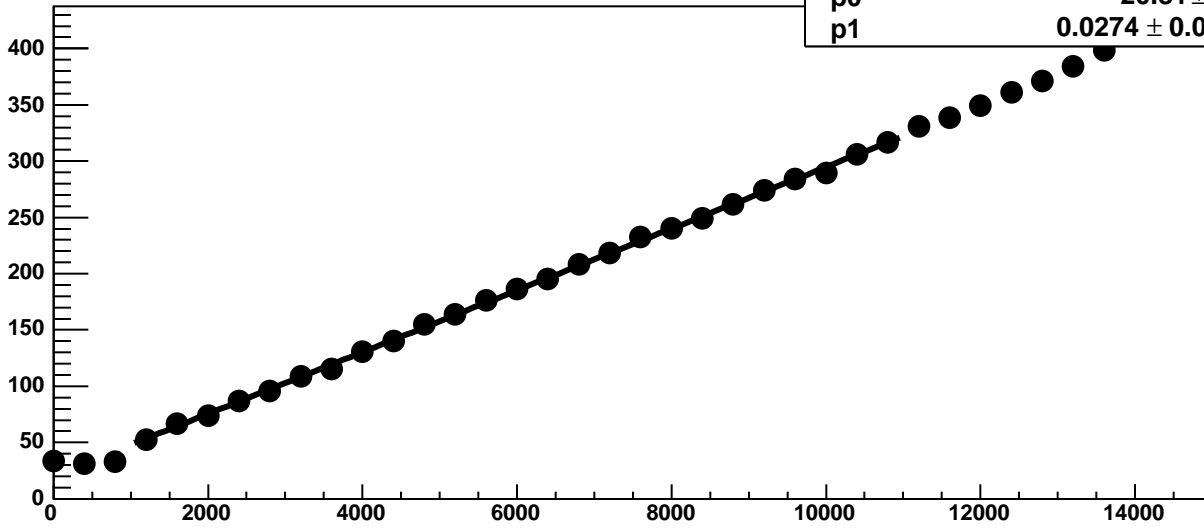
Chip 0, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

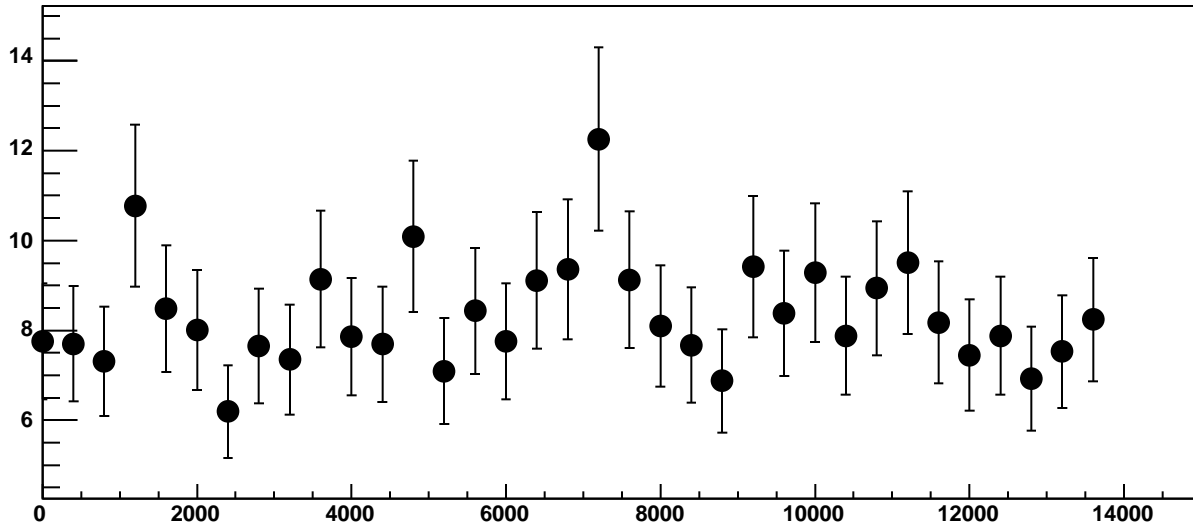


Chip 0, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

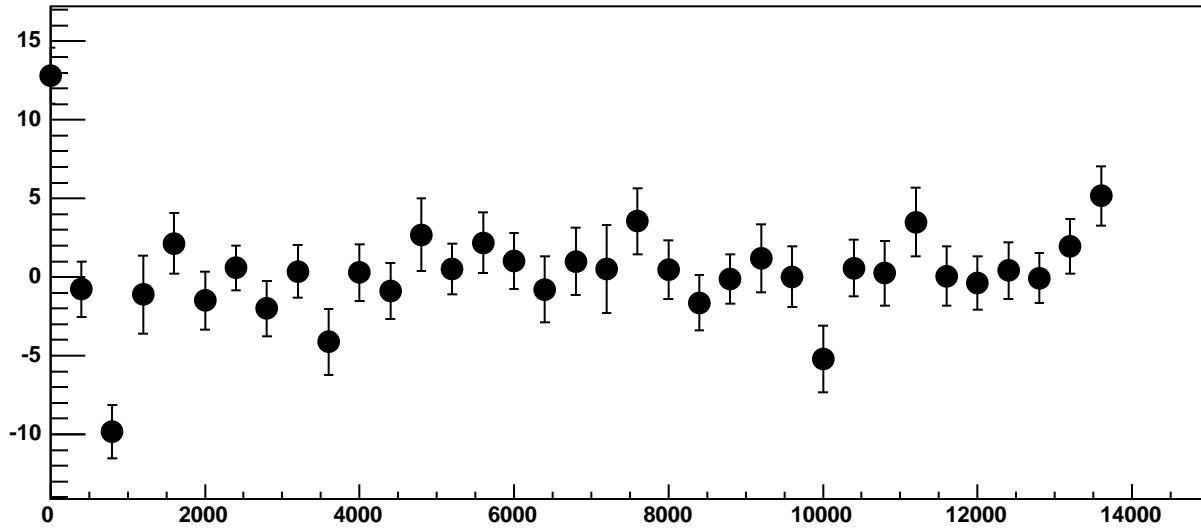


χ^2 / ndf 21.36 / 23
p0 20.81 ± 0.8603
p1 0.0274 ± 0.0001317

Chip 0, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

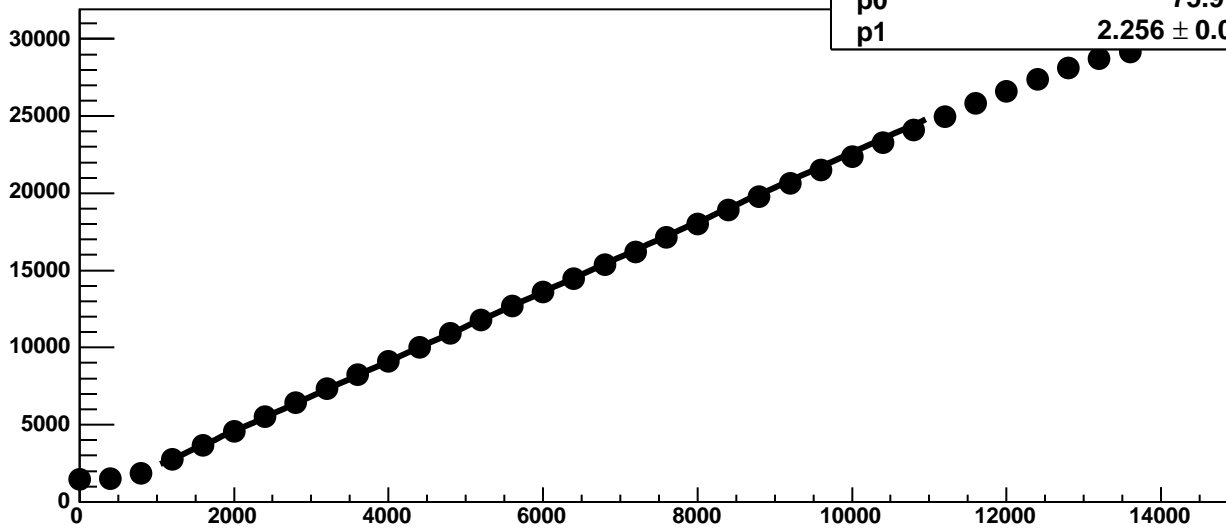


Chip 0, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

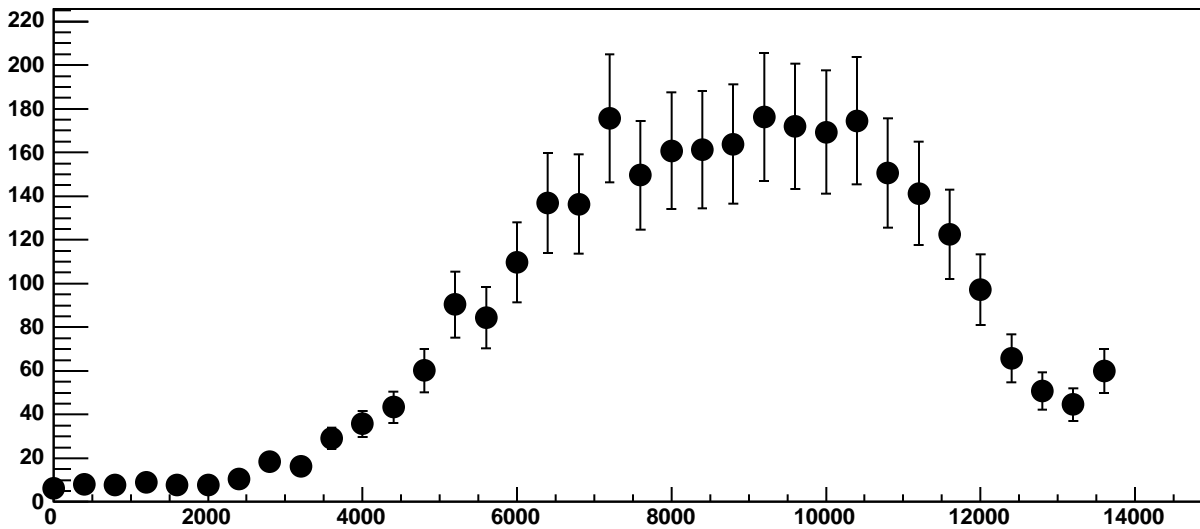


Chip 0, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

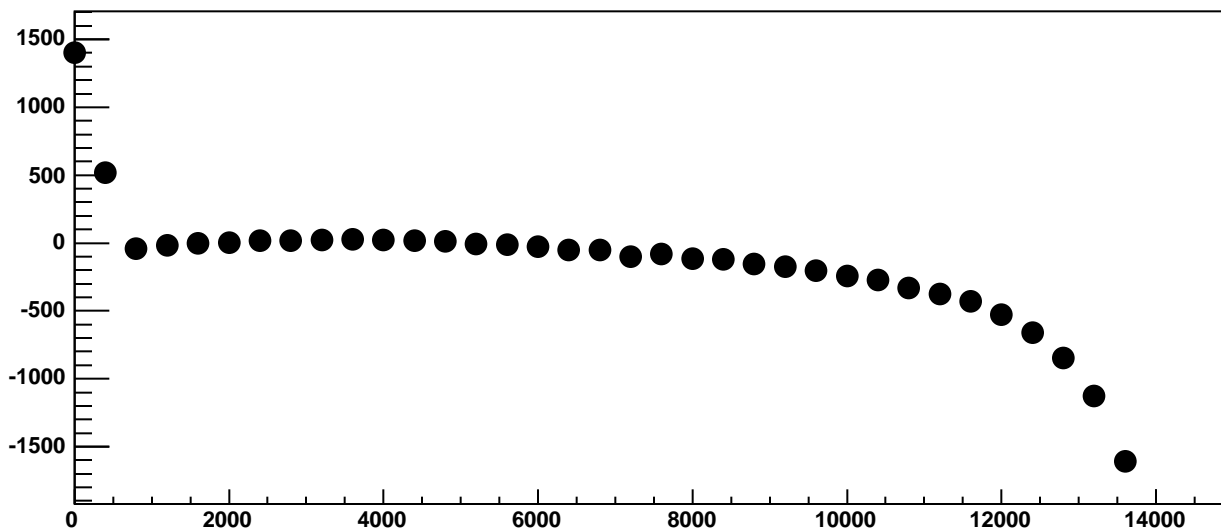
χ^2 / ndf 494.6 / 23
p0 75.9 ± 2.194
p1 2.256 ± 0.0009726



Chip 0, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC

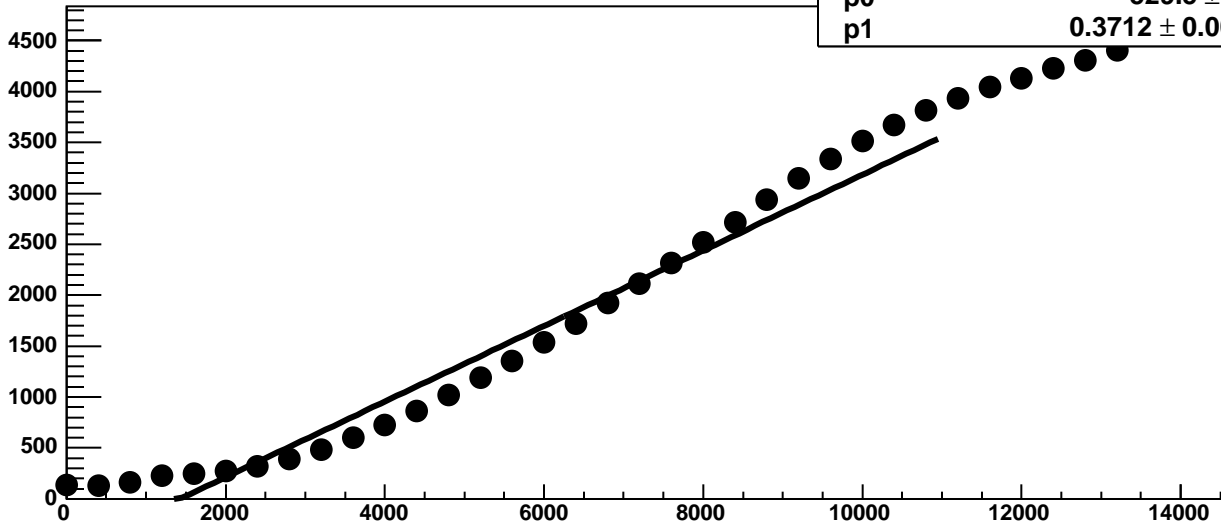


Chip 0, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC

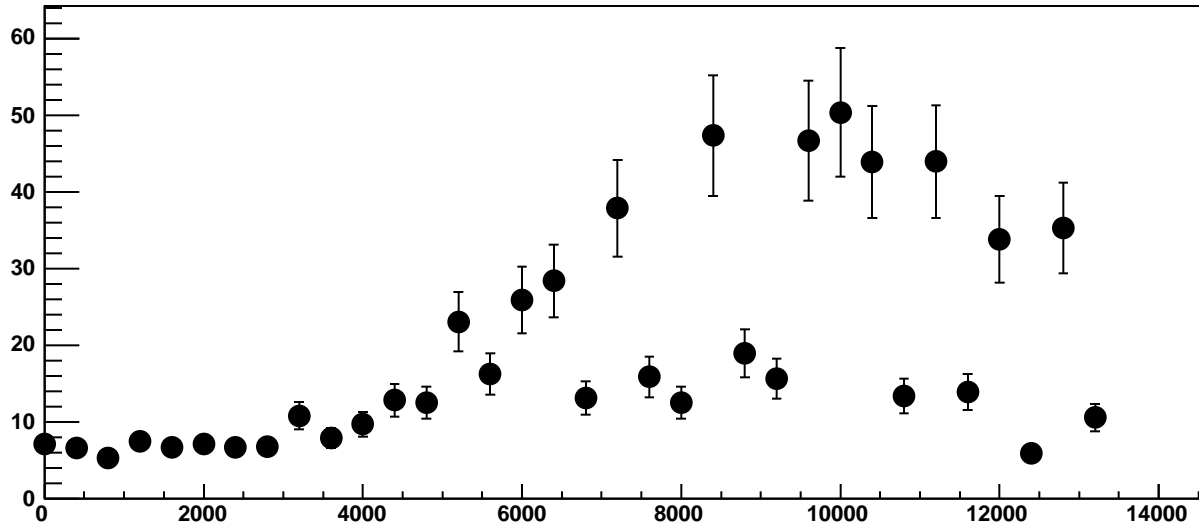


Chip 0, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

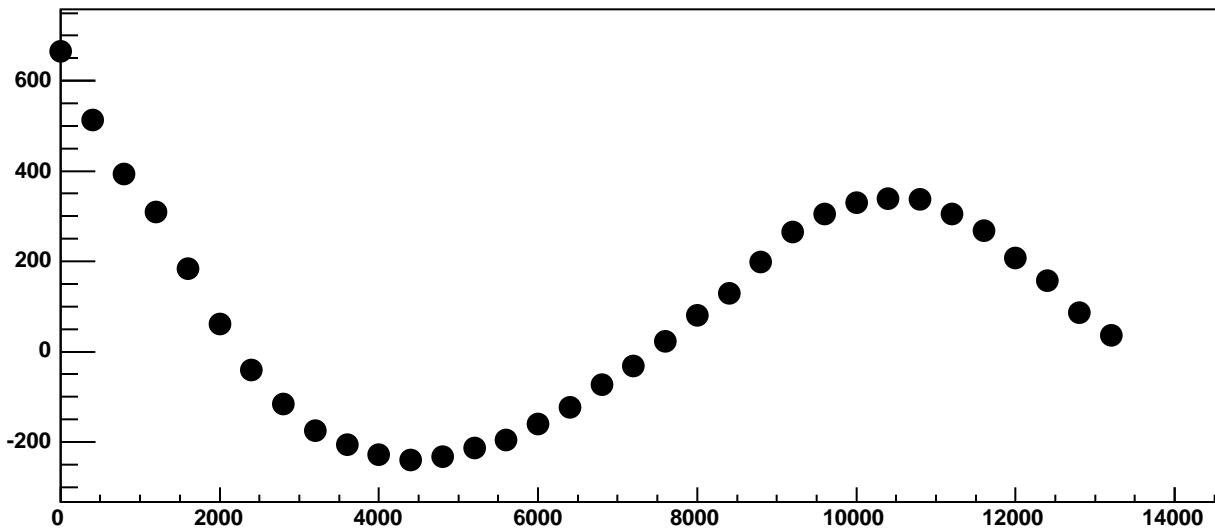
χ^2 / ndf 1.251e+05 / 23
p0 -529.5 ± 0.9518
p1 0.3712 ± 0.0002136



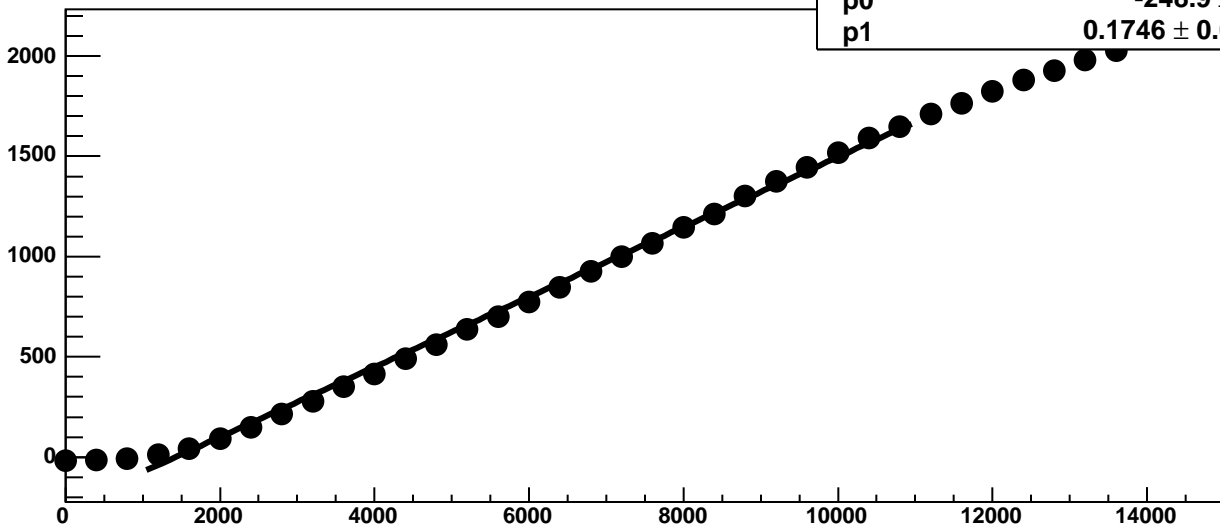
Chip 0, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

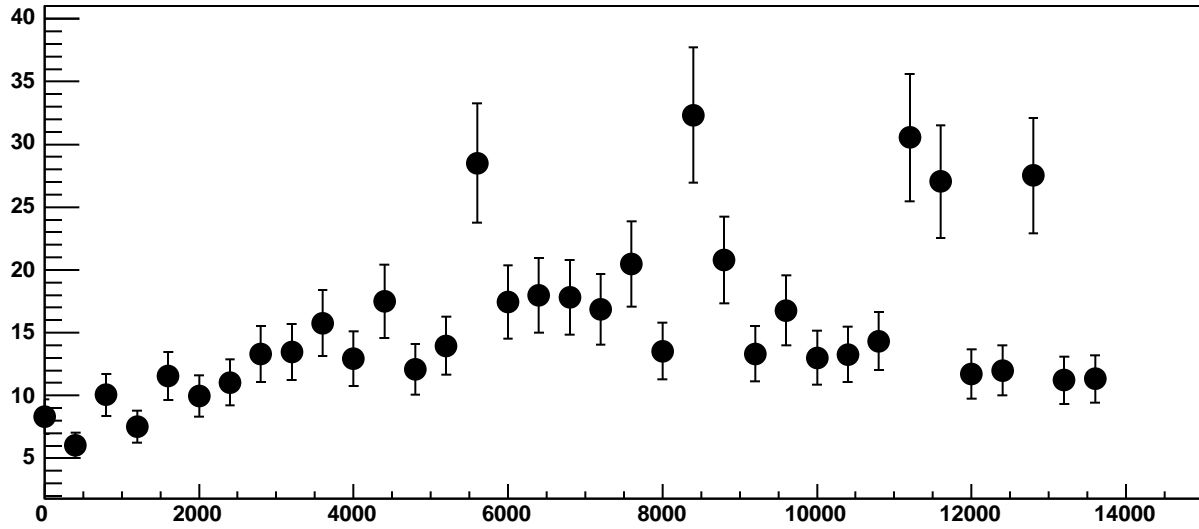


Chip 0, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

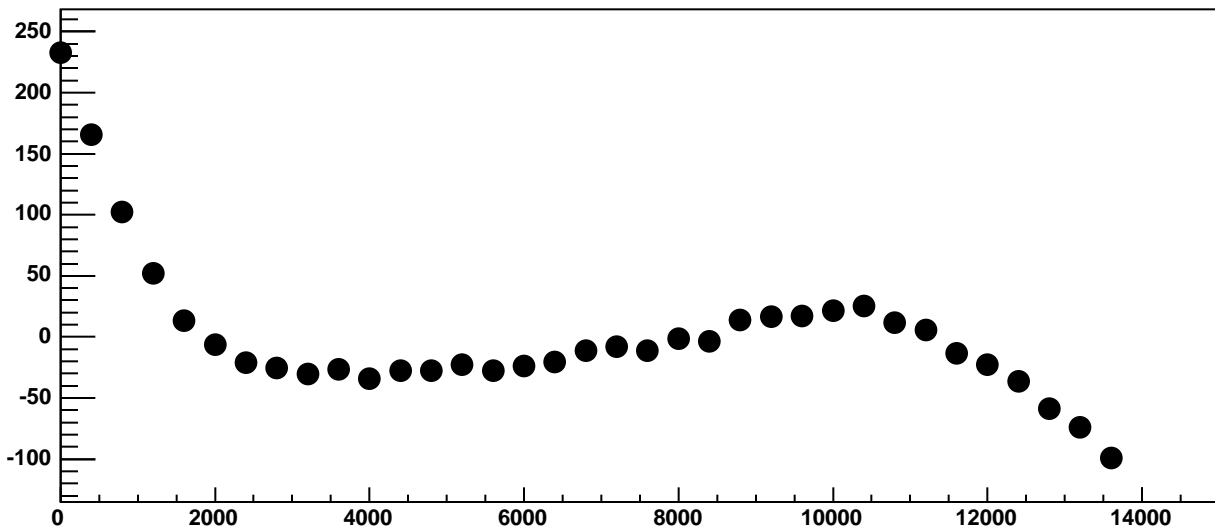


χ^2 / ndf 1848 / 23
p0 -248.9 ± 1.178
p1 0.1746 ± 0.000197

Chip 0, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

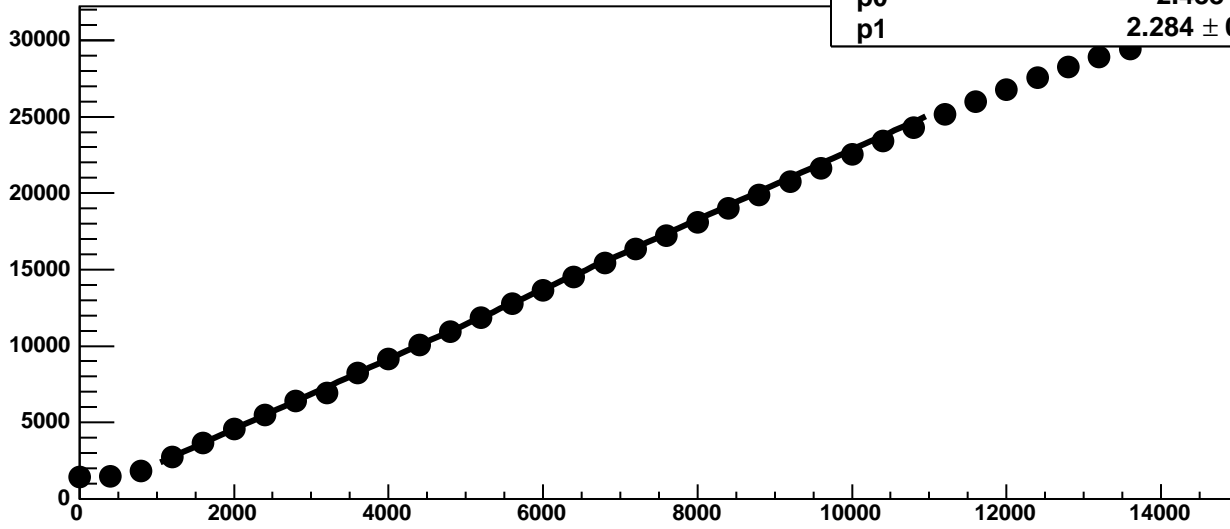


Chip 0, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

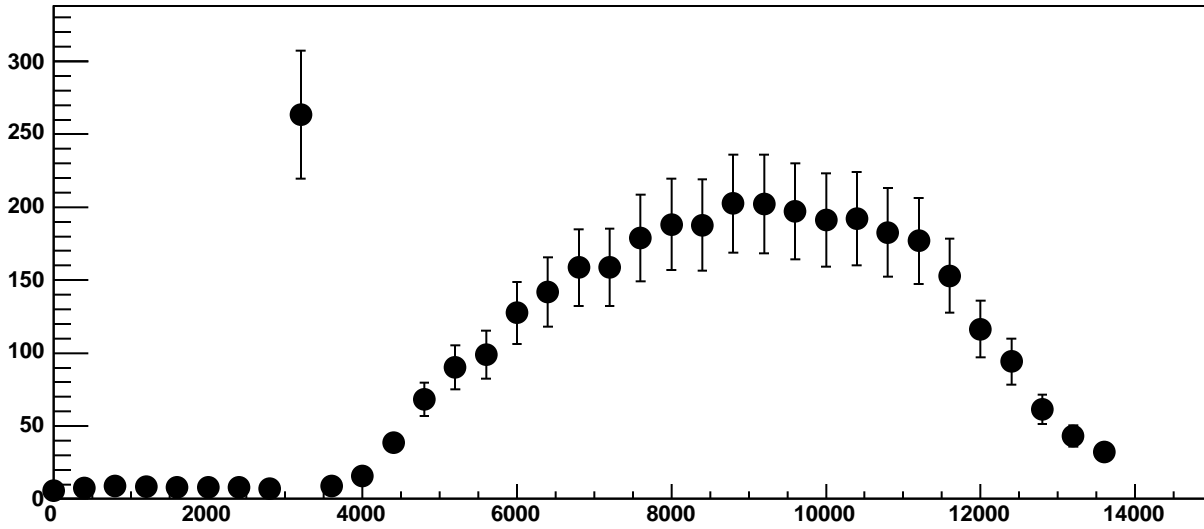


Chip 0, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC

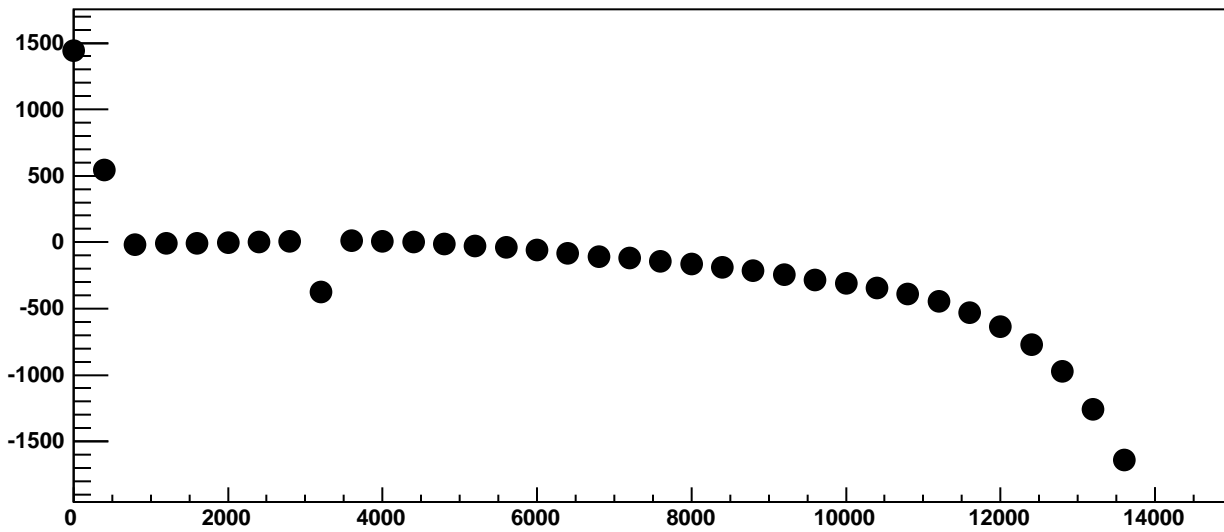
χ^2 / ndf 509.9 / 23
p0 2.488 ± 2.028
p1 2.284 ± 0.00079



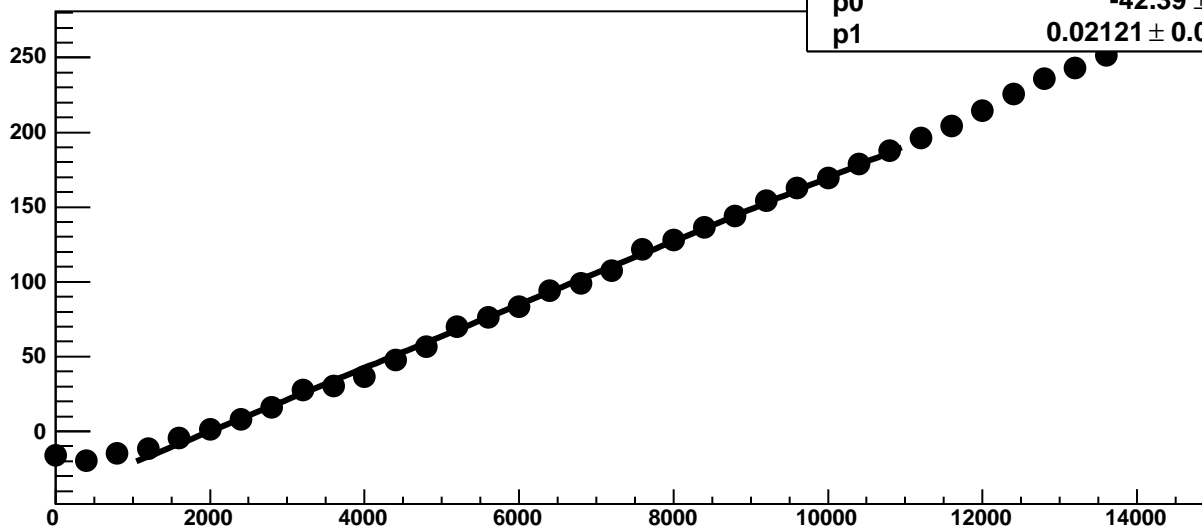
Chip 0, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



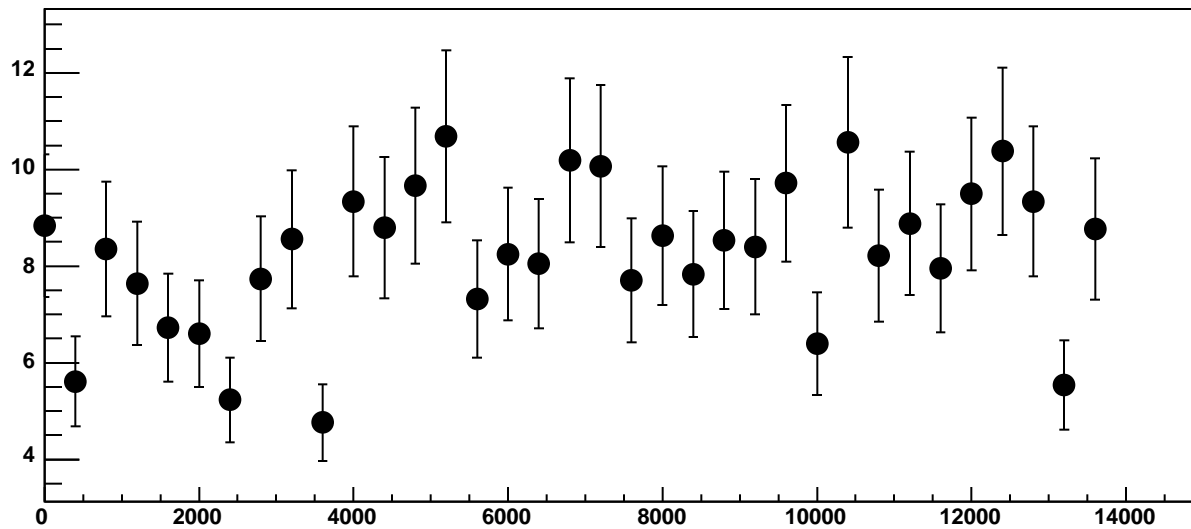
Chip 0, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC



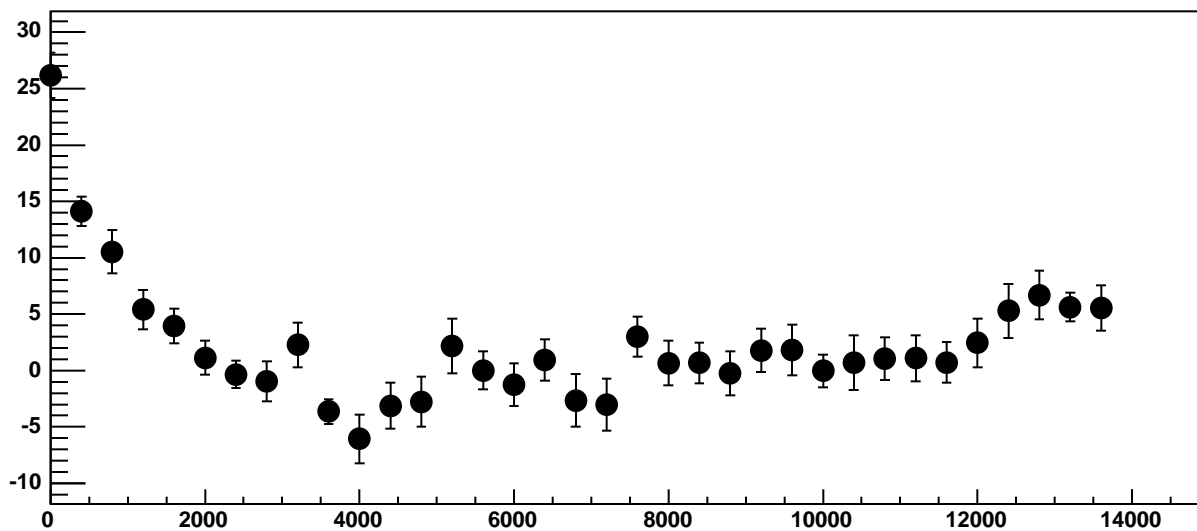
Chip 0, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



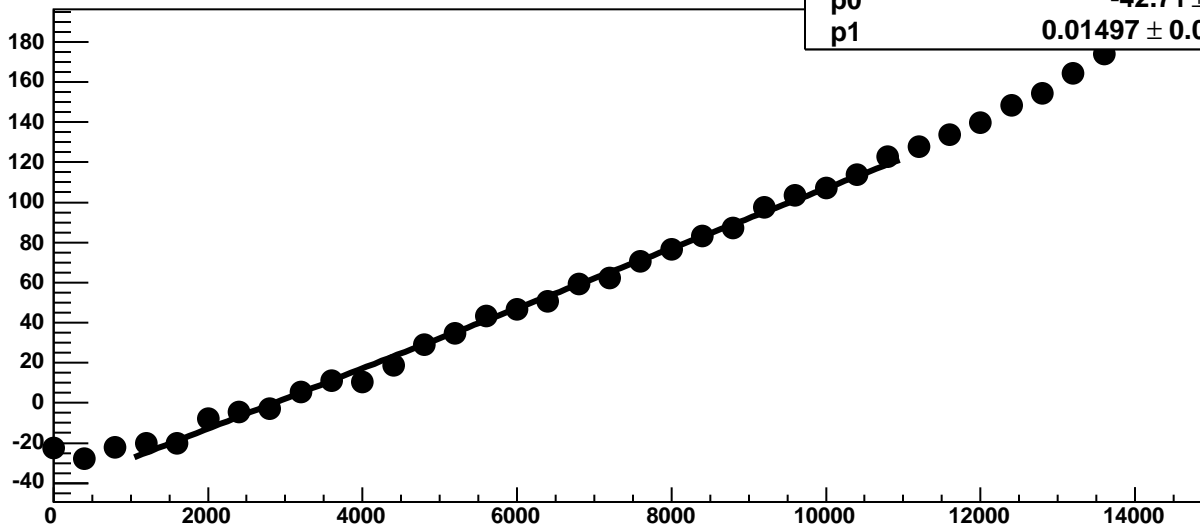
Chip 0, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

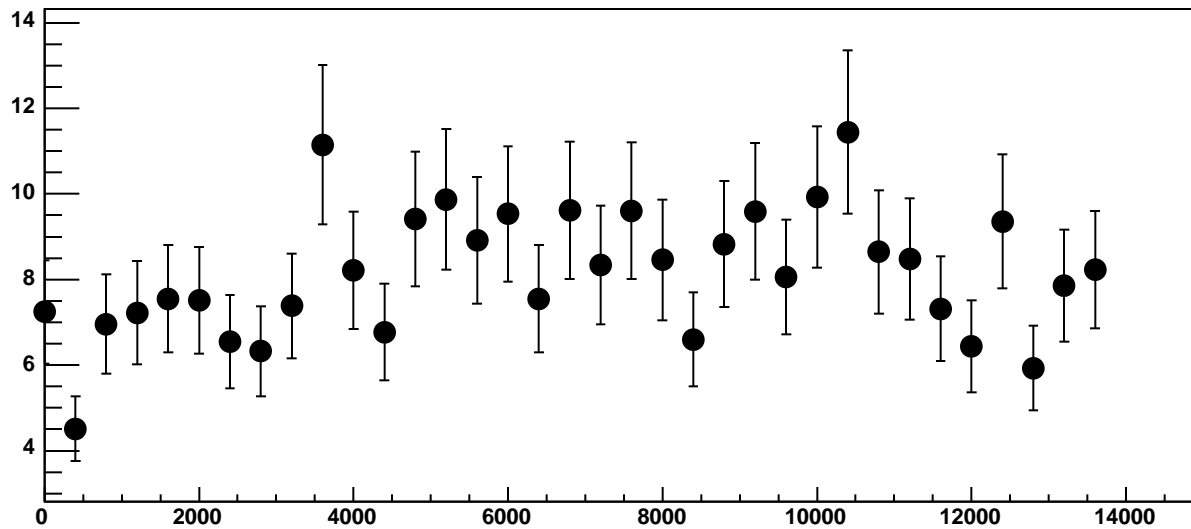


Chip 0, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

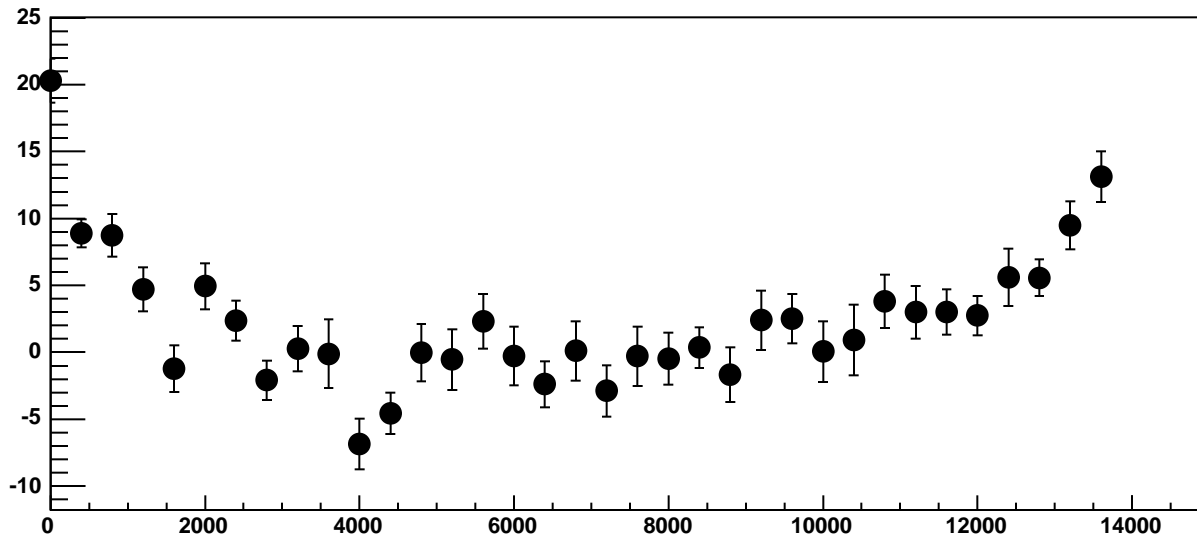


χ^2 / ndf 56.35 / 23
p0 -42.71 ± 0.8095
p1 0.01497 ± 0.0001292

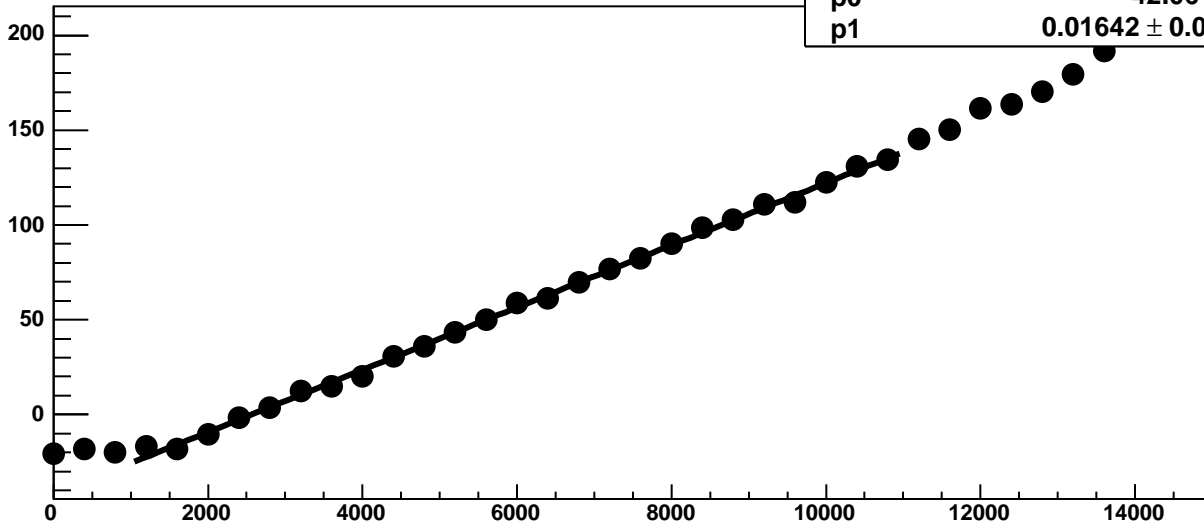
Chip 0, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

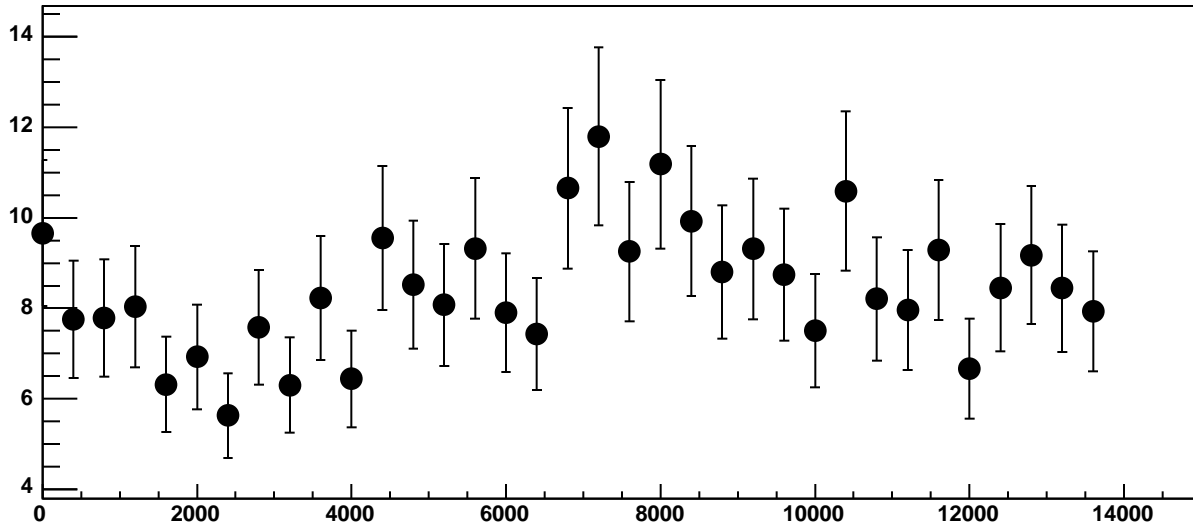


Chip 0, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

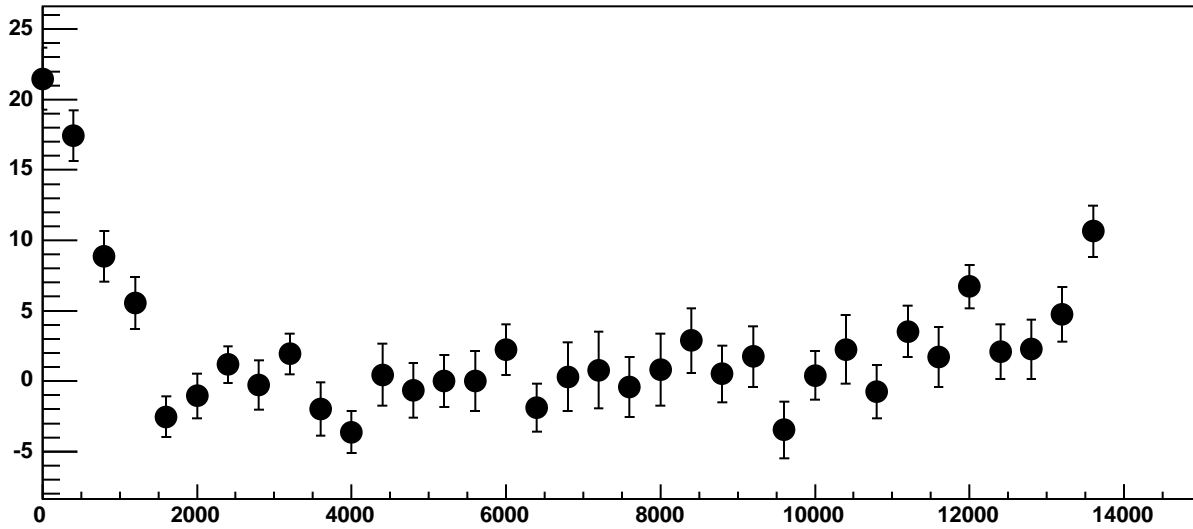


χ^2 / ndf 31.83 / 23
p0 -42.06 ± 0.763
p1 0.01642 ± 0.0001253

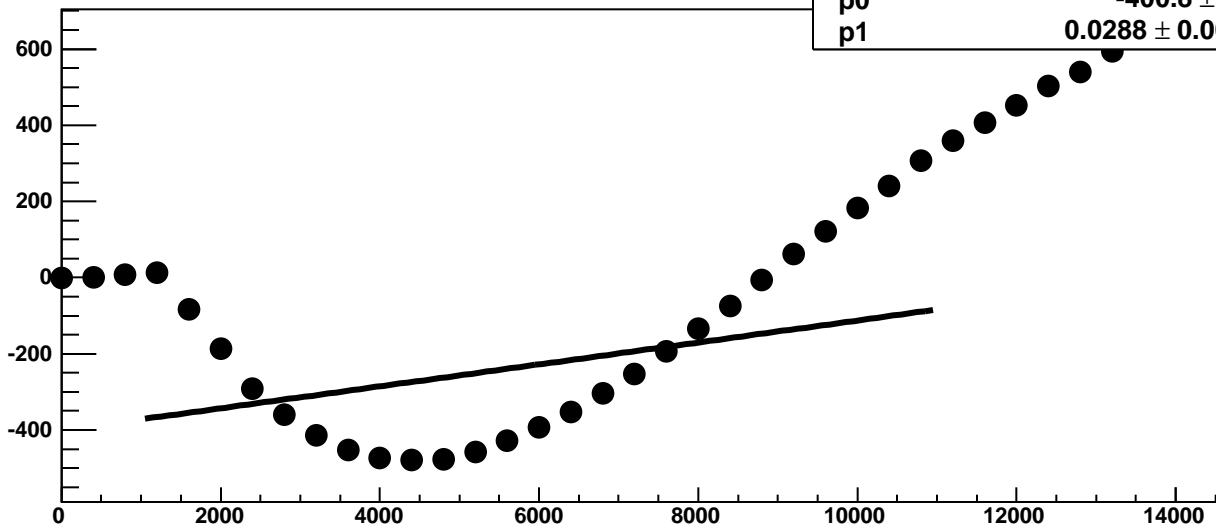
Chip 0, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



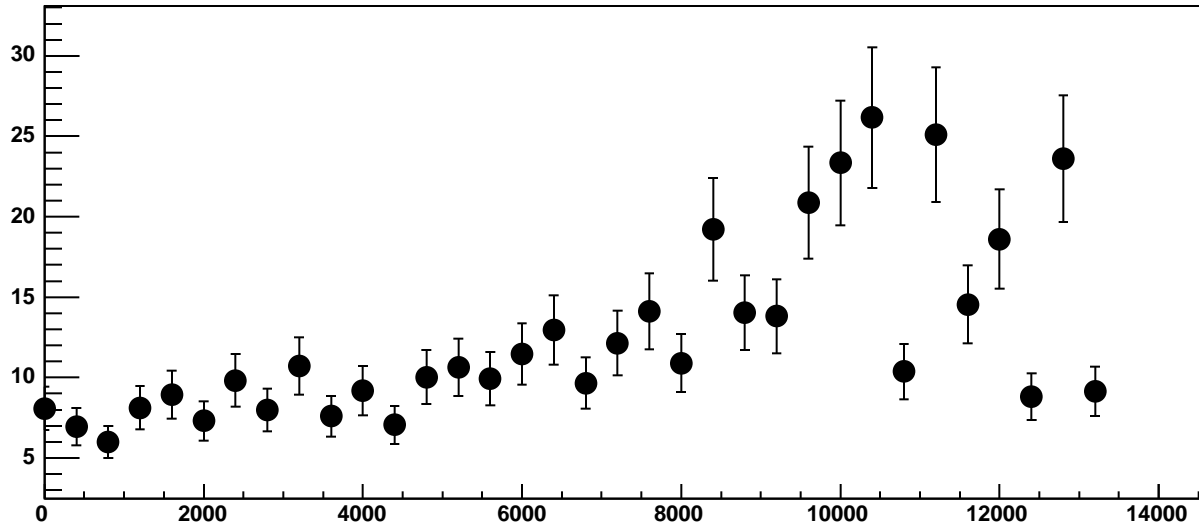
Chip 0, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC



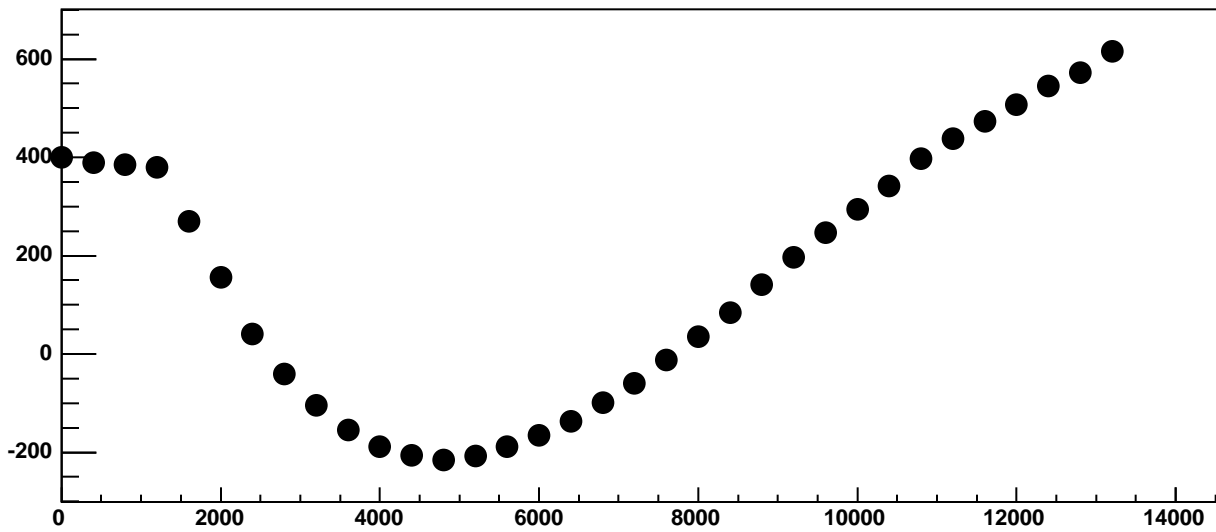
Chip 0, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



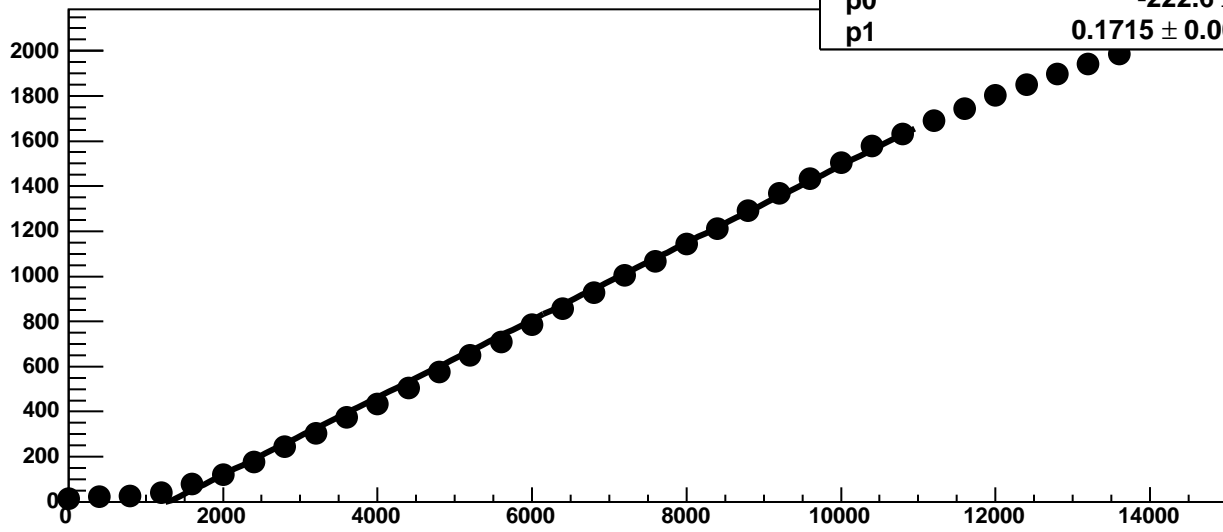
Chip 0, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

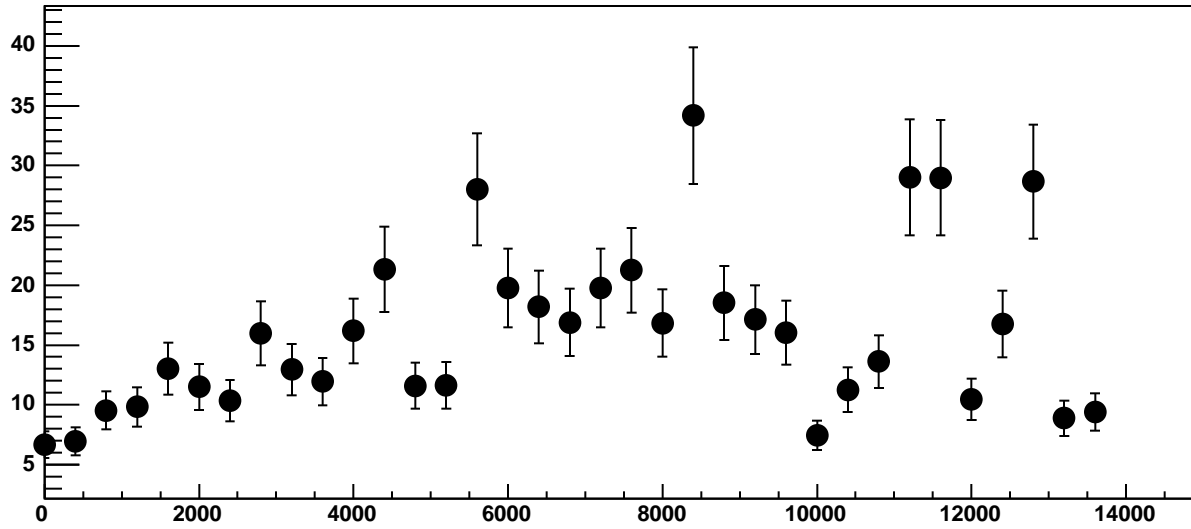


Chip 0, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

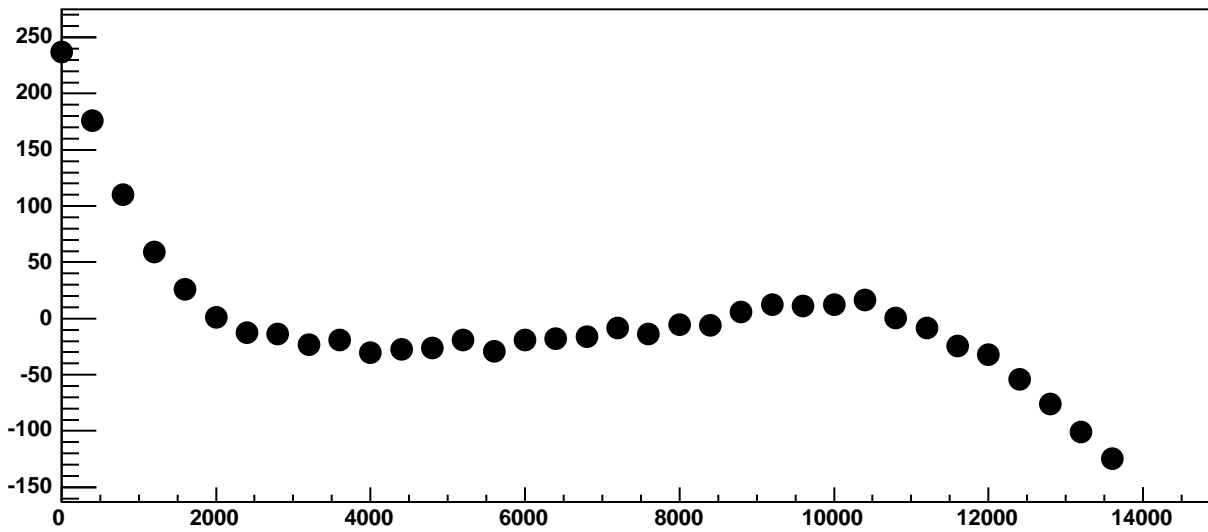


χ^2 / ndf 1361 / 23
p0 -222.6 ± 1.269
p1 0.1715 ± 0.0001895

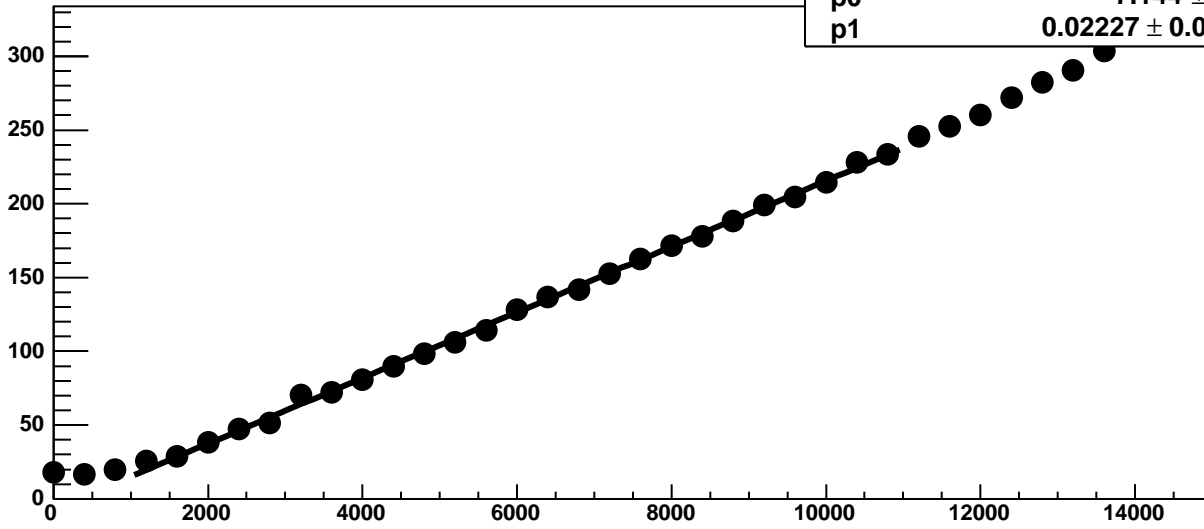
Chip 0, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

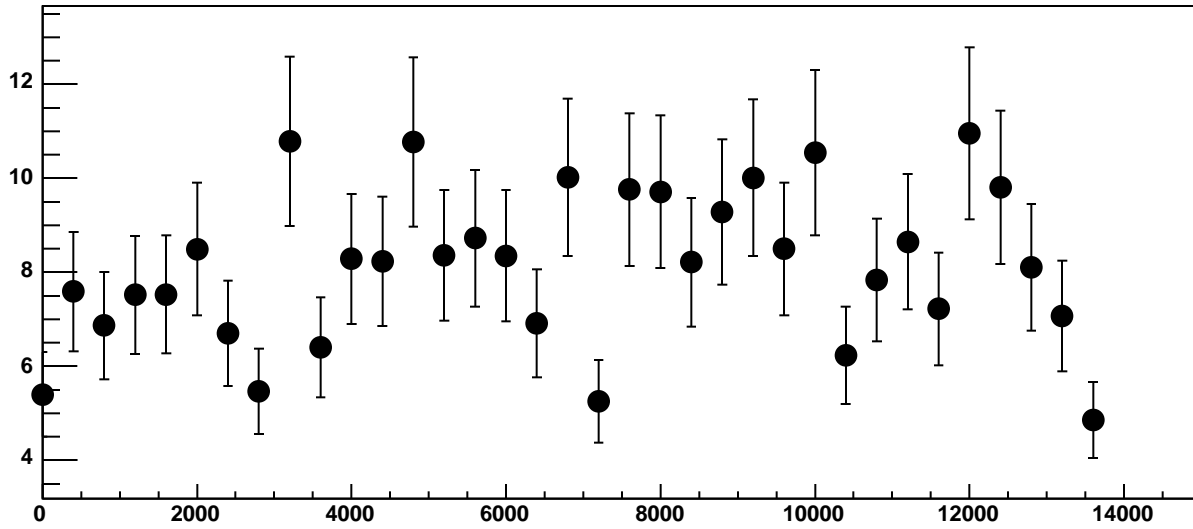


Chip 0, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

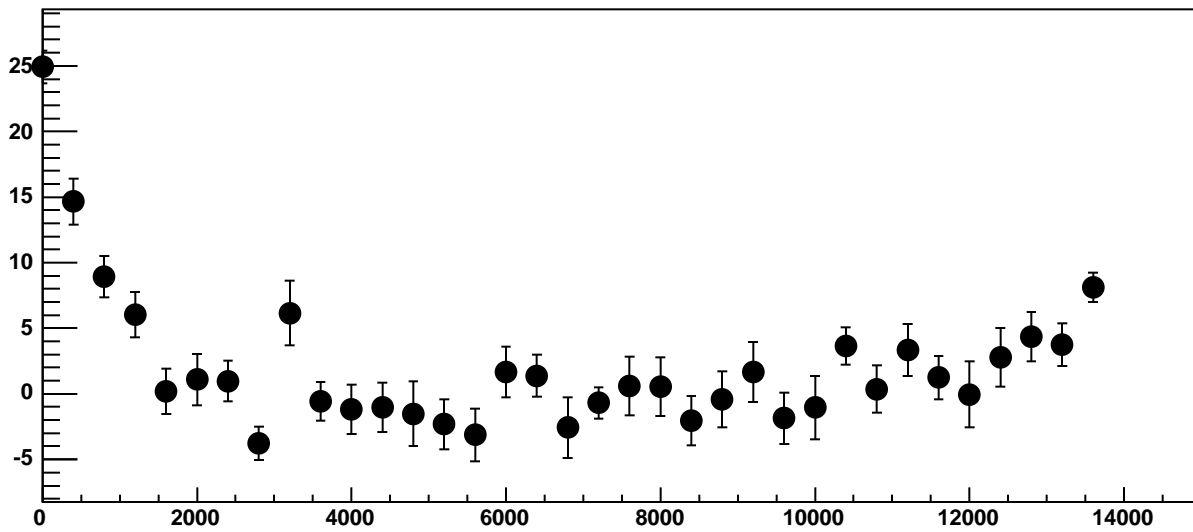


χ^2 / ndf 46.01 / 23
p0 -7.144 ± 0.7927
p1 0.02227 ± 0.0001224

Chip 0, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

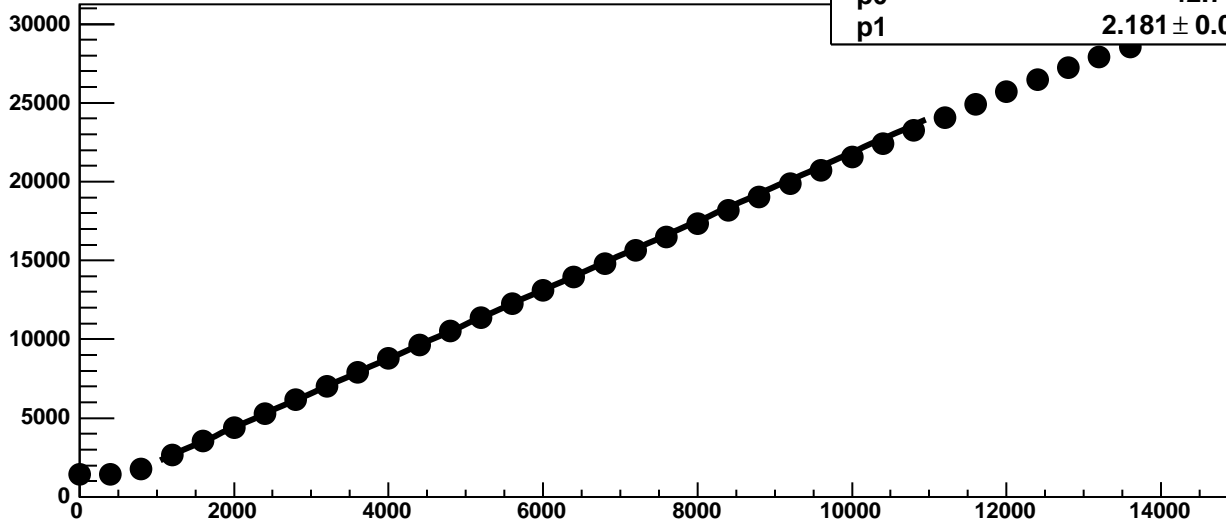


Chip 0, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

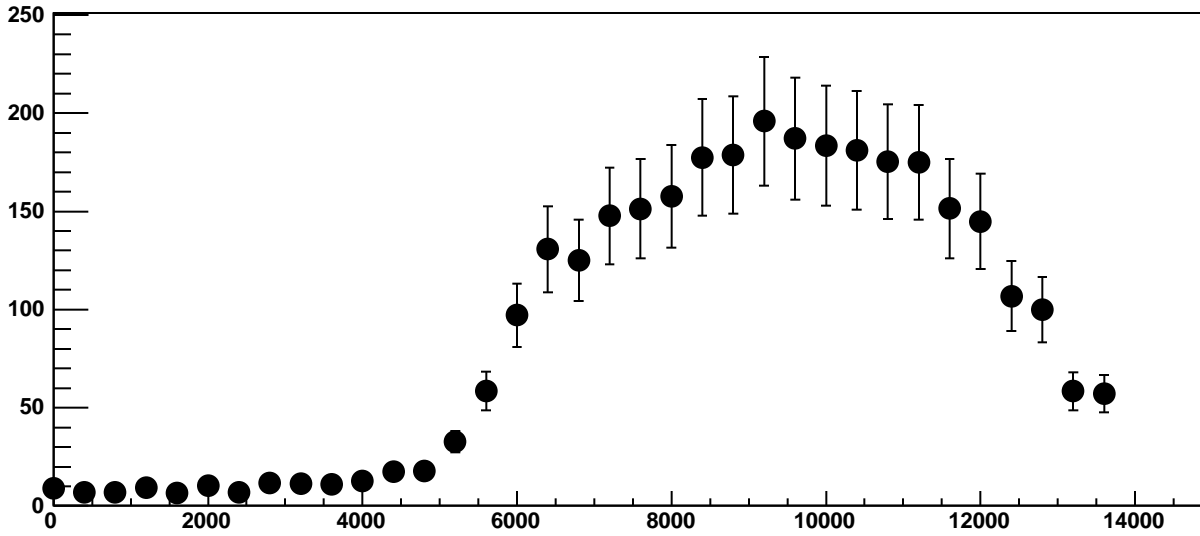


Chip 0, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC

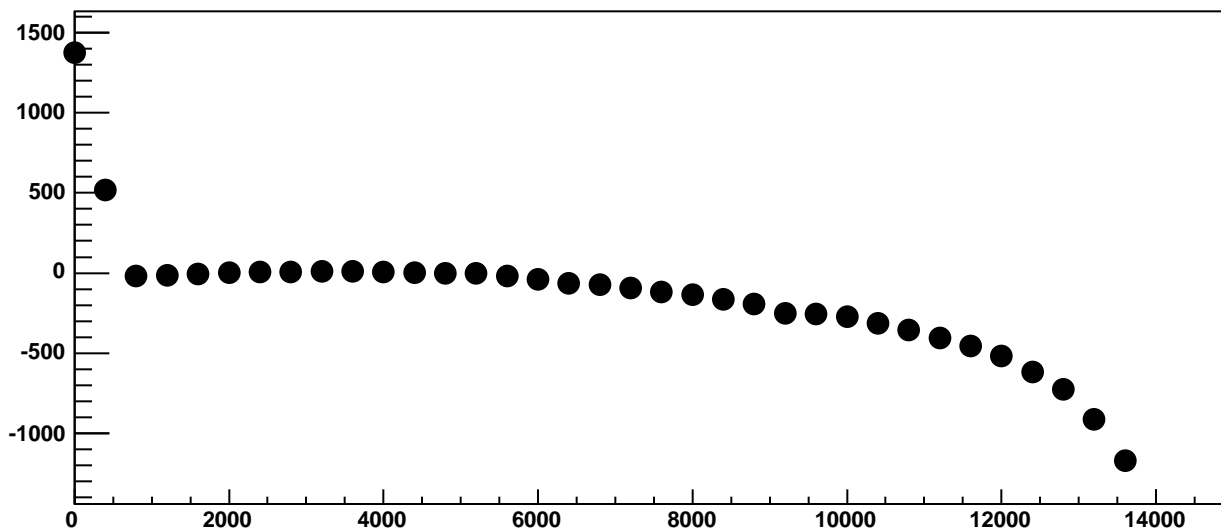
χ^2 / ndf 458.1 / 23
p0 42.7 ± 1.816
p1 2.181 ± 0.0006543



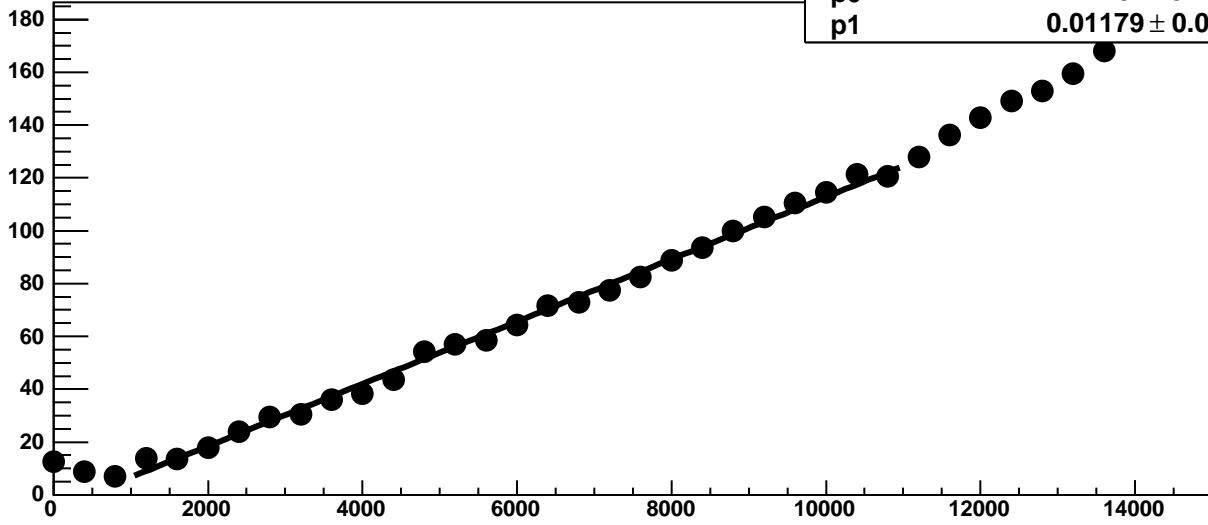
Chip 0, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

45.67 / 23

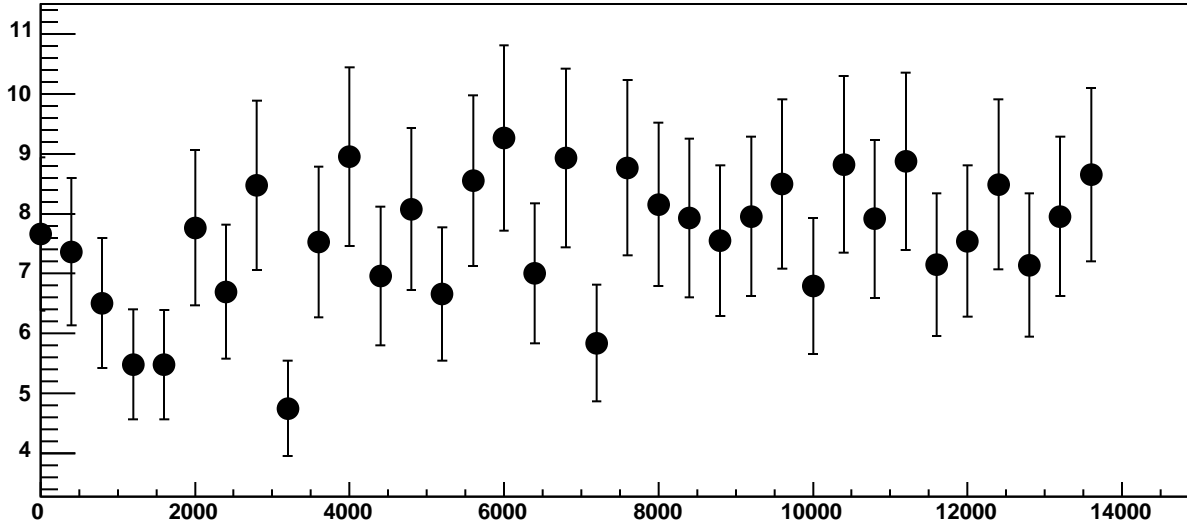
p0

-5.113 ± 0.6937

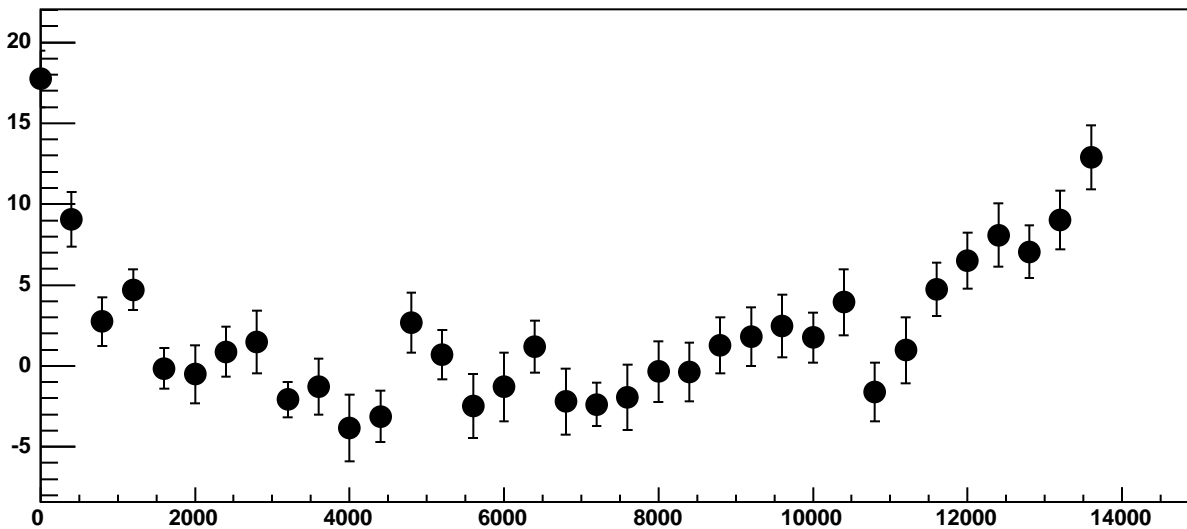
p1

0.01179 ± 0.0001113

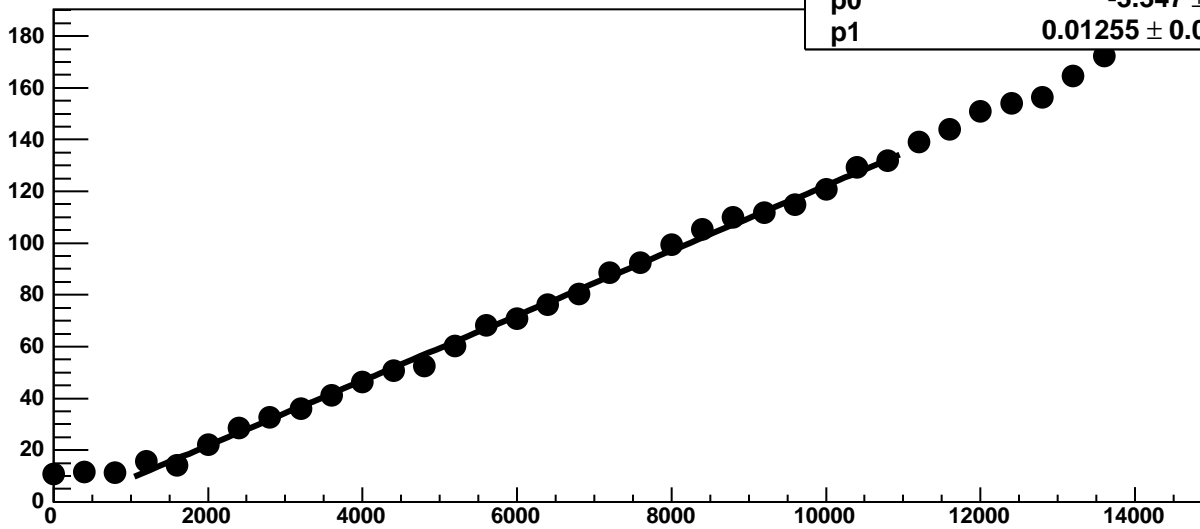
Chip 0, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

28.12 / 23

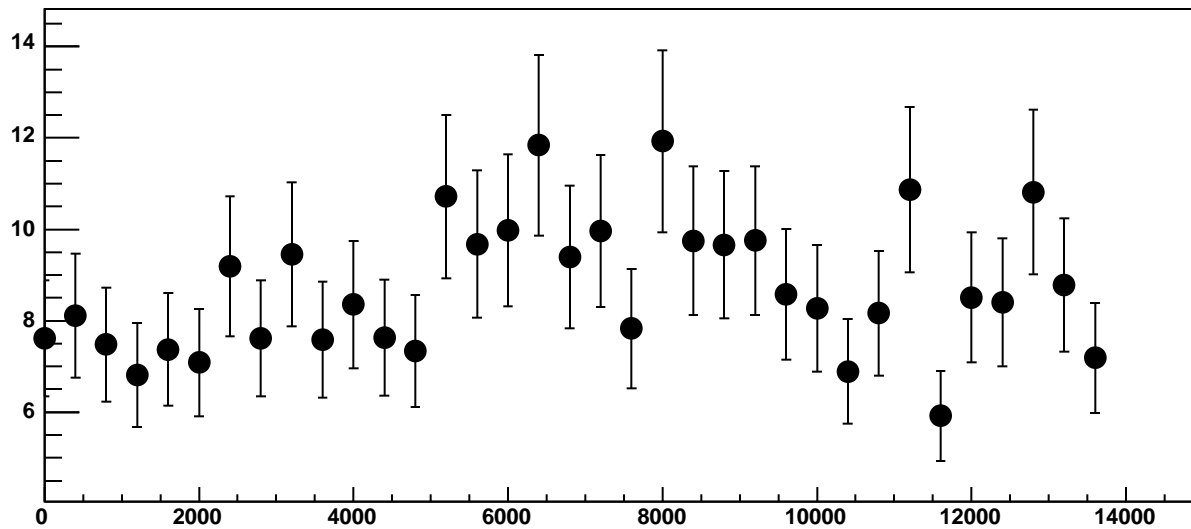
p0

-3.347 ± 0.8199

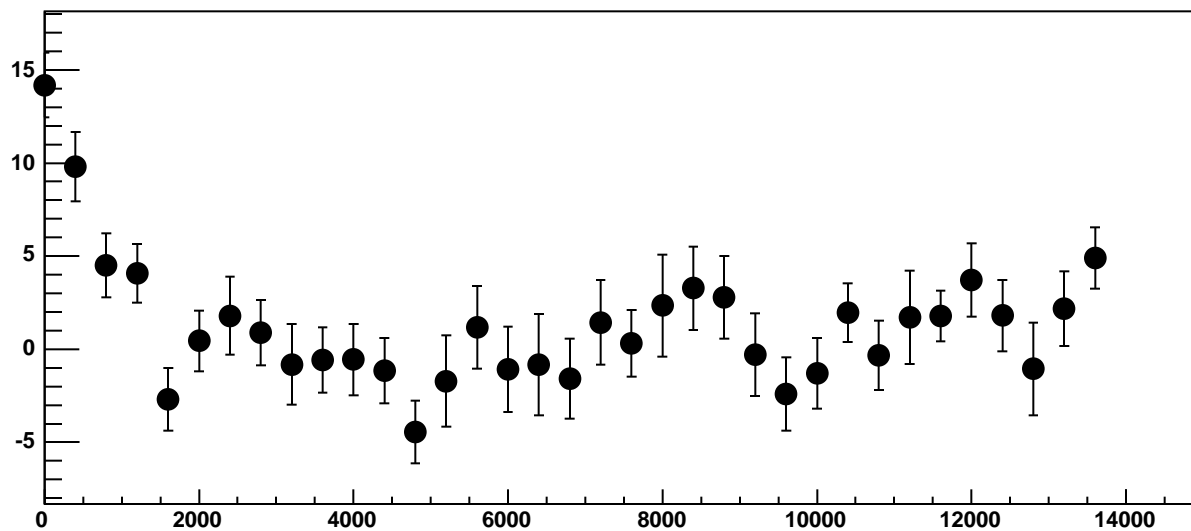
p1

0.01255 ± 0.0001262

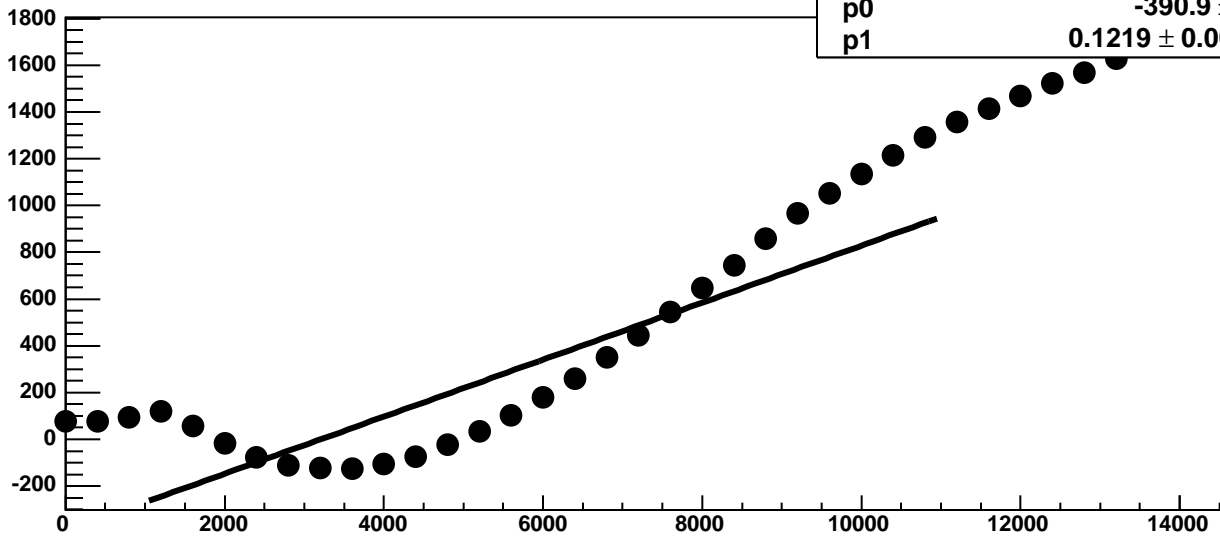
Chip 0, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

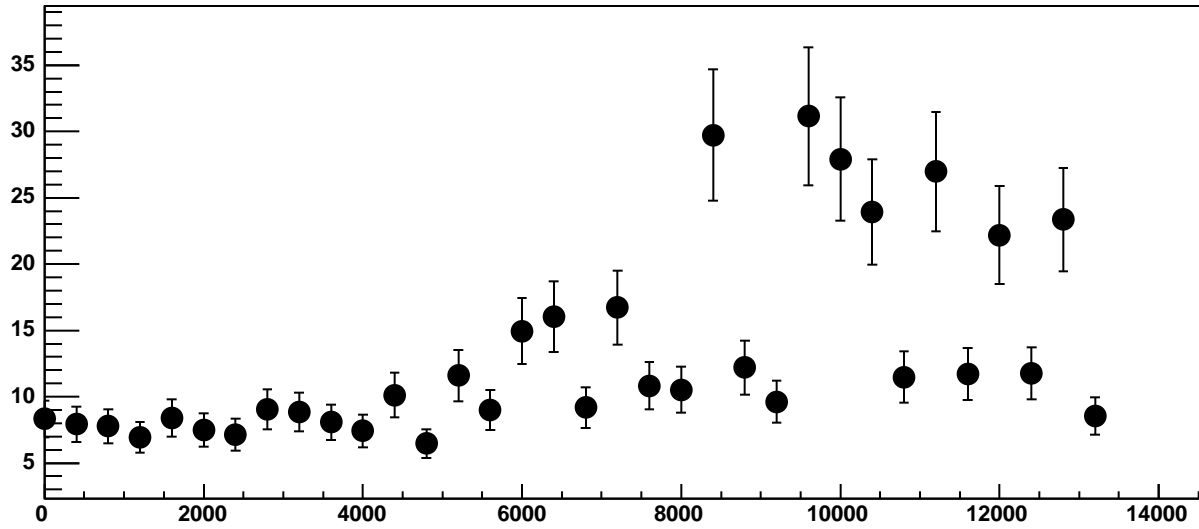


Chip 0, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC

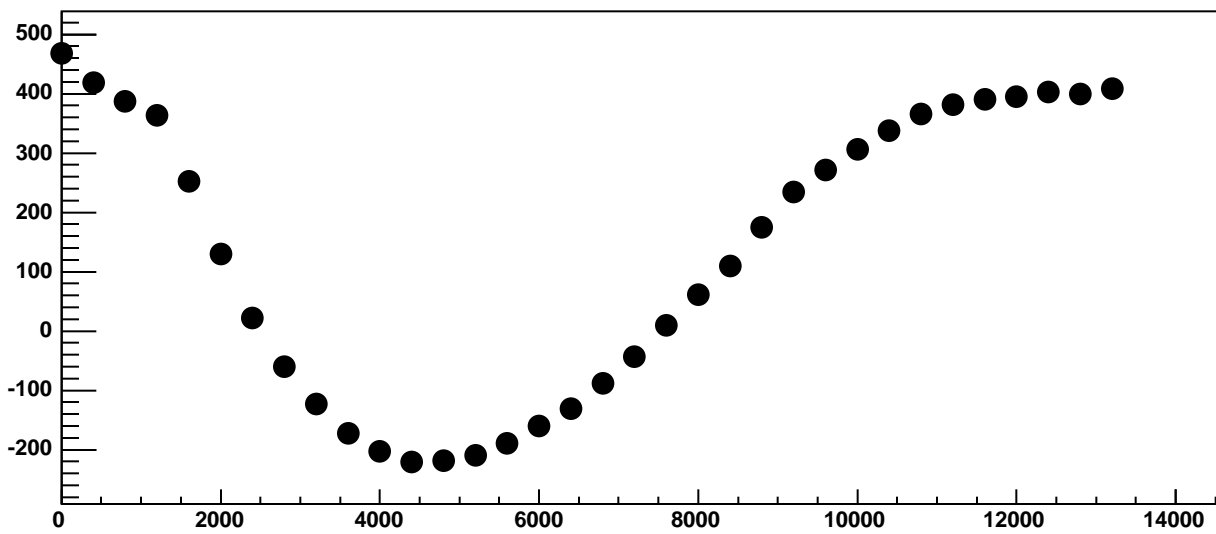


χ^2 / ndf 1.959e+05 / 23
p0 -390.9 ± 0.923
p1 0.1219 ± 0.0001737

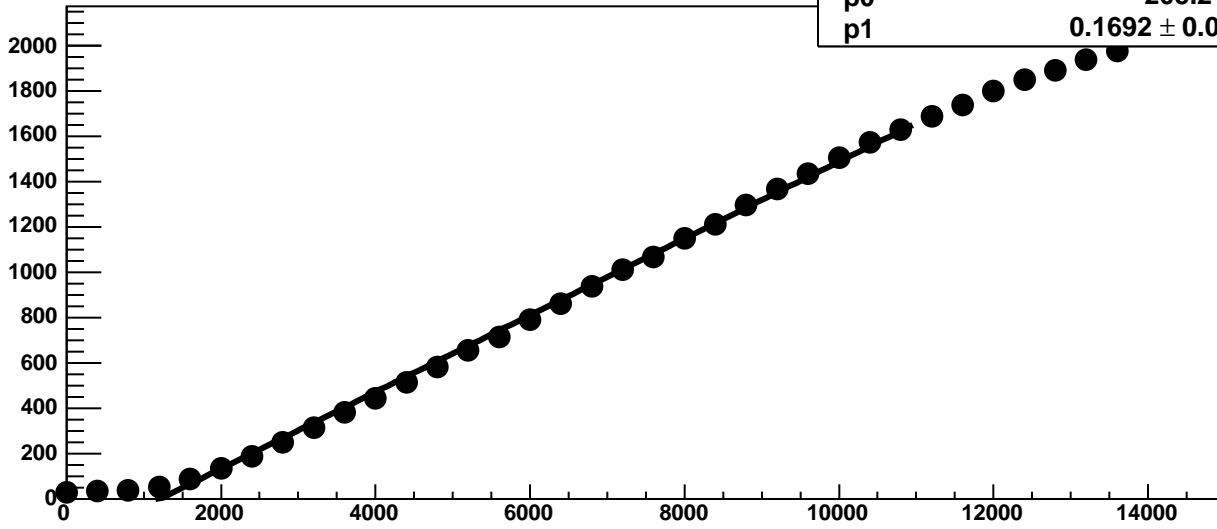
Chip 0, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



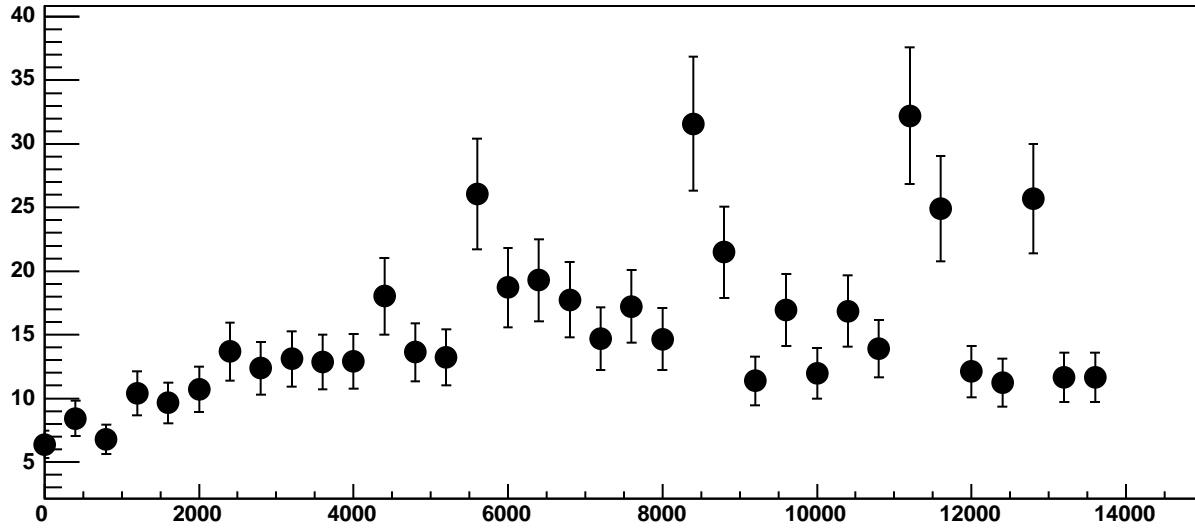
Chip 0, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC



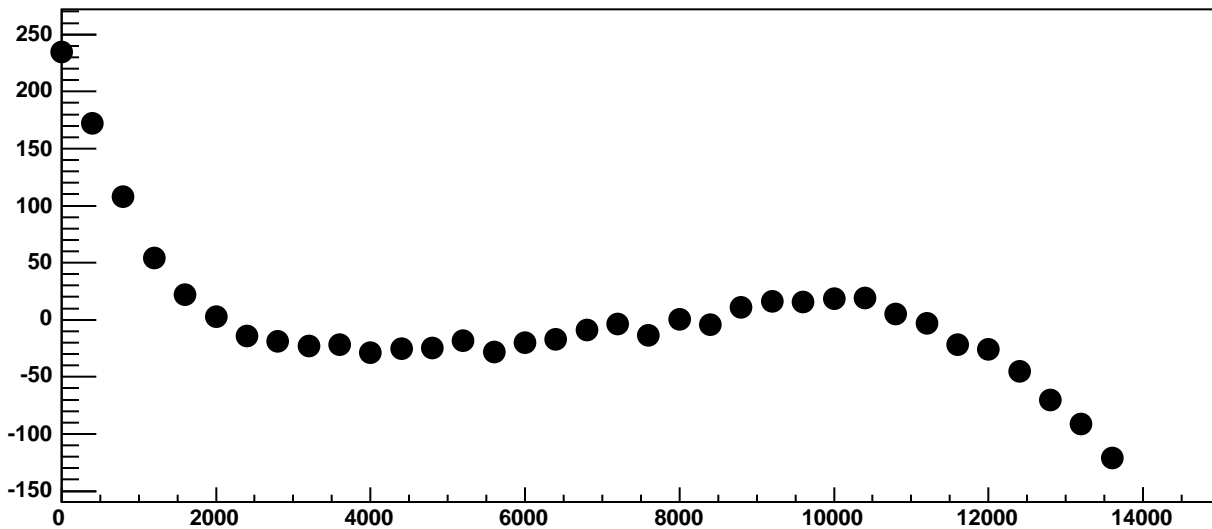
Chip 0, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



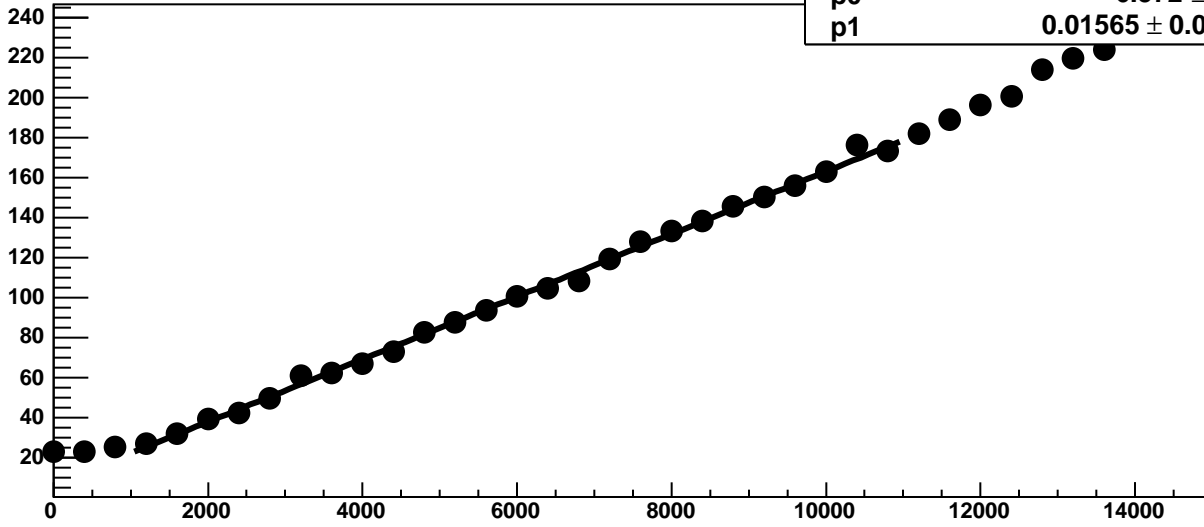
Chip 0, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

45.52 / 23

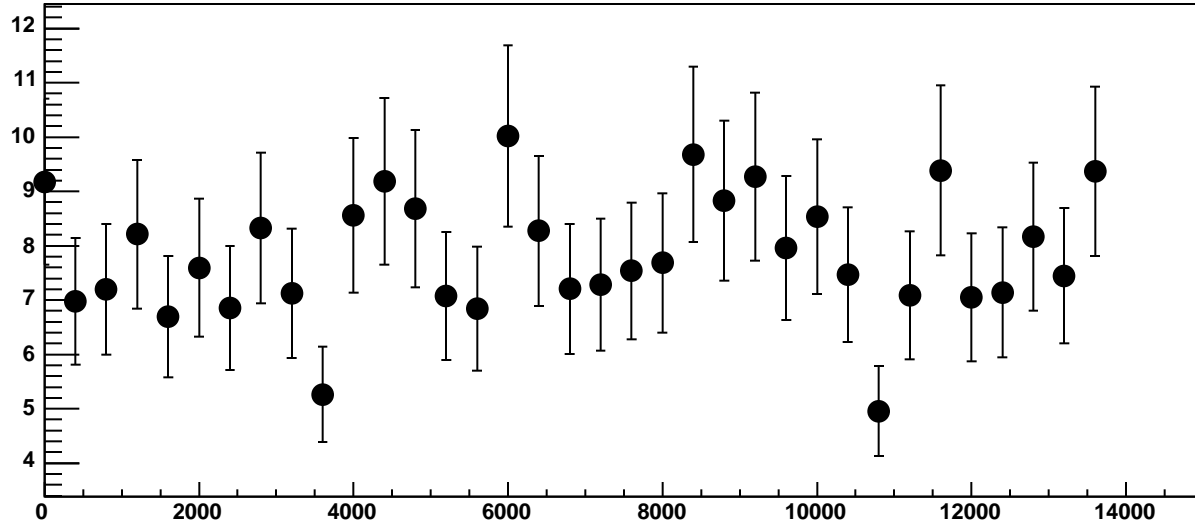
p0

6.572 ± 0.7616

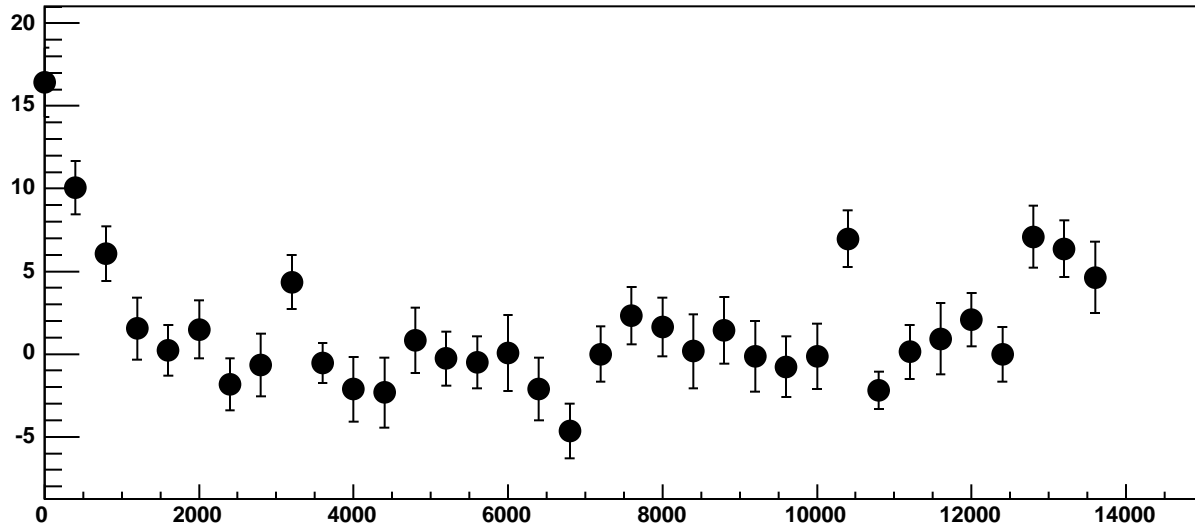
p1

0.01565 ± 0.0001133

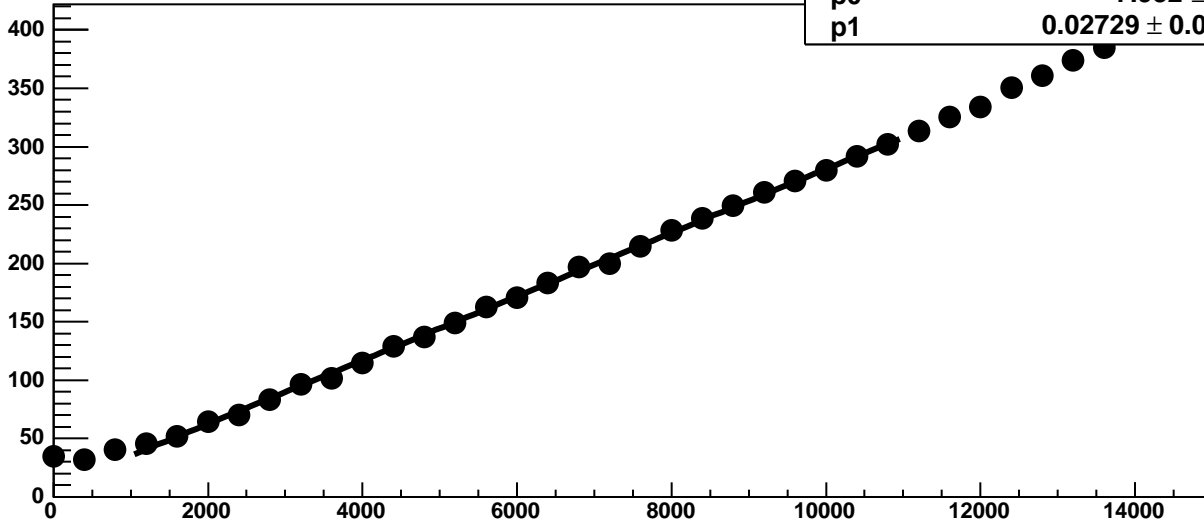
Chip 0, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

50.22 / 23

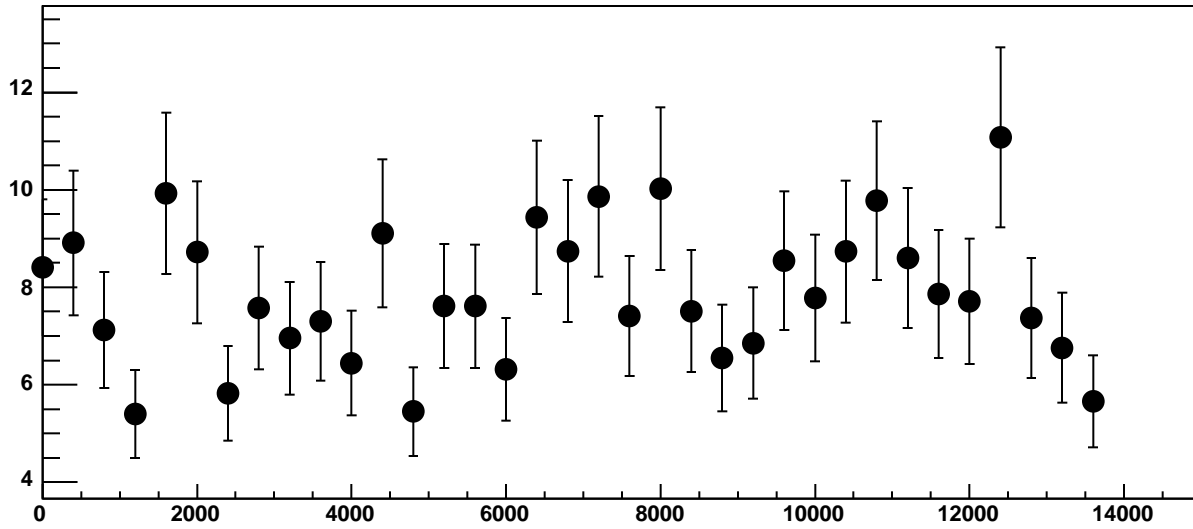
p0

7.932 ± 0.7505

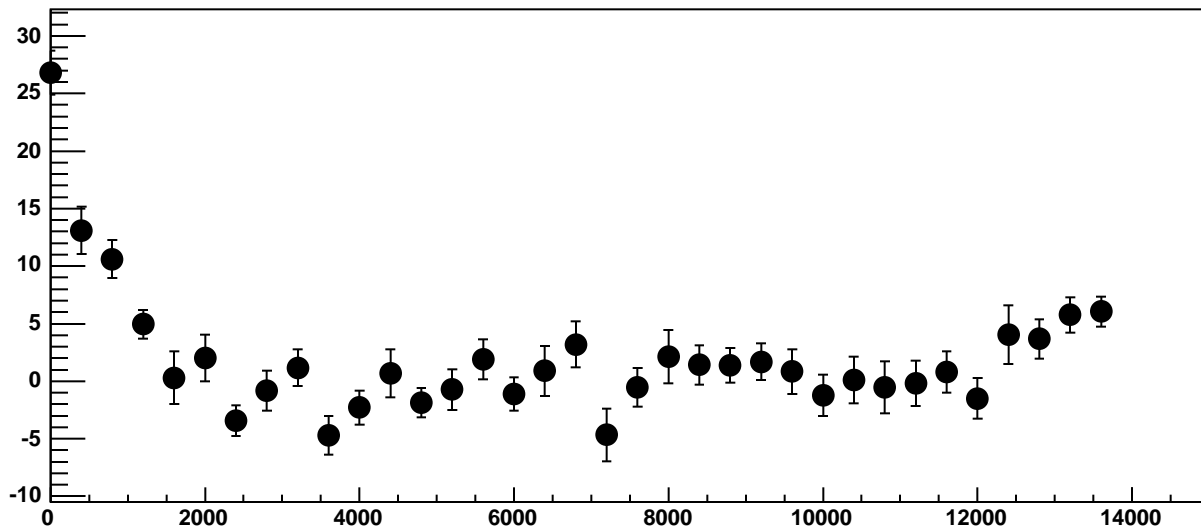
p1

0.02729 ± 0.0001193

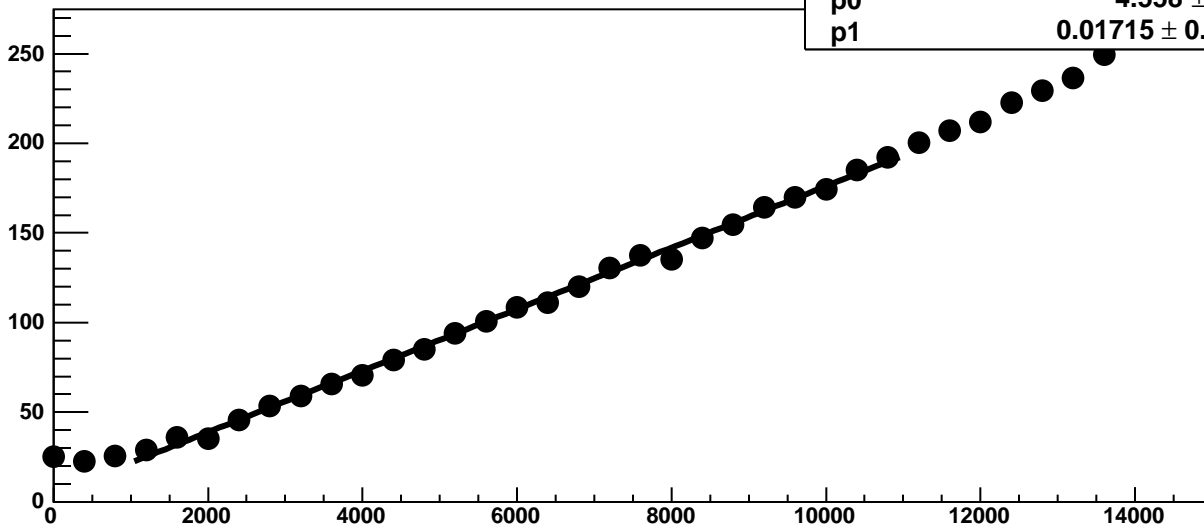
Chip 0, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

40.96 / 23

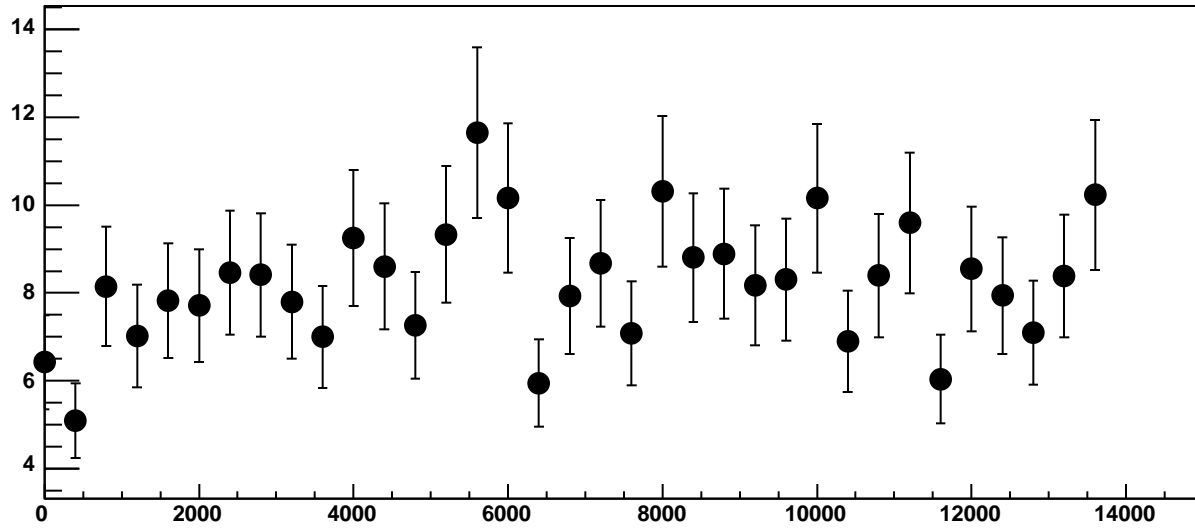
p0

4.558 ± 0.8336

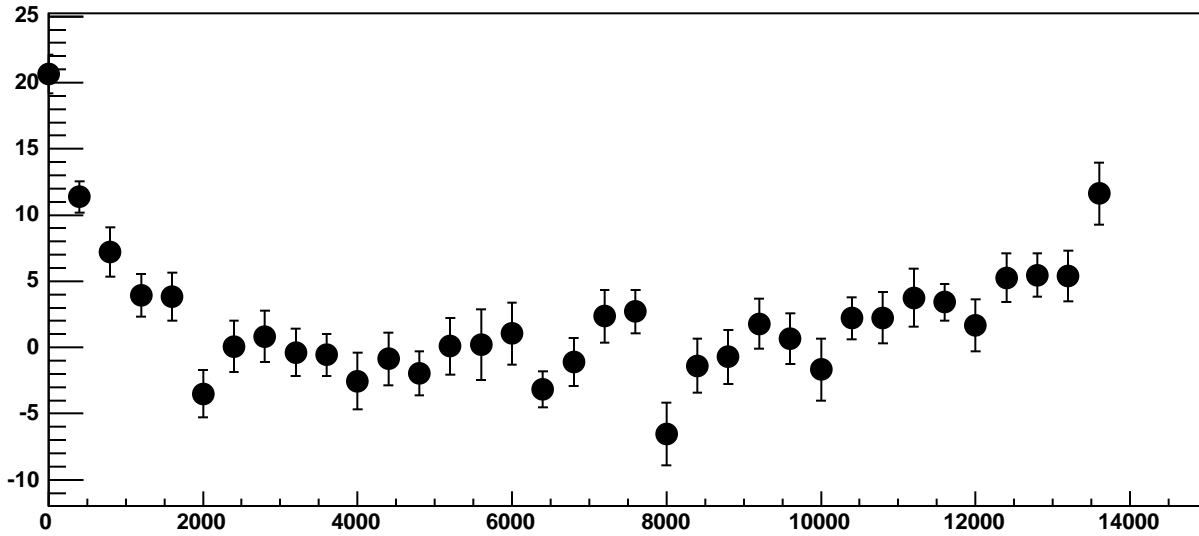
p1

0.01715 ± 0.000127

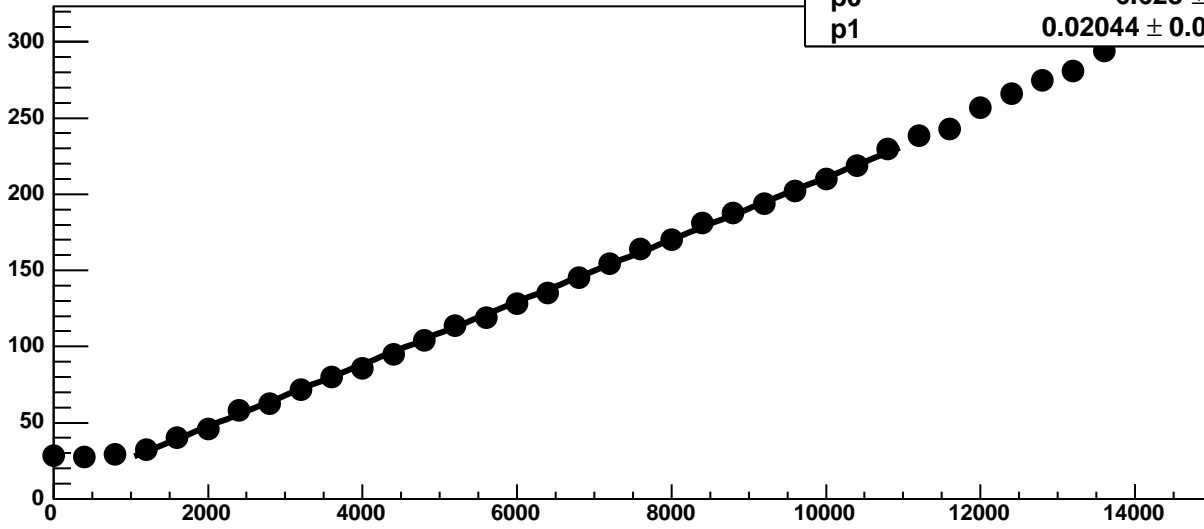
Chip 0, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

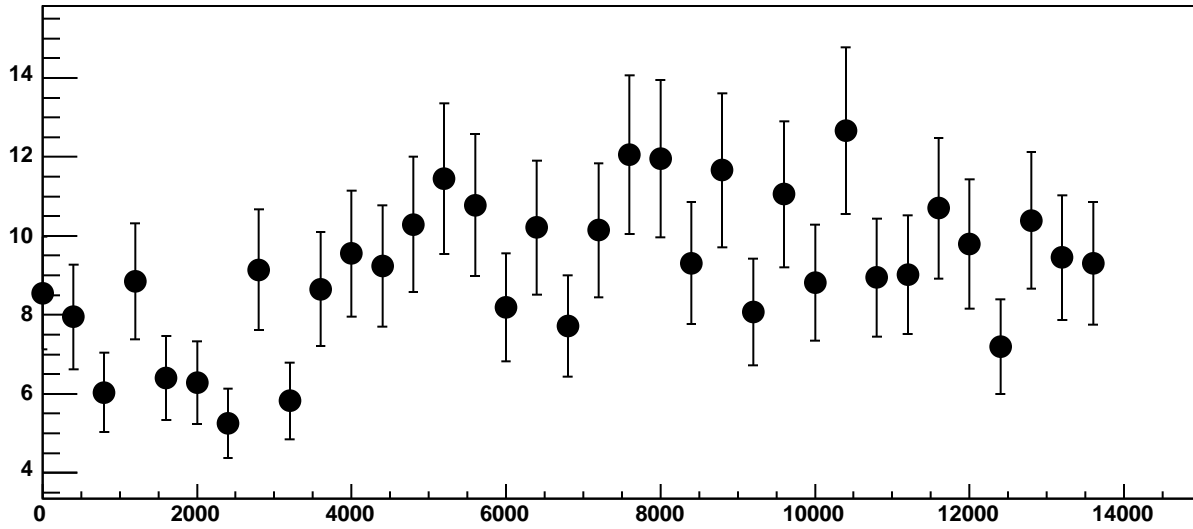


Chip 0, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

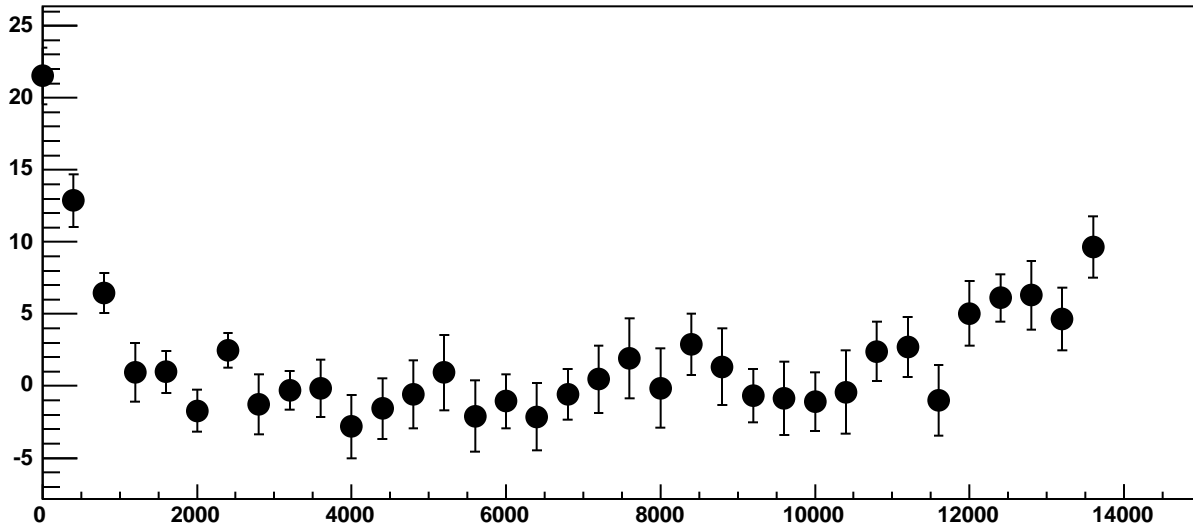


χ^2 / ndf 15.6 / 23
p0 6.628 ± 0.7866
p1 0.02044 ± 0.0001328

Chip 0, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

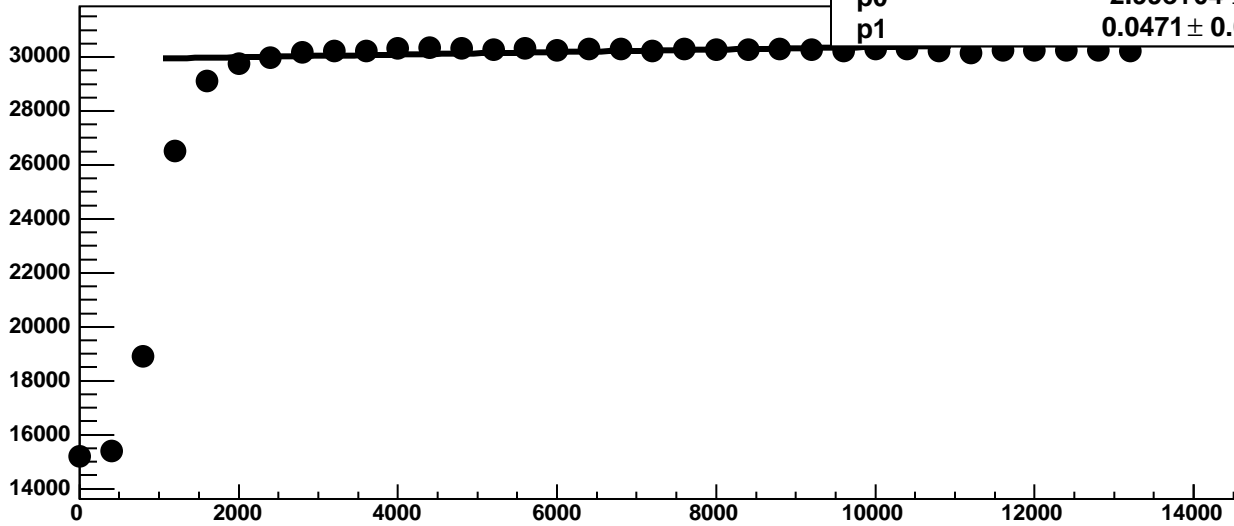
4787 / 23

p0

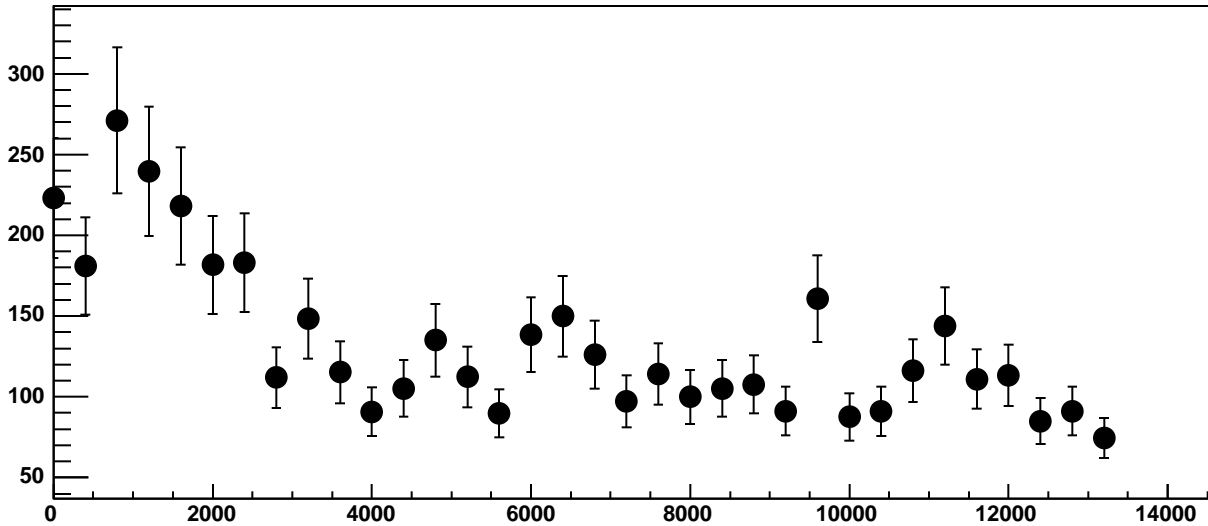
$2.99\text{e}+04 \pm 14.55$

p1

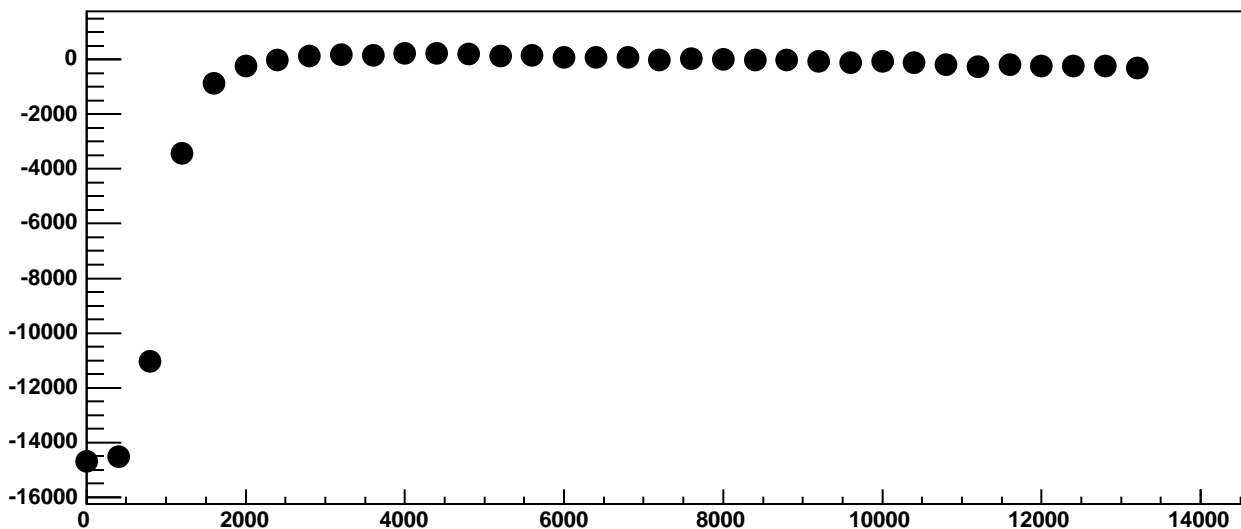
0.0471 ± 0.002012



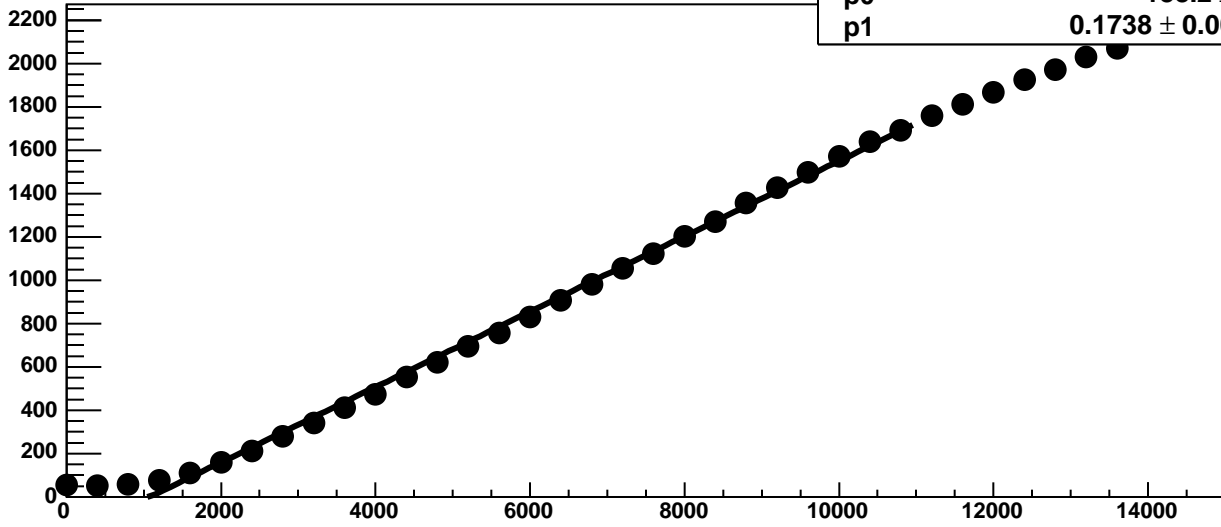
Chip 0, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC



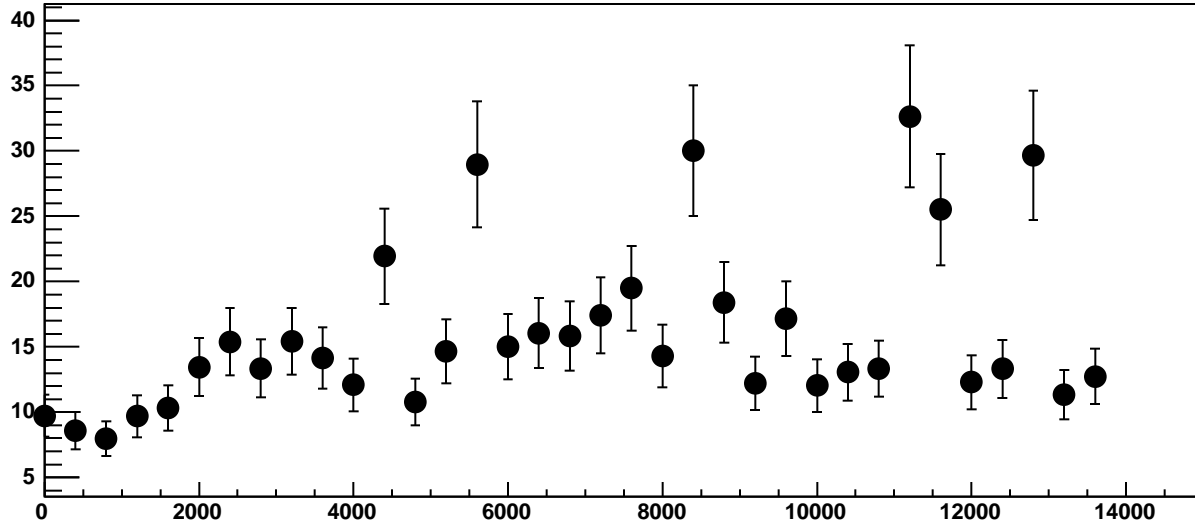
Chip 0, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC



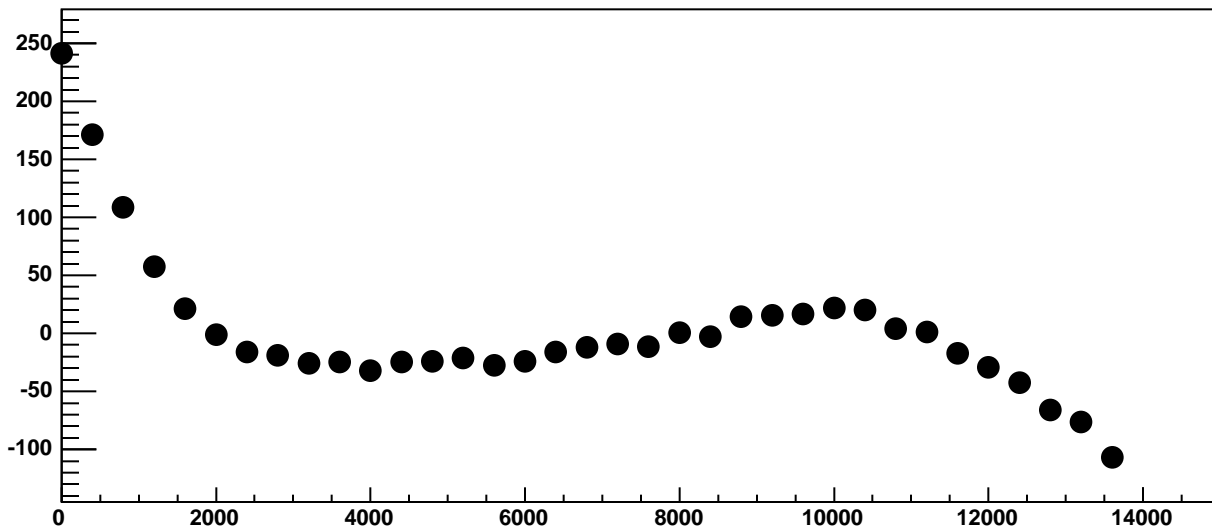
Chip 0, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



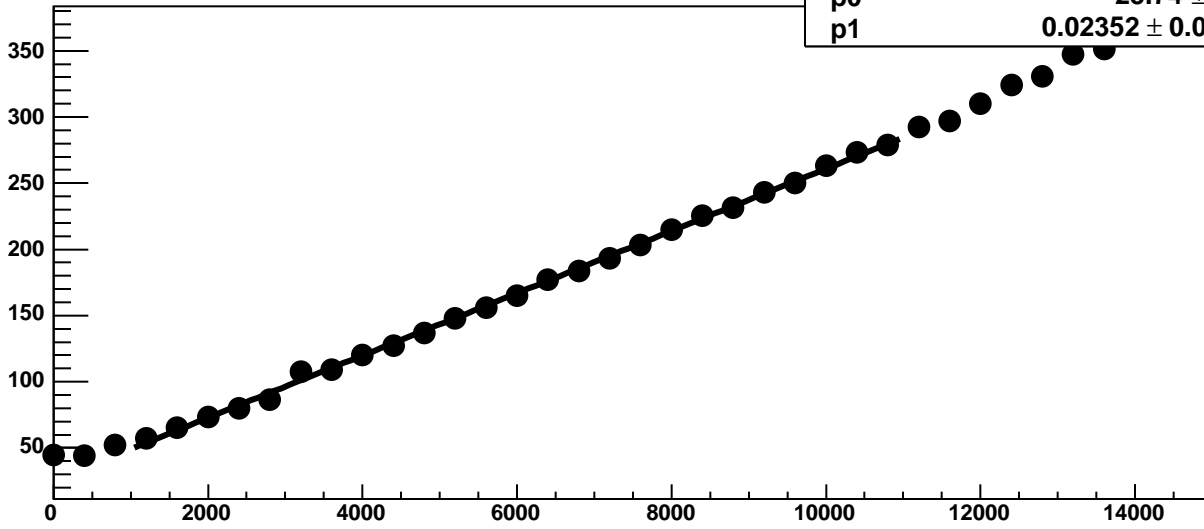
Chip 0, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

34.59 / 23

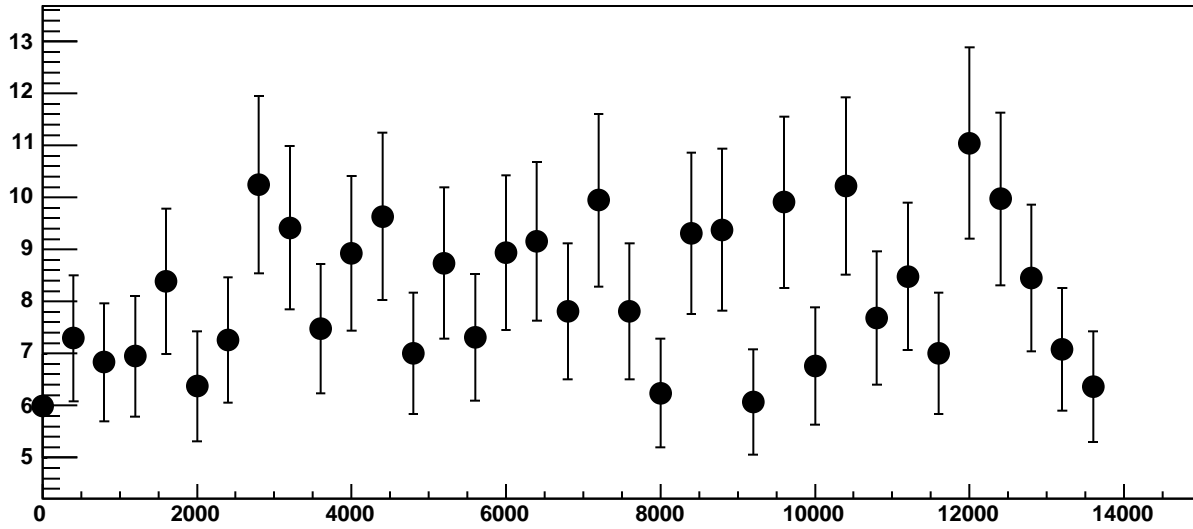
p0

25.74 ± 0.8203

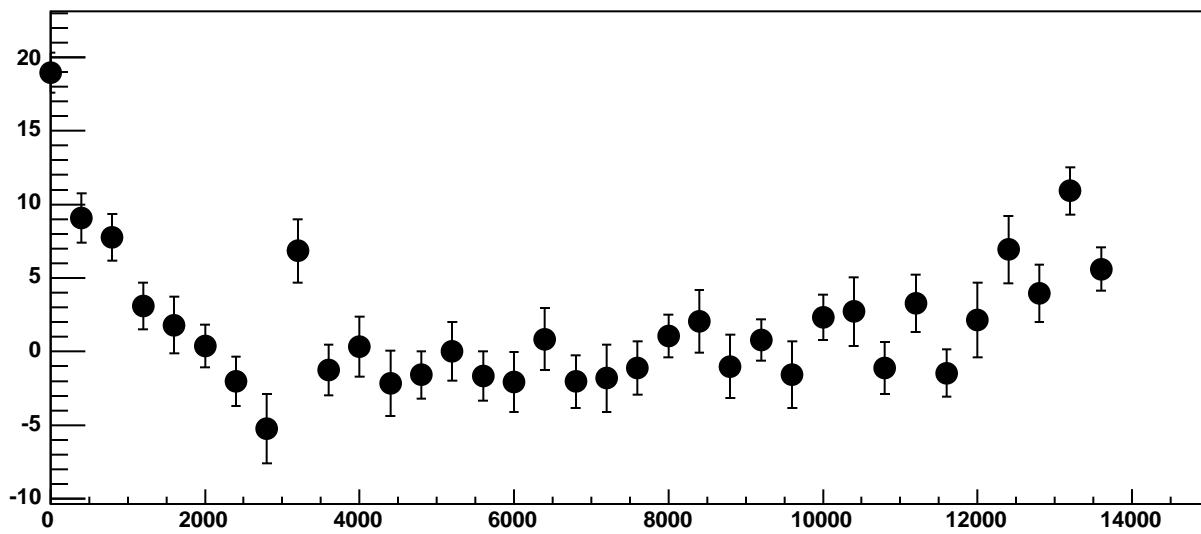
p1

0.02352 ± 0.0001233

Chip 0, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

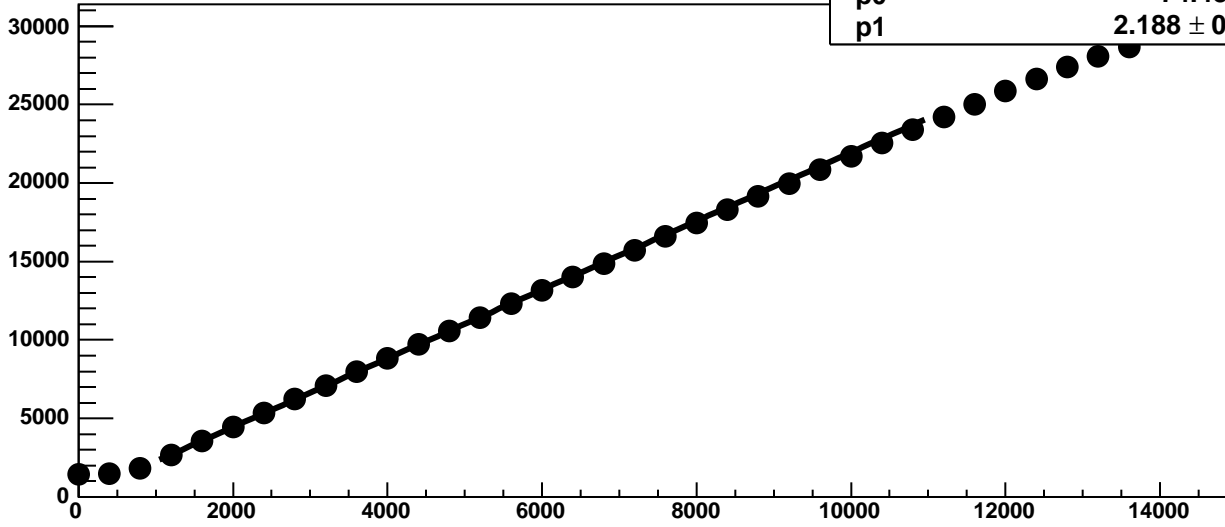


Chip 0, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

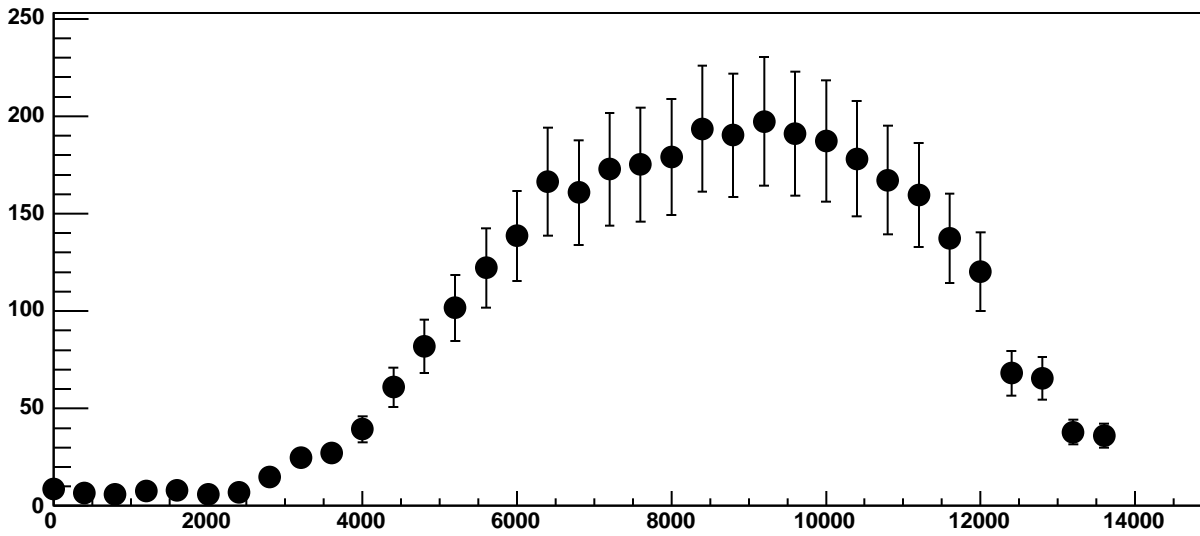


Chip 0, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC

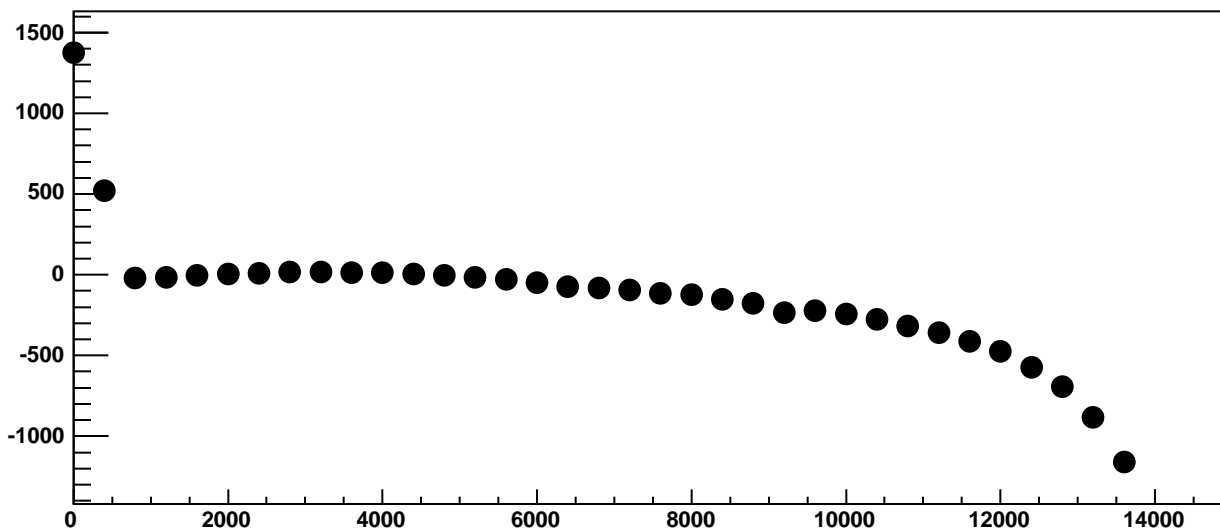
χ^2 / ndf 451.4 / 23
p0 74.48 ± 2.221
p1 2.188 ± 0.001031



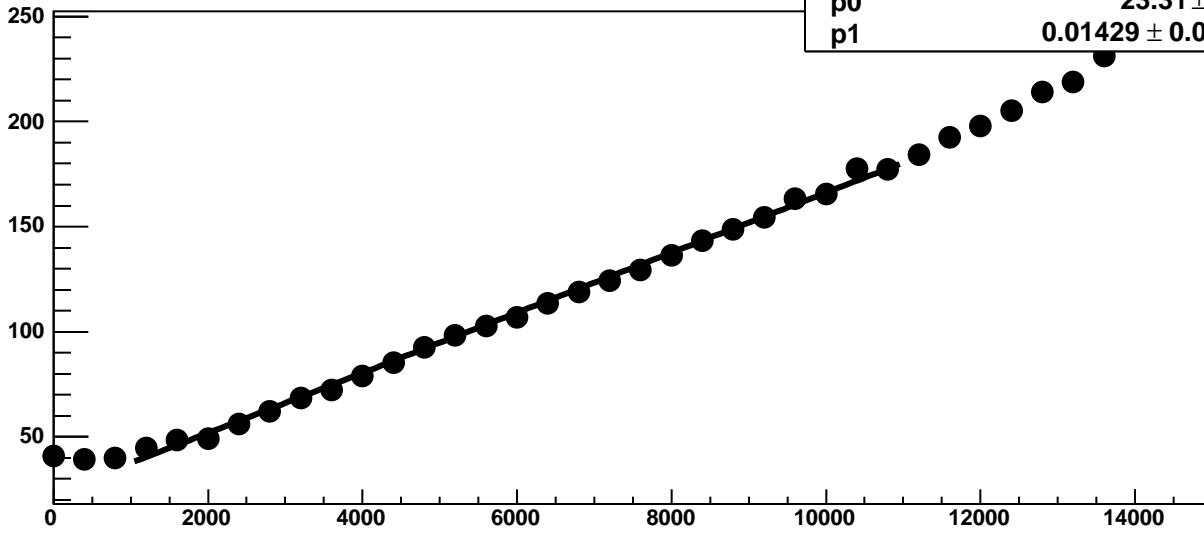
Chip 0, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC

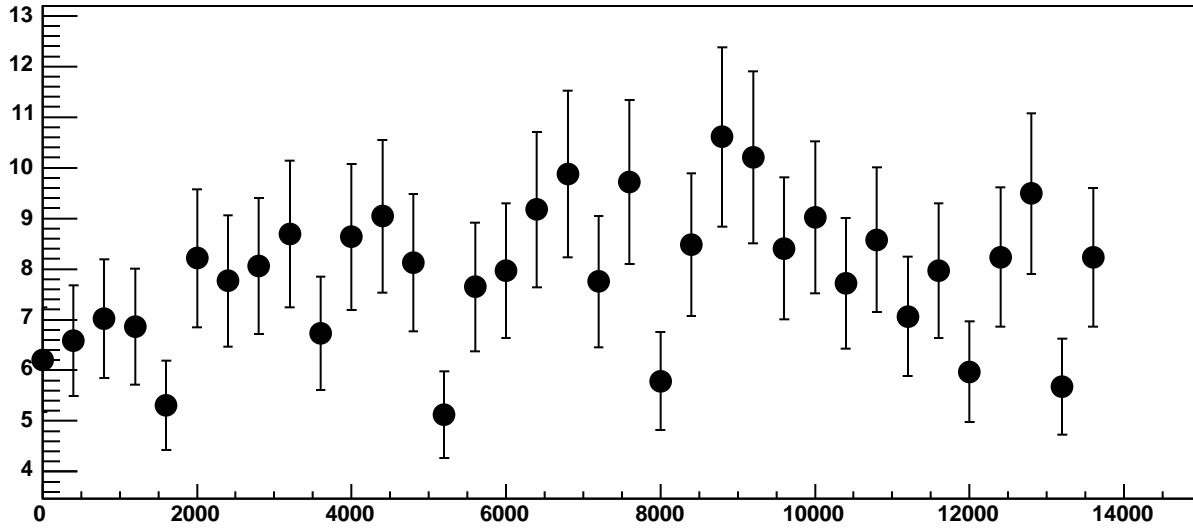


Chip 0, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

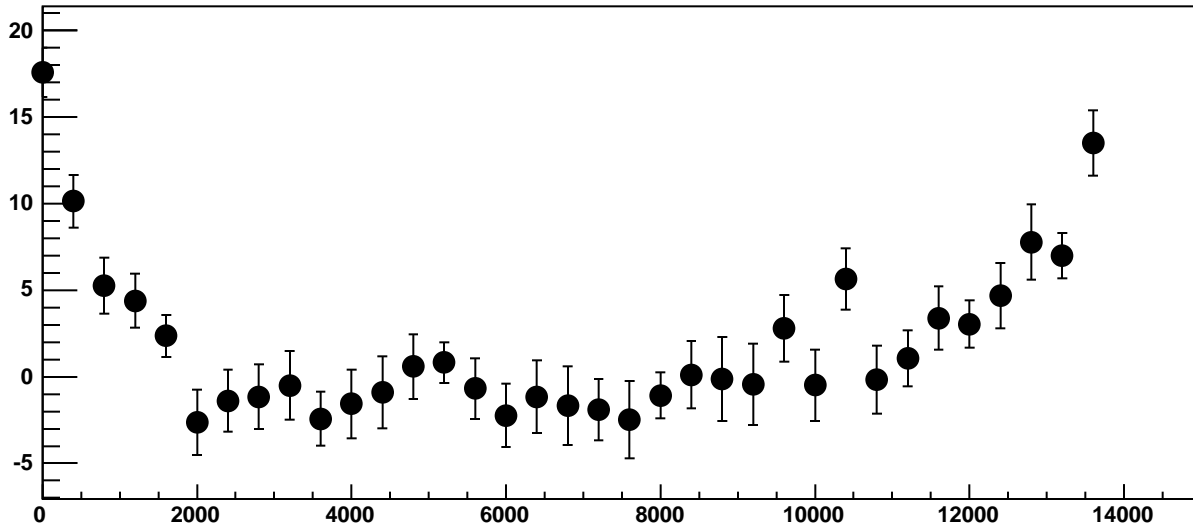


χ^2 / ndf 36.25 / 23
p0 23.31 ± 0.7679
p1 0.01429 ± 0.0001223

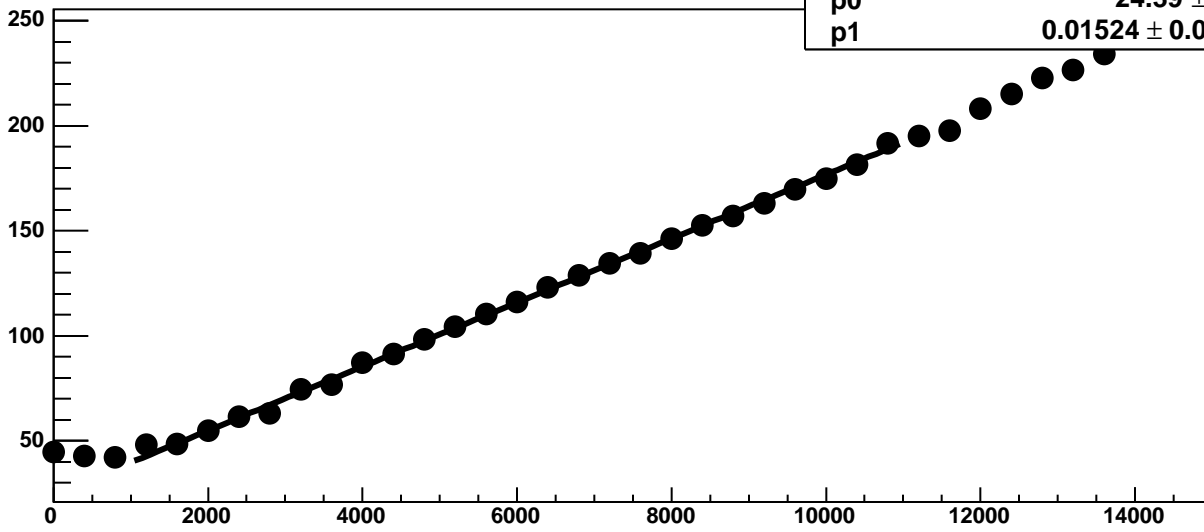
Chip 0, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

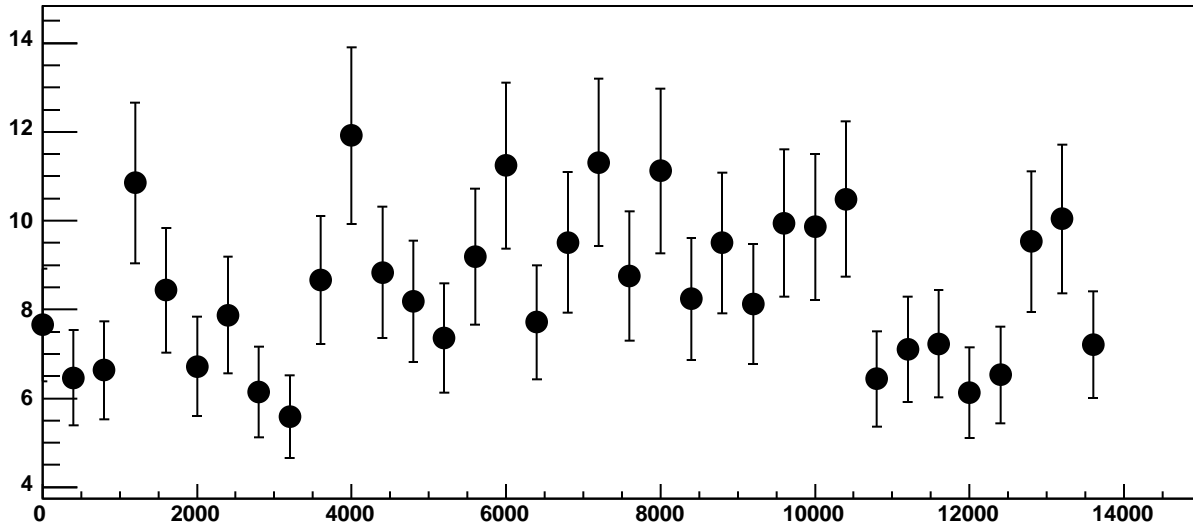


Chip 0, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

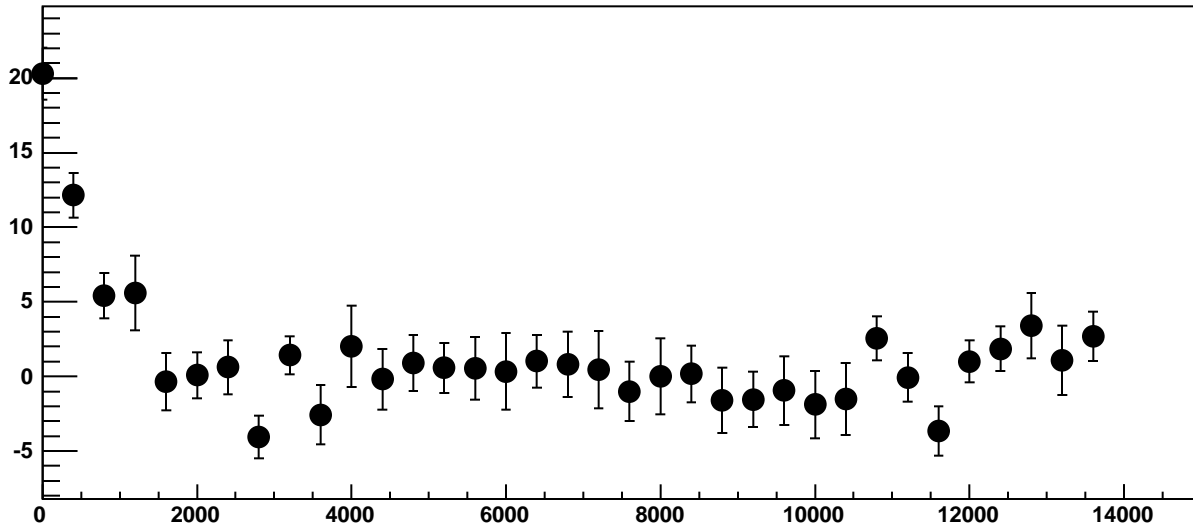


χ^2 / ndf 23.58 / 23
p0 24.39 ± 0.8299
p1 0.01524 ± 0.0001297

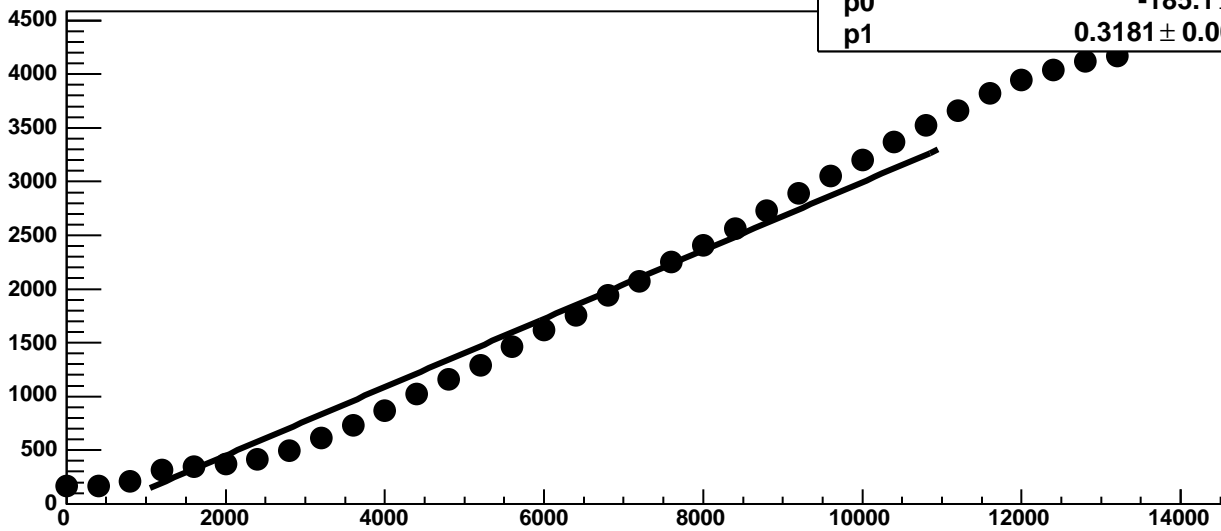
Chip 0, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



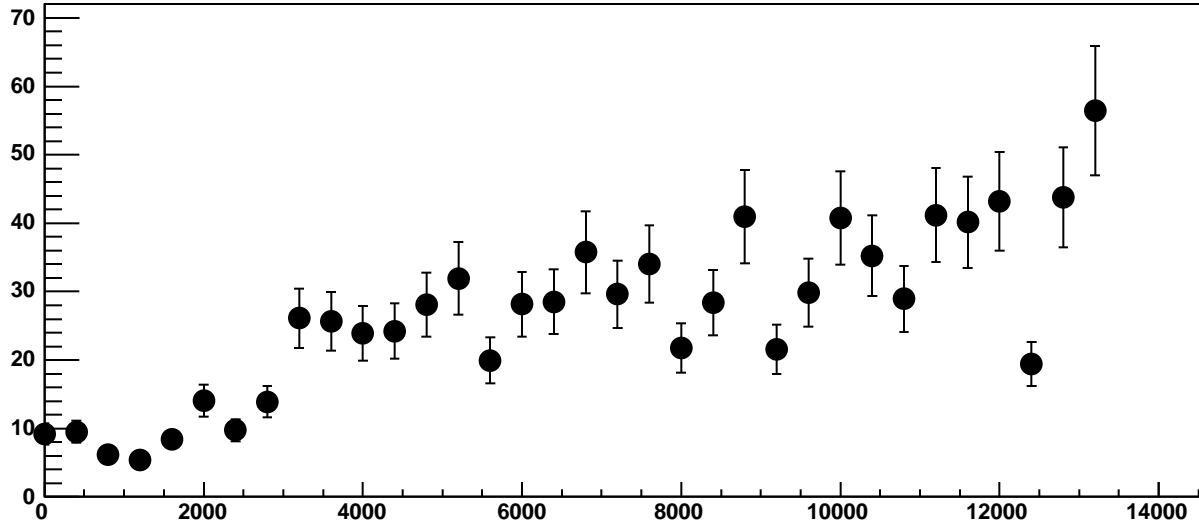
Chip 0, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



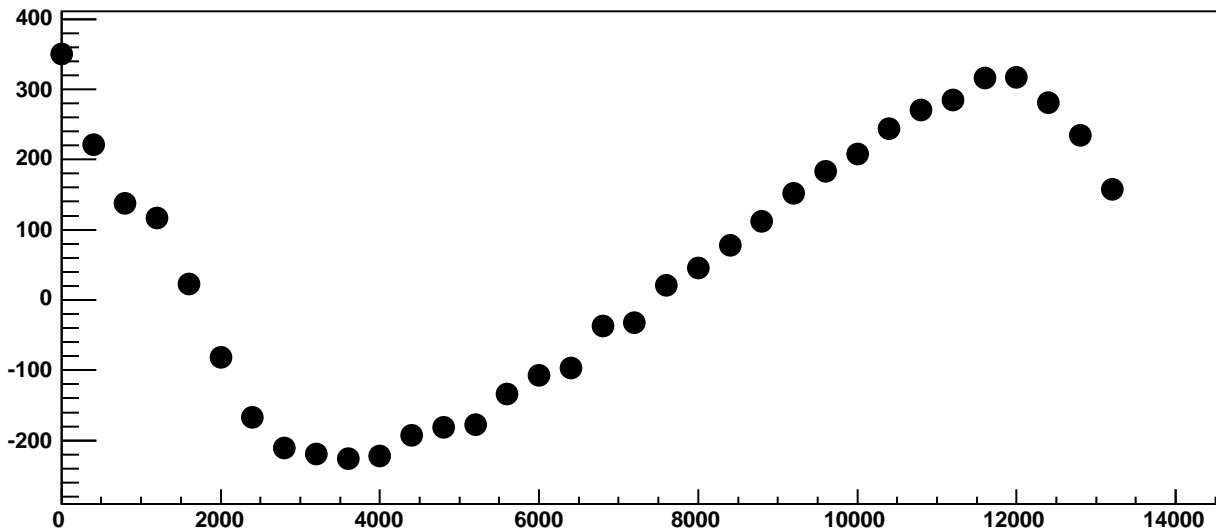
Chip 0, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC



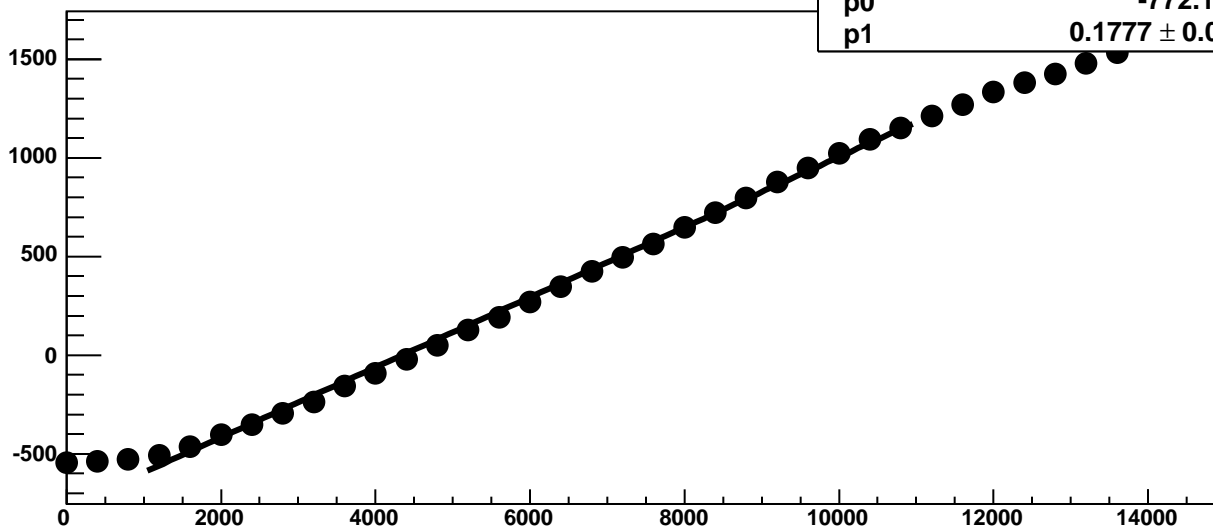
Chip 0, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC



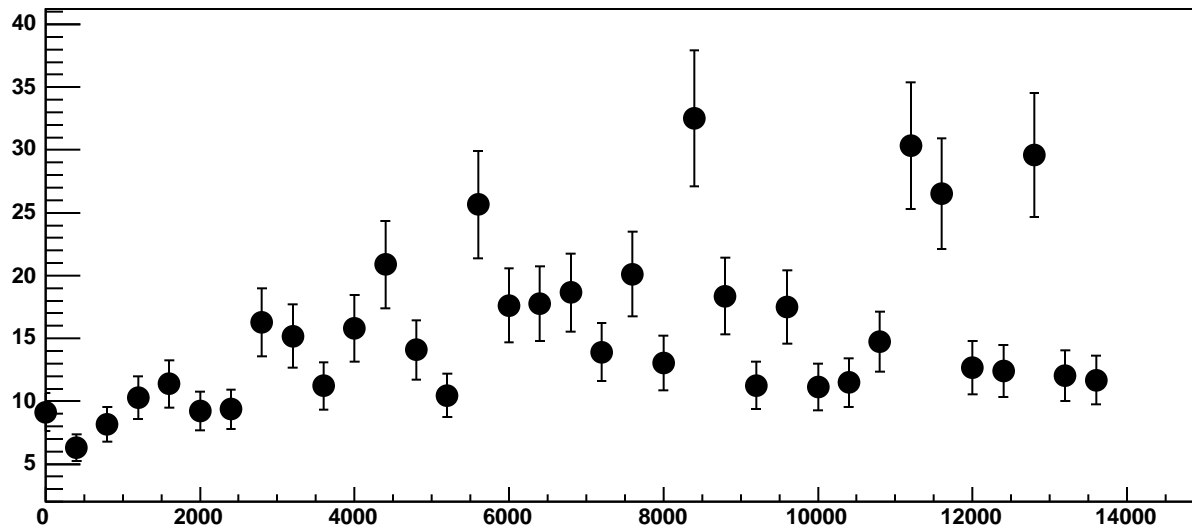
Chip 0, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC



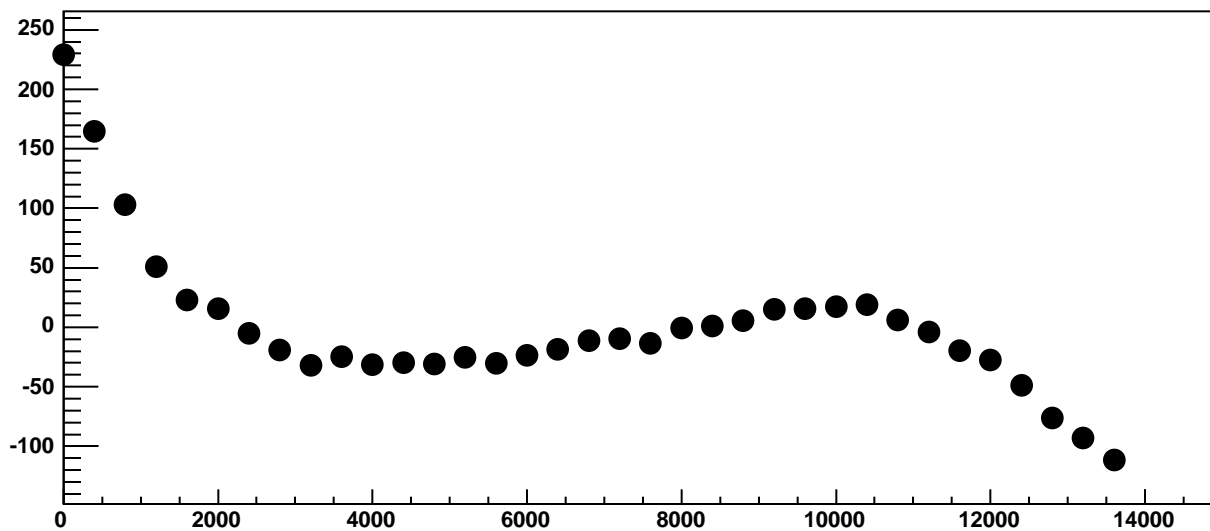
Chip 1, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC



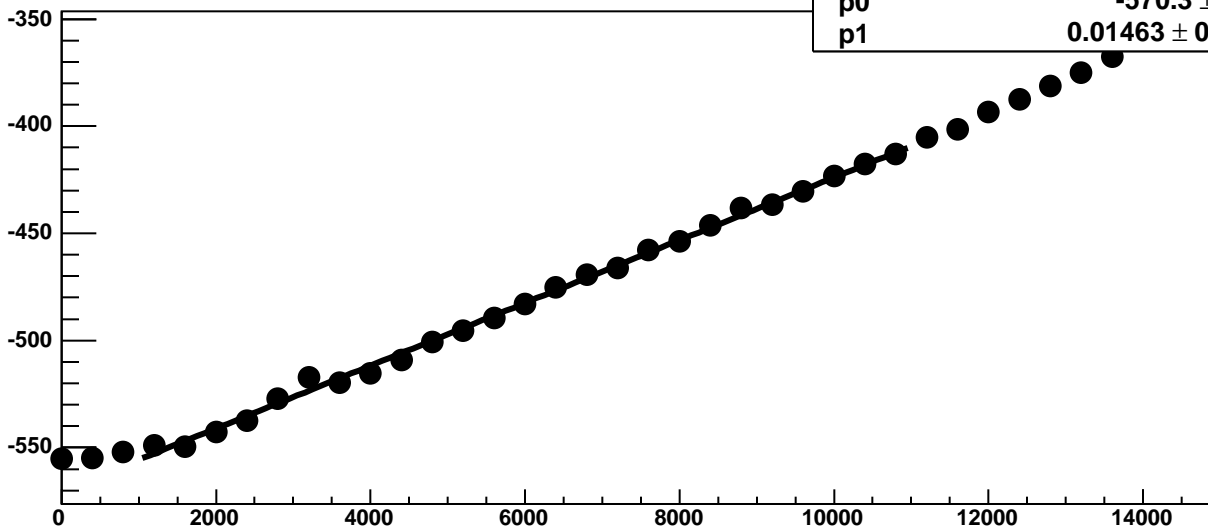
Chip 1, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



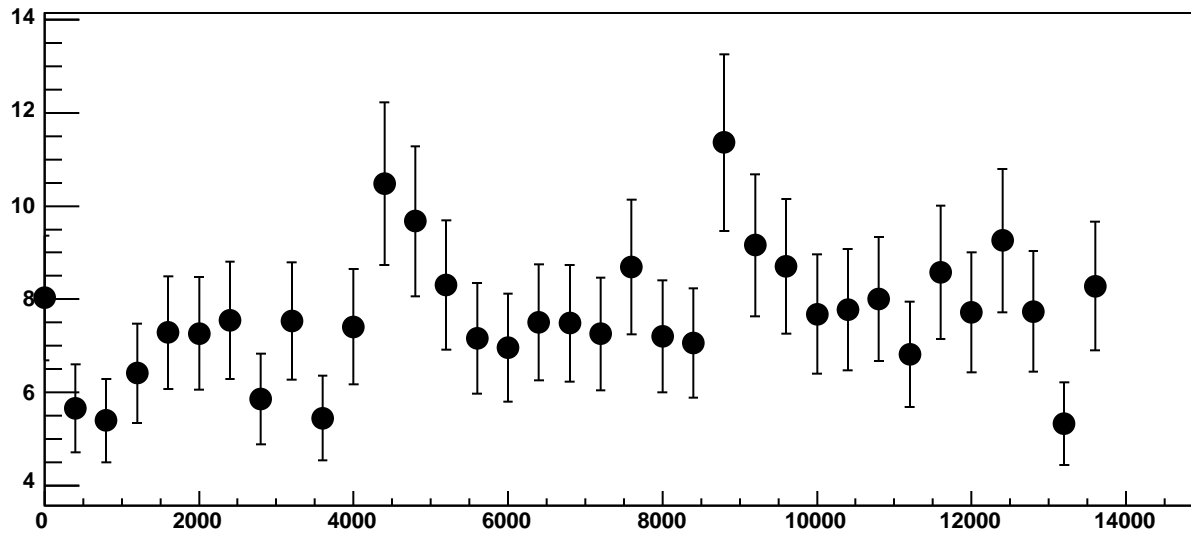
Chip 1, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC



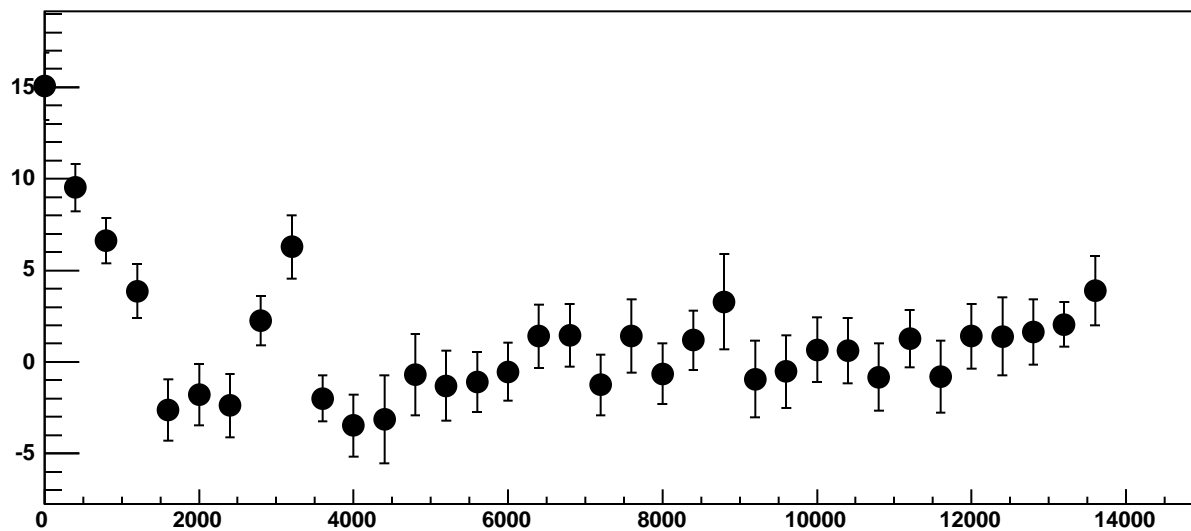
Chip 1, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC



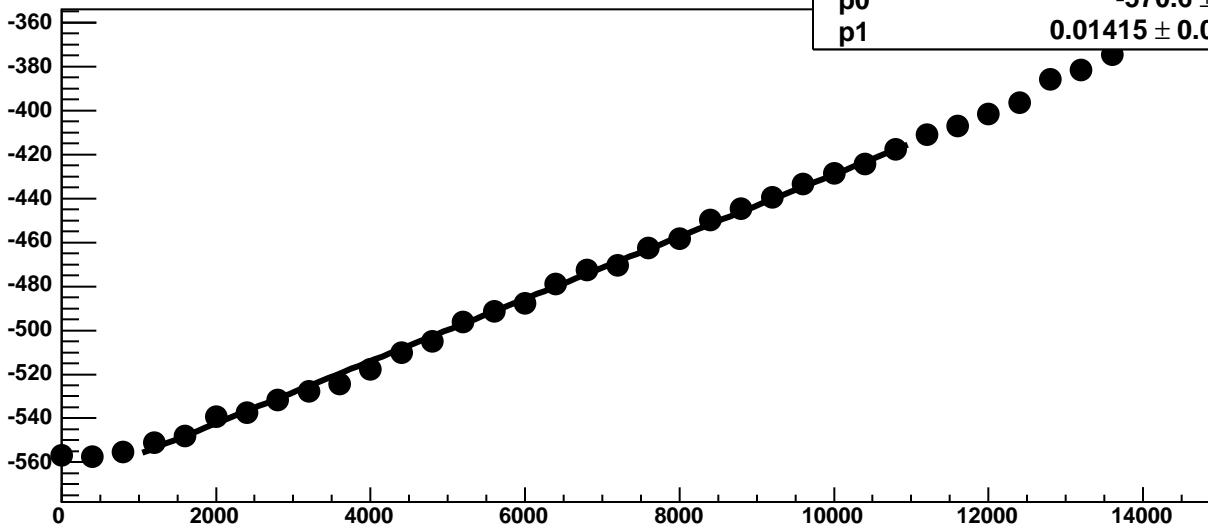
Chip 1, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

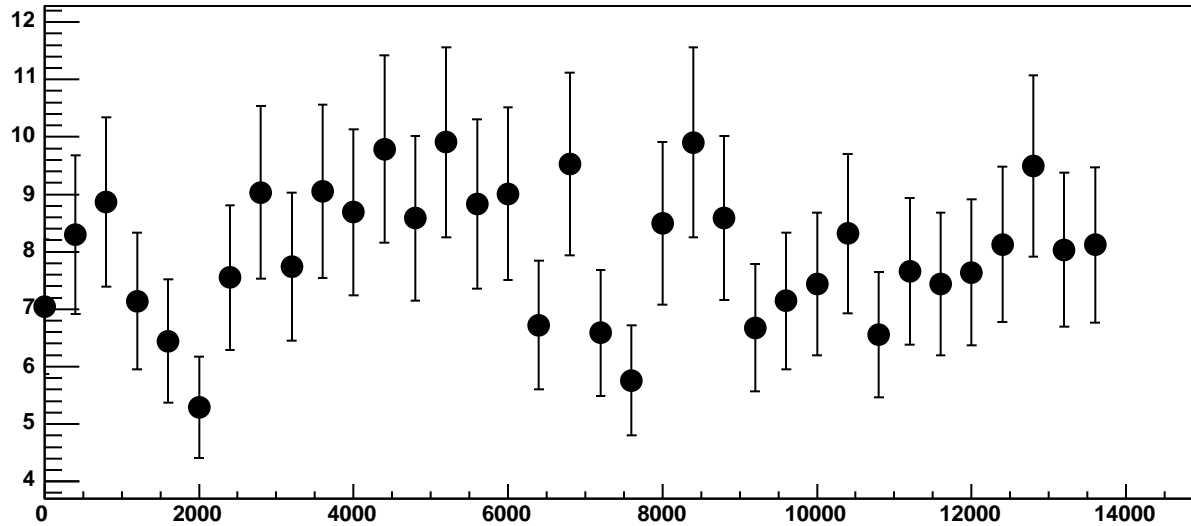


Chip 1, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

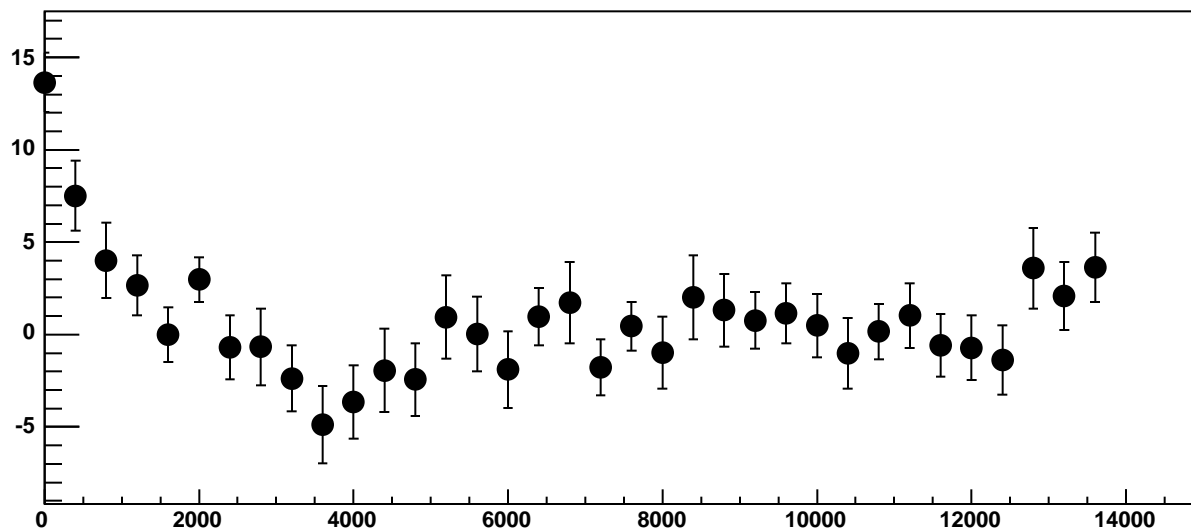


χ^2 / ndf 28.02 / 23
p0 -570.6 ± 0.7607
p1 0.01415 ± 0.0001136

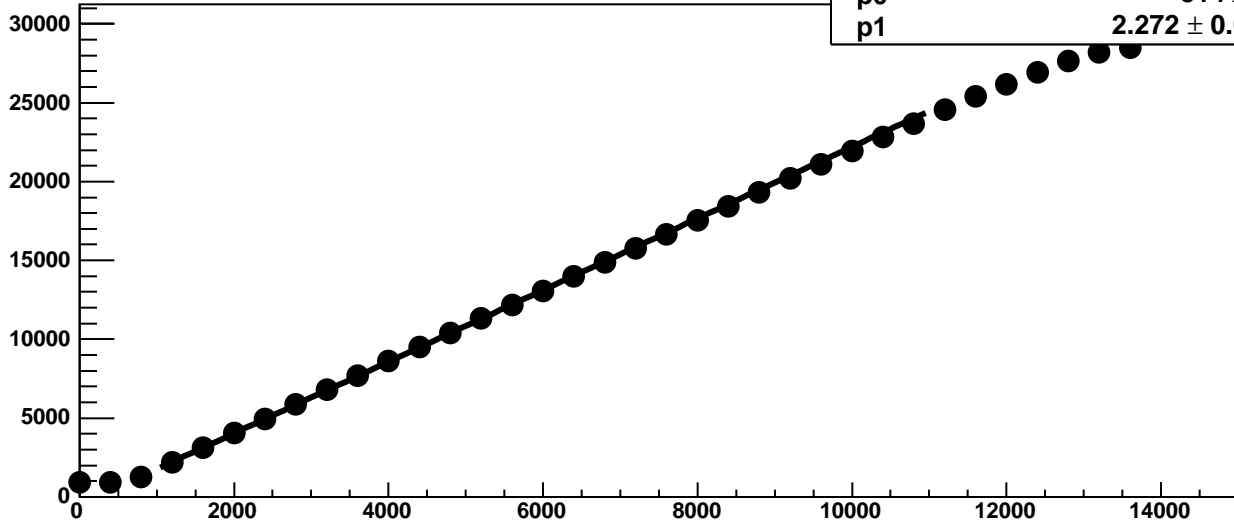
Chip 1, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

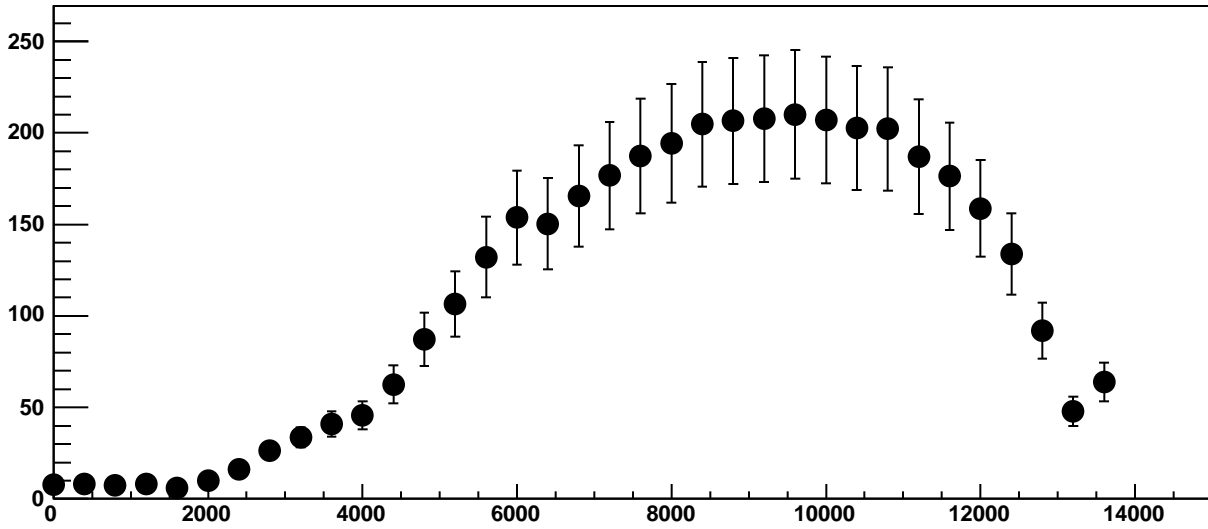


Chip 1, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC

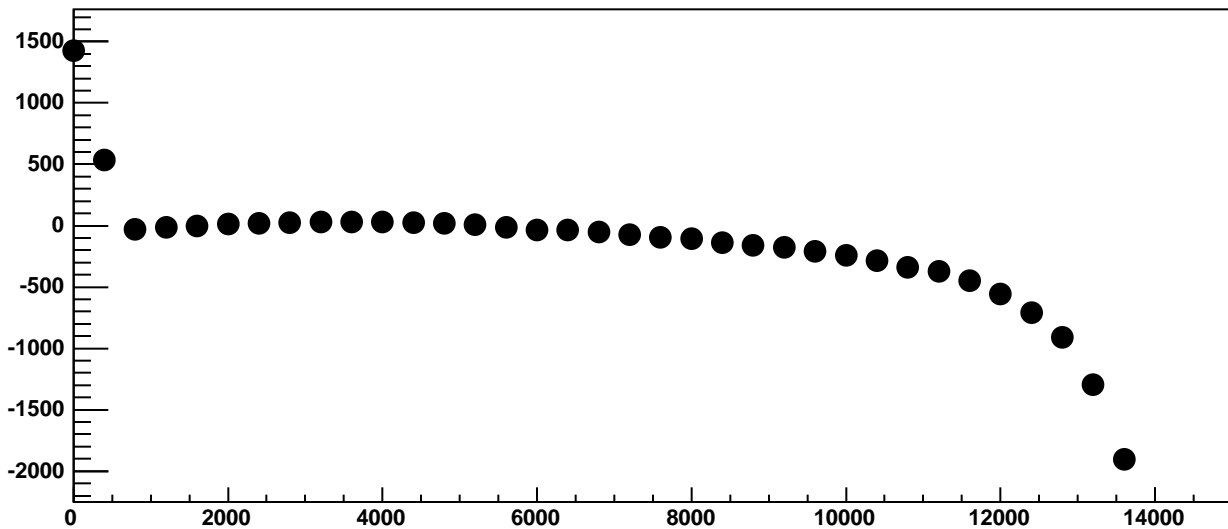


χ^2 / ndf 354.4 / 23
p0 -514 ± 2.326
p1 2.272 ± 0.001206

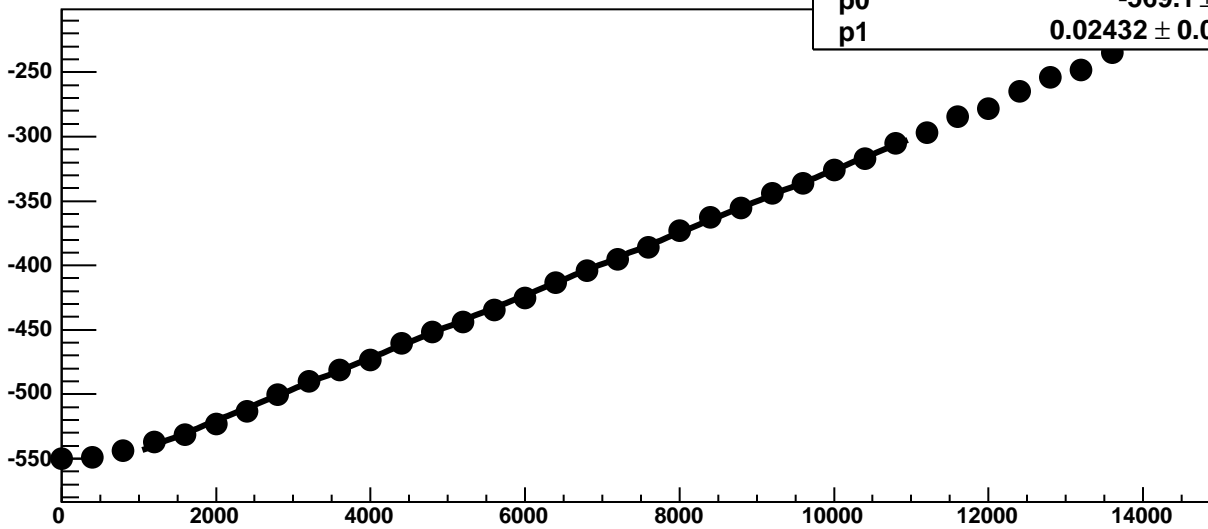
Chip 1, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC

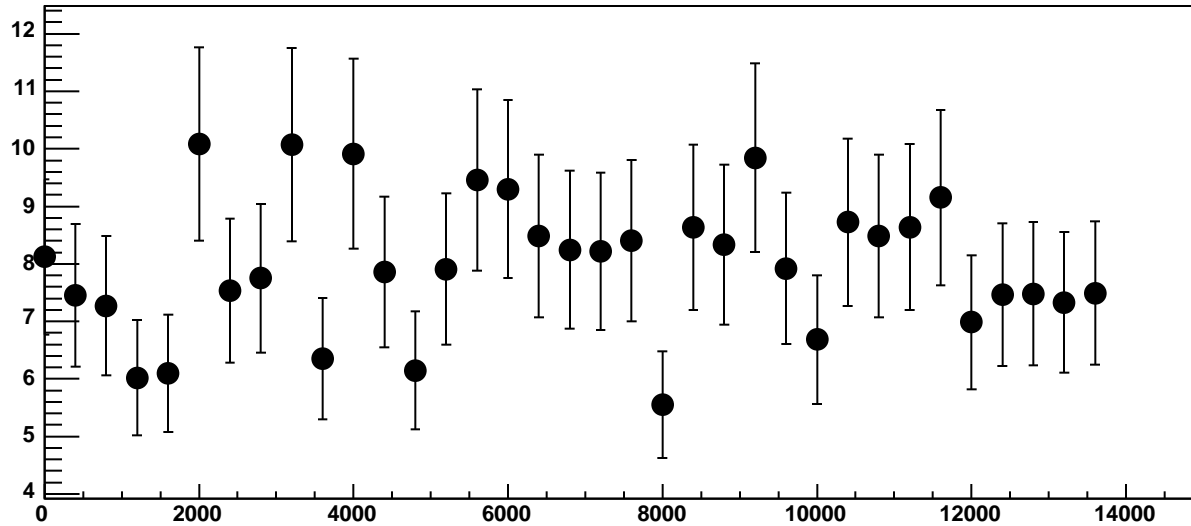


Chip 1, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

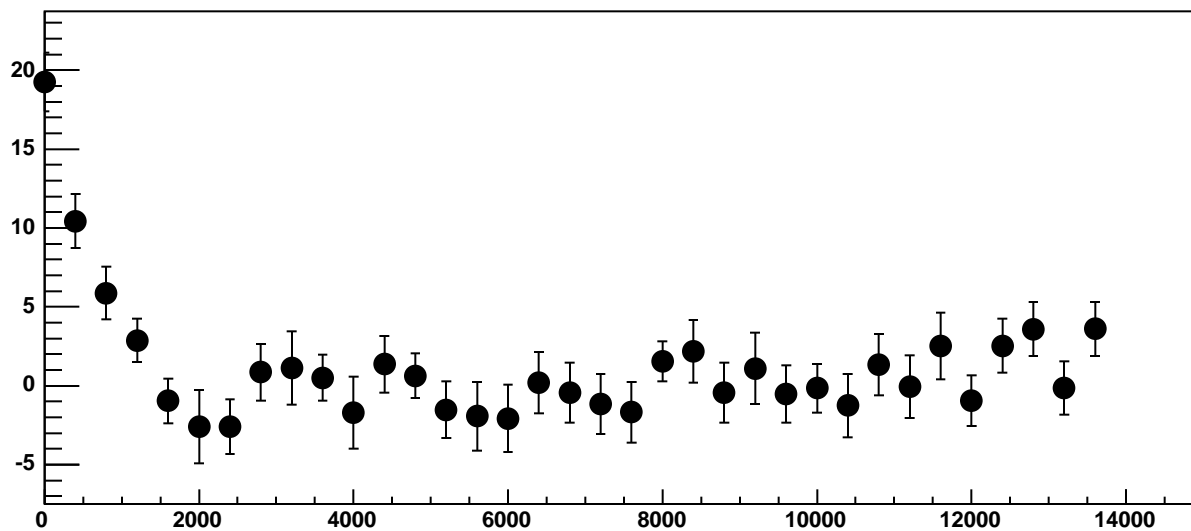


χ^2 / ndf 17.74 / 23
p0 -569.1 ± 0.7776
p1 0.02432 ± 0.0001196

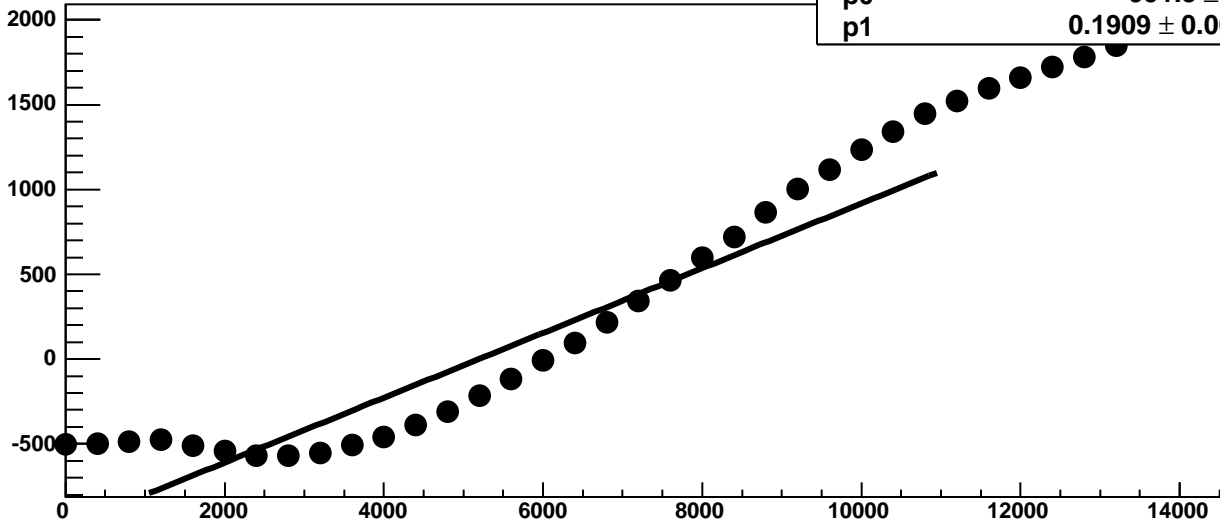
Chip 1, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



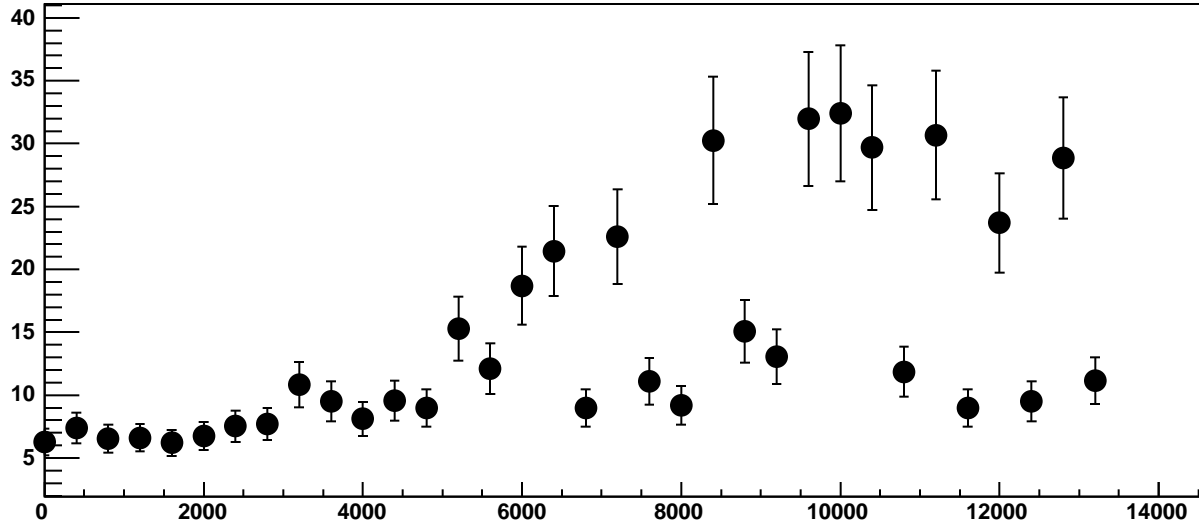
Chip 1, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



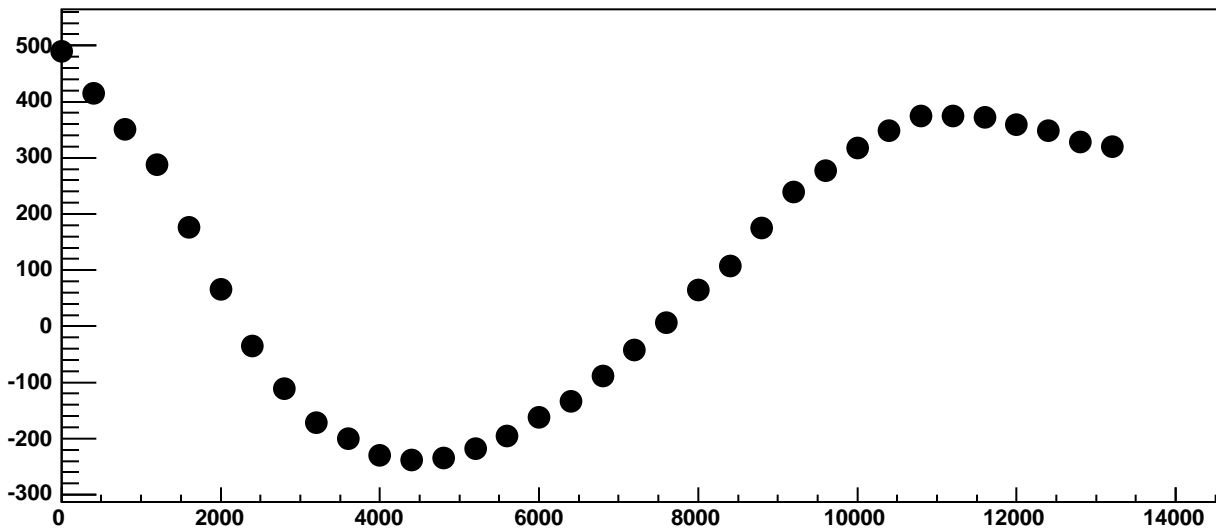
Chip 1, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



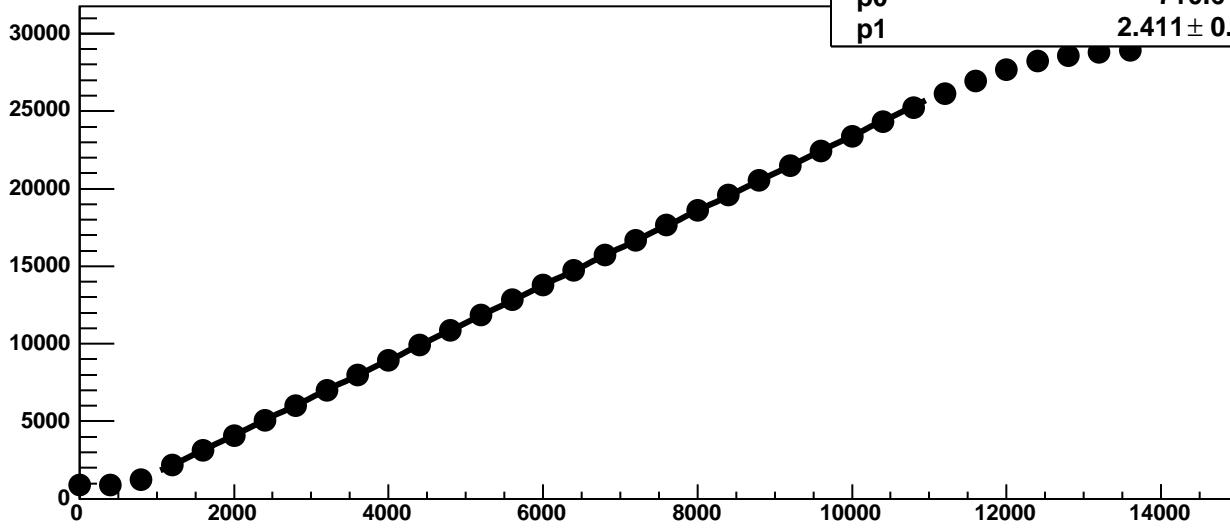
Chip 1, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



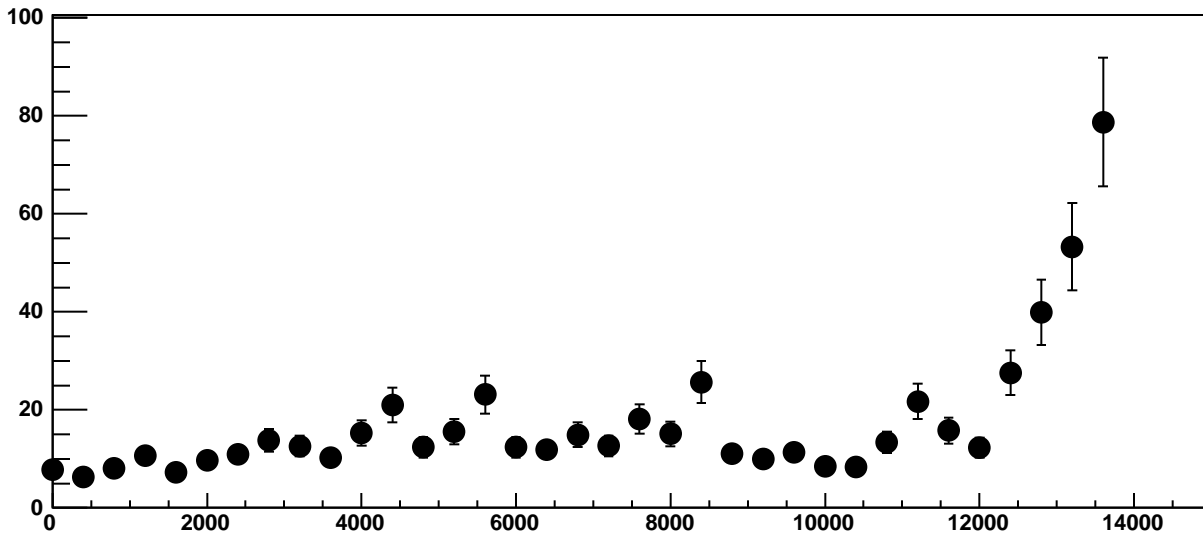
Chip 1, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



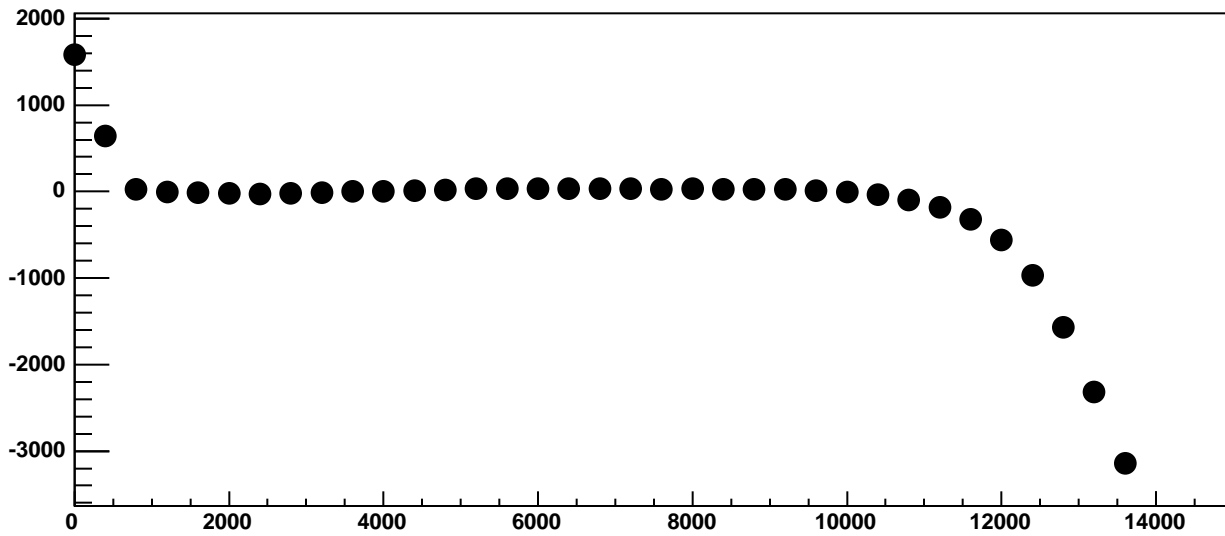
Chip 1, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



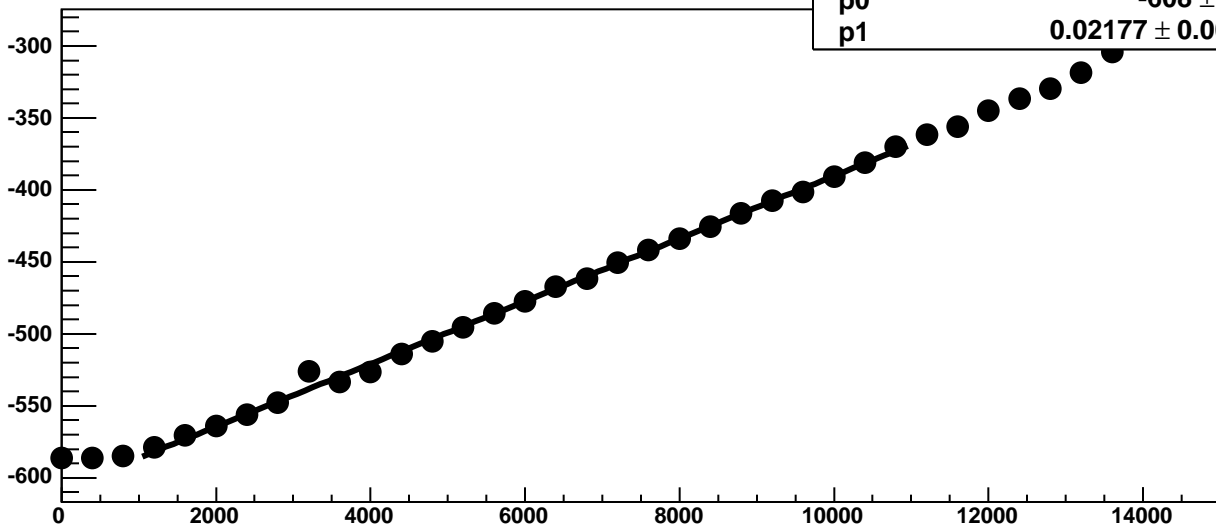
Chip 1, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC

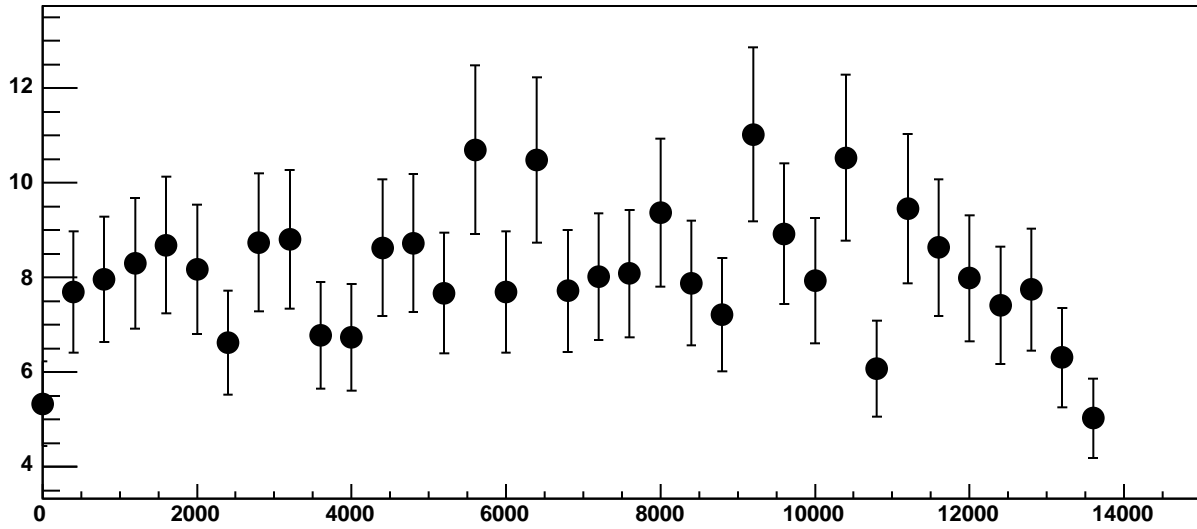


Chip 1, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

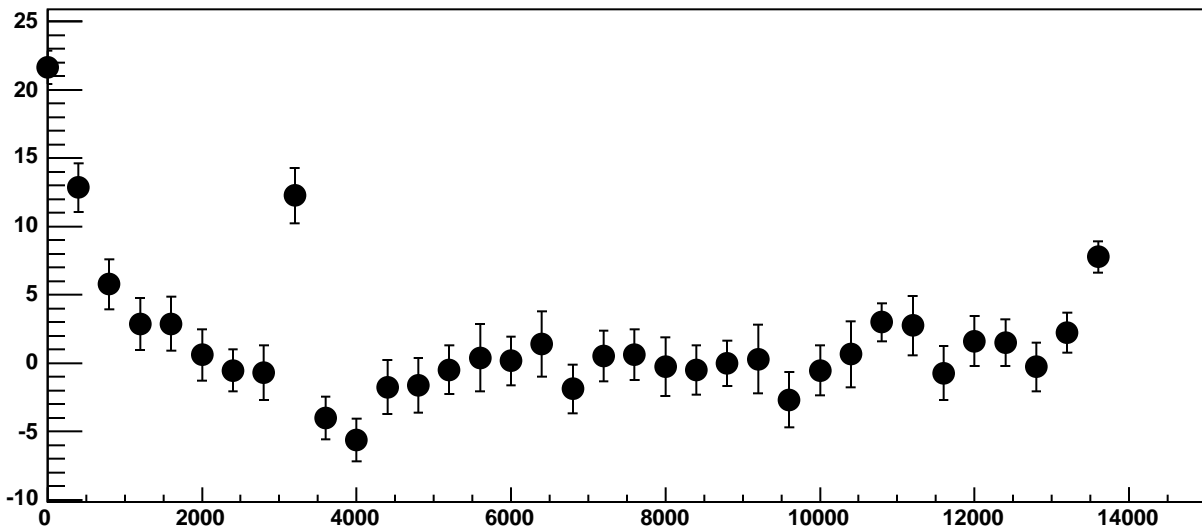


χ^2 / ndf 71.28 / 23
p0 -608 ± 0.8383
p1 0.02177 ± 0.0001262

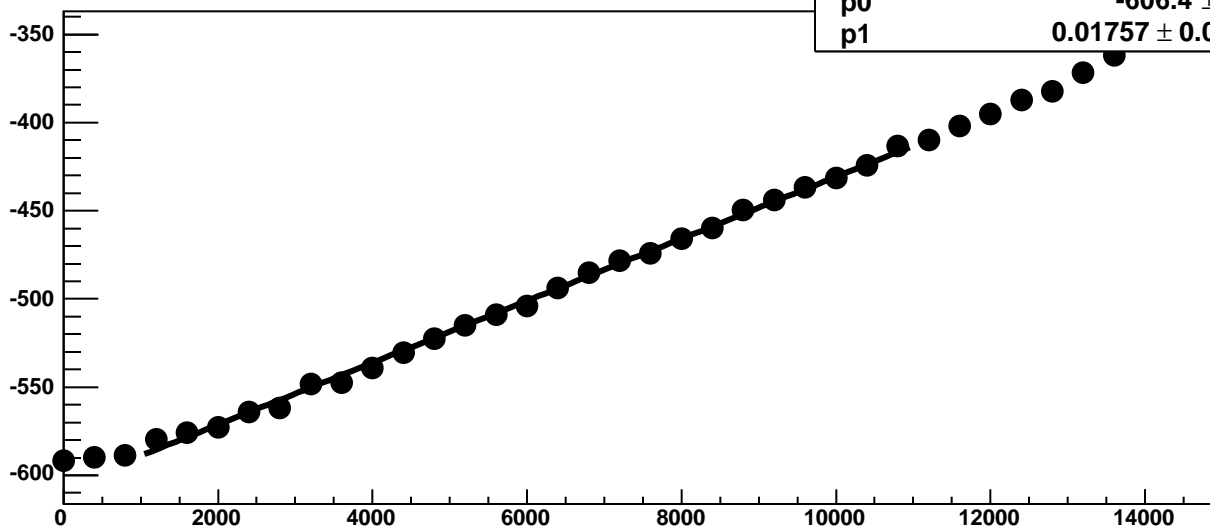
Chip 1, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

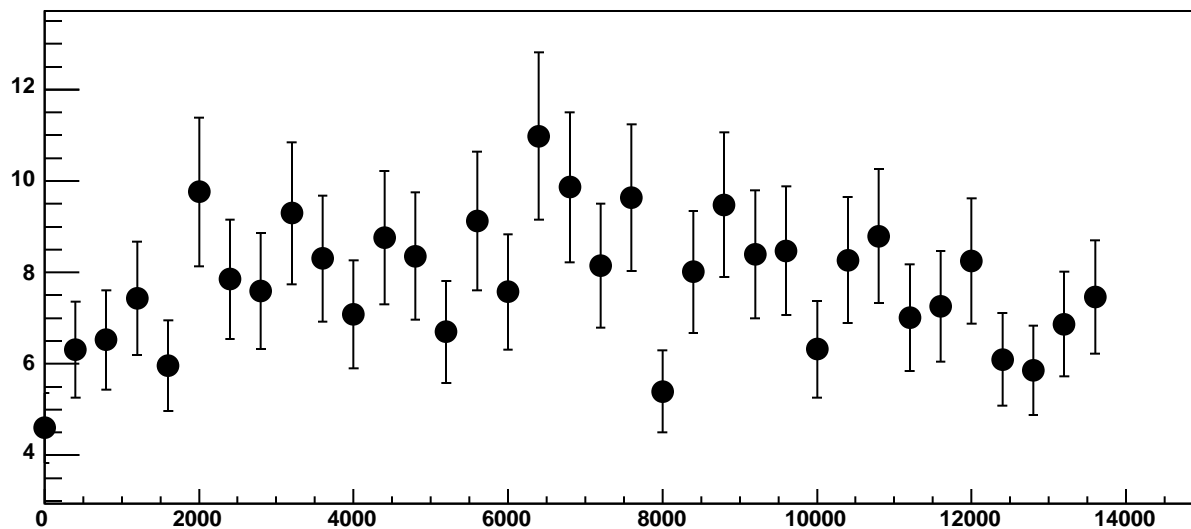


Chip 1, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

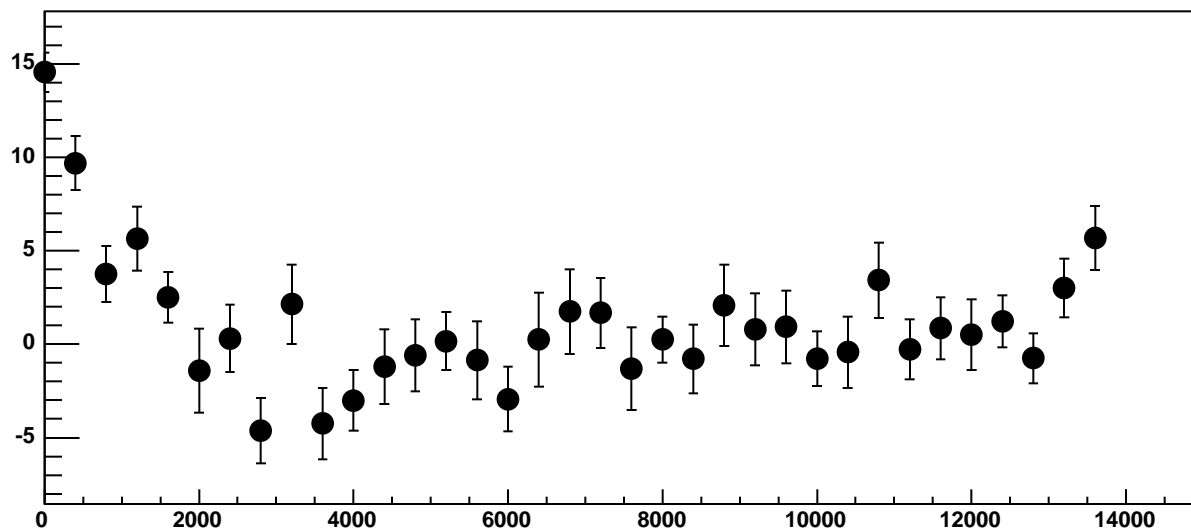


χ^2 / ndf 41.22 / 23
p0 -606.4 ± 0.8106
p1 0.01757 ± 0.0001223

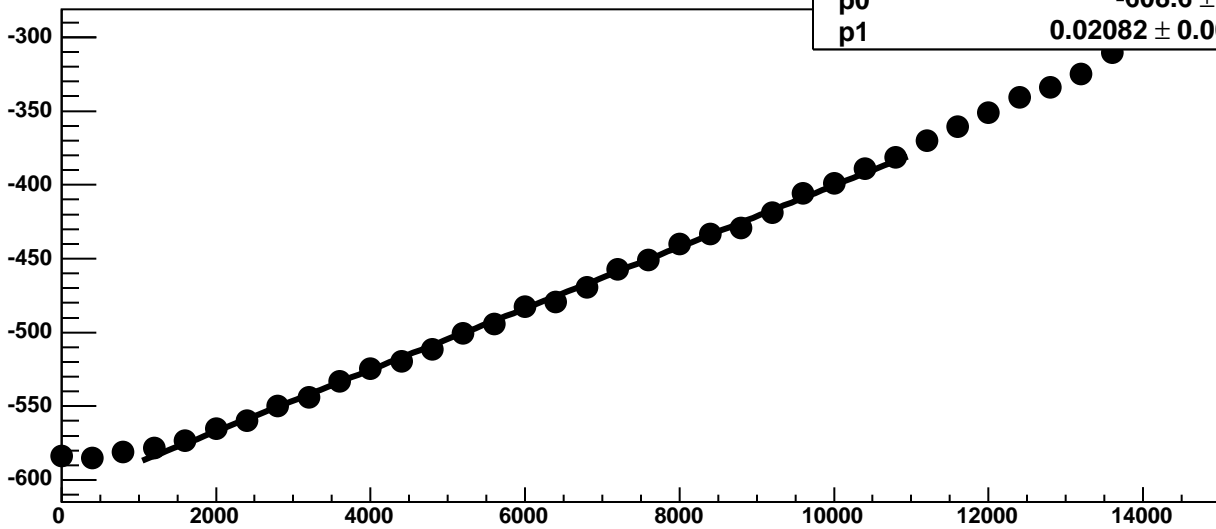
Chip 1, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

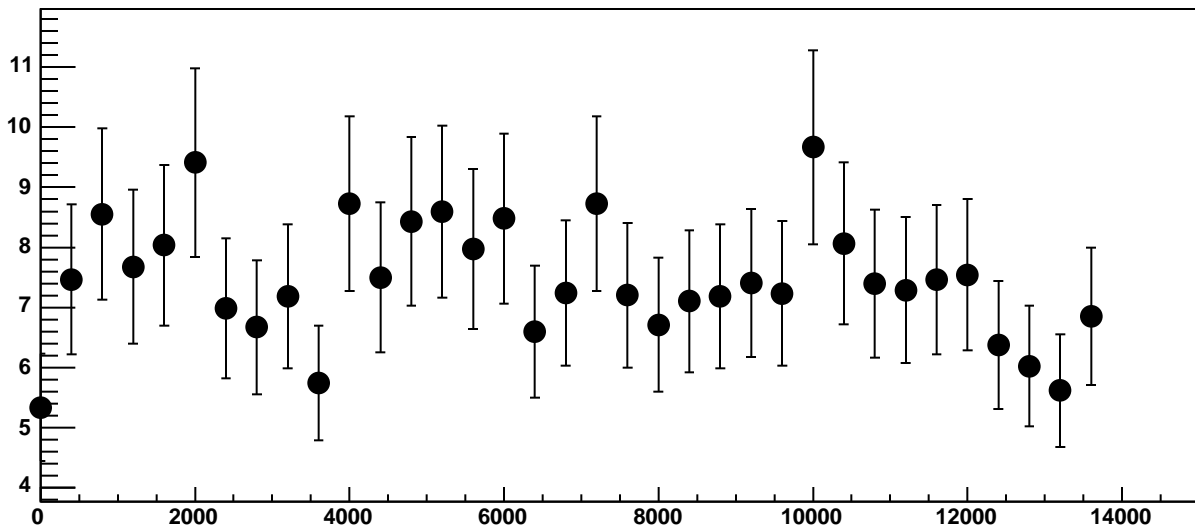


Chip 1, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC

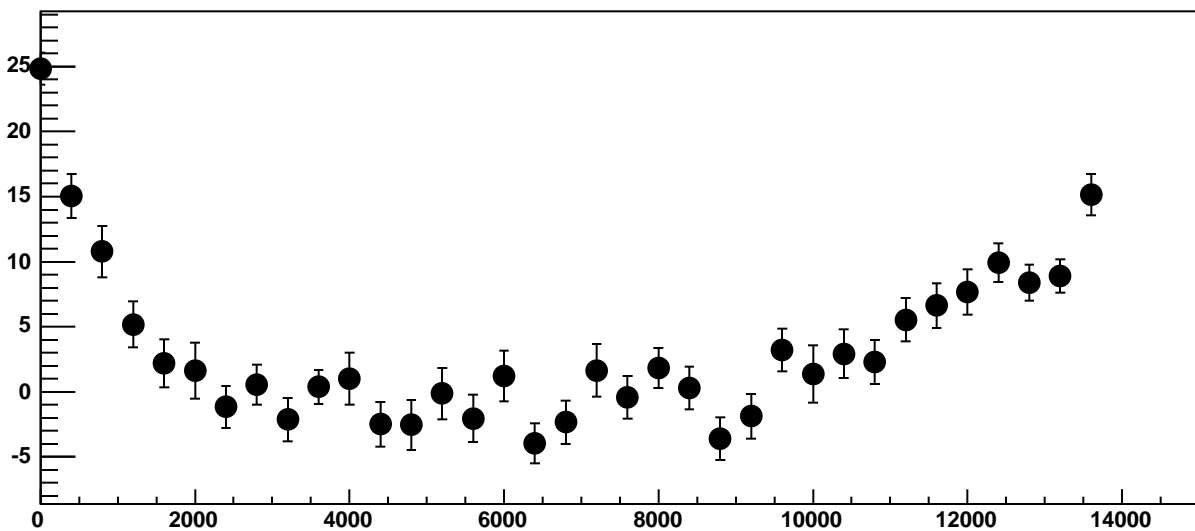


χ^2 / ndf 44.23 / 23
p0 -608.6 ± 0.8013
p1 0.02082 ± 0.0001212

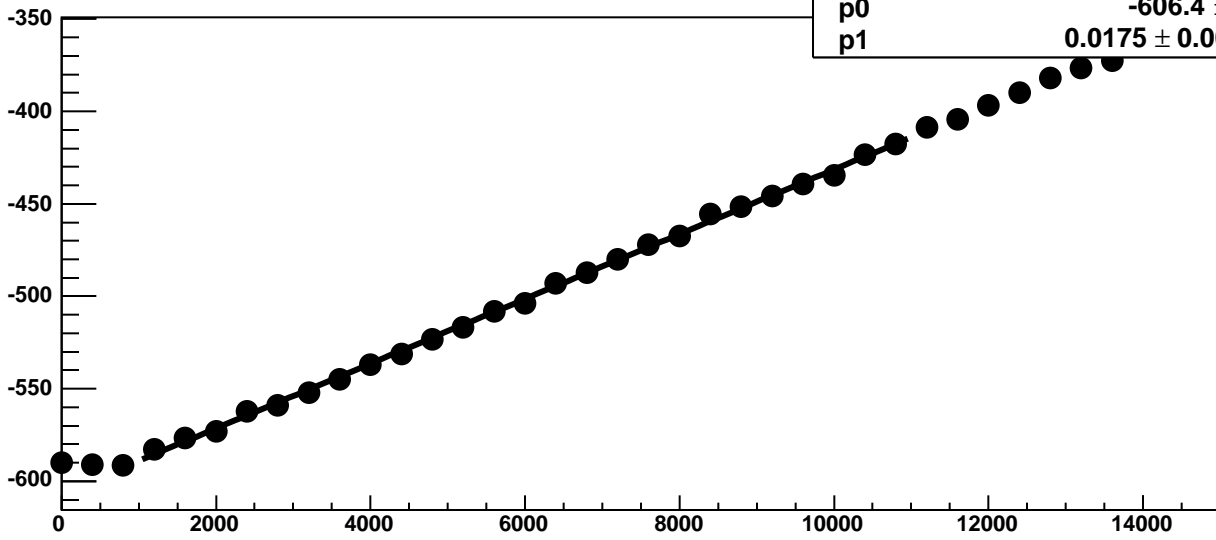
Chip 1, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC

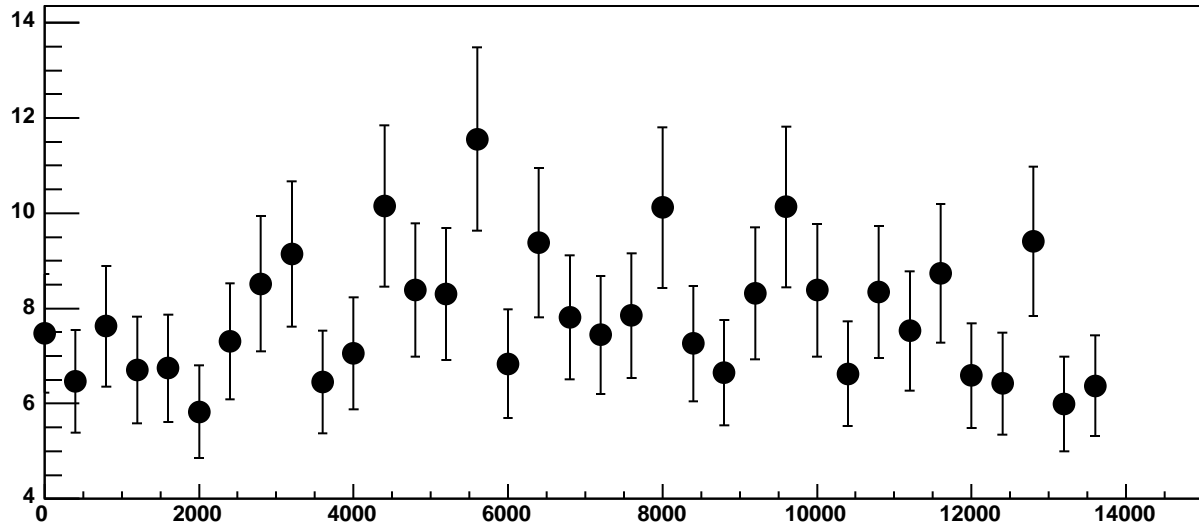


Chip 1, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

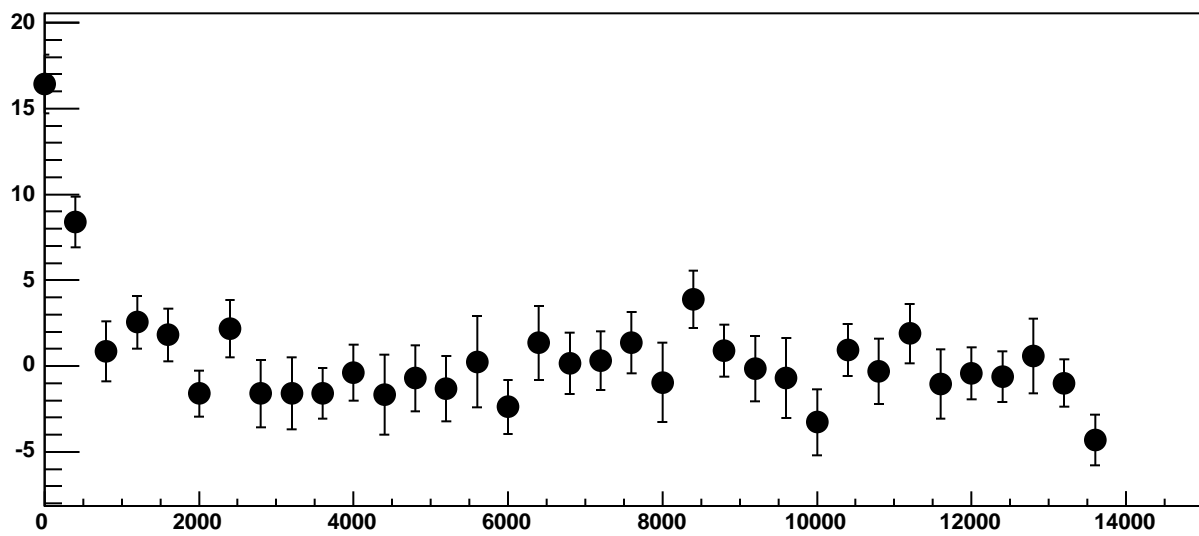


χ^2 / ndf 23.54 / 23
p0 -606.4 ± 0.762
p1 0.0175 ± 0.0001175

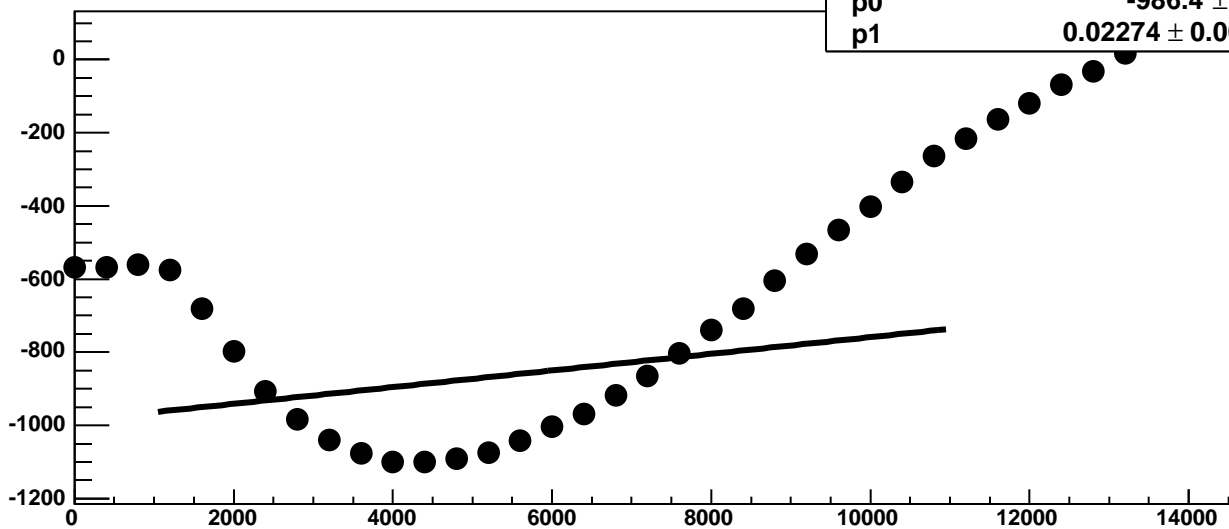
Chip 1, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

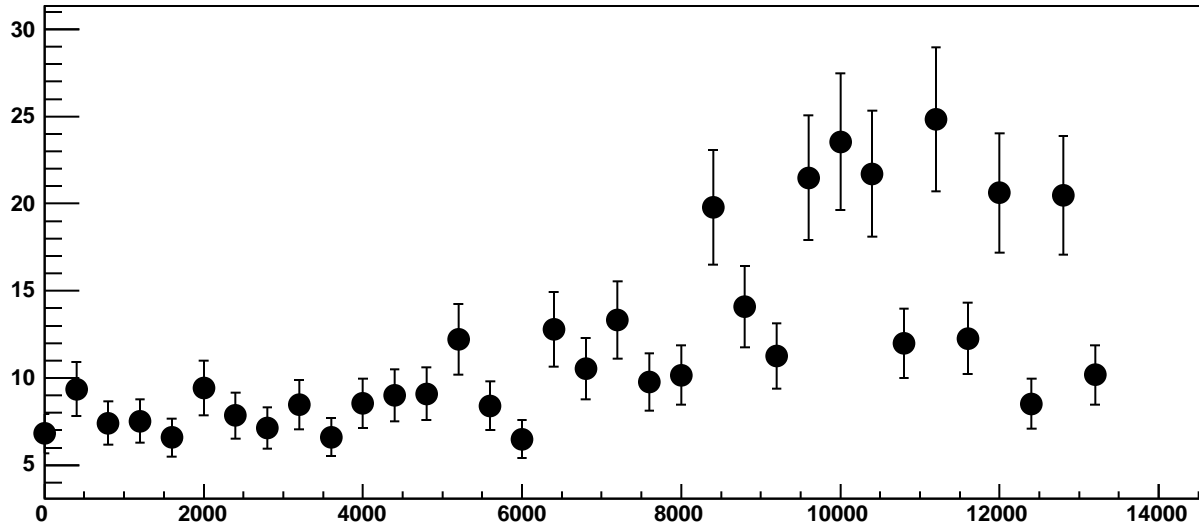


Chip 1, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

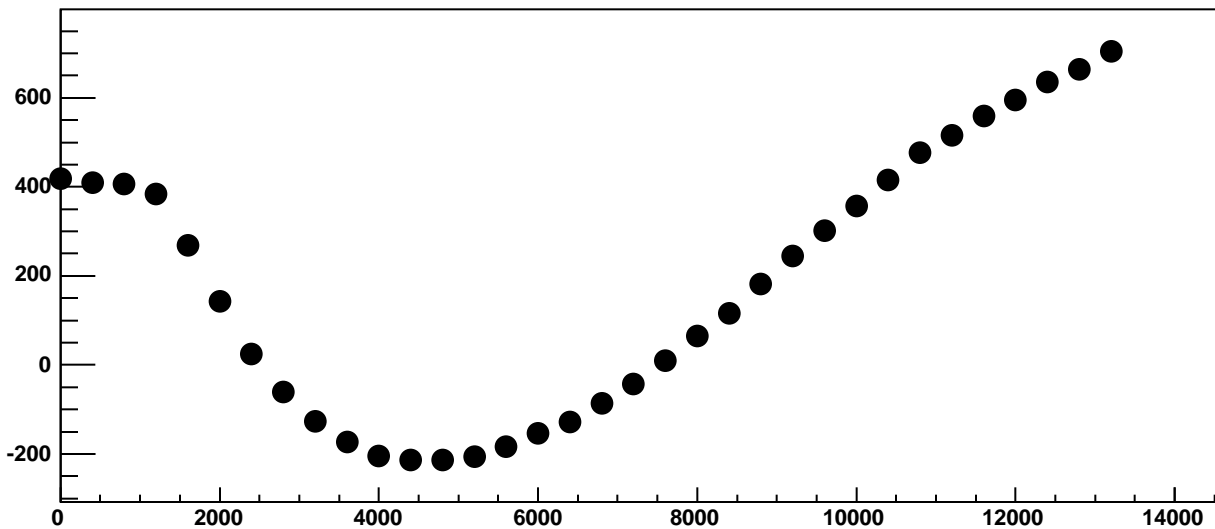


χ^2 / ndf 2.234e+05 / 23
p0 -986.4 ± 0.9135
p1 0.02274 ± 0.0001713

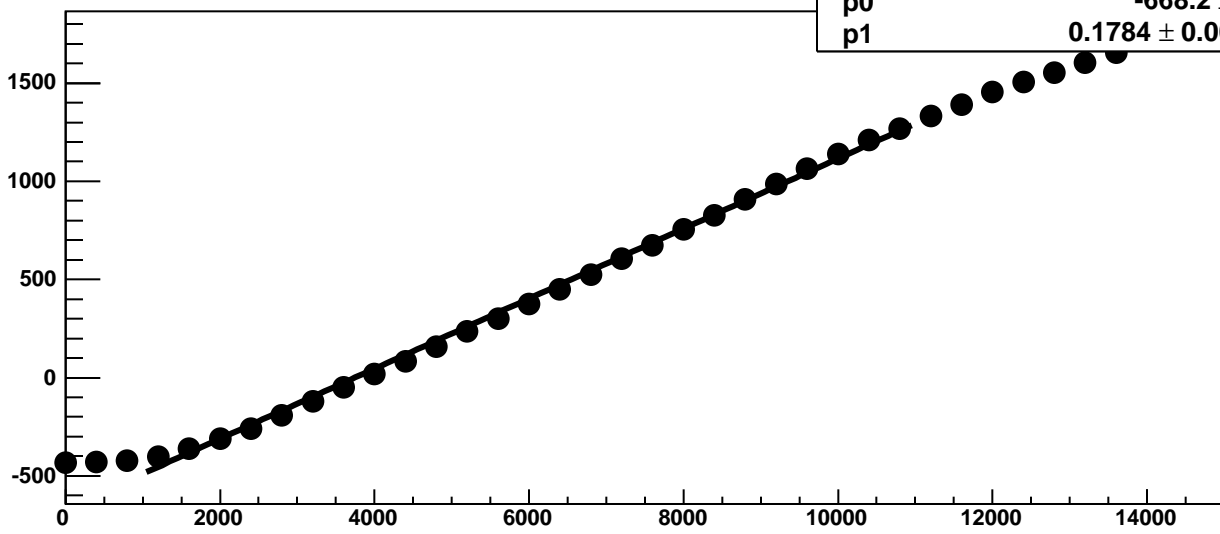
Chip 1, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

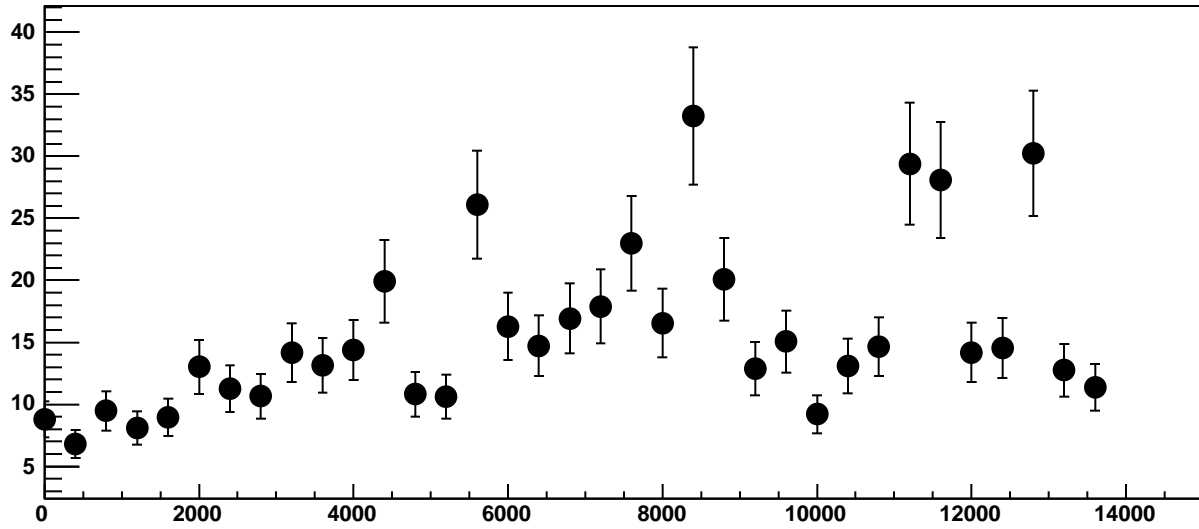


Chip 1, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC

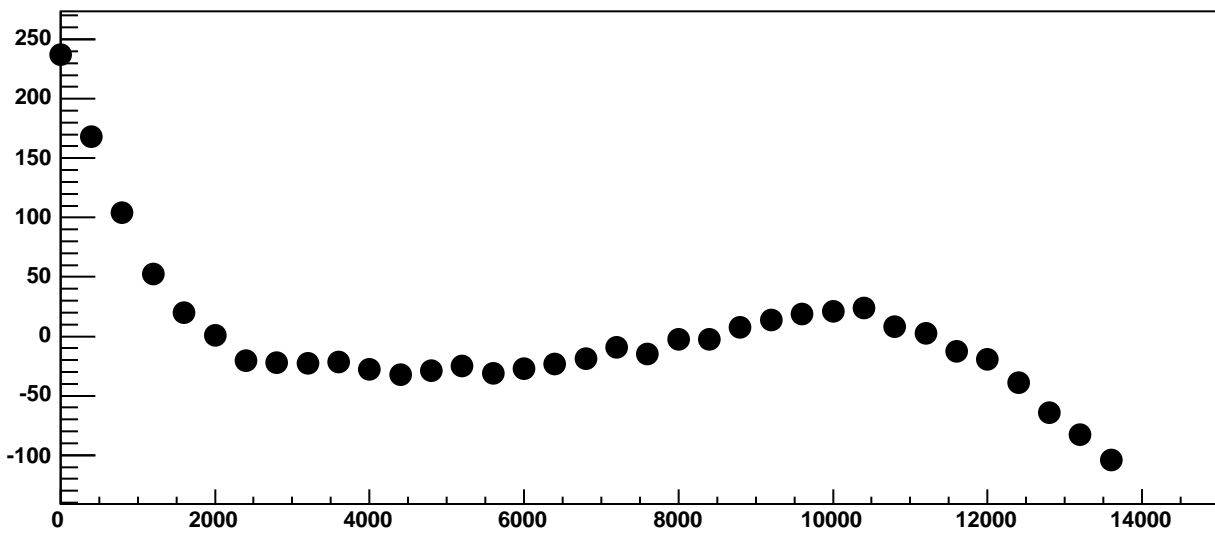


χ^2 / ndf 1884 / 23
p0 -668.2 ± 1.154
p1 0.1784 ± 0.0001874

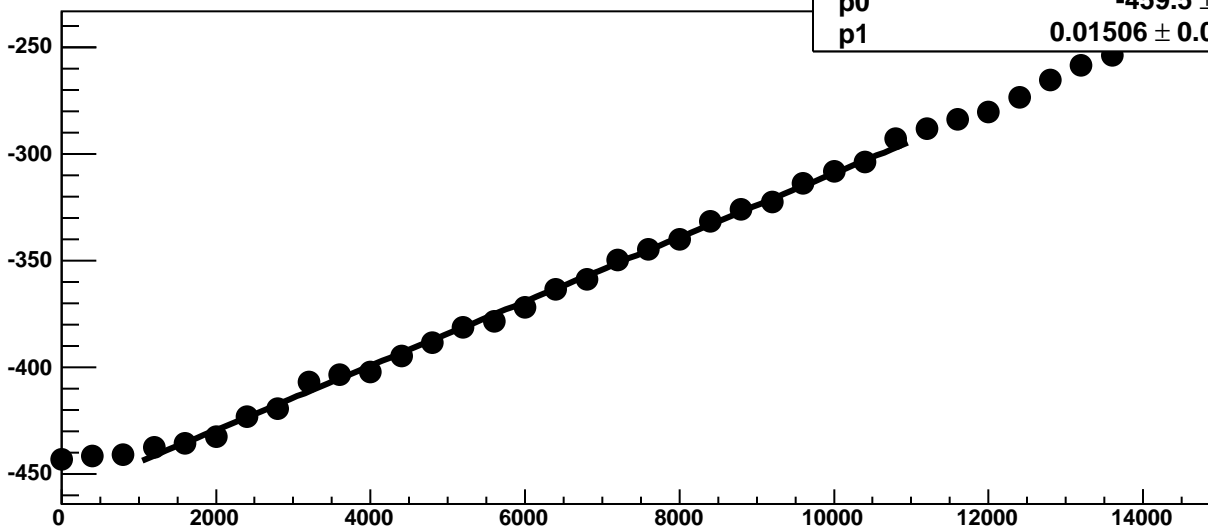
Chip 1, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



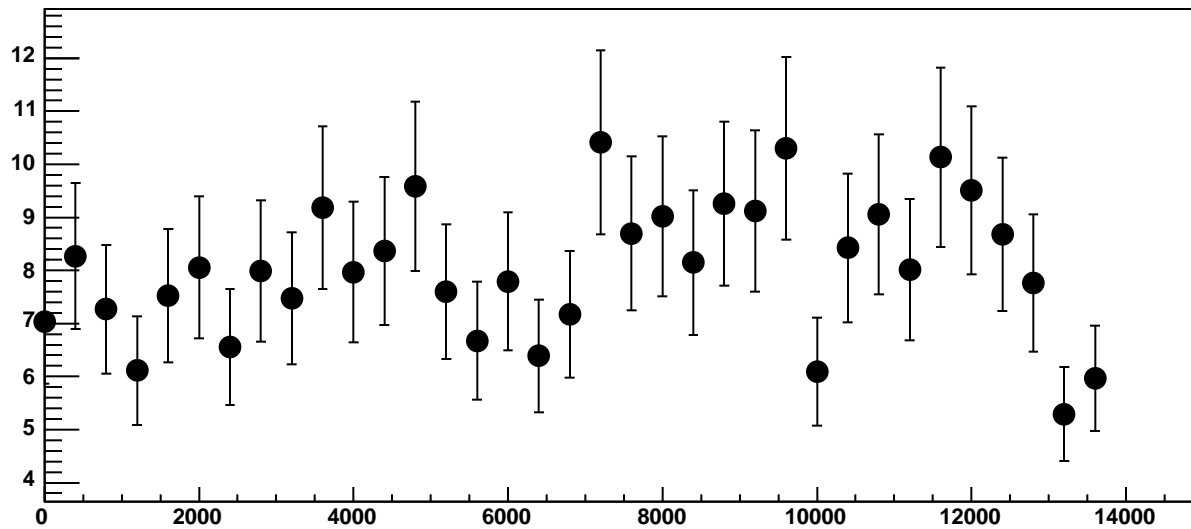
Chip 1, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC



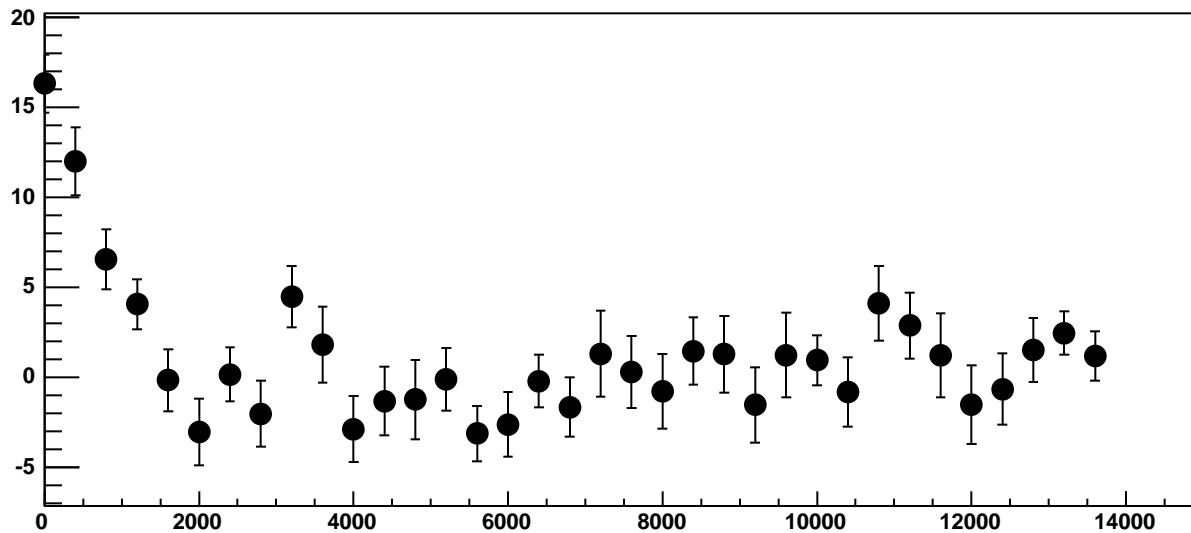
Chip 1, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



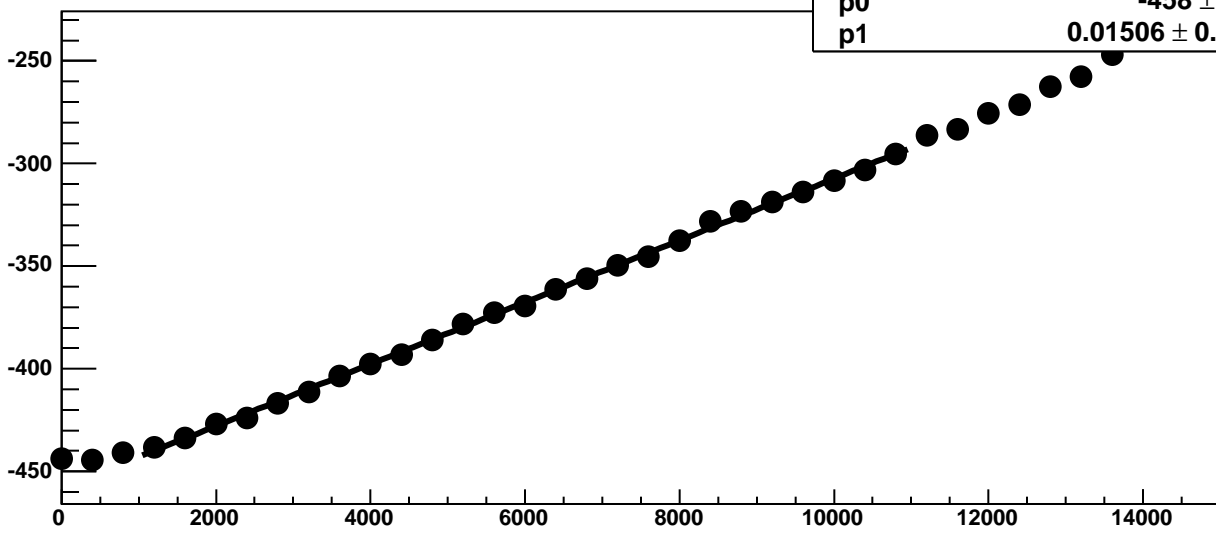
Chip 1, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

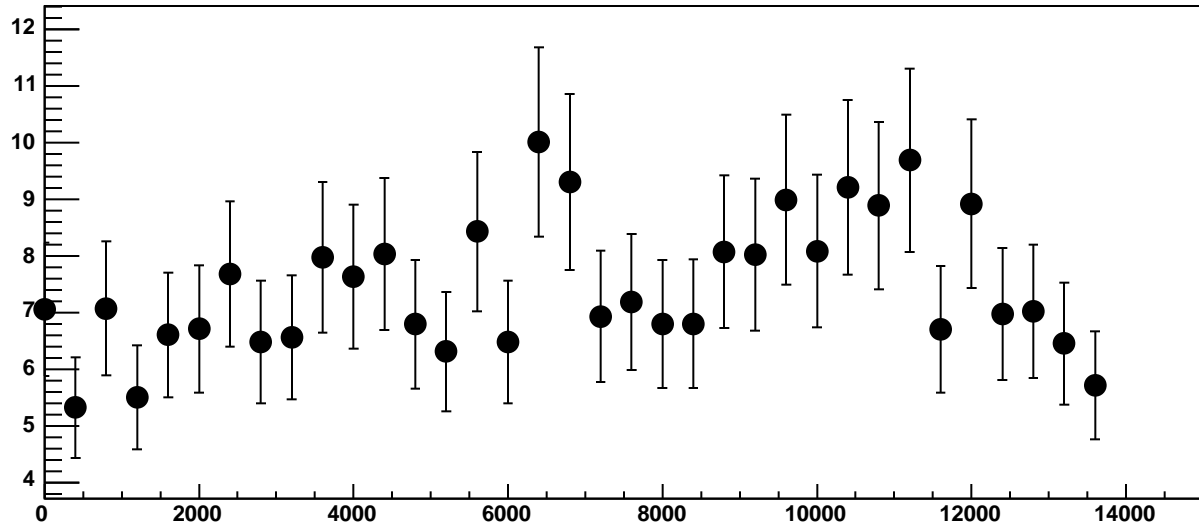


Chip 1, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

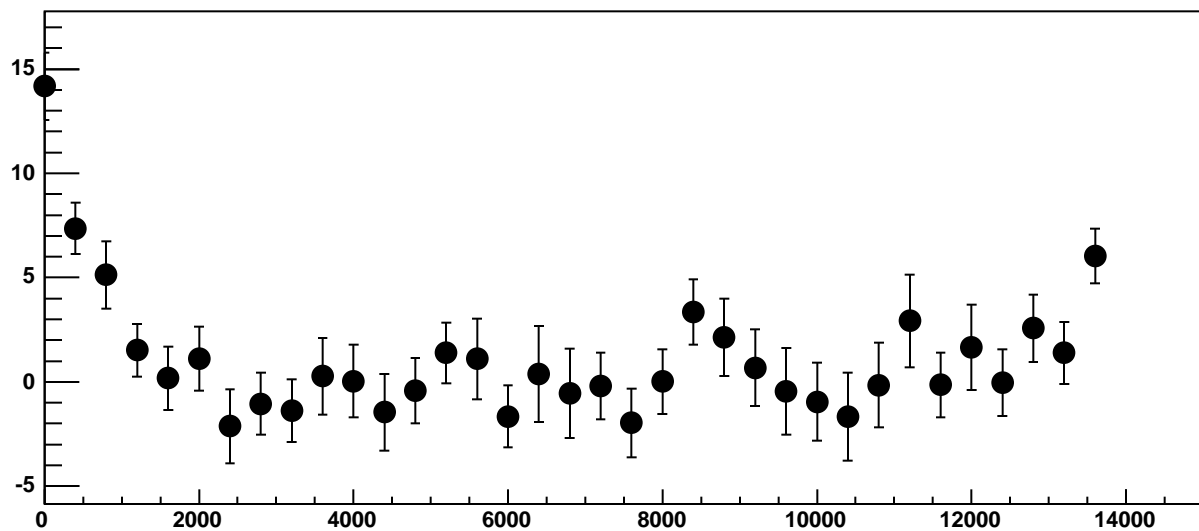


χ^2 / ndf 16.51 / 23
p0 -458 ± 0.7284
p1 0.01506 ± 0.000117

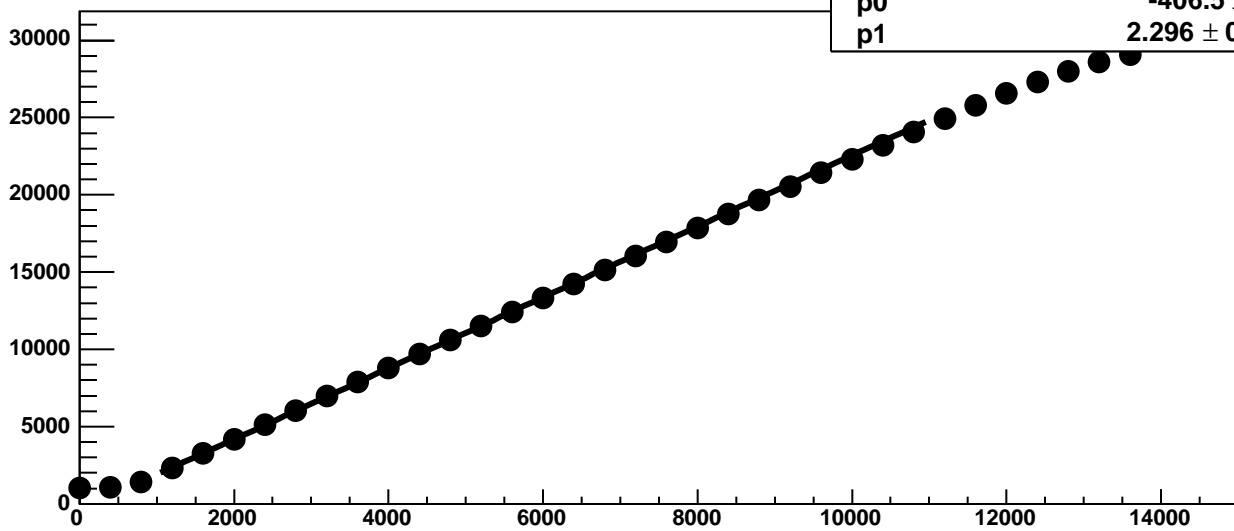
Chip 1, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

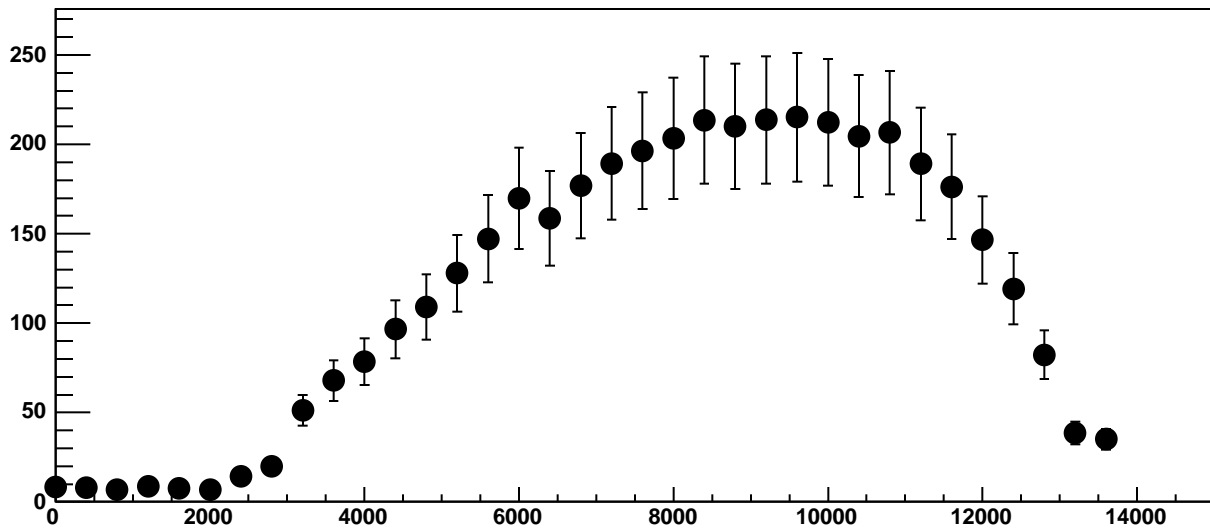


Chip 1, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC

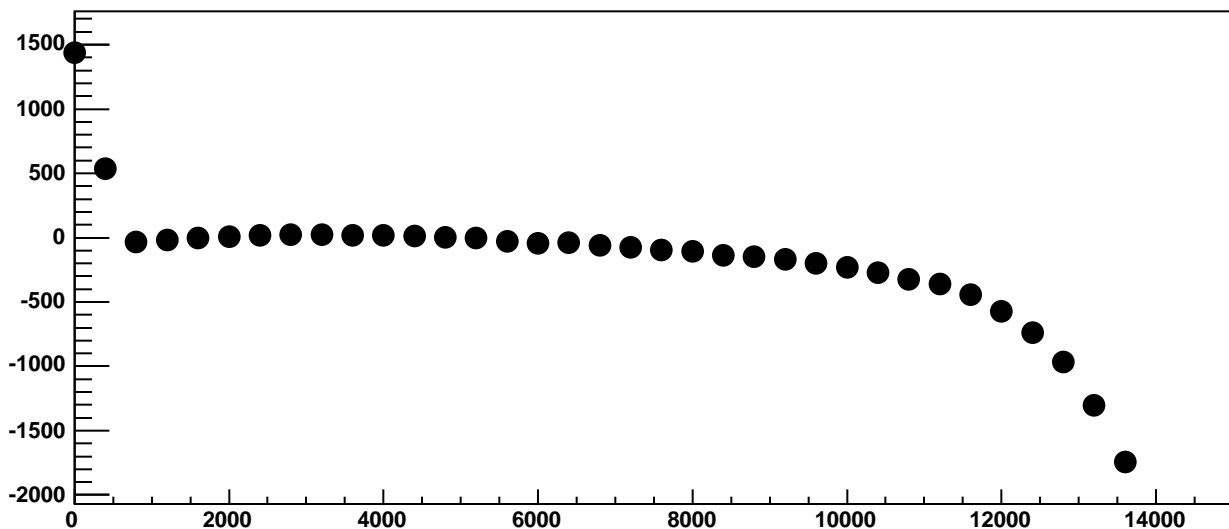


χ^2 / ndf 350.4 / 23
p0 -406.5 ± 2.607
p1 2.296 ± 0.00132

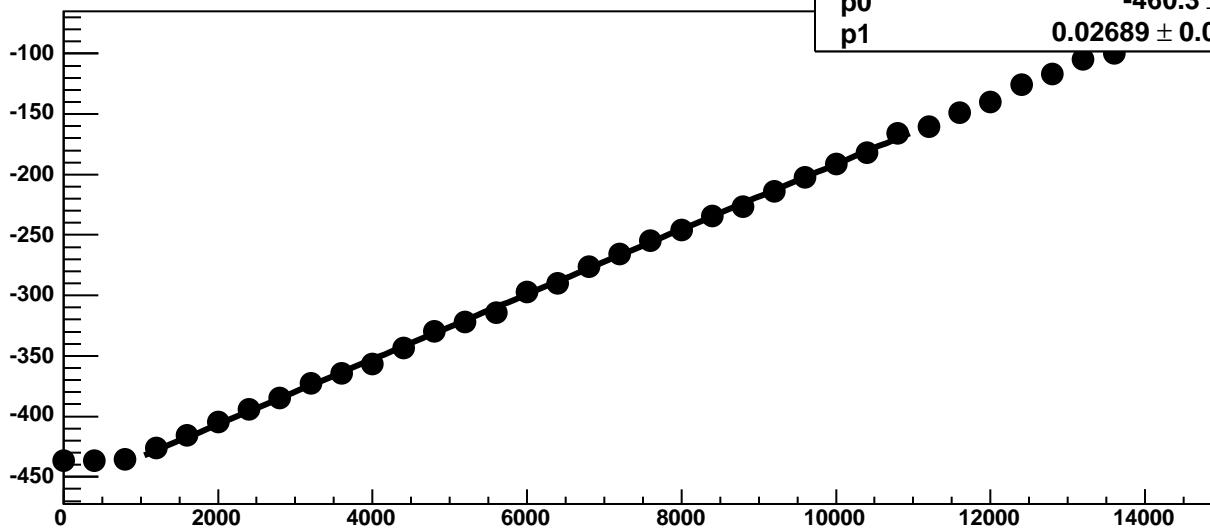
Chip 1, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



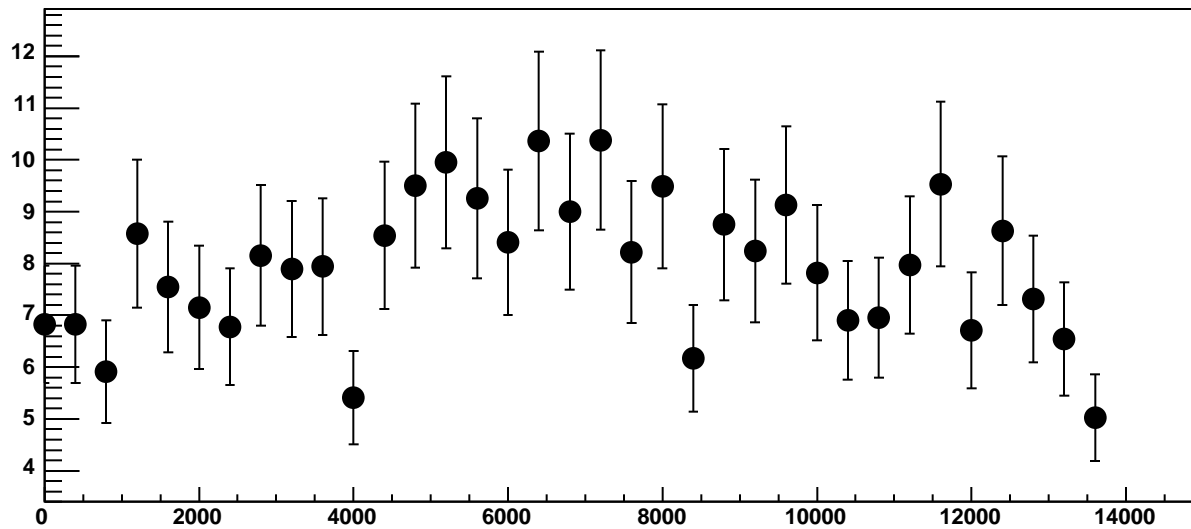
Chip 1, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC



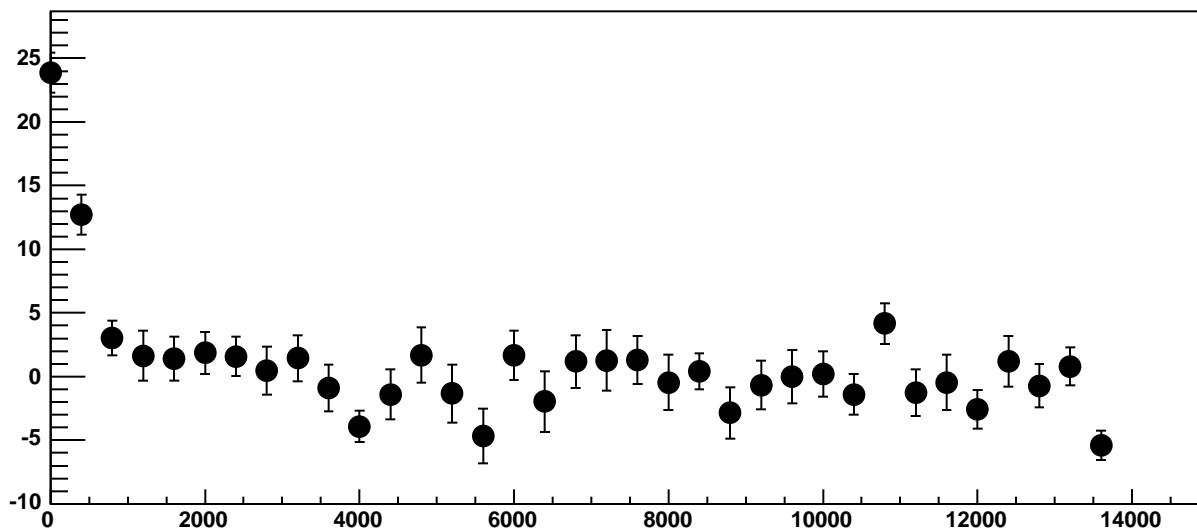
Chip 1, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC



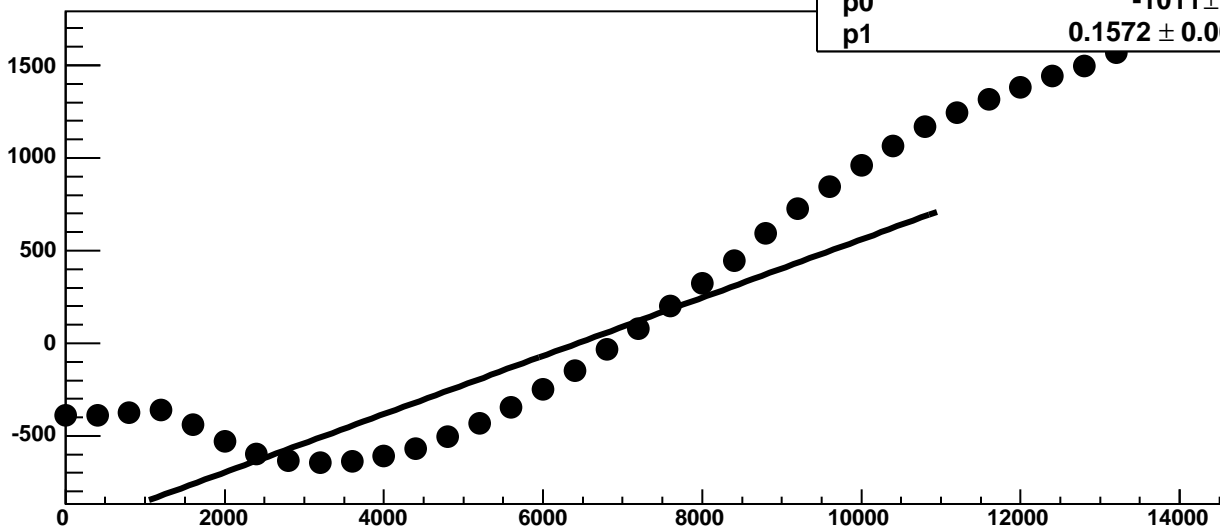
Chip 1, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



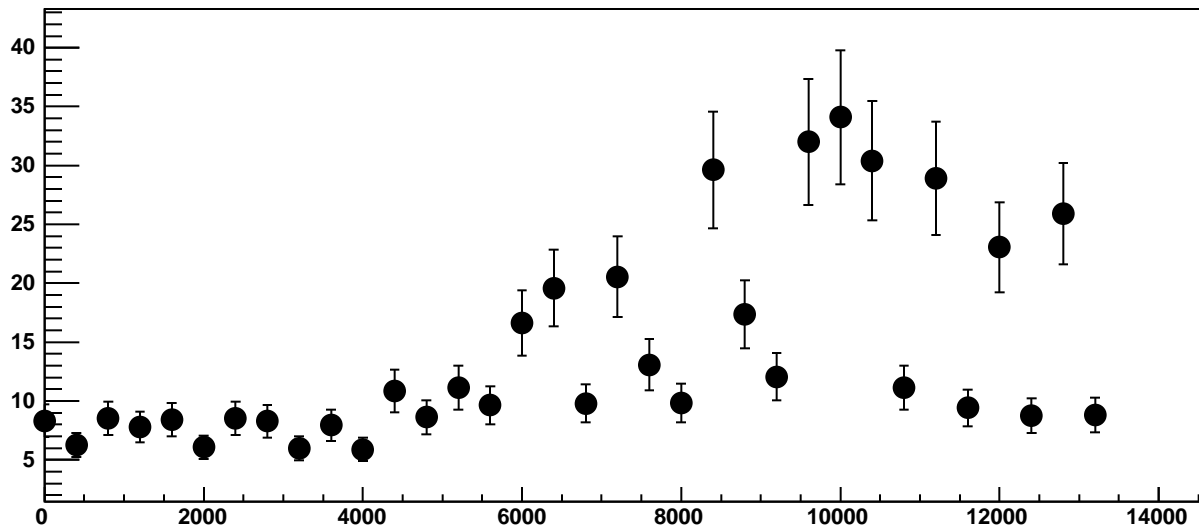
Chip 1, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



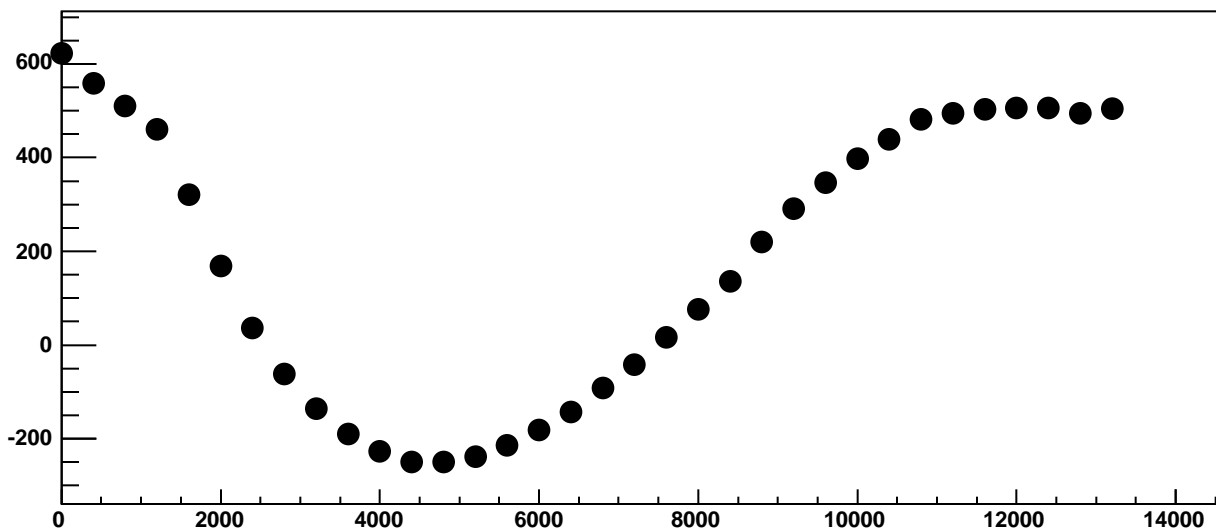
Chip 1, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



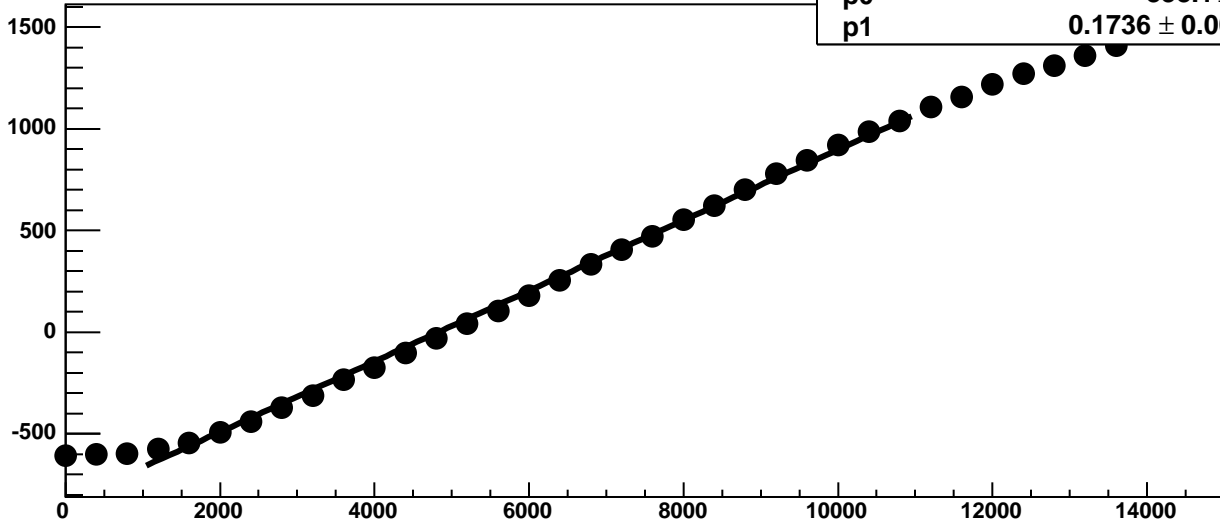
Chip 1, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



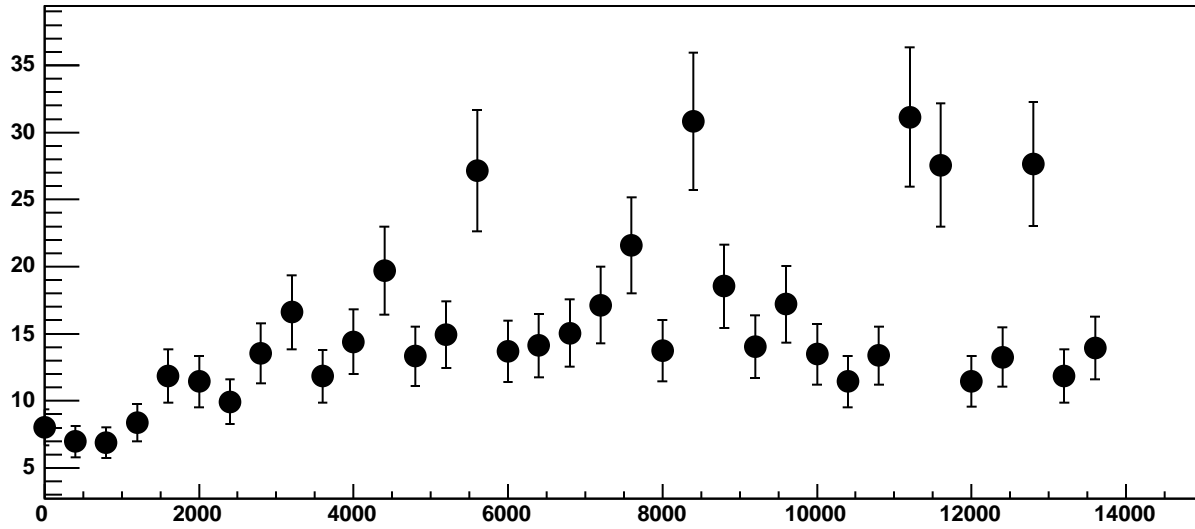
Chip 1, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



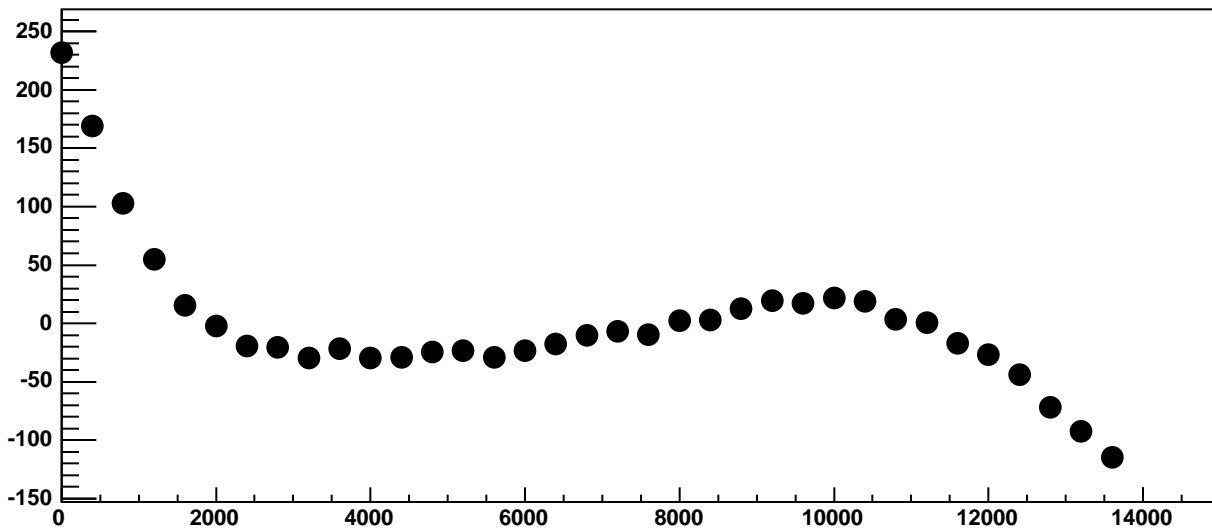
Chip 1, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



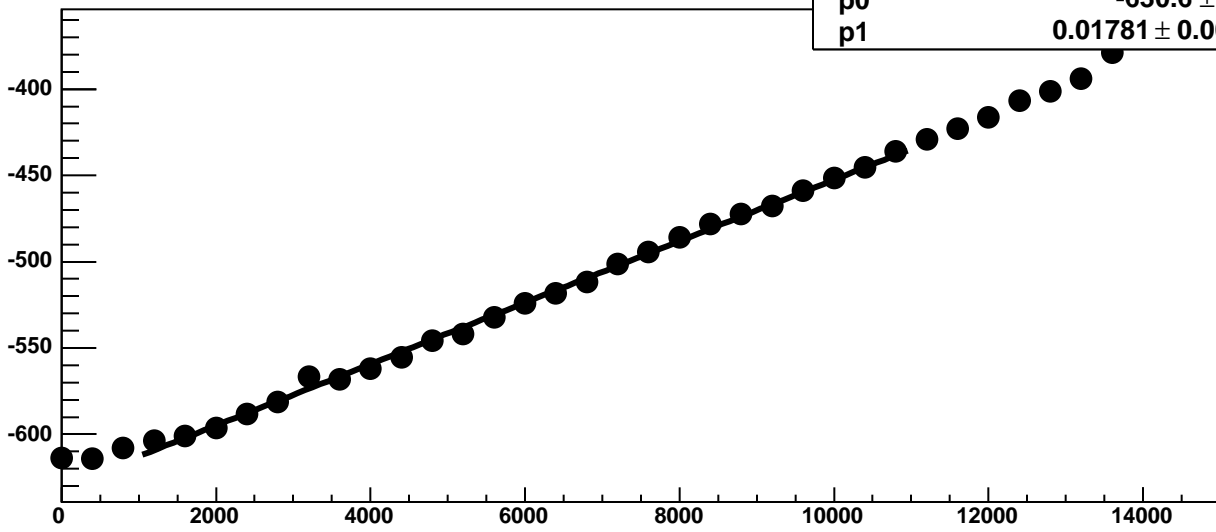
Chip 1, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

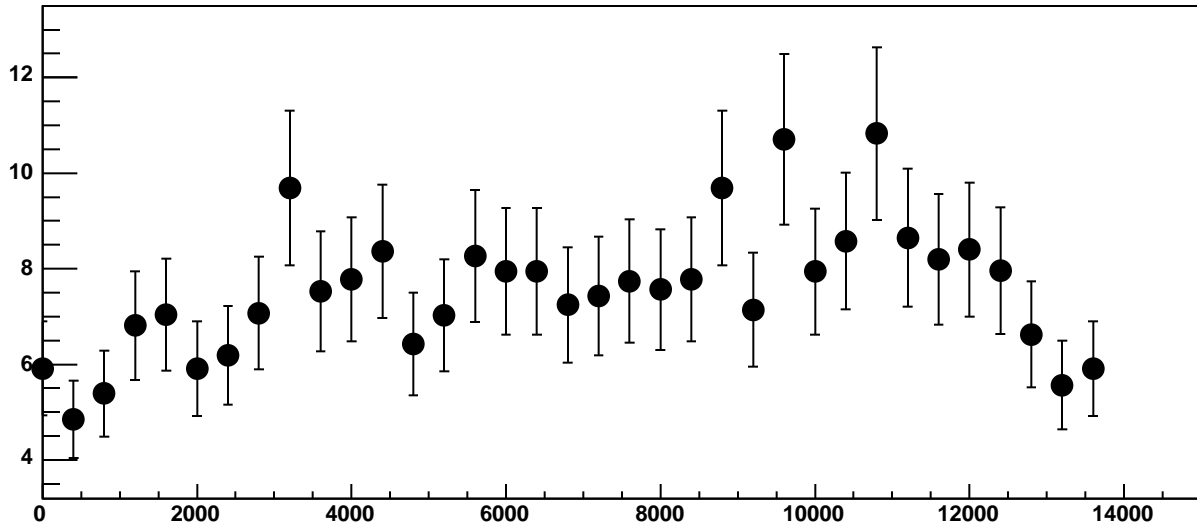


Chip 1, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC

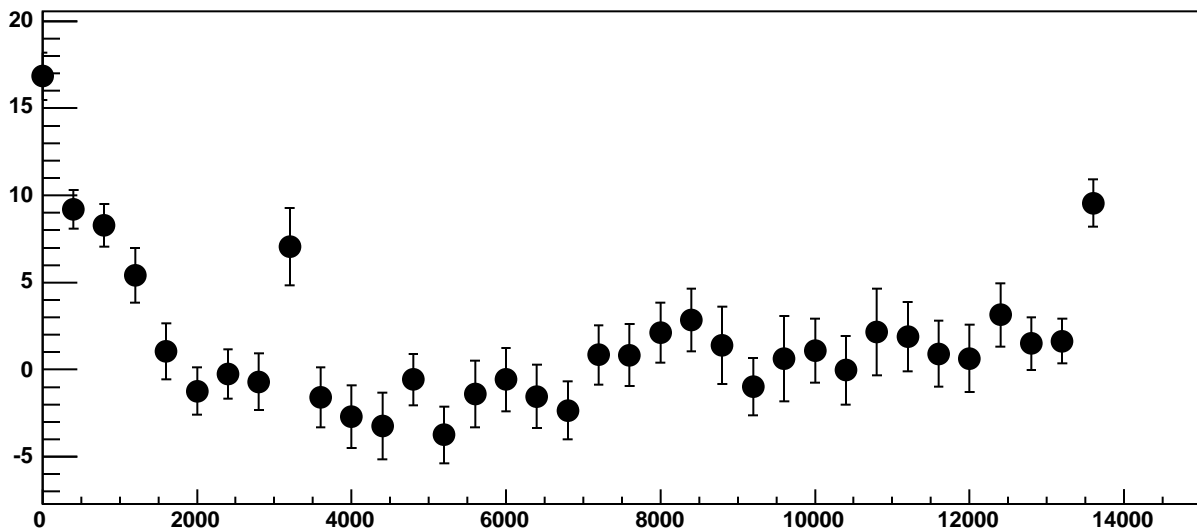


χ^2 / ndf 45 / 23
p0 -630.6 ± 0.7622
p1 0.01781 ± 0.0001229

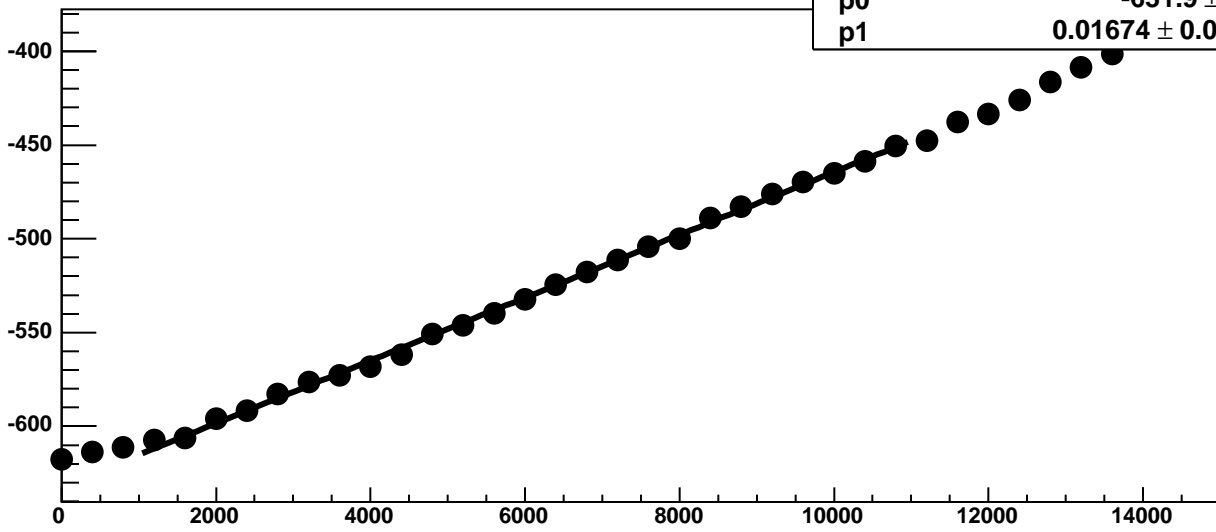
Chip 1, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



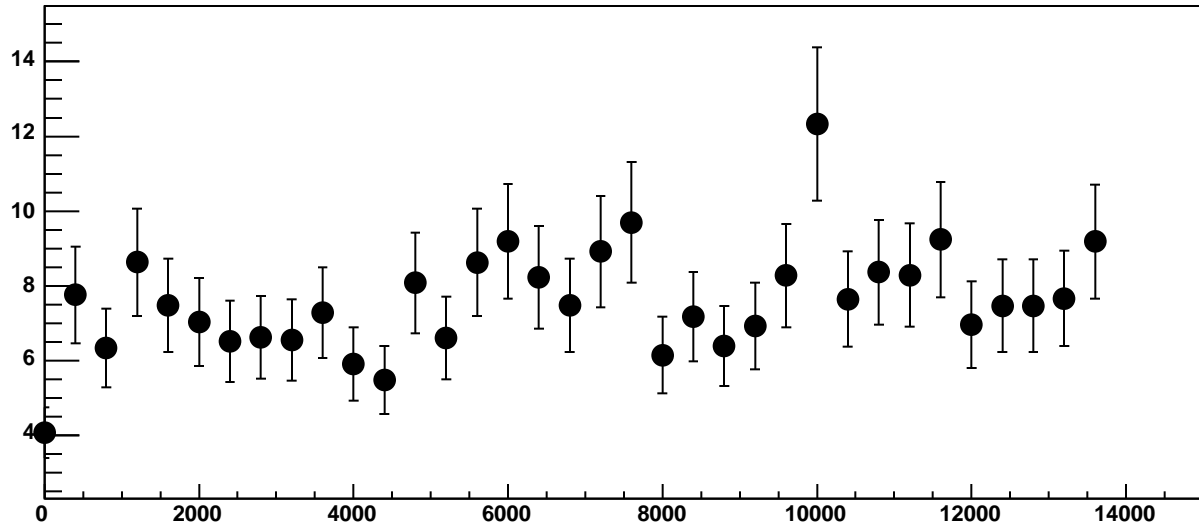
Chip 1, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC



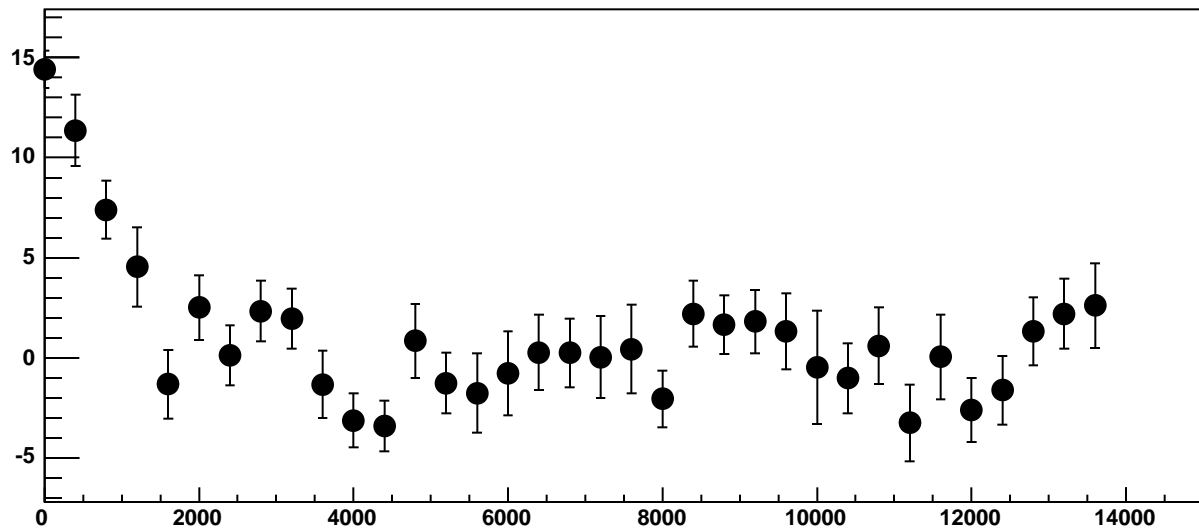
Chip 1, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



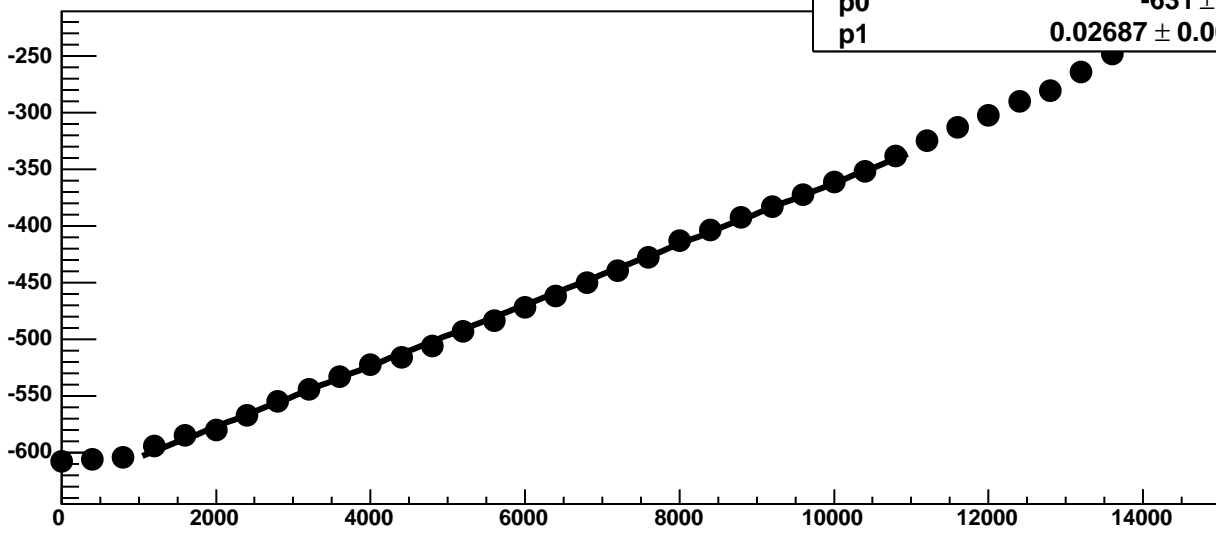
Chip 1, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

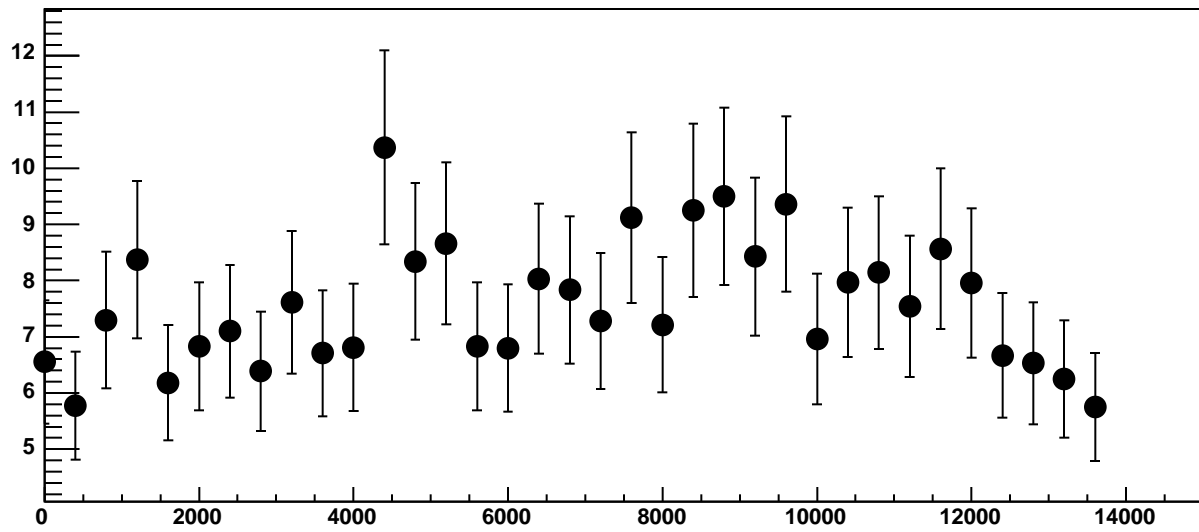


Chip 1, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

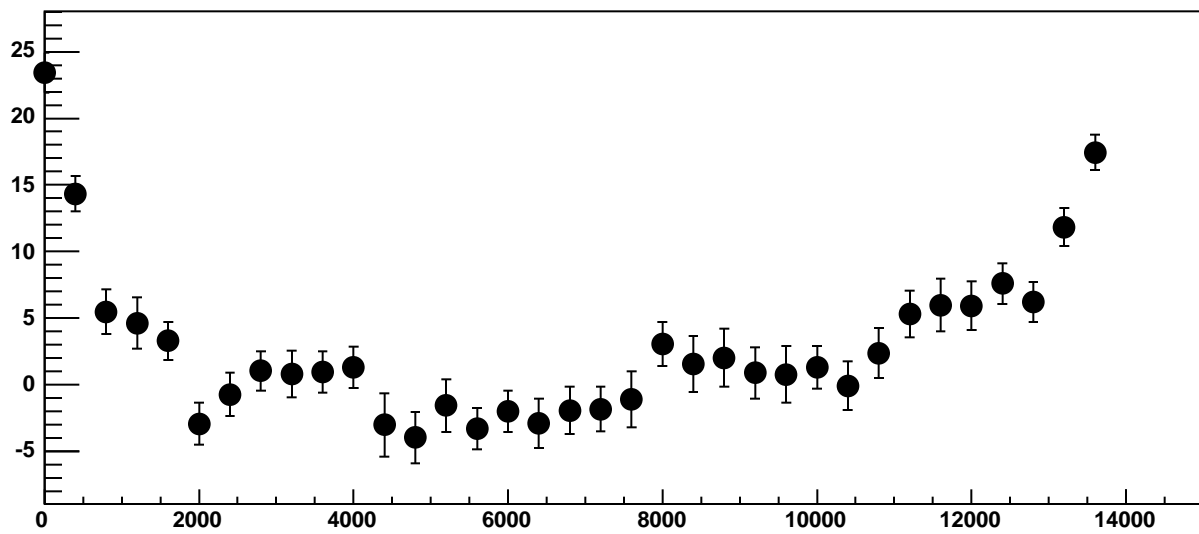


χ^2 / ndf 41.82 / 23
p0 -631 ± 0.7672
p1 0.02687 ± 0.0001206

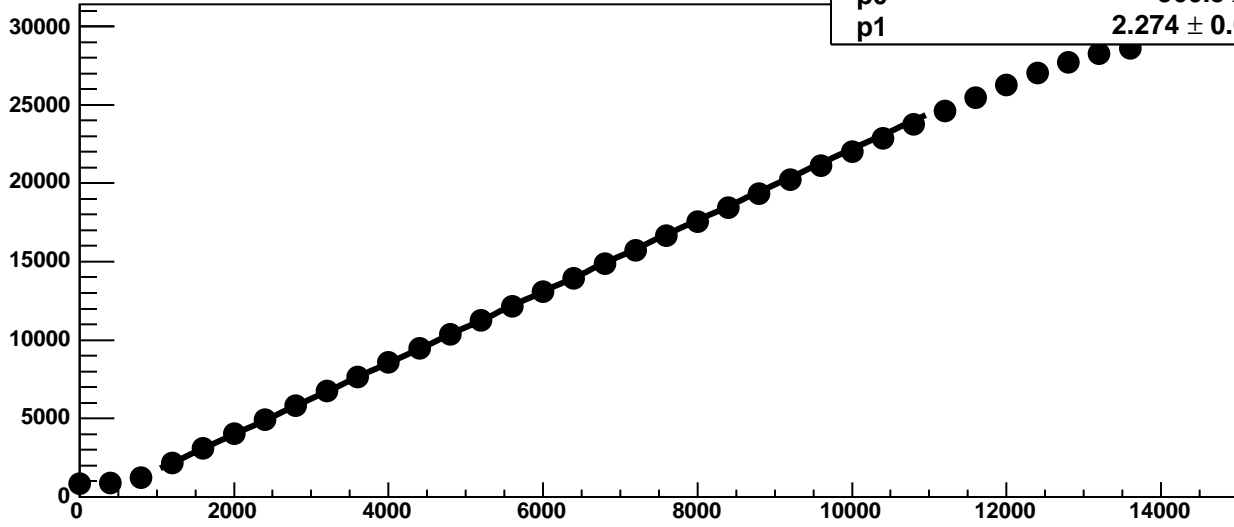
Chip 1, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

289 / 23

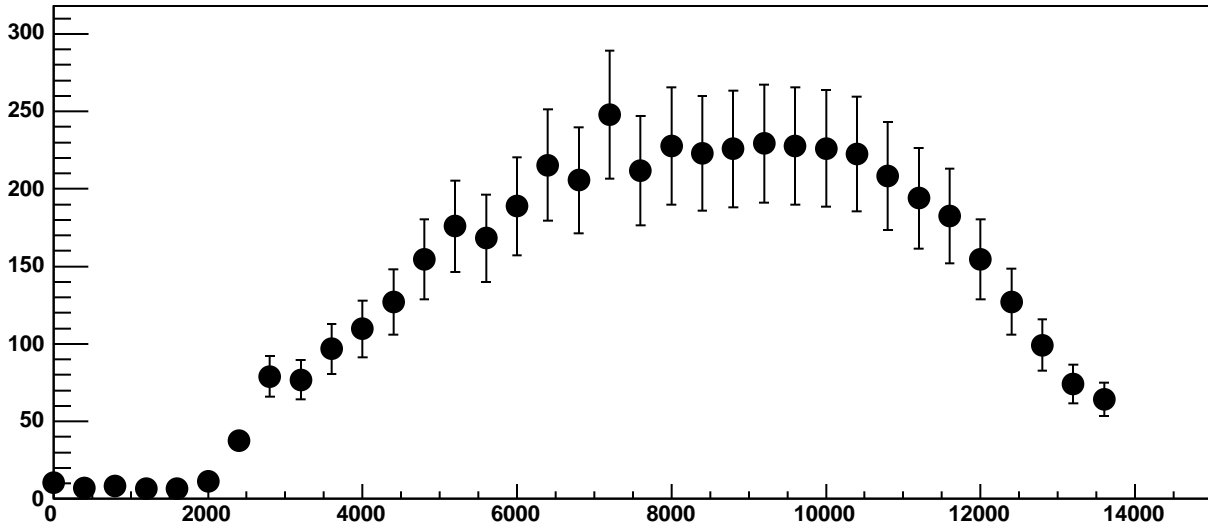
p0

-560.5 ± 2.604

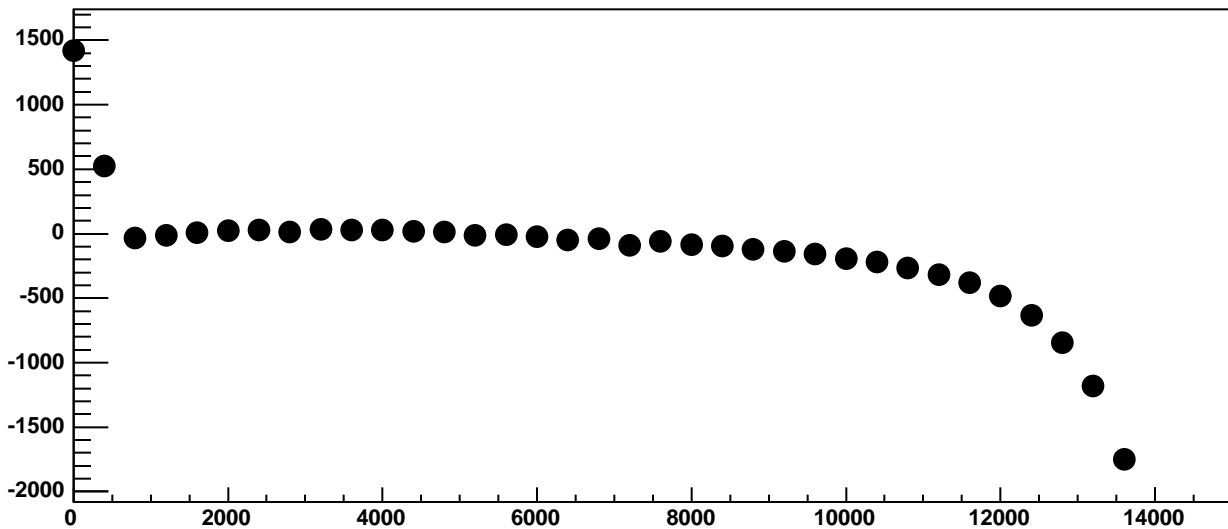
p1

2.274 ± 0.001553

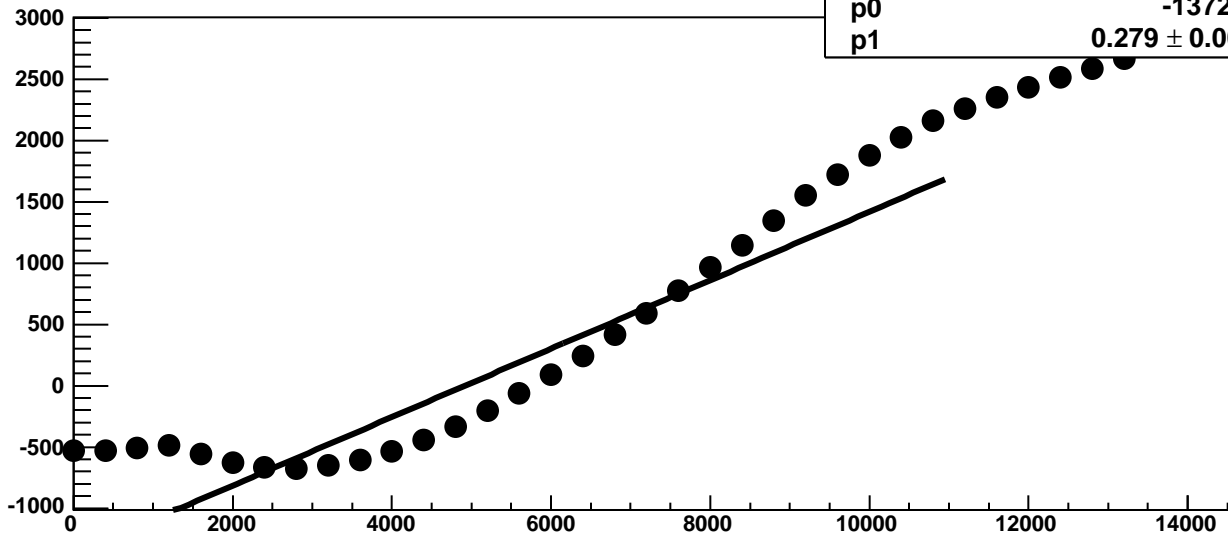
Chip 1, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



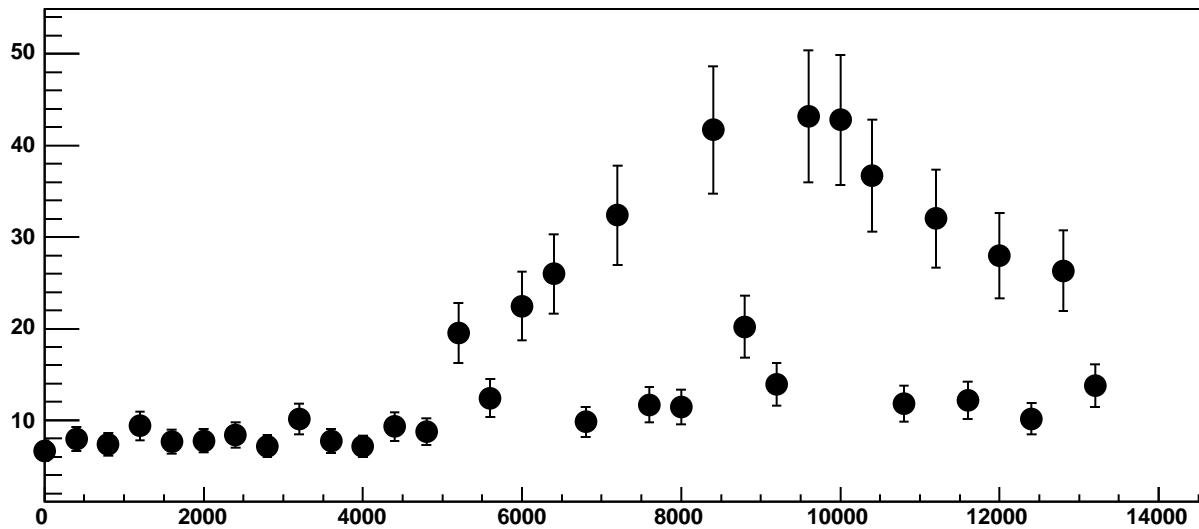
Chip 1, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC



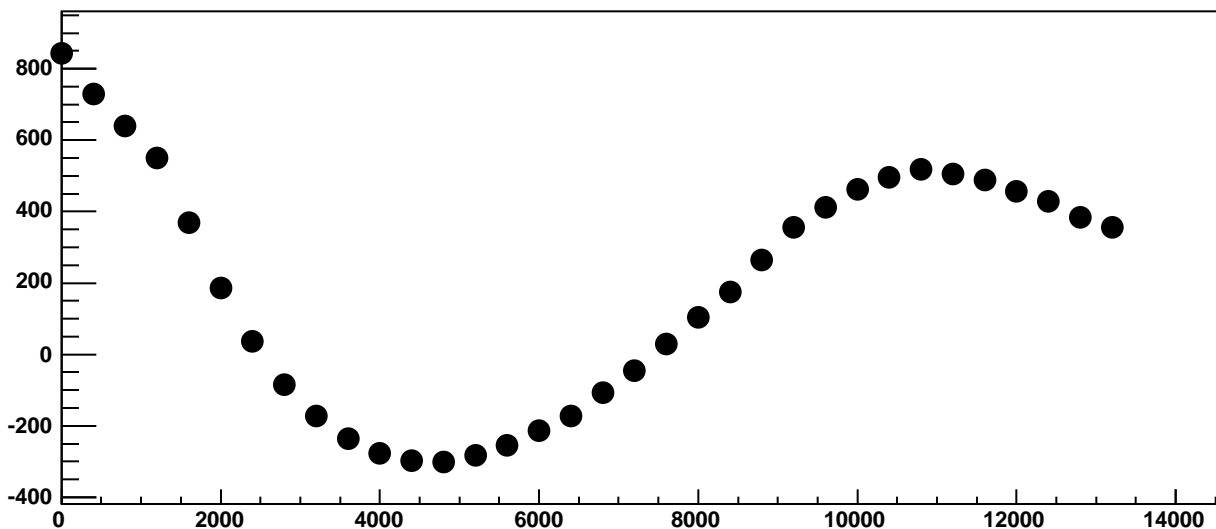
Chip 1, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



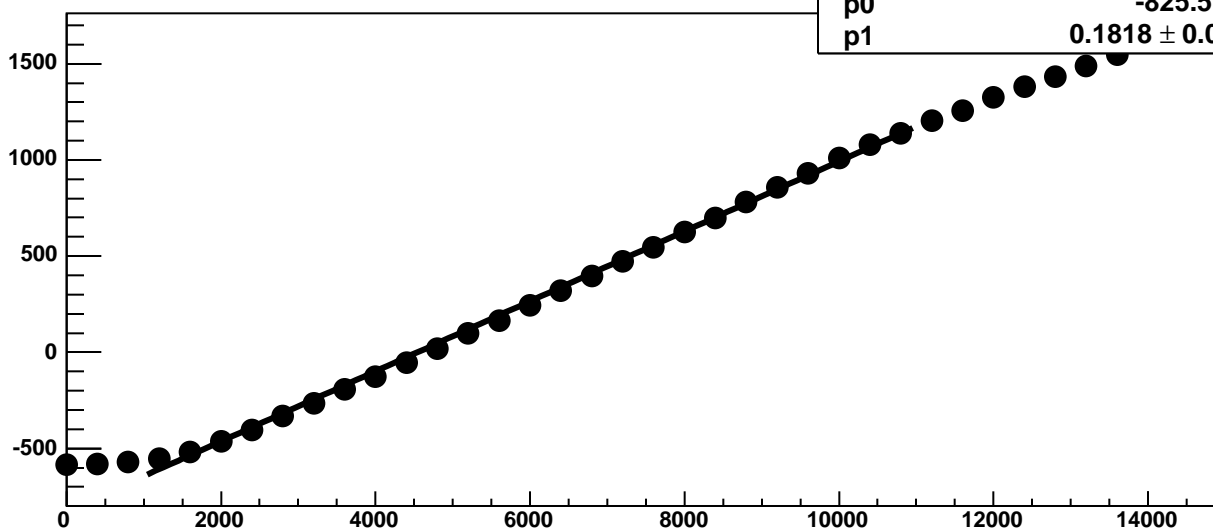
Chip 1, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

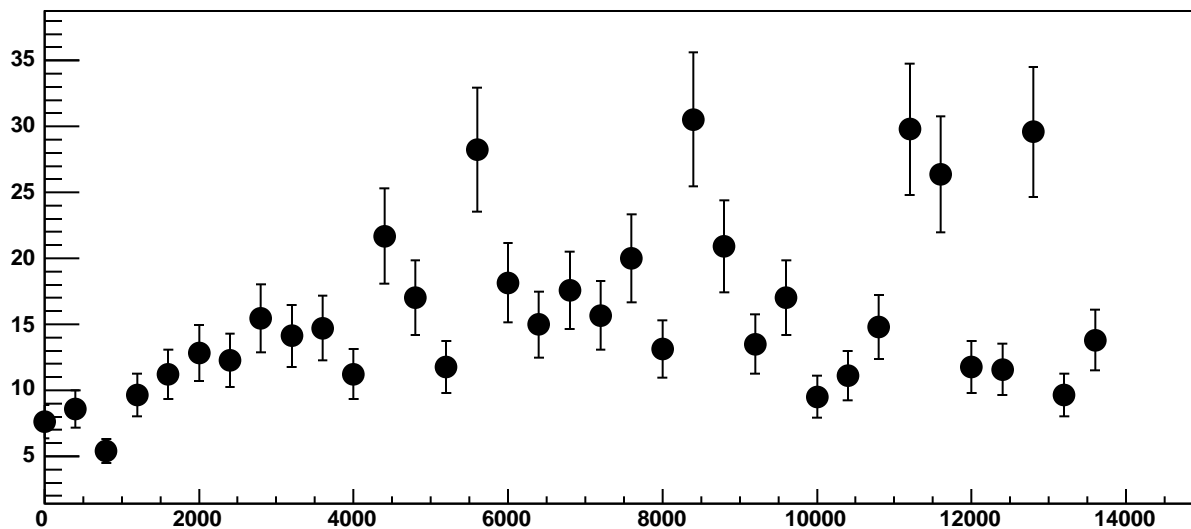


Chip 1, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

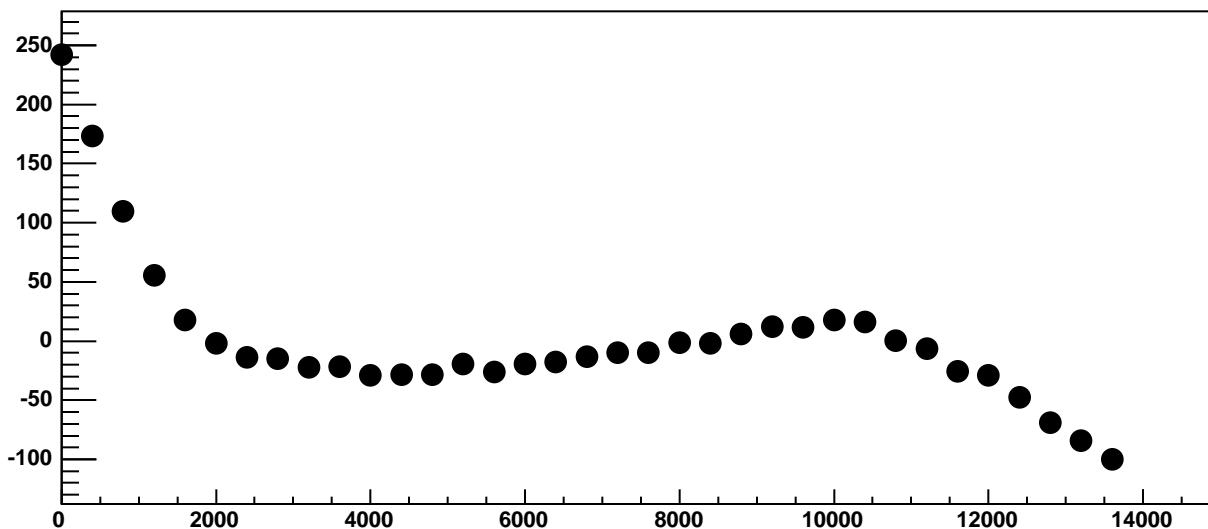


χ^2 / ndf 1298 / 23
p0 -825.5 ± 1.304
p1 0.1818 ± 0.0001984

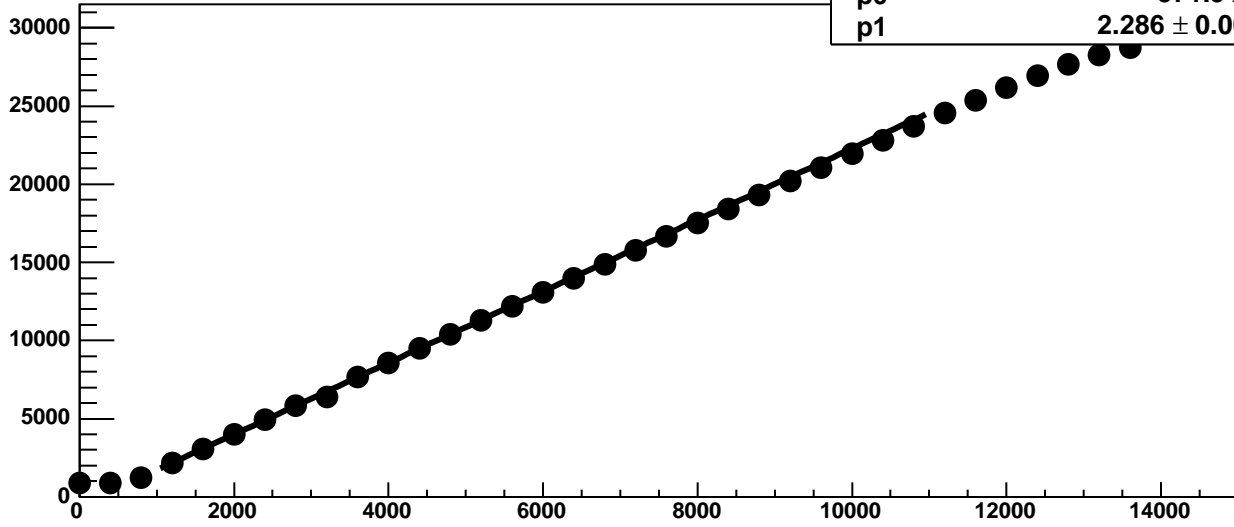
Chip 1, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



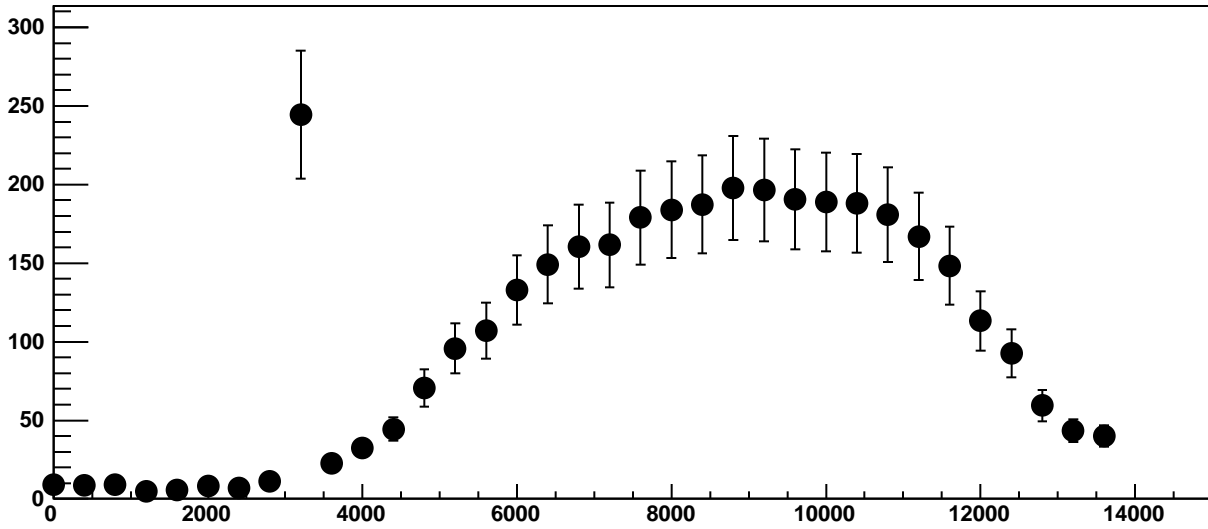
Chip 1, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



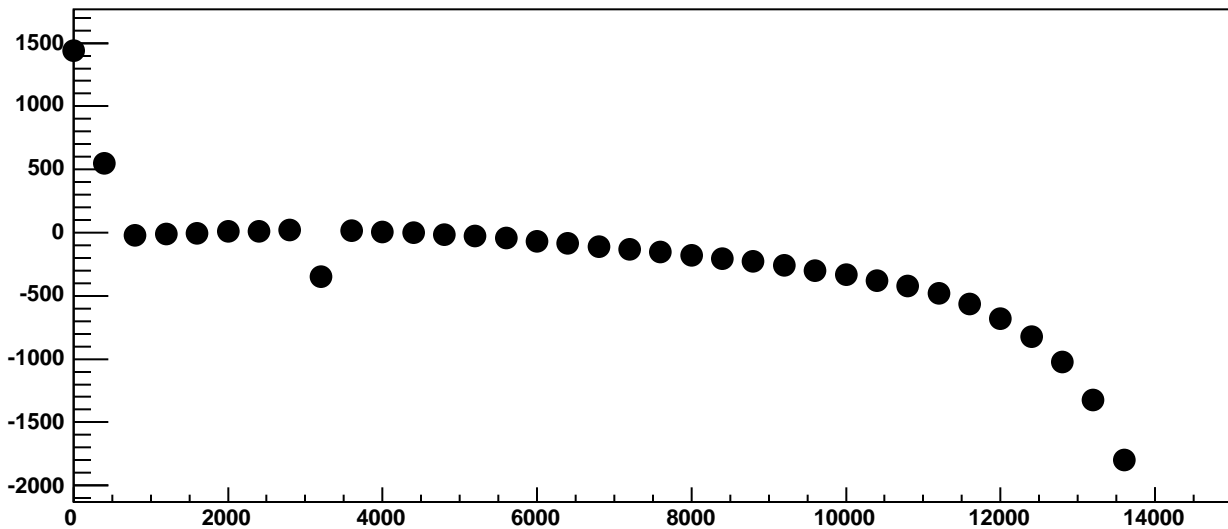
Chip 1, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC



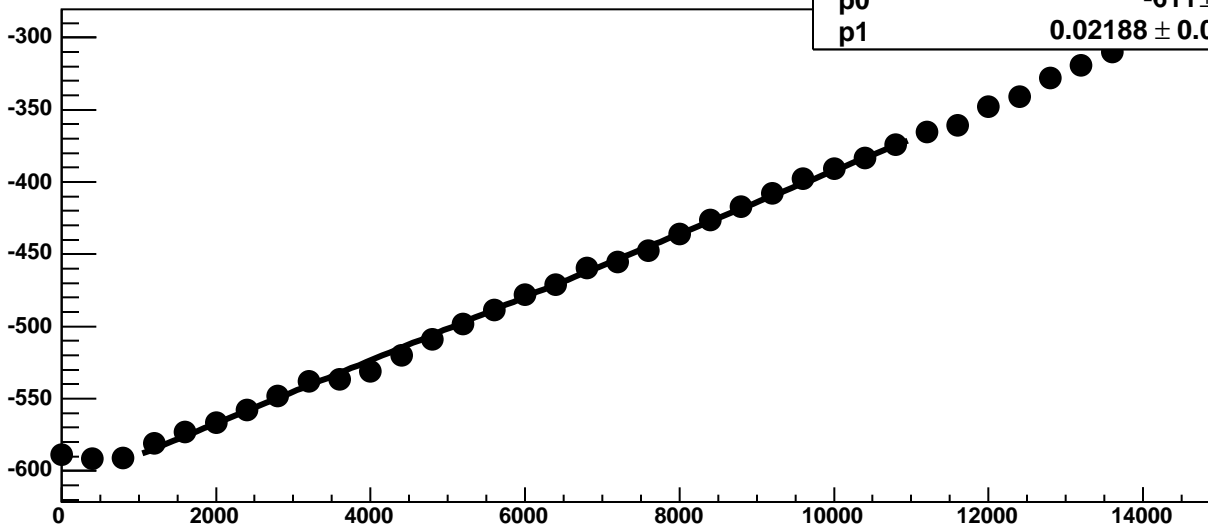
Chip 1, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC

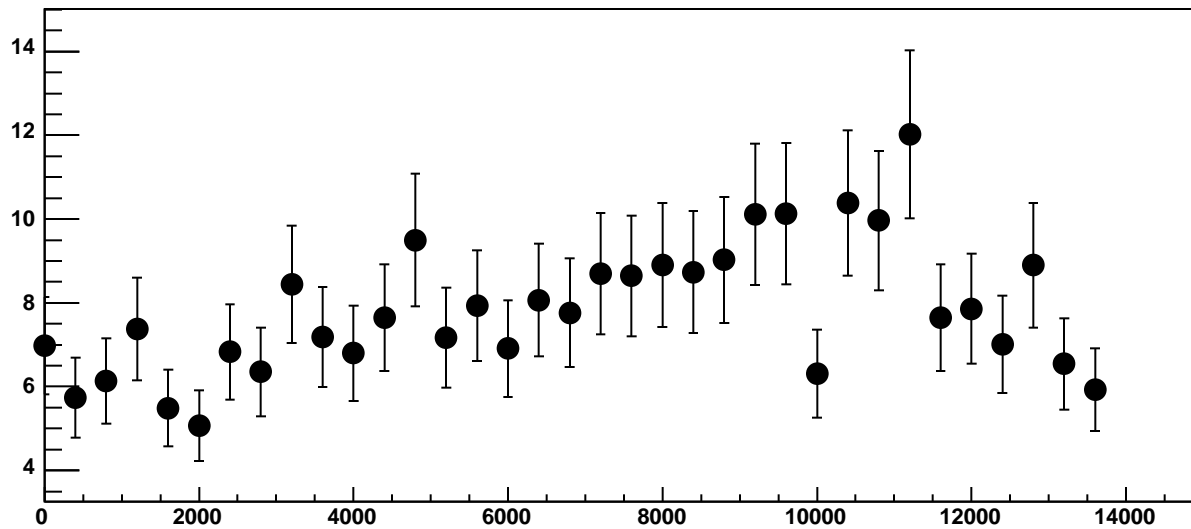


Chip 1, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

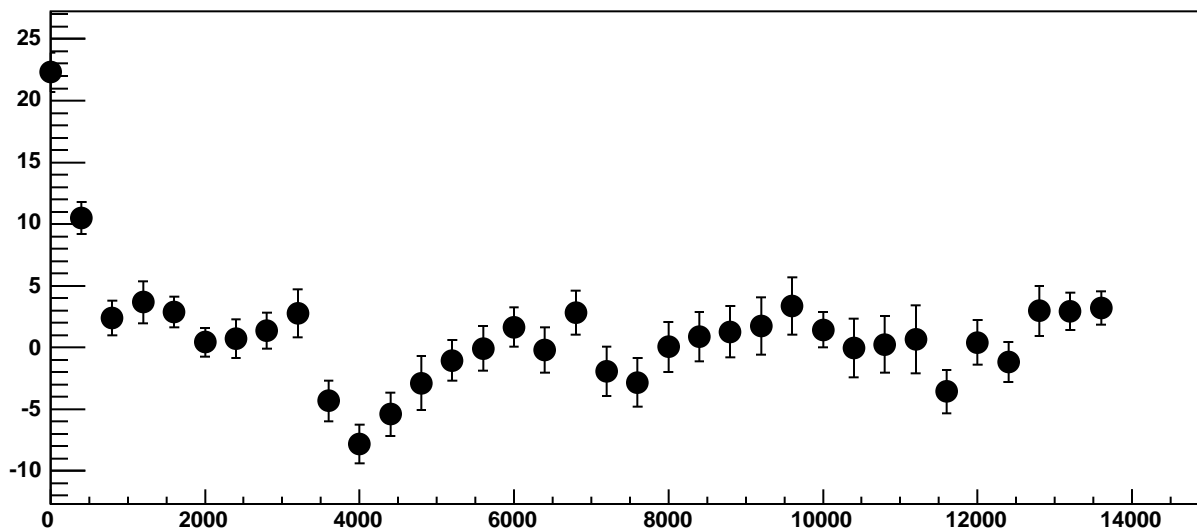


χ^2 / ndf 67.45 / 23
p0 -611 ± 0.7162
p1 0.02188 ± 0.0001197

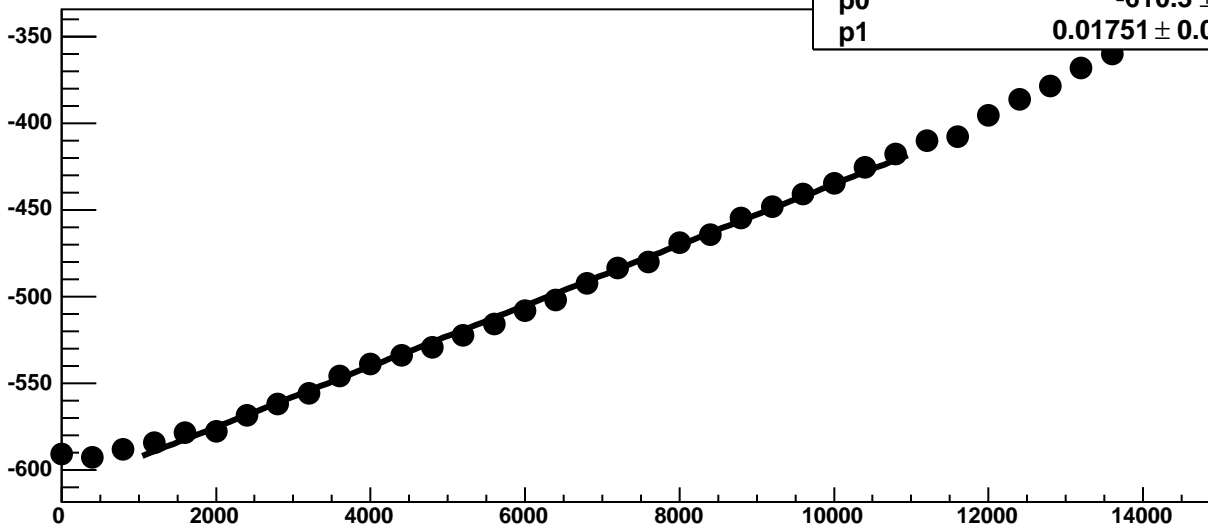
Chip 1, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

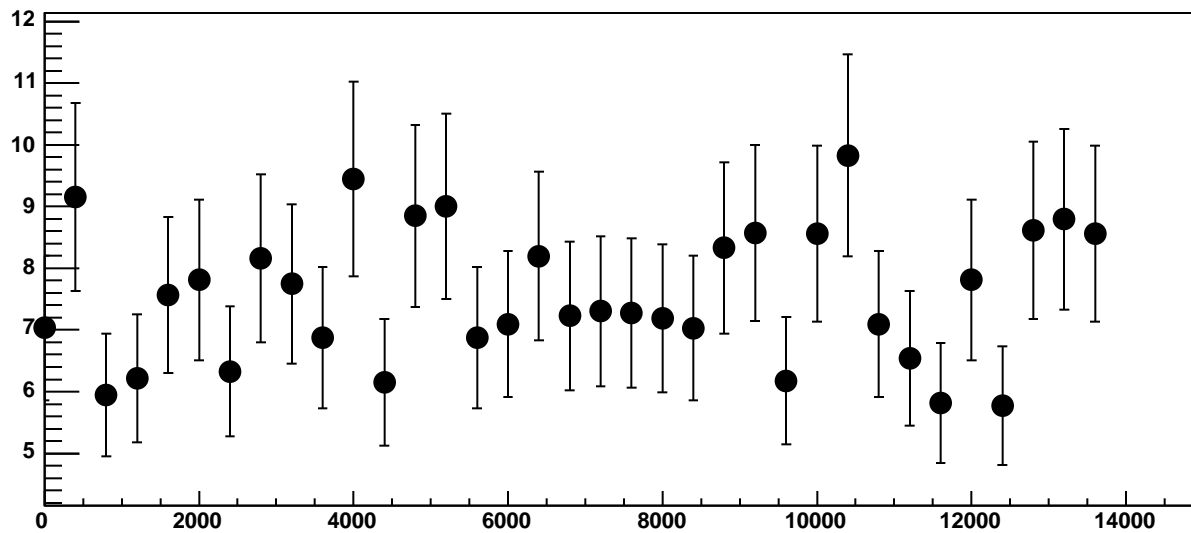


Chip 1, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

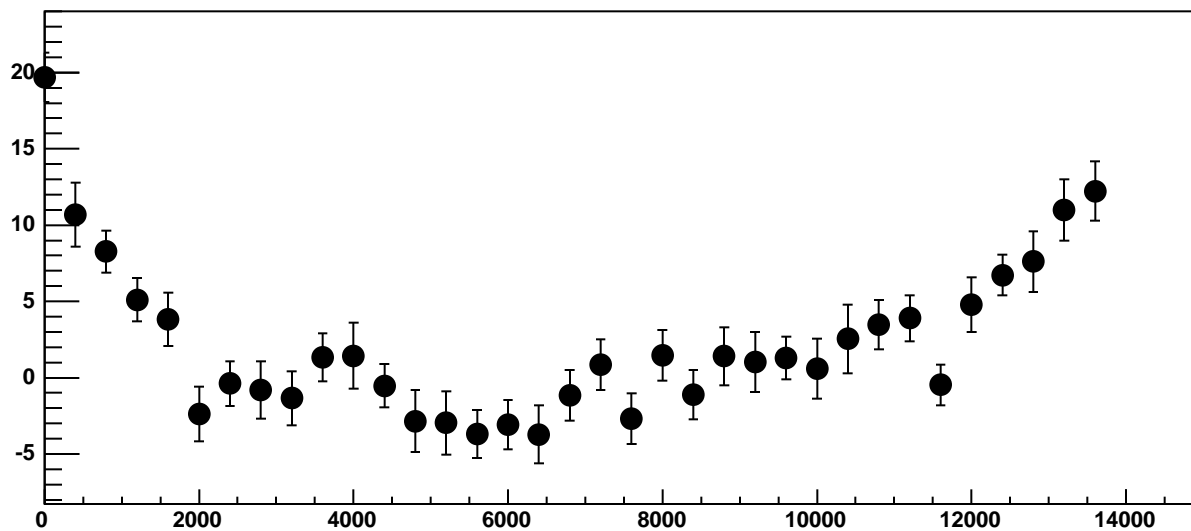


χ^2 / ndf 50.71 / 23
p0 -610.3 ± 0.7684
p1 0.01751 ± 0.0001175

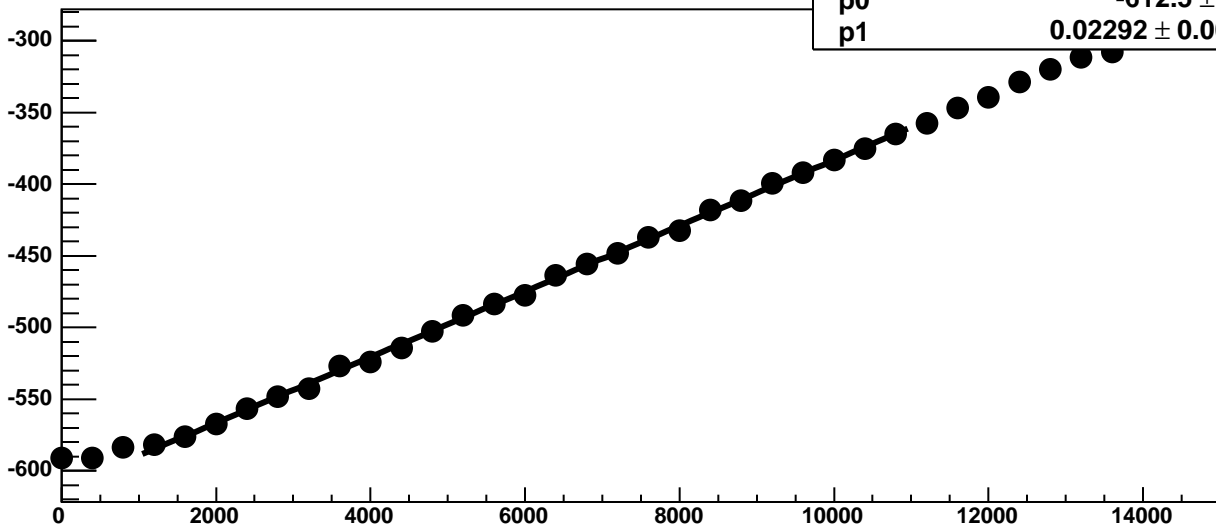
Chip 1, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

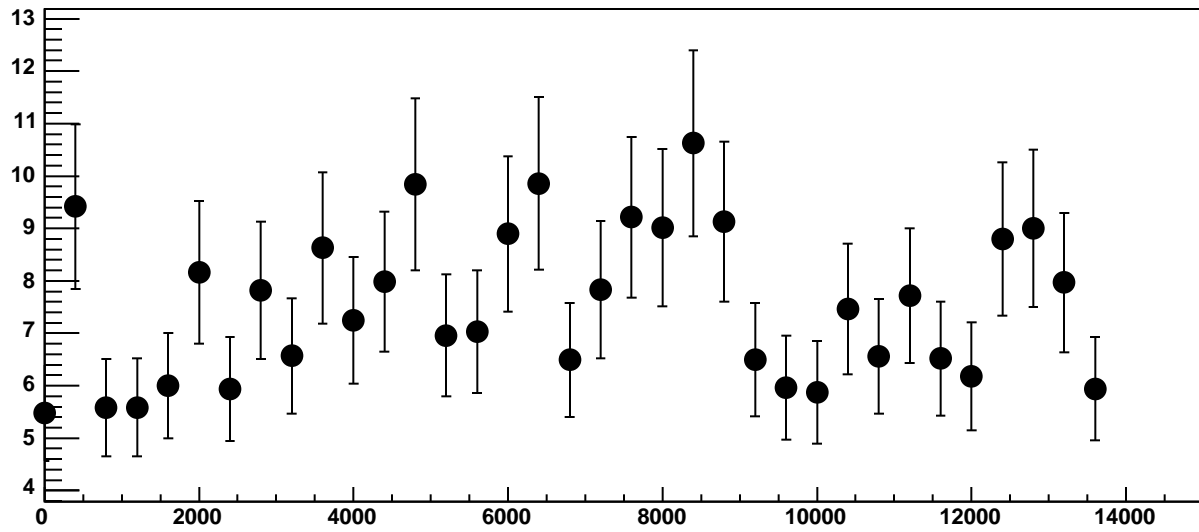


Chip 1, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

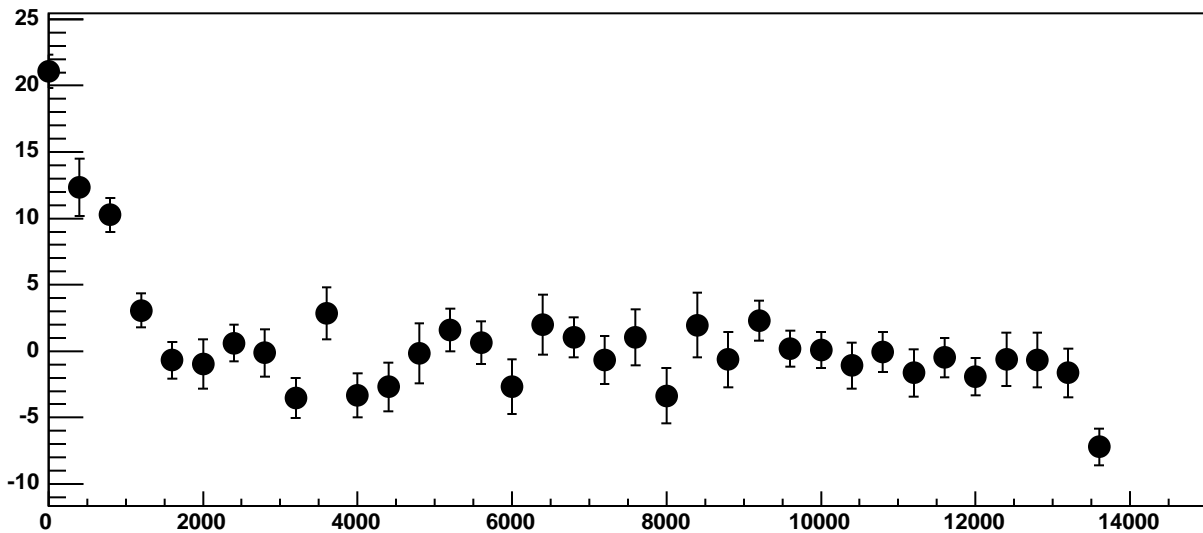


χ^2 / ndf 30.77 / 23
p0 -612.5 ± 0.7068
p1 0.02292 ± 0.0001062

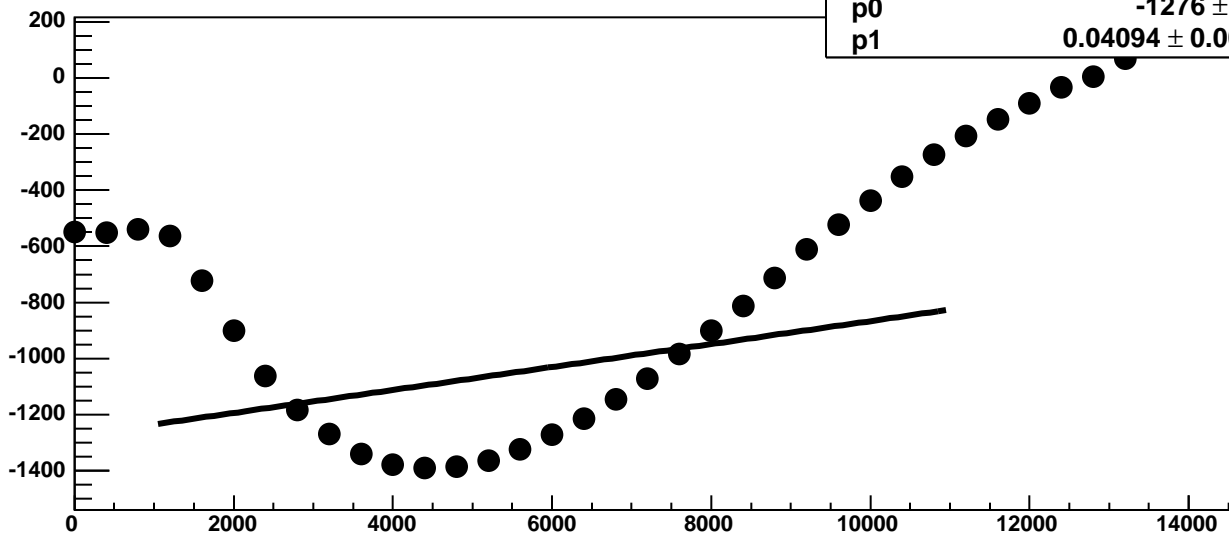
Chip 1, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC

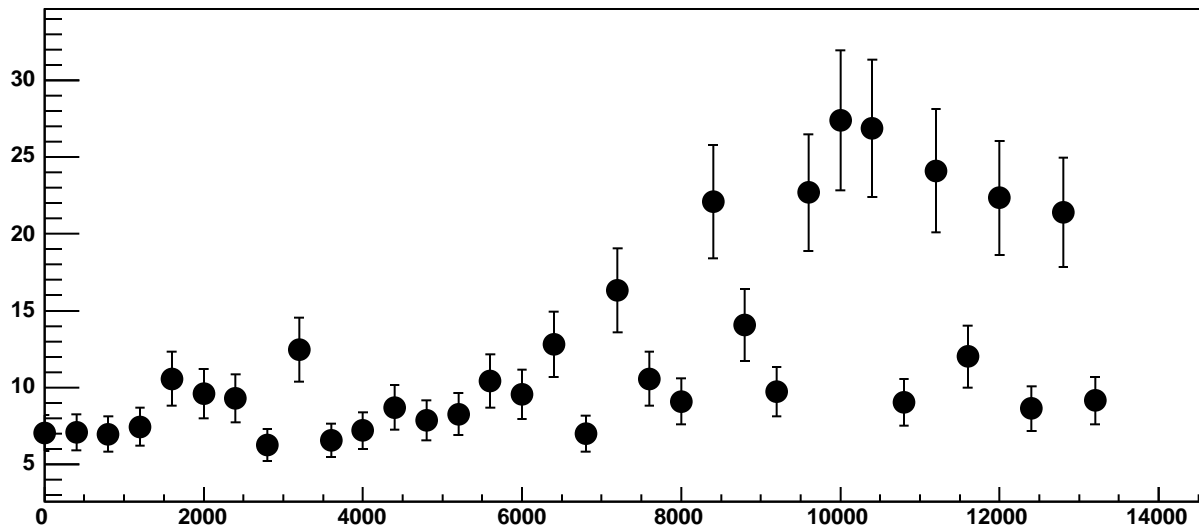


Chip 1, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

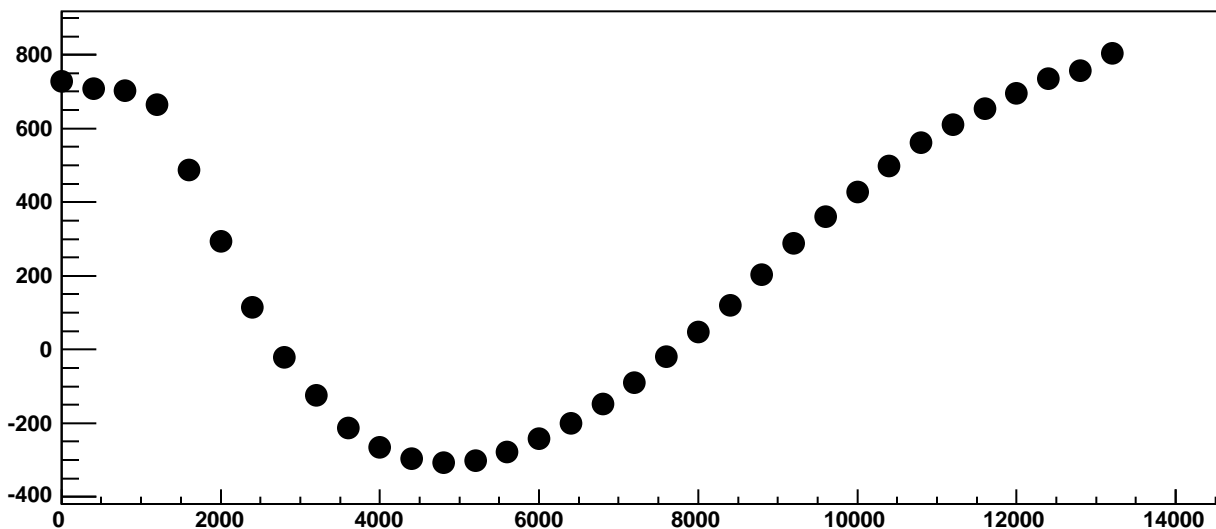


χ^2 / ndf 4.872e+05 / 23
p0 -1276 ± 0.9538
p1 0.04094 ± 0.0001689

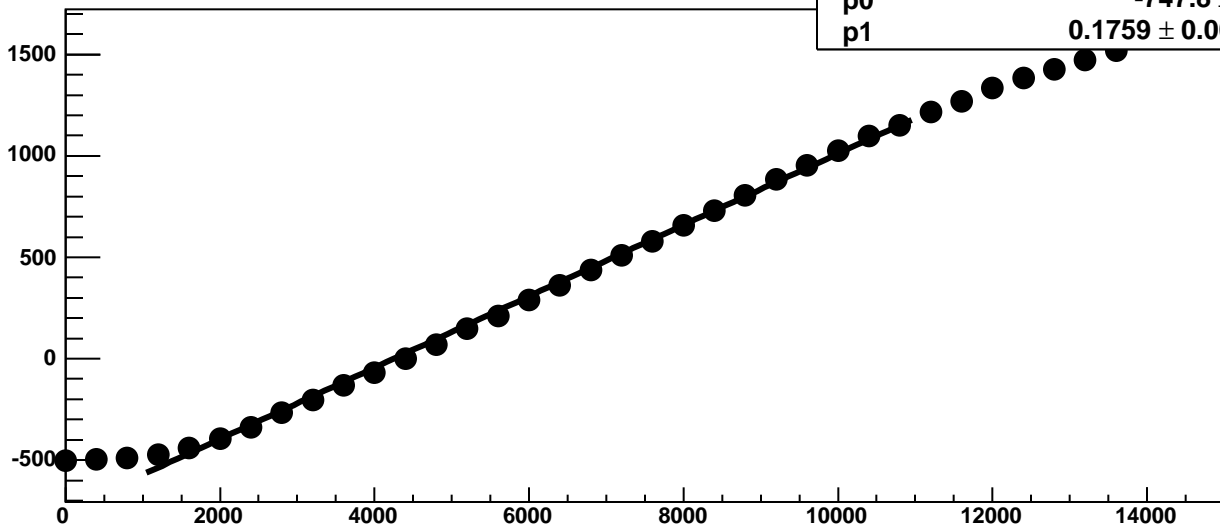
Chip 1, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

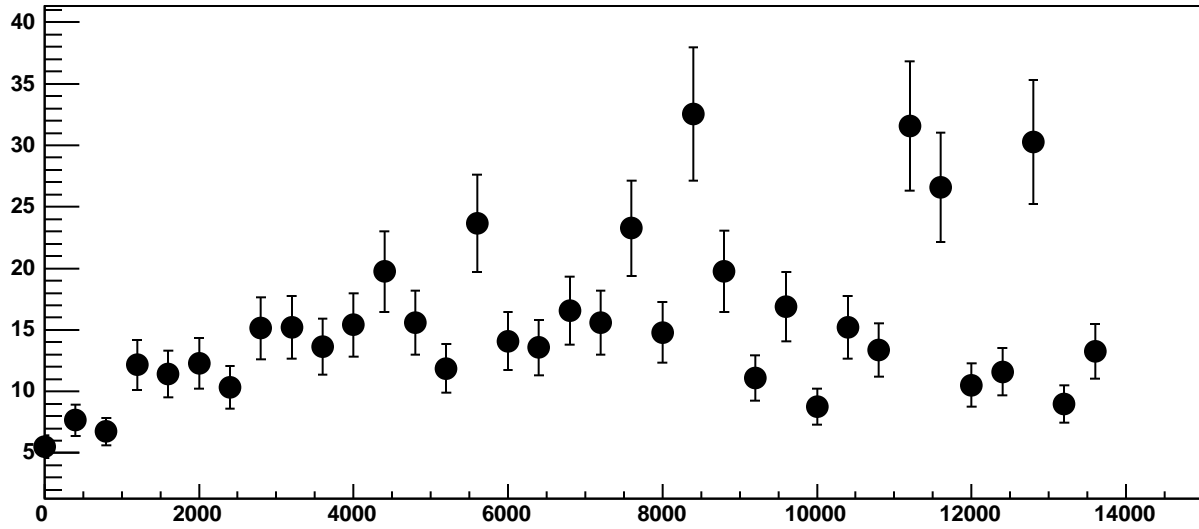


Chip 1, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

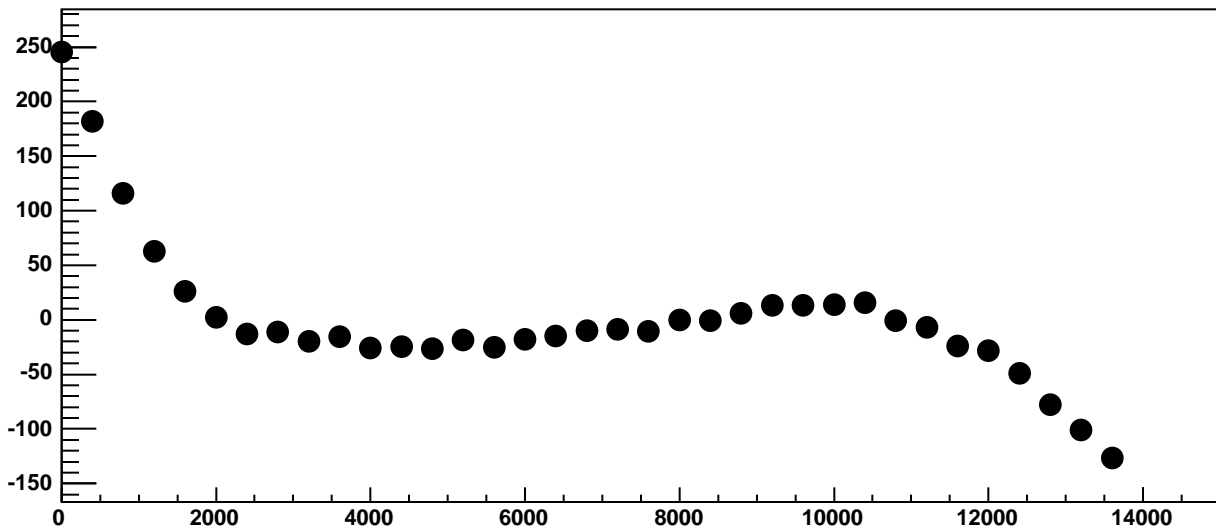


χ^2 / ndf 1089 / 23
p0 -747.8 ± 1.346
p1 0.1759 ± 0.0002023

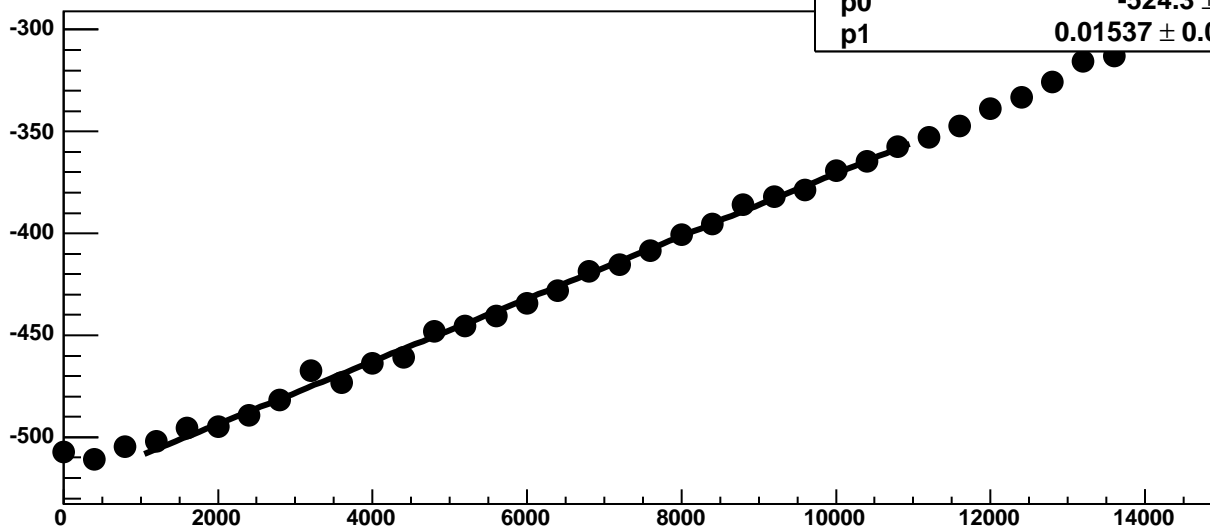
Chip 1, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

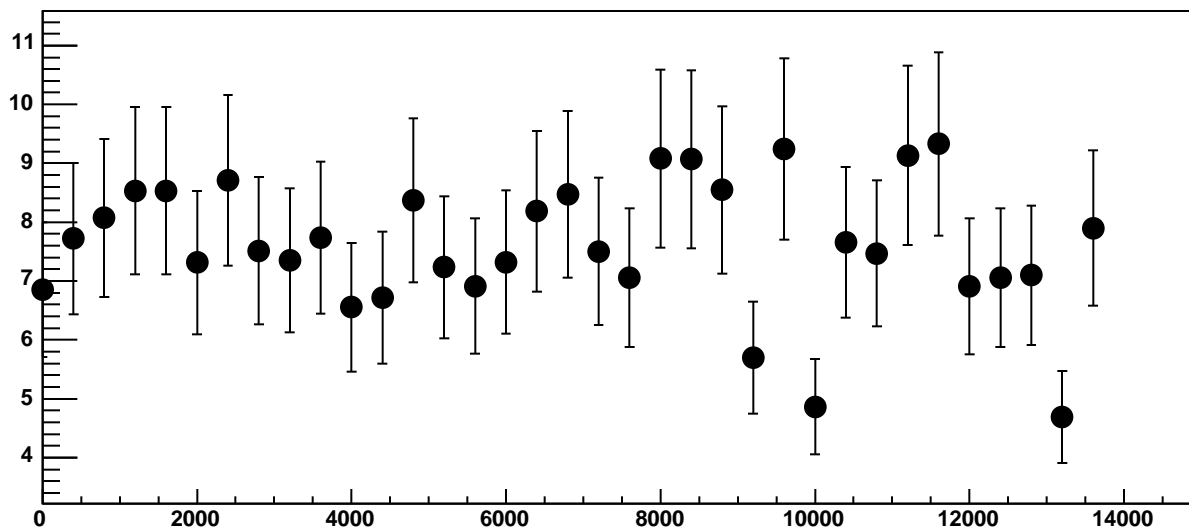


Chip 1, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

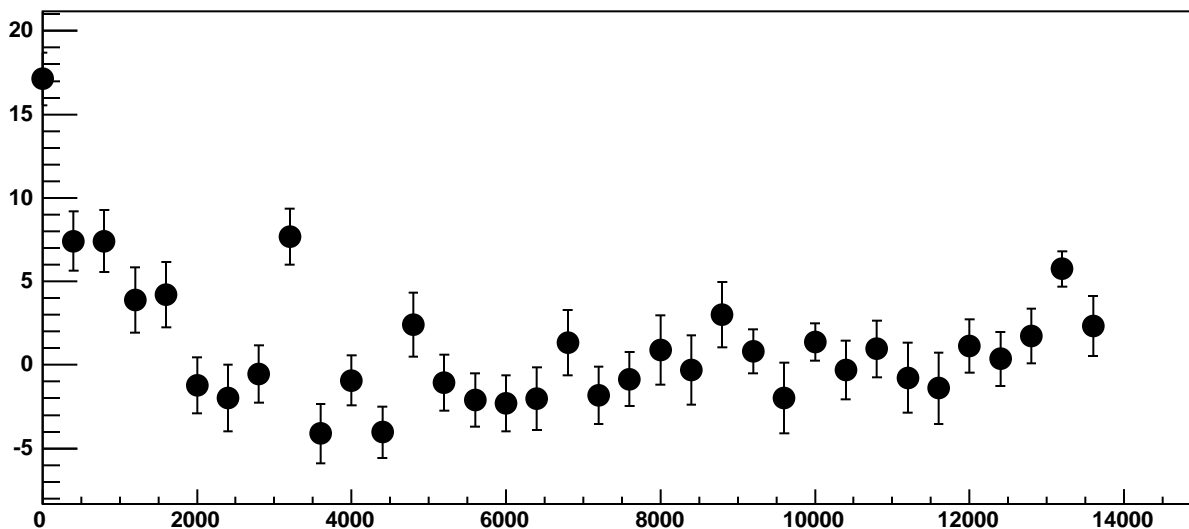


χ^2 / ndf 57.85 / 23
p0 -524.3 ± 0.8073
p1 0.01537 ± 0.0001172

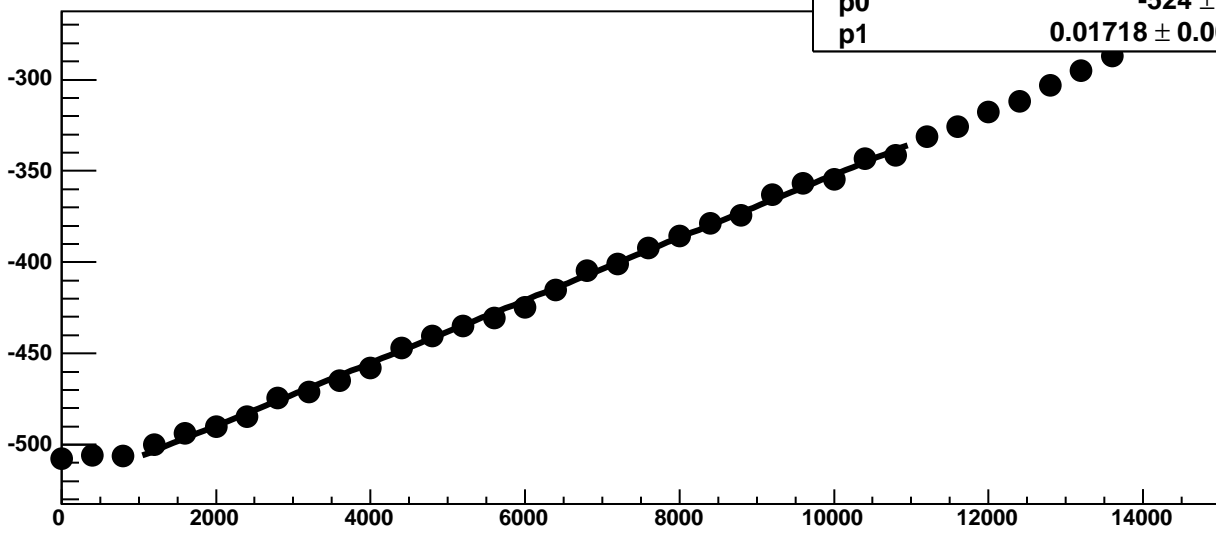
Chip 1, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

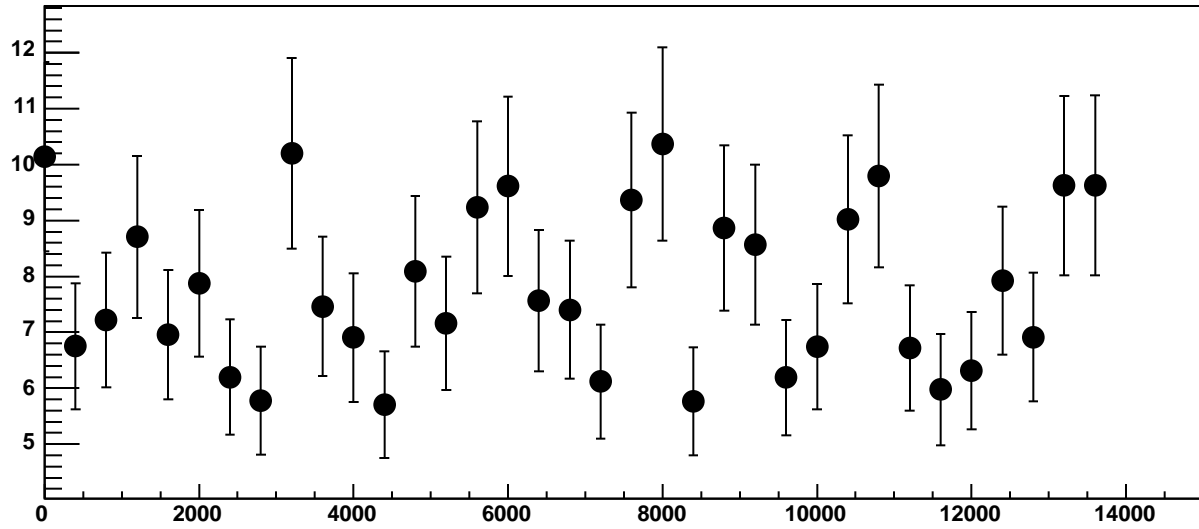


Chip 1, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

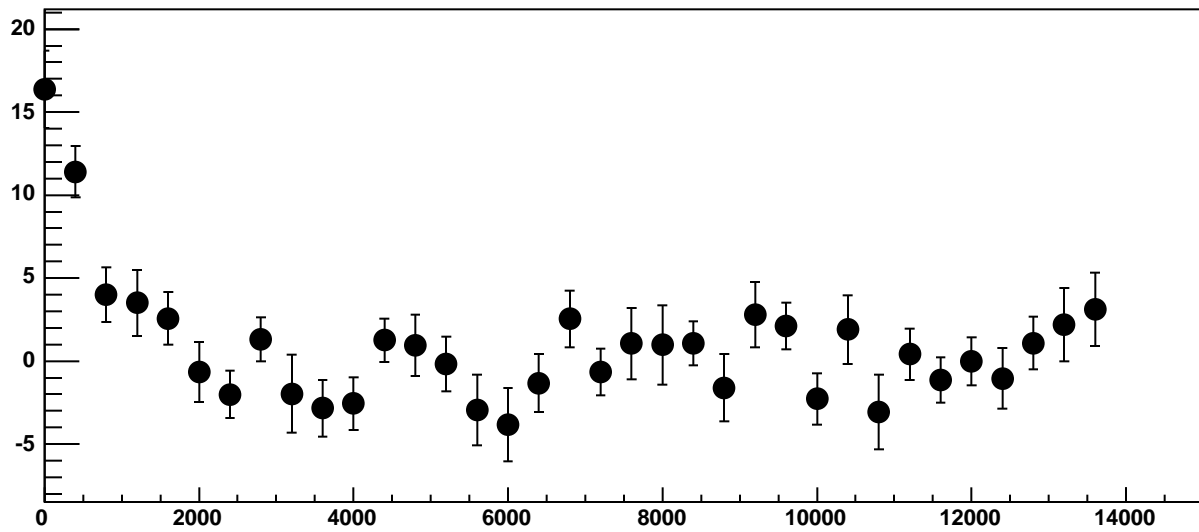


χ^2 / ndf 34.82 / 23
p0 -524 ± 0.7764
p1 0.01718 ± 0.0001198

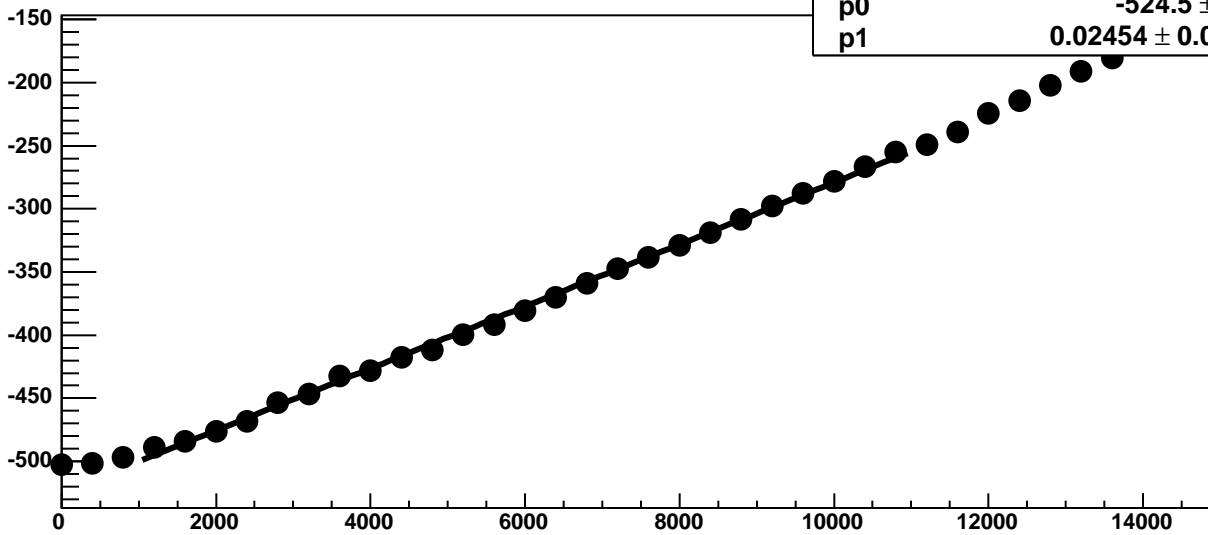
Chip 1, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



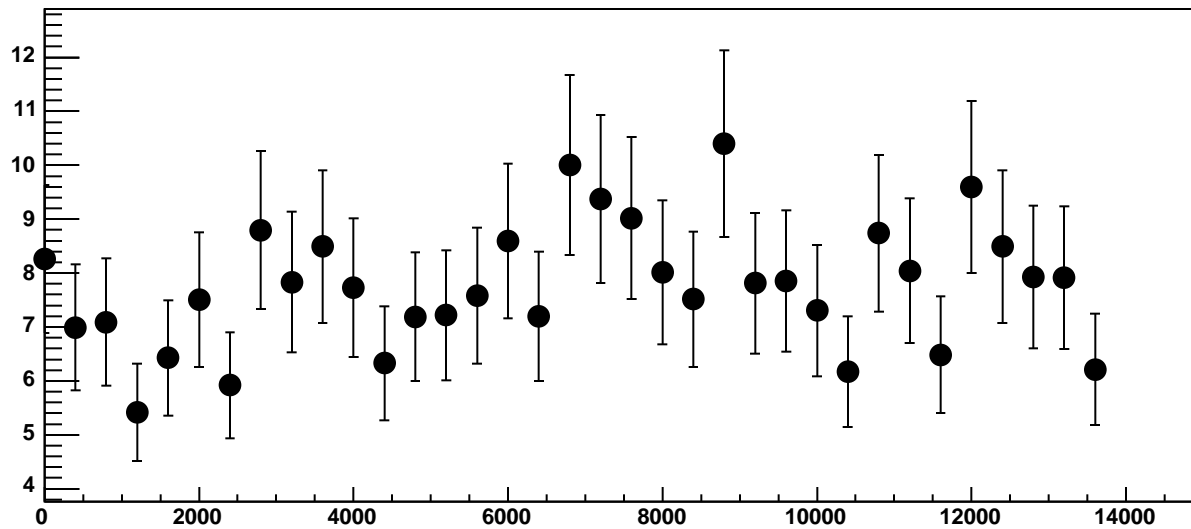
Chip 1, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



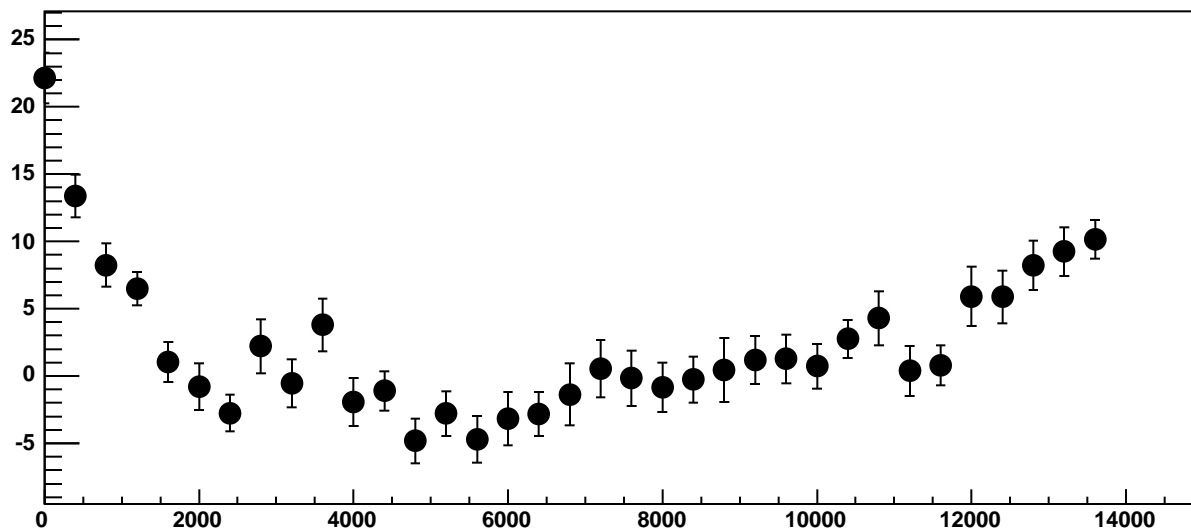
Chip 1, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



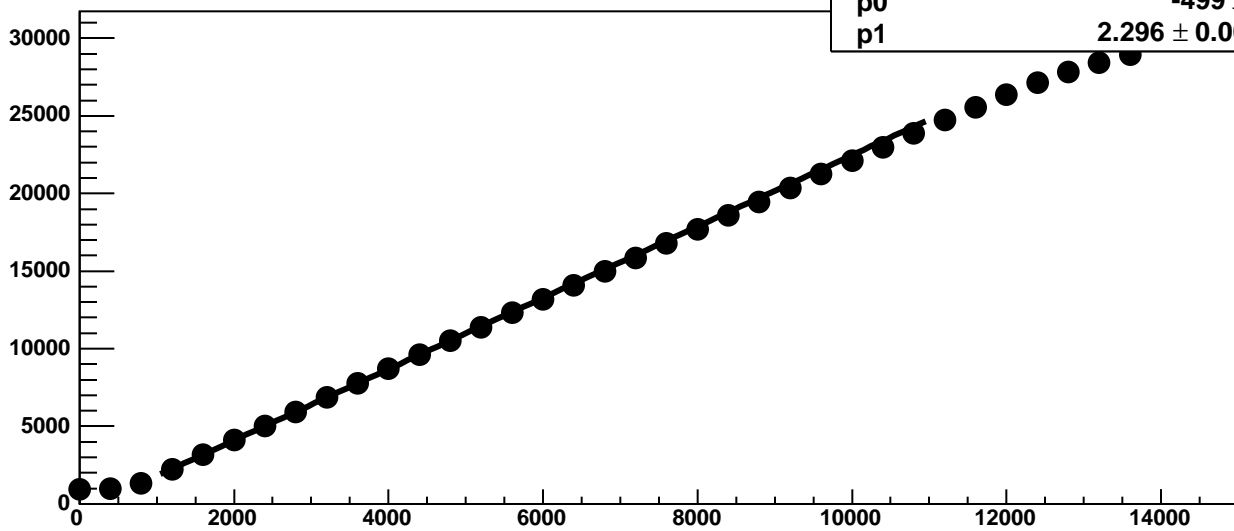
Chip 1, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

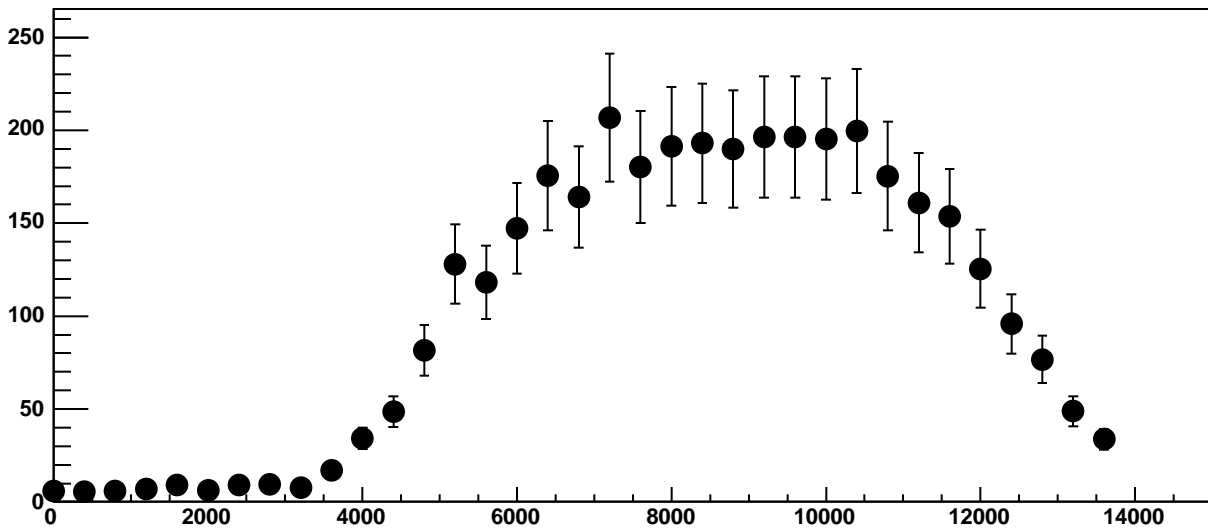


Chip 1, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC

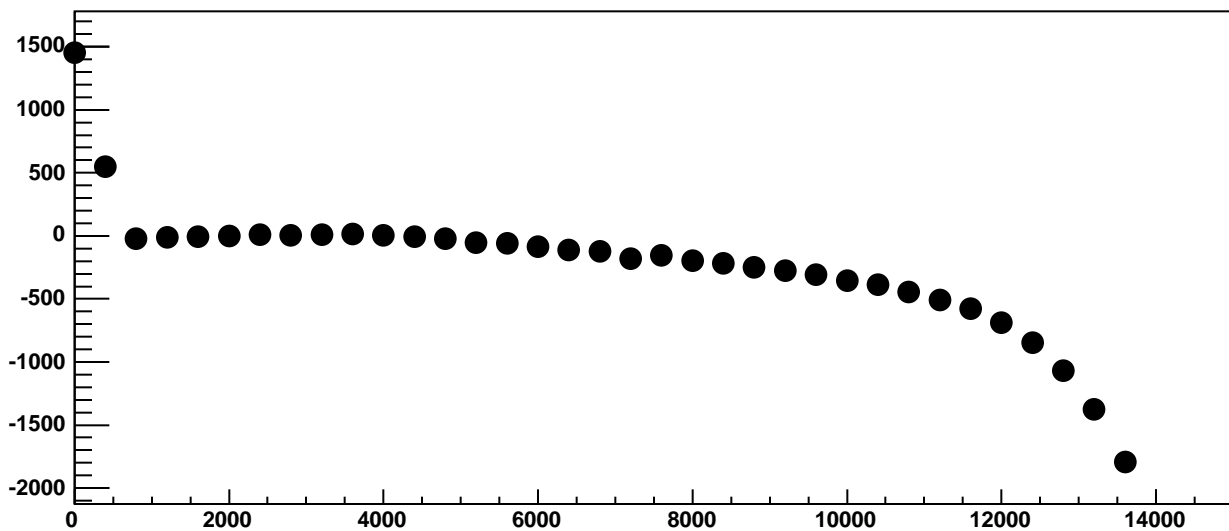


χ^2 / ndf 630 / 23
p0 -499 ± 1.989
p1 2.296 ± 0.0008316

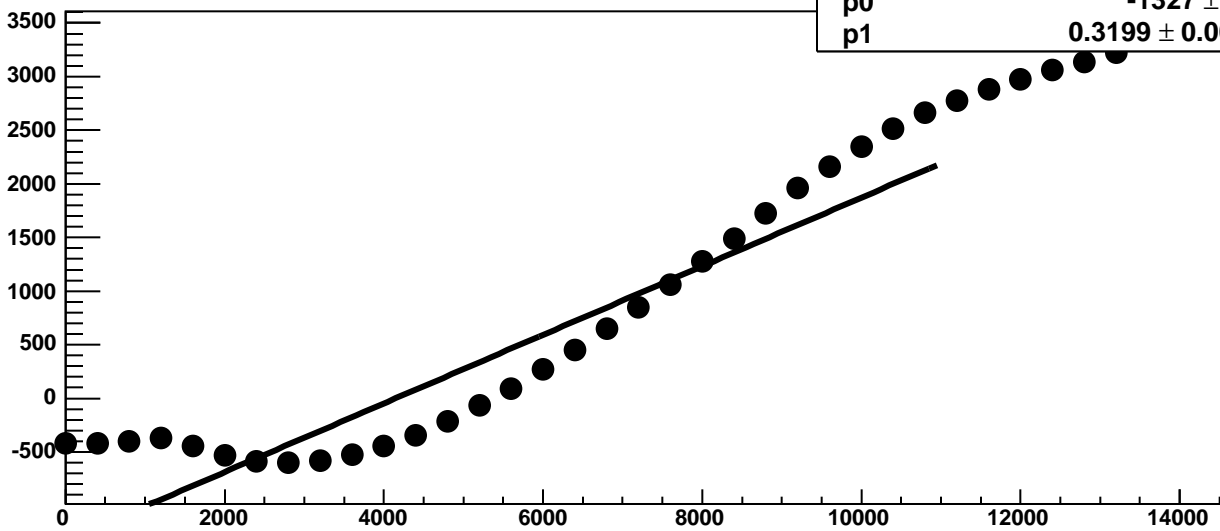
Chip 1, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC

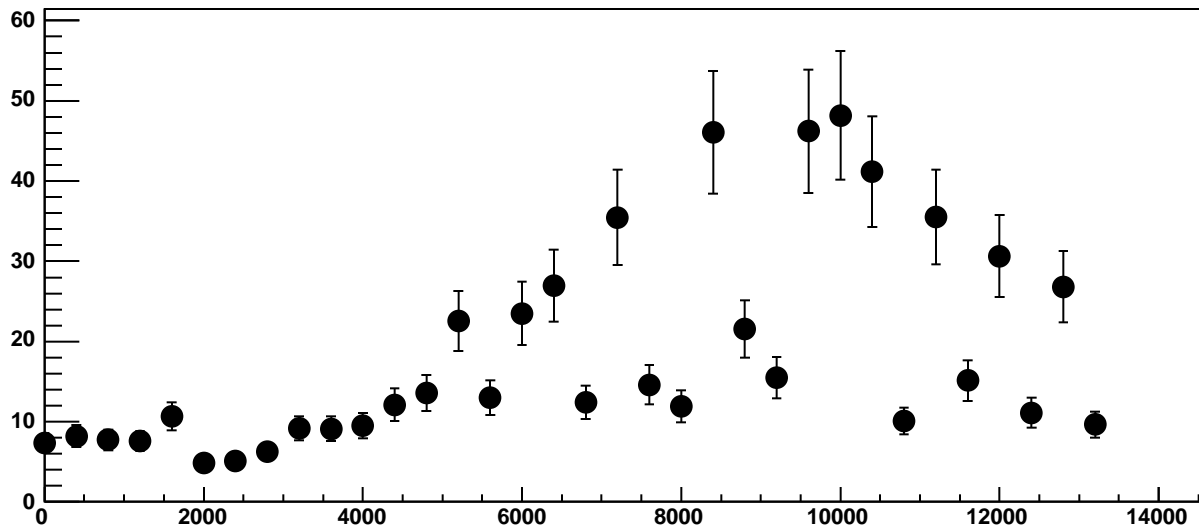


Chip 1, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

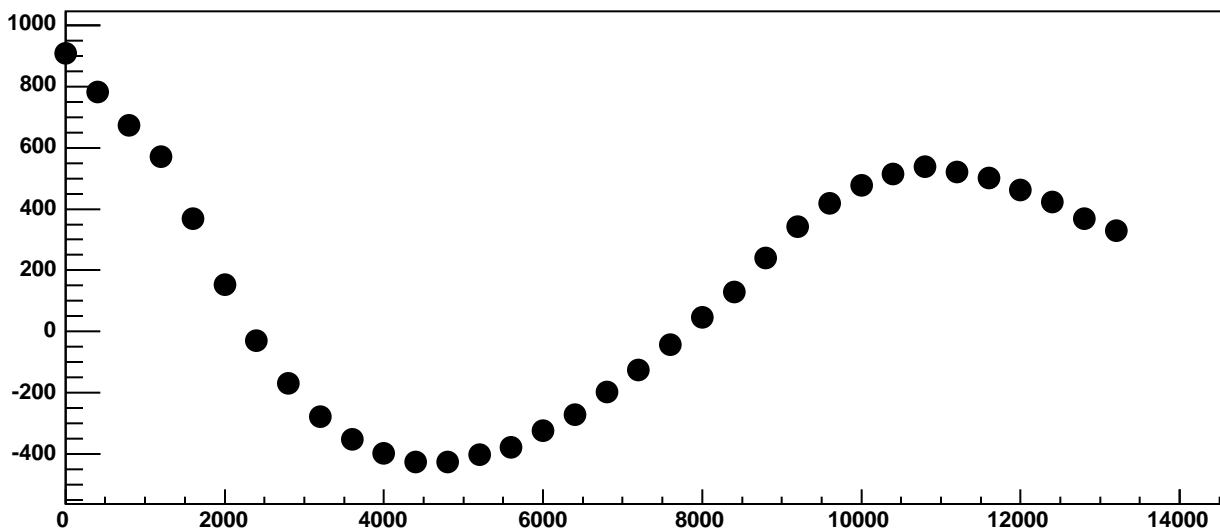


χ^2 / ndf 3.892e+05 / 23
p0 -1327 ± 0.8616
p1 0.3199 ± 0.0001914

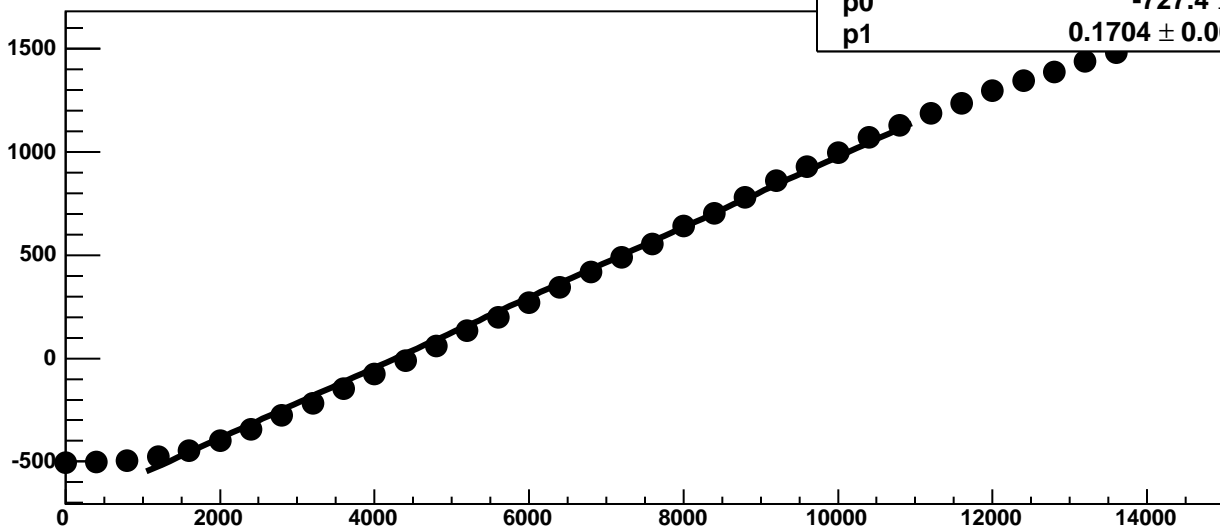
Chip 1, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



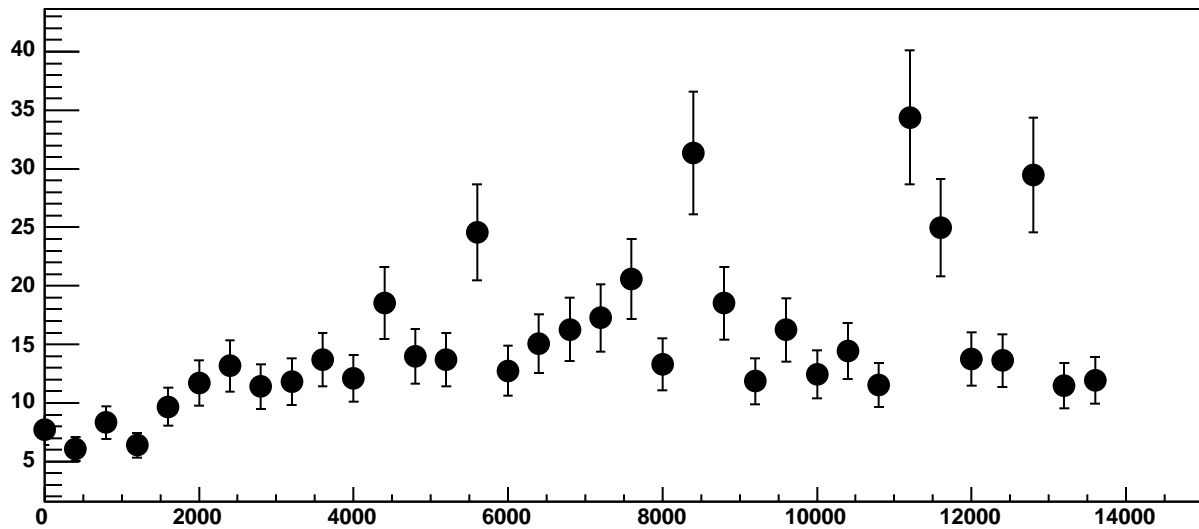
Chip 1, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



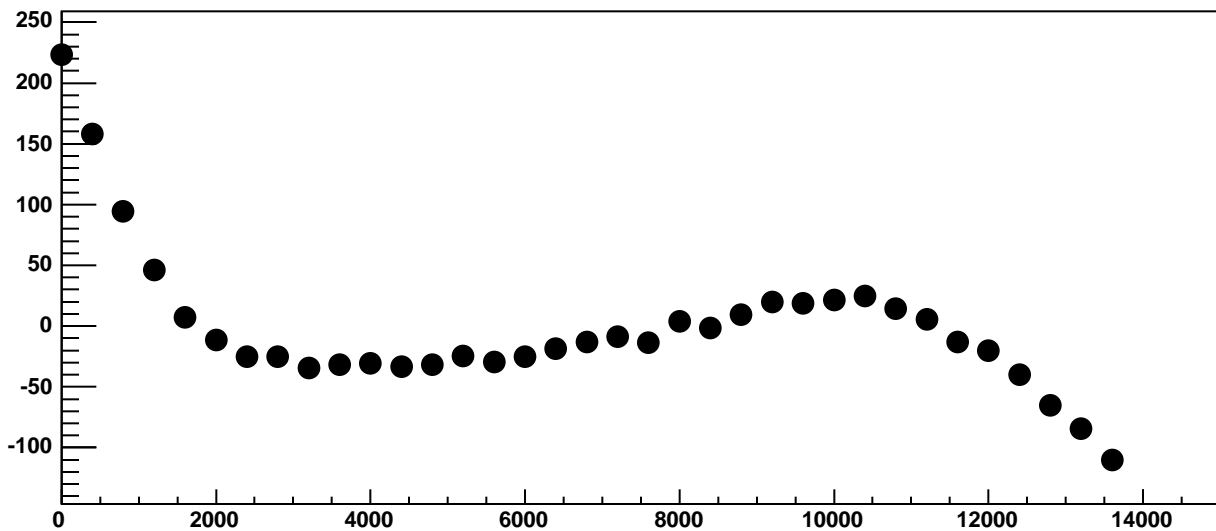
Chip 1, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



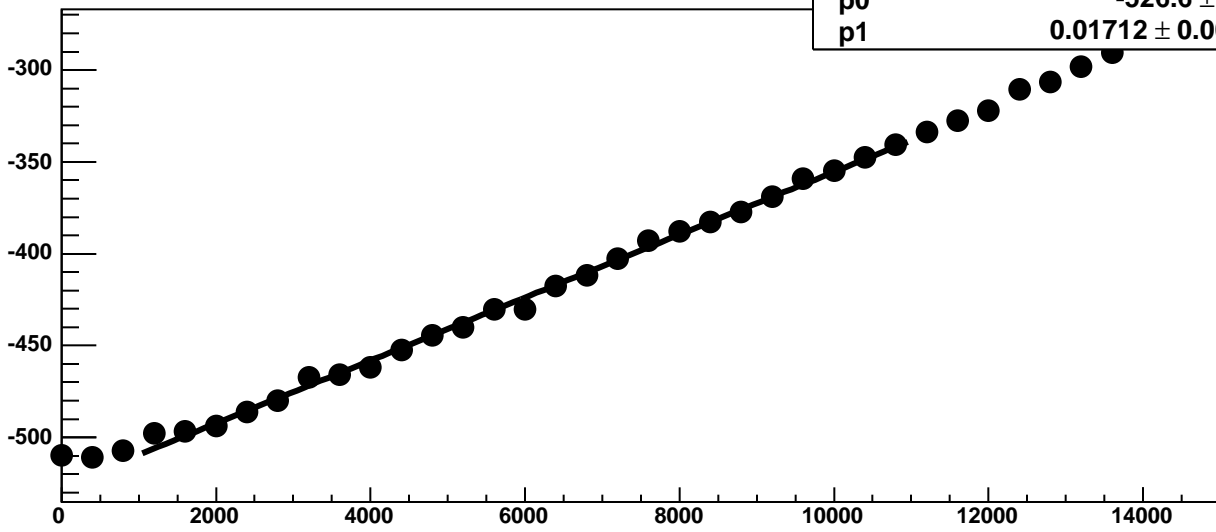
Chip 1, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

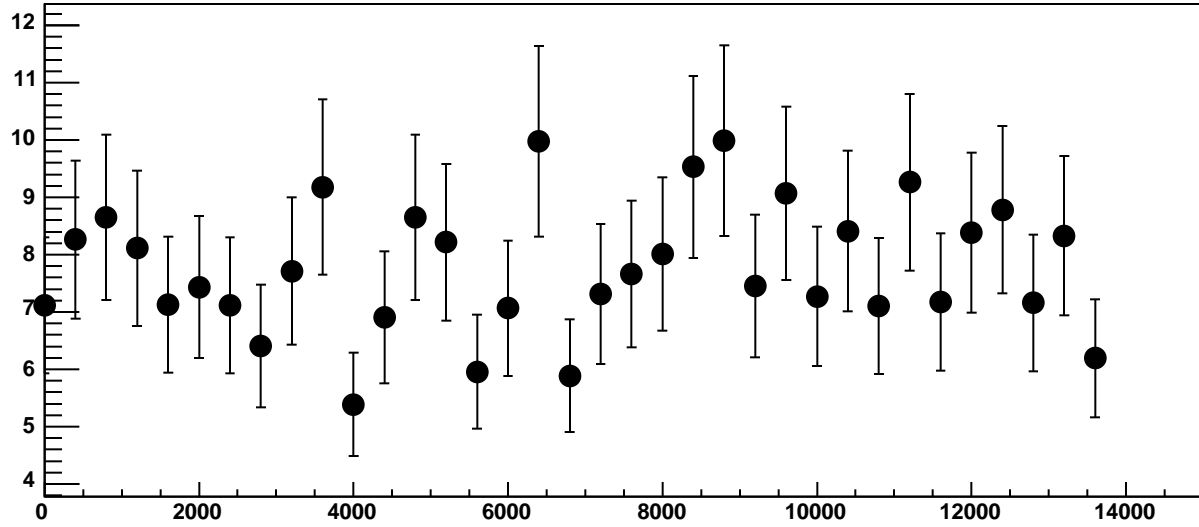


Chip 1, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

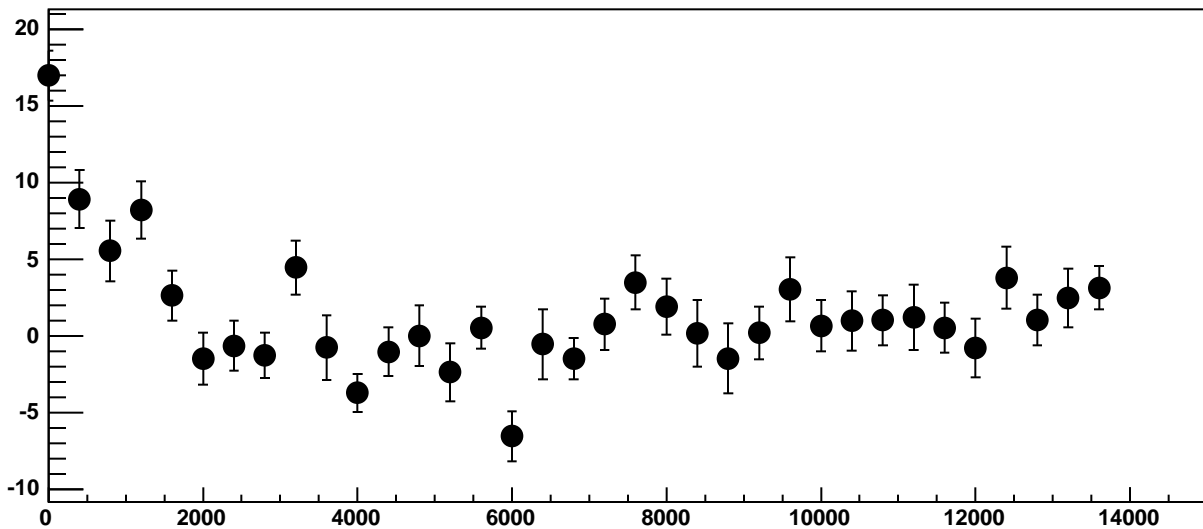


χ^2 / ndf 67.57 / 23
p0 -526.6 ± 0.7775
p1 0.01712 ± 0.0001209

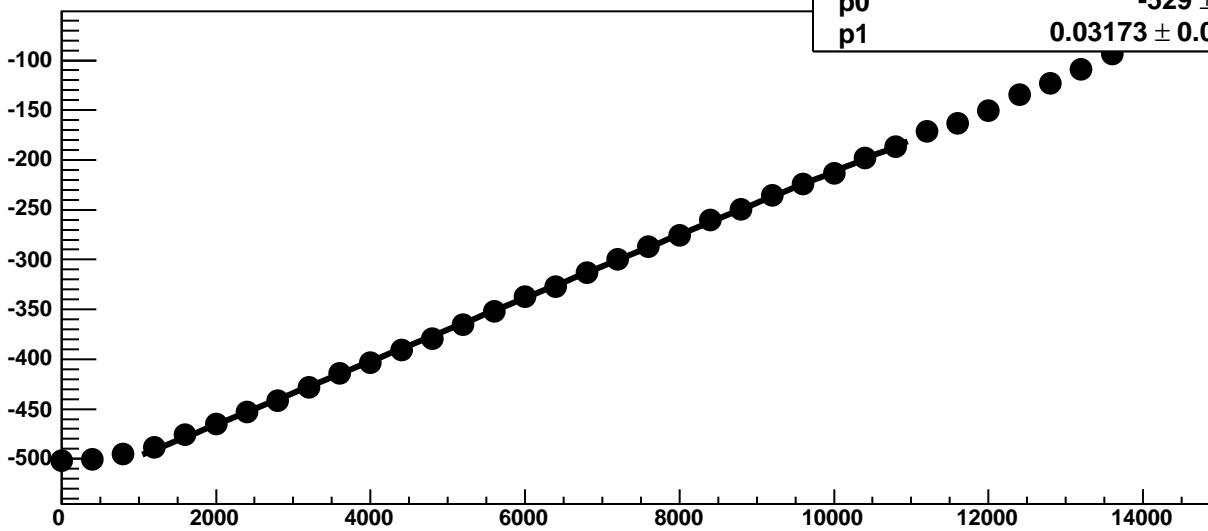
Chip 1, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

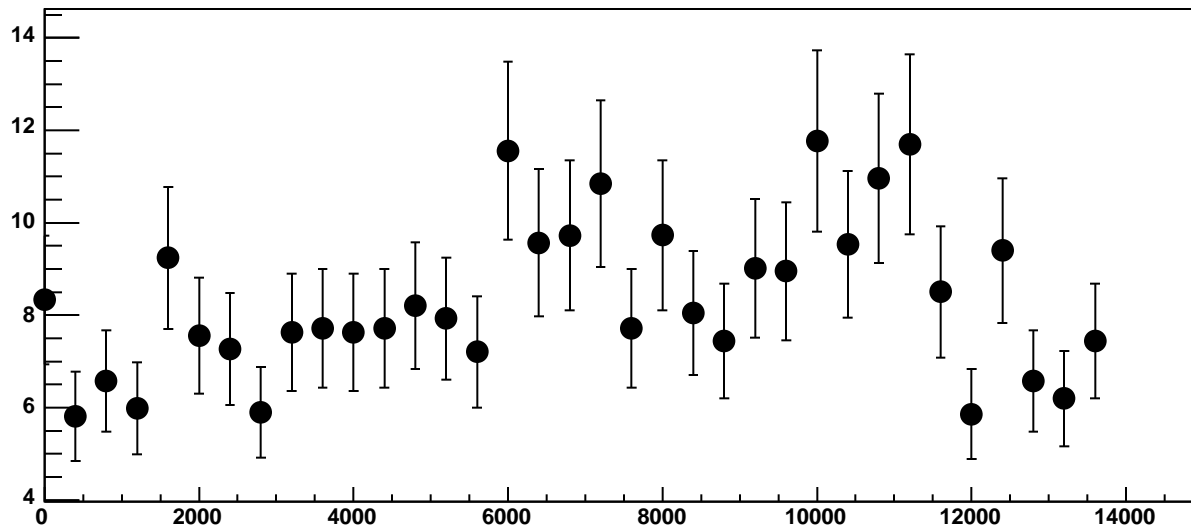


Chip 1, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC

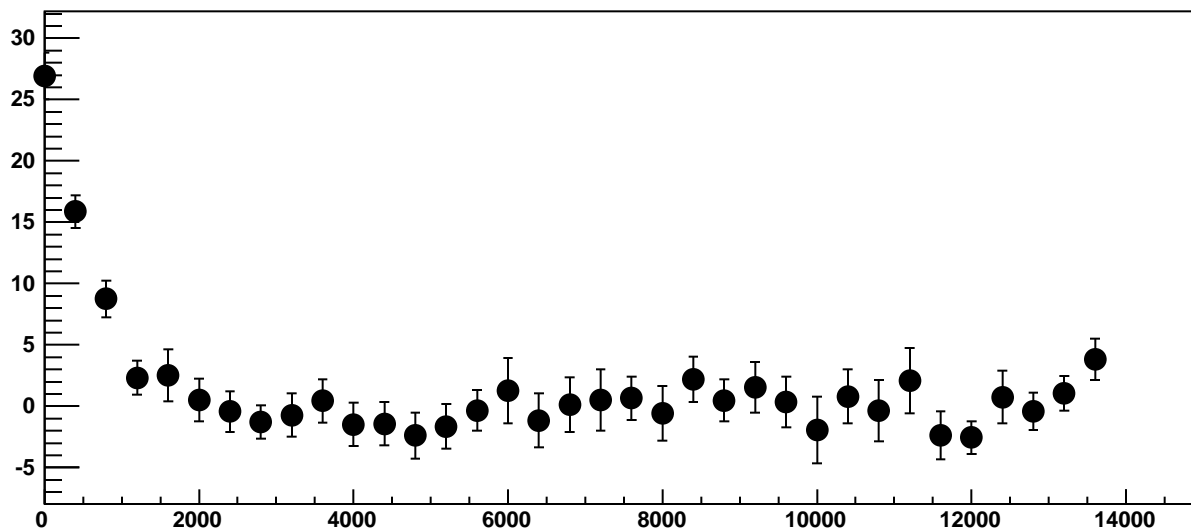


χ^2 / ndf 12.98 / 23
p0 -529 ± 0.7962
p1 0.03173 ± 0.0001313

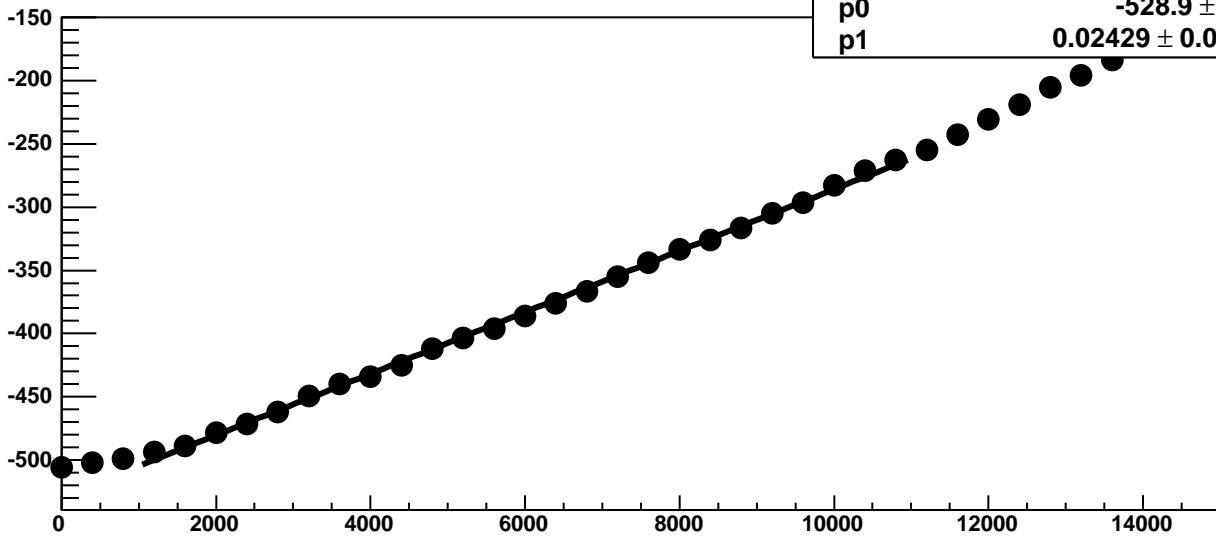
Chip 1, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



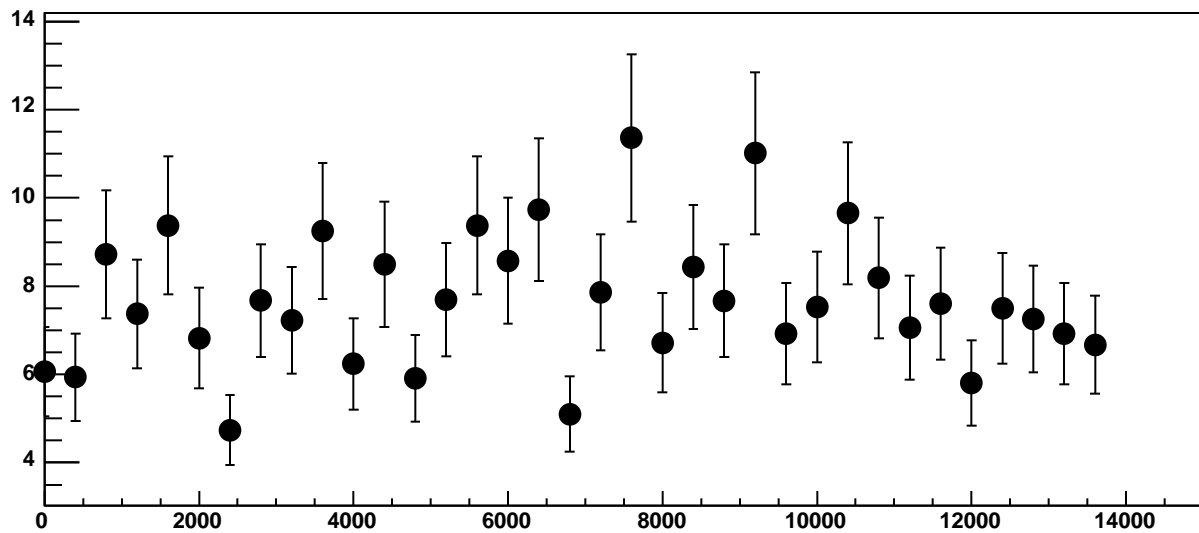
Chip 1, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC



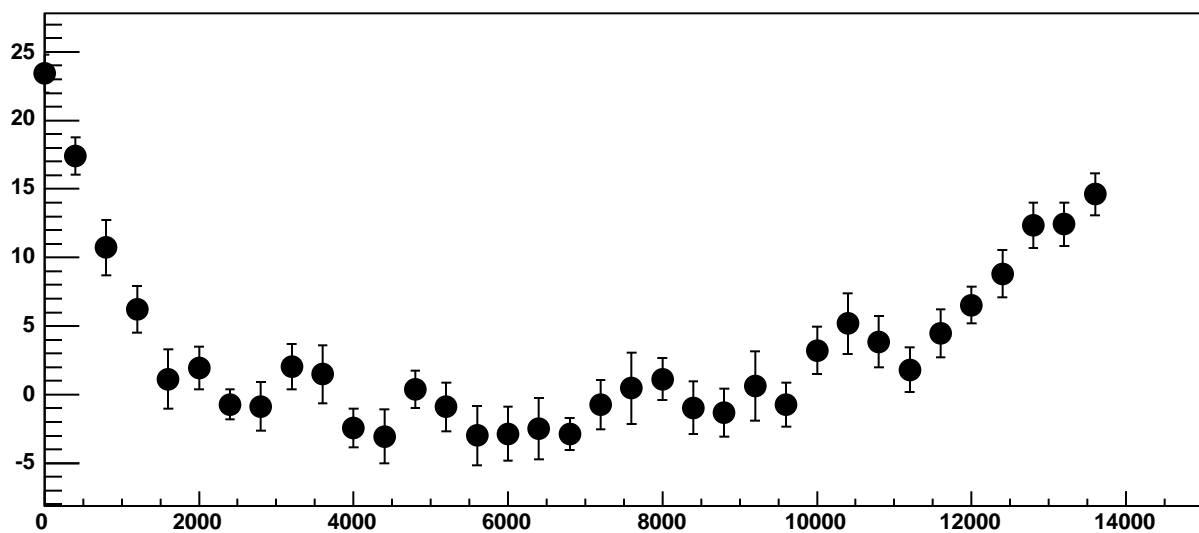
Chip 1, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



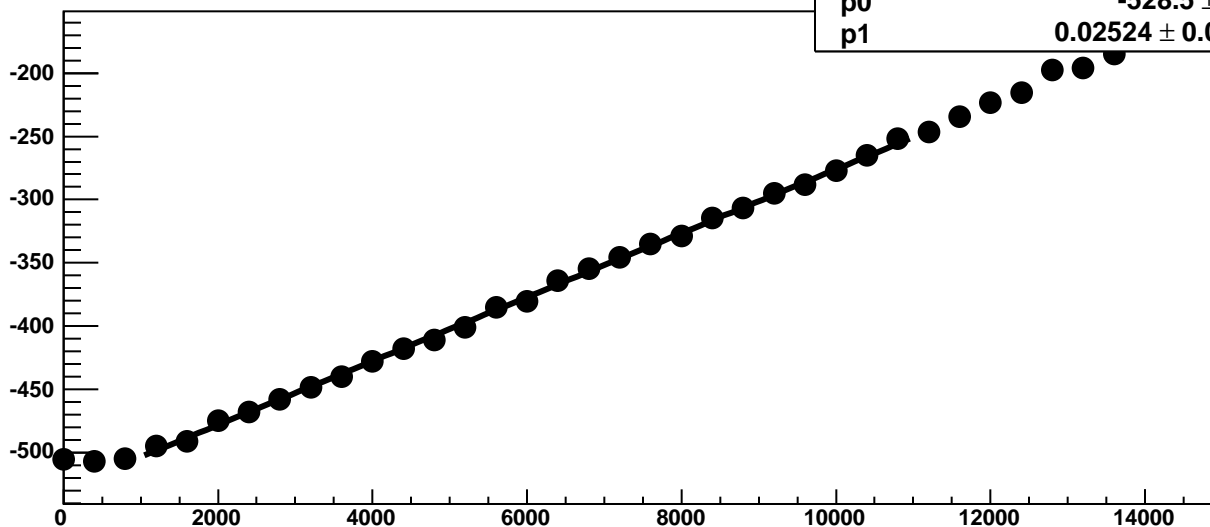
Chip 1, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

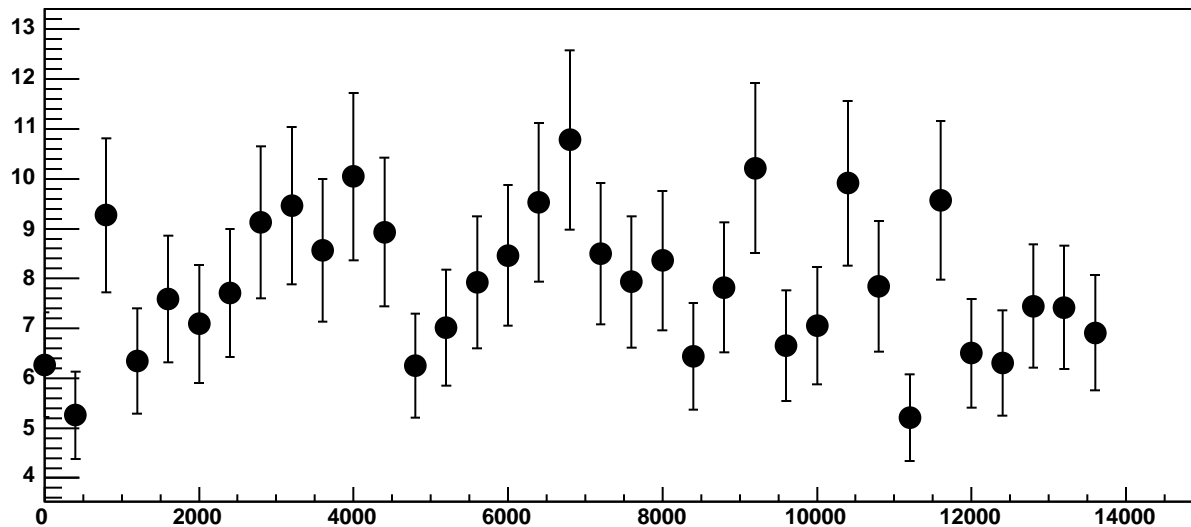


Chip 1, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

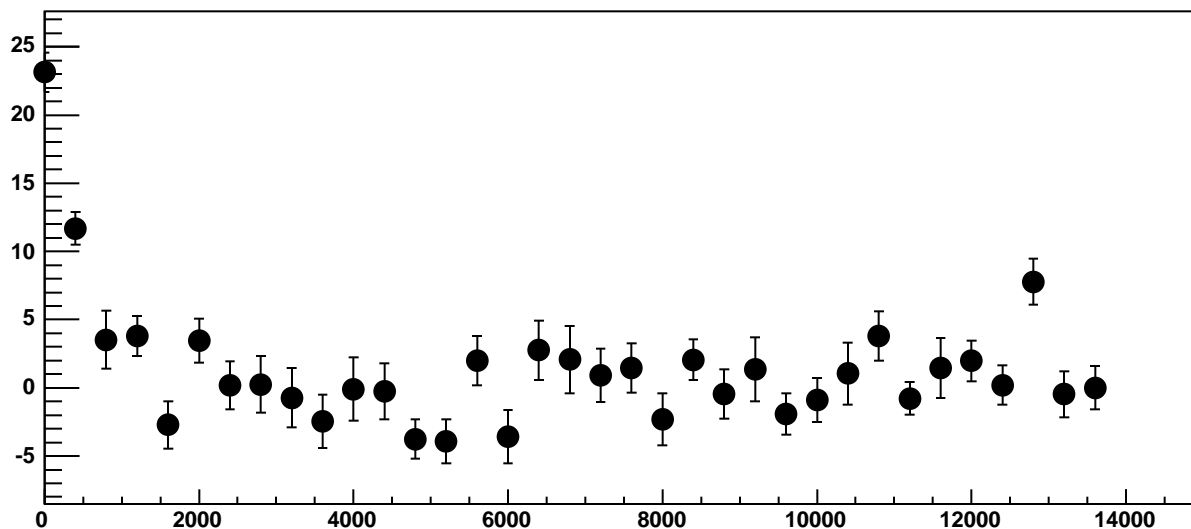


χ^2 / ndf 46.3 / 23
p0 -528.5 ± 0.8096
p1 0.02524 ± 0.0001221

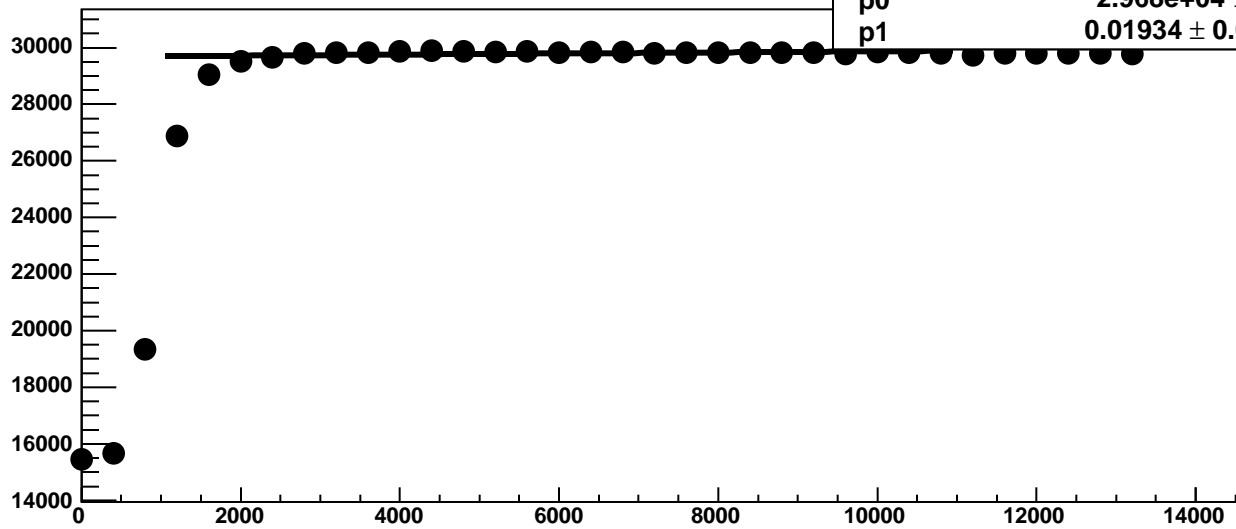
Chip 1, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



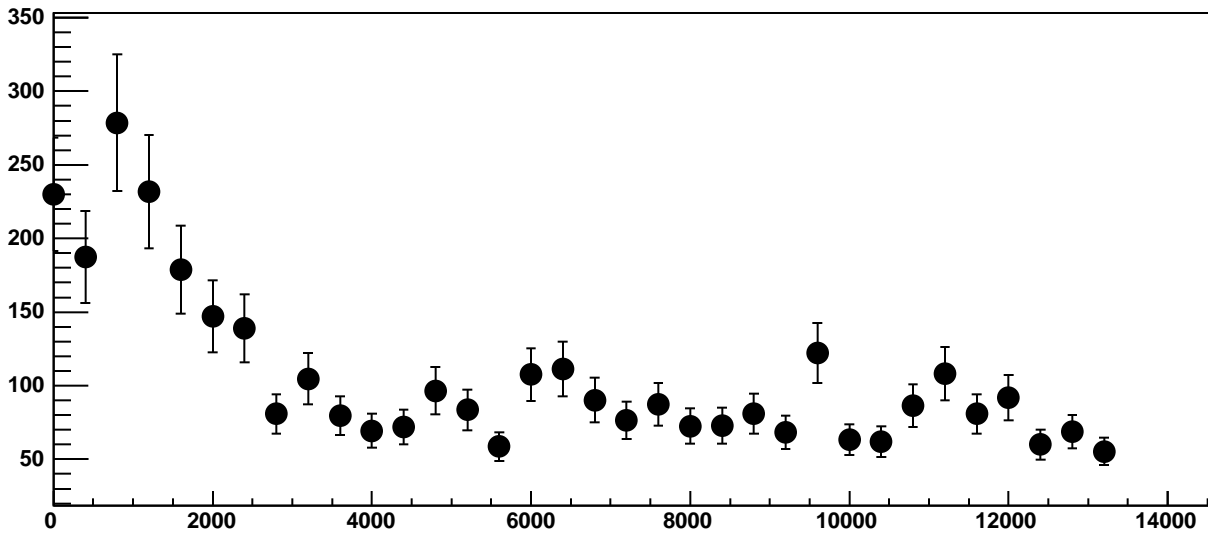
Chip 1, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



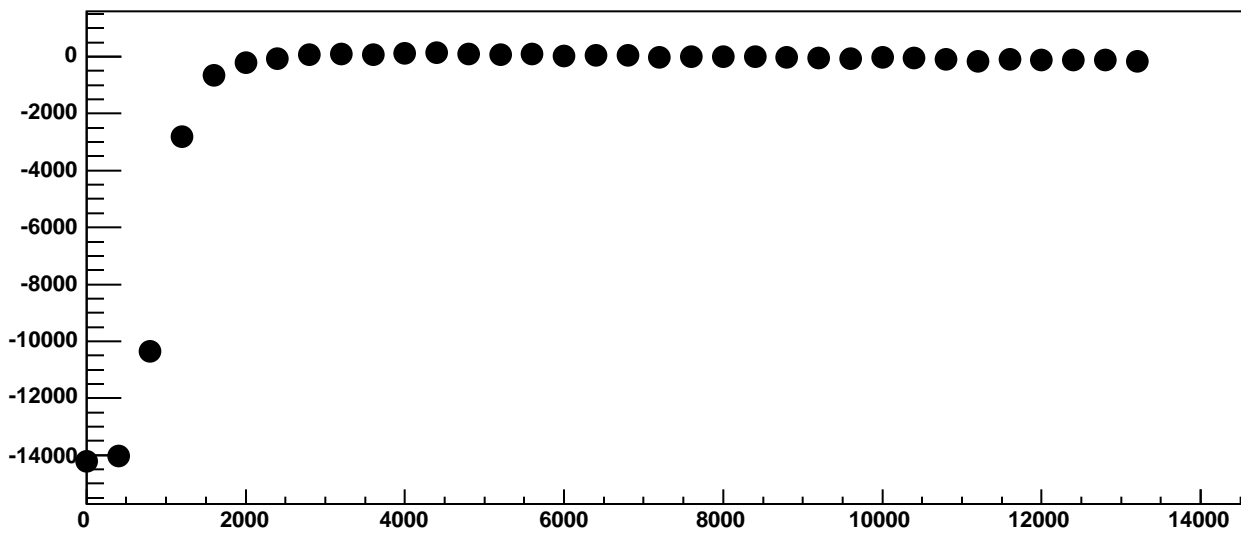
Chip 1, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC



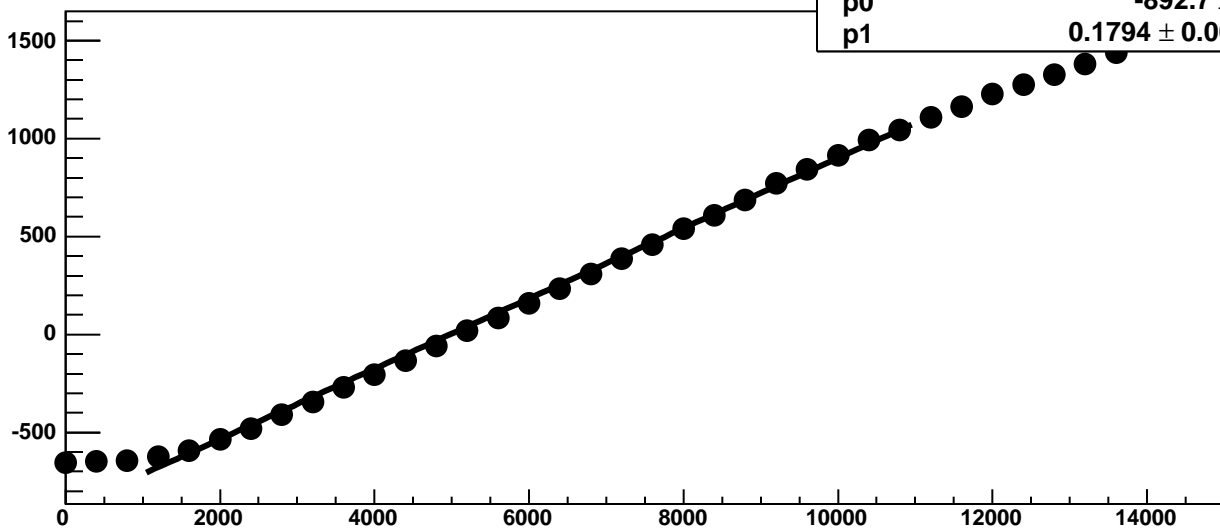
Chip 1, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC



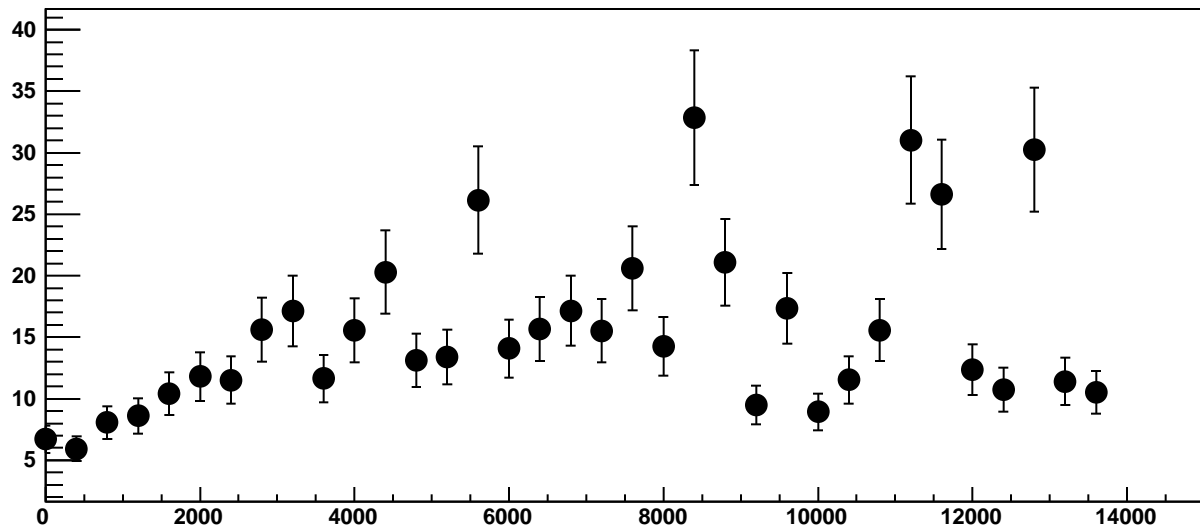
Chip 1, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC



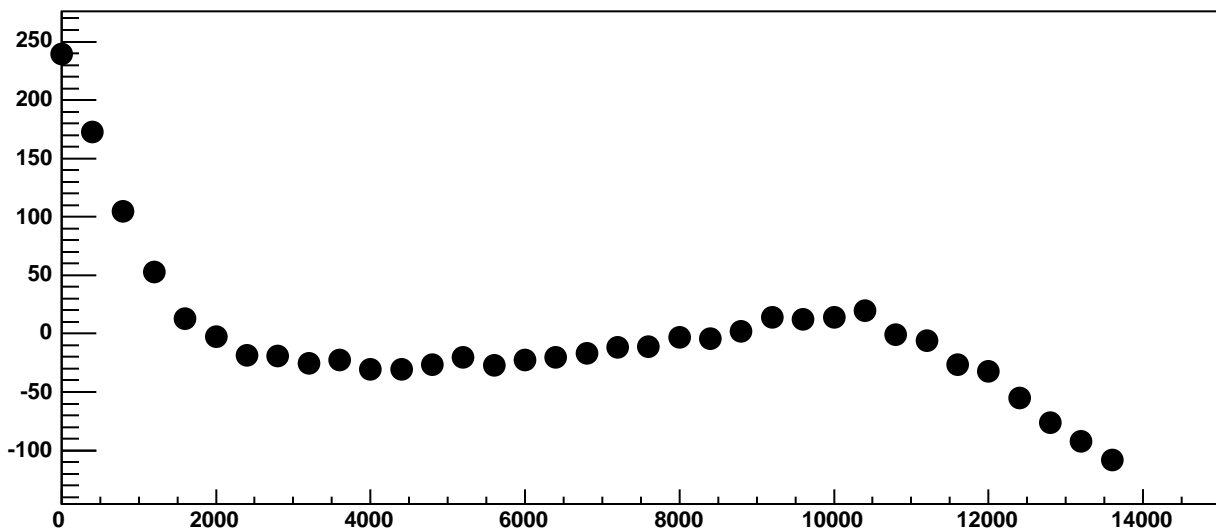
Chip 1, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



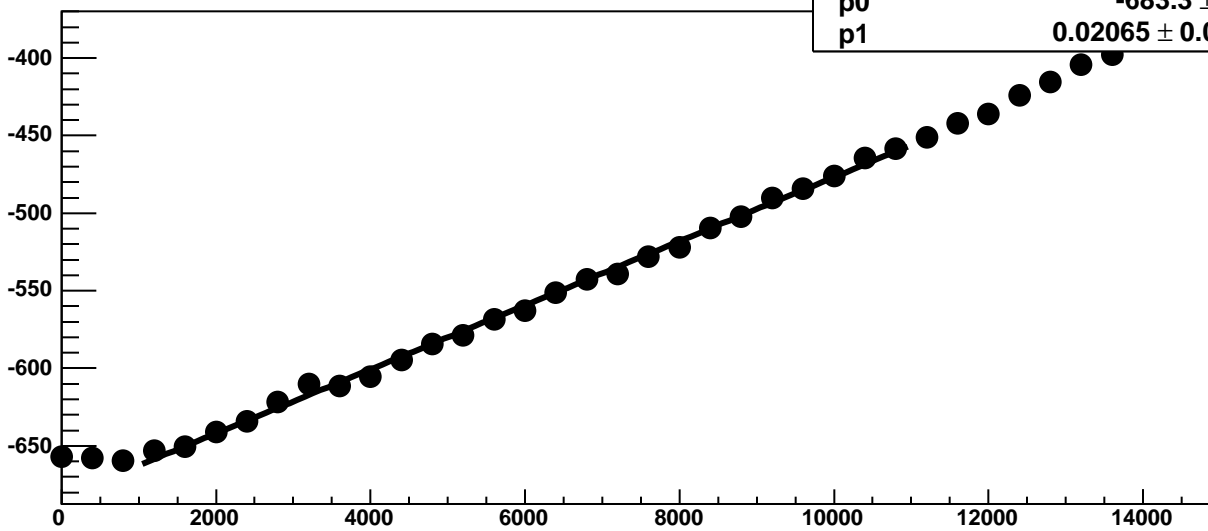
Chip 1, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

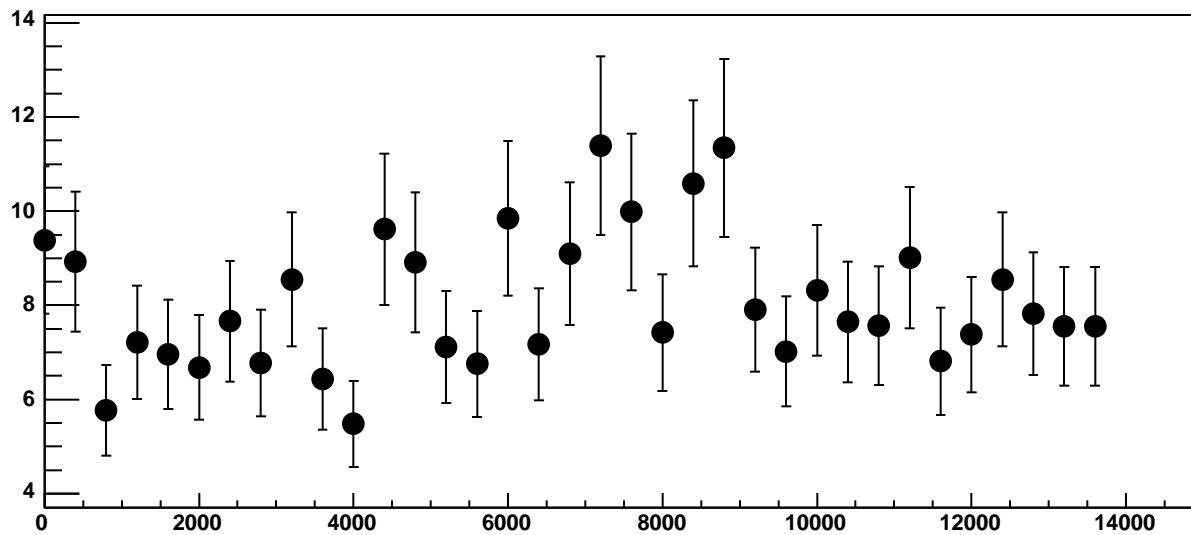


Chip 1, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

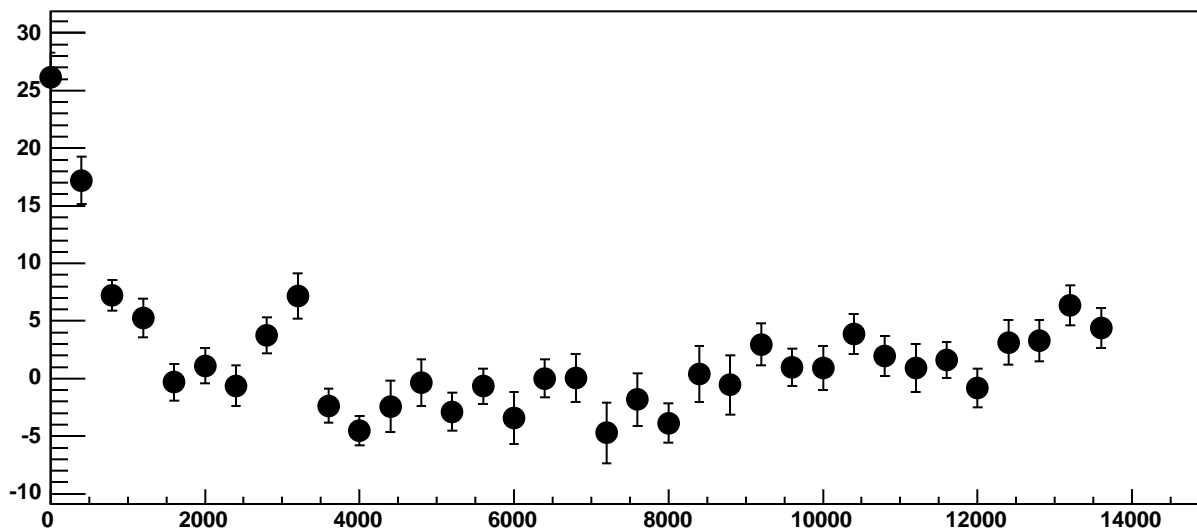


χ^2 / ndf 70.81 / 23
p0 -683.3 ± 0.7622
p1 0.02065 ± 0.0001201

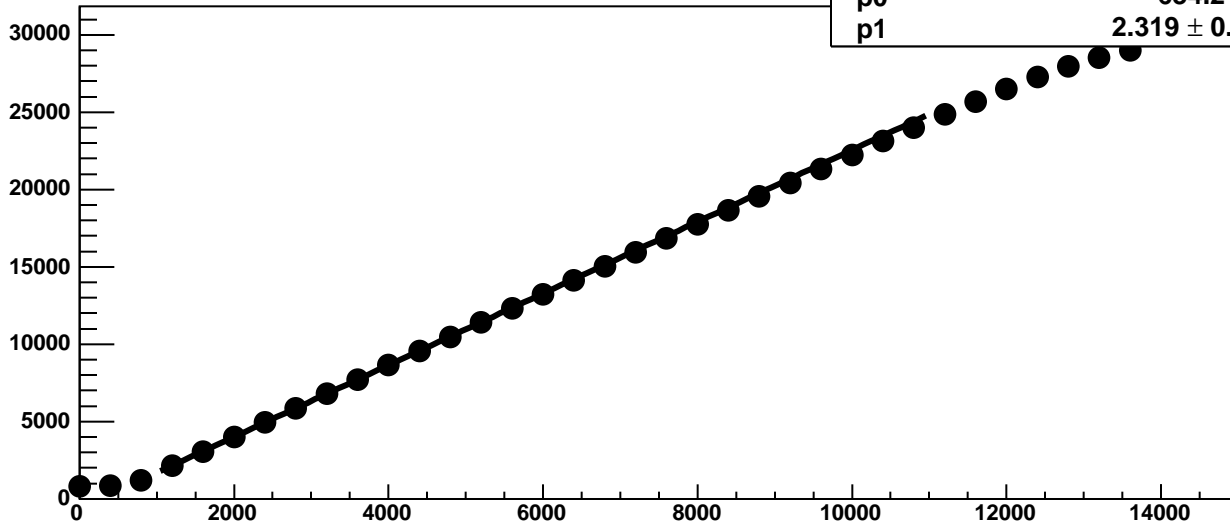
Chip 1, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

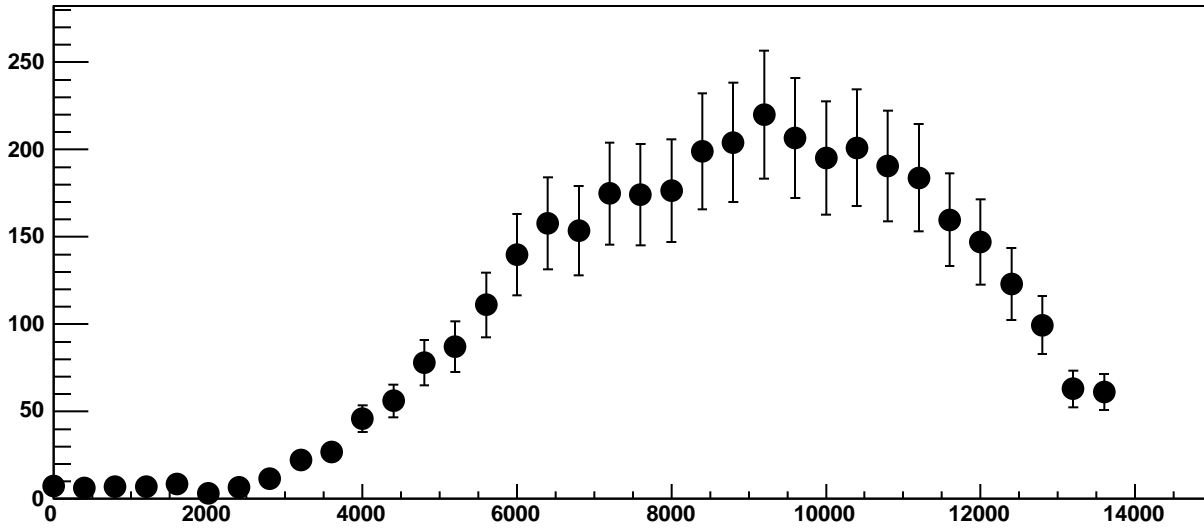


Chip 1, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC

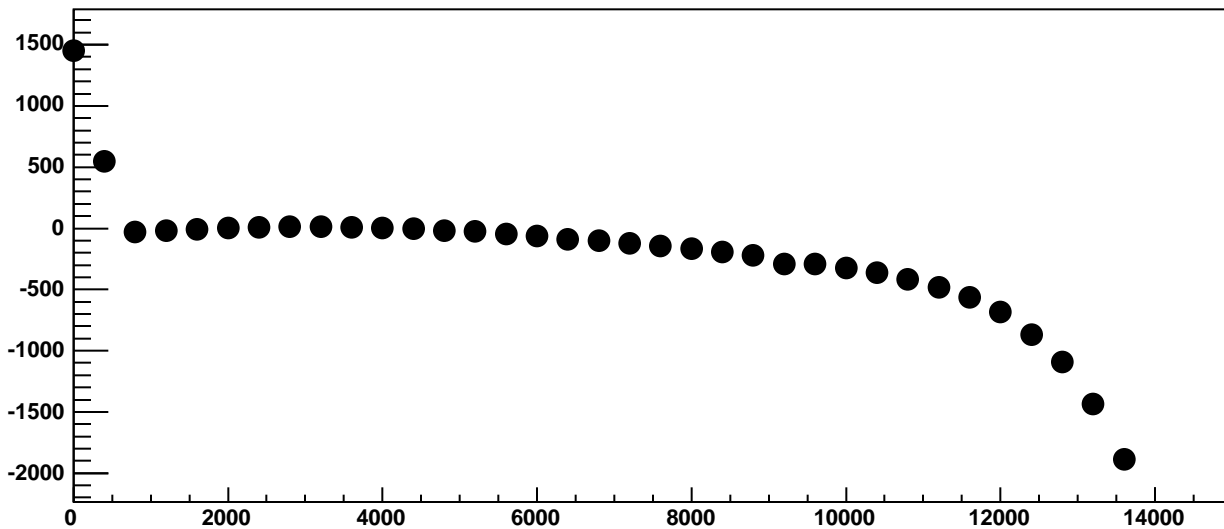


χ^2 / ndf 616.3 / 23
p0 -634.2 ± 2.103
p1 2.319 ± 0.000999

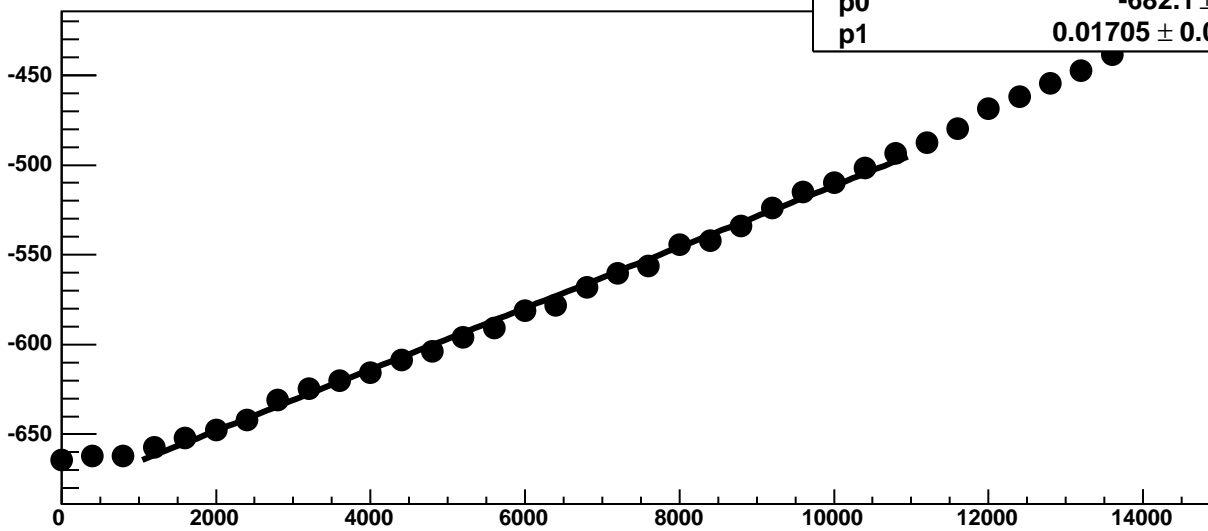
Chip 1, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC

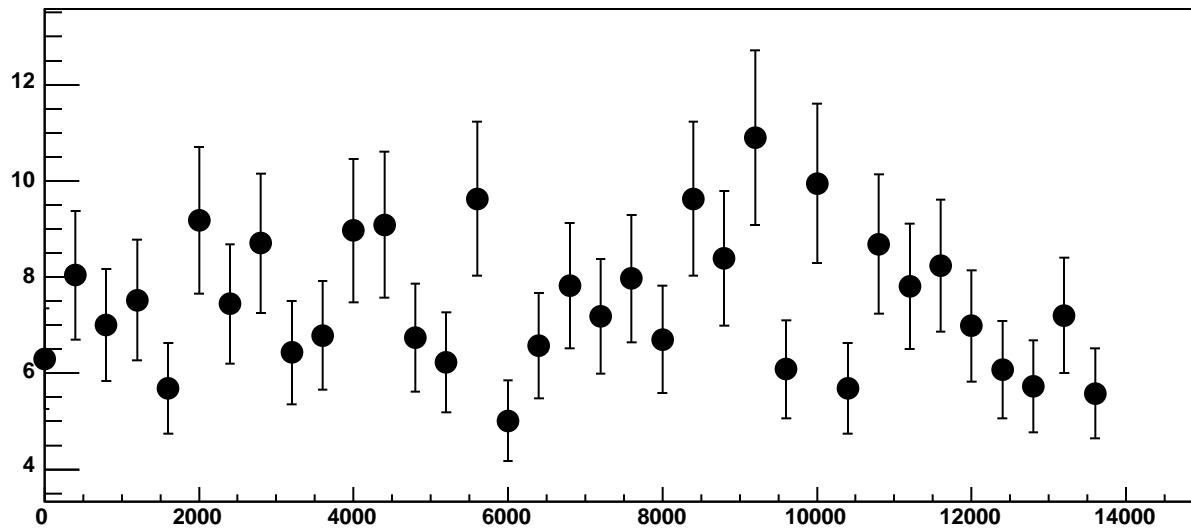


Chip 1, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

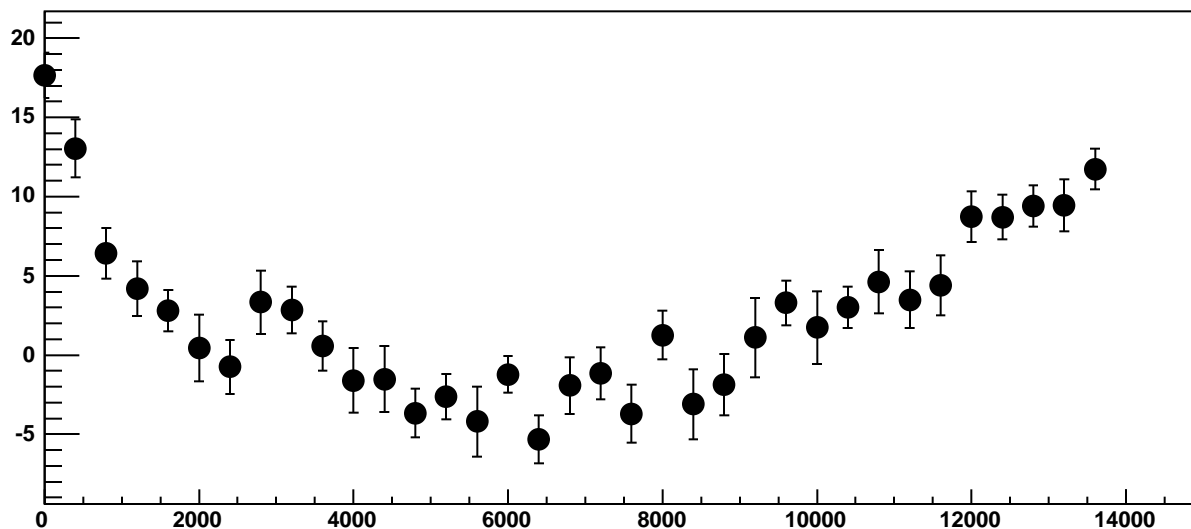


χ^2 / ndf 71.02 / 23
p0 -682.1 ± 0.7664
p1 0.01705 ± 0.0001169

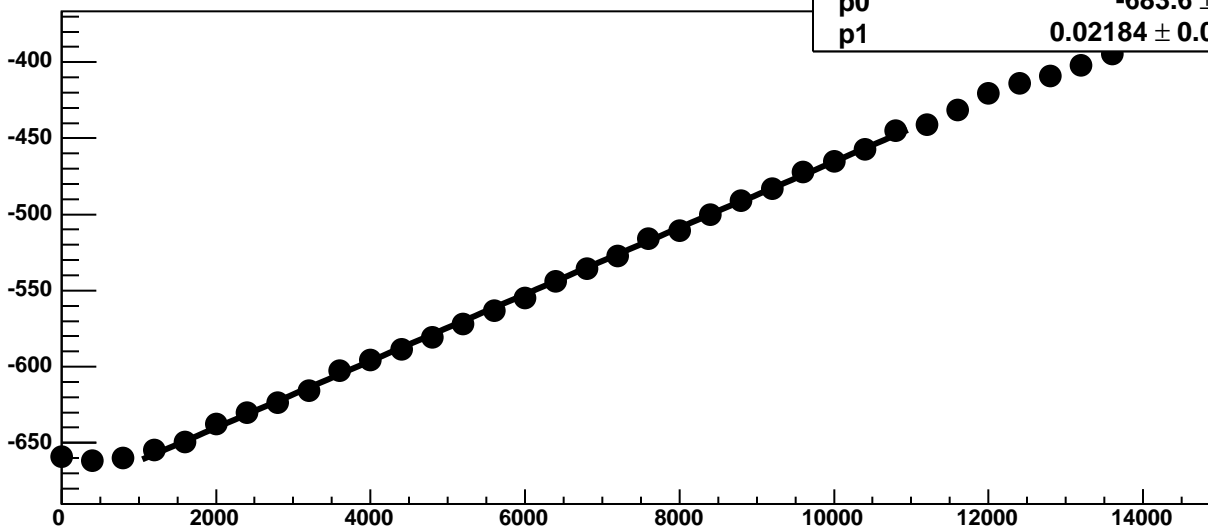
Chip 1, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

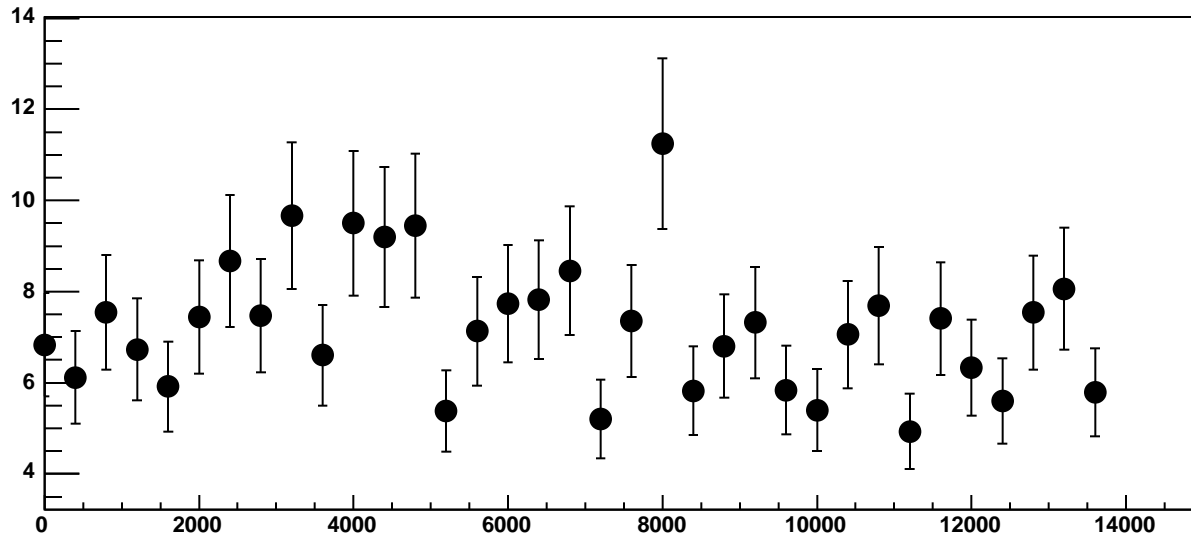


Chip 1, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

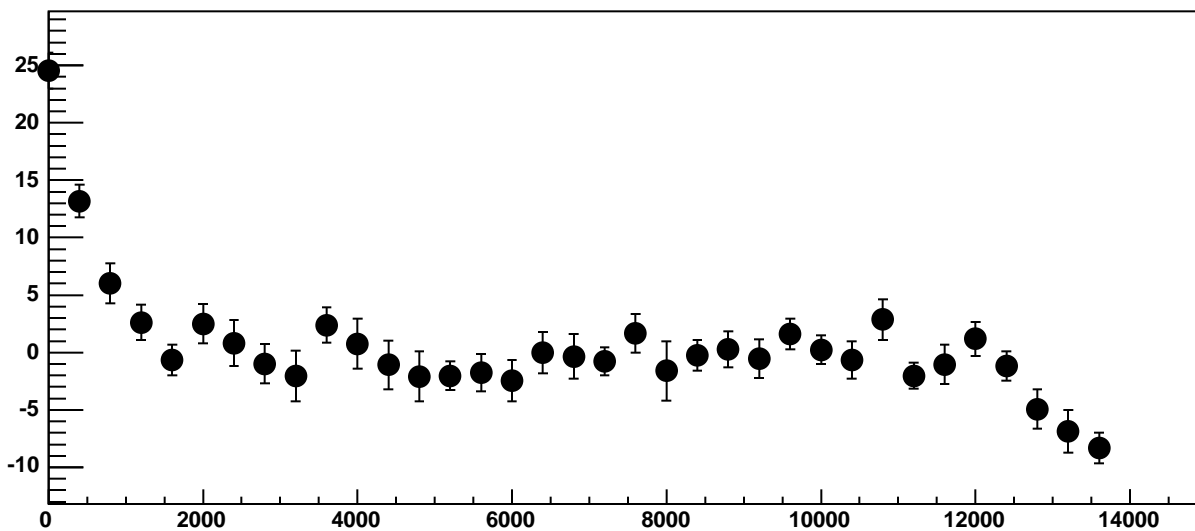


χ^2 / ndf 22.36 / 23
p0 -683.6 ± 0.7573
p1 0.02184 ± 0.0001095

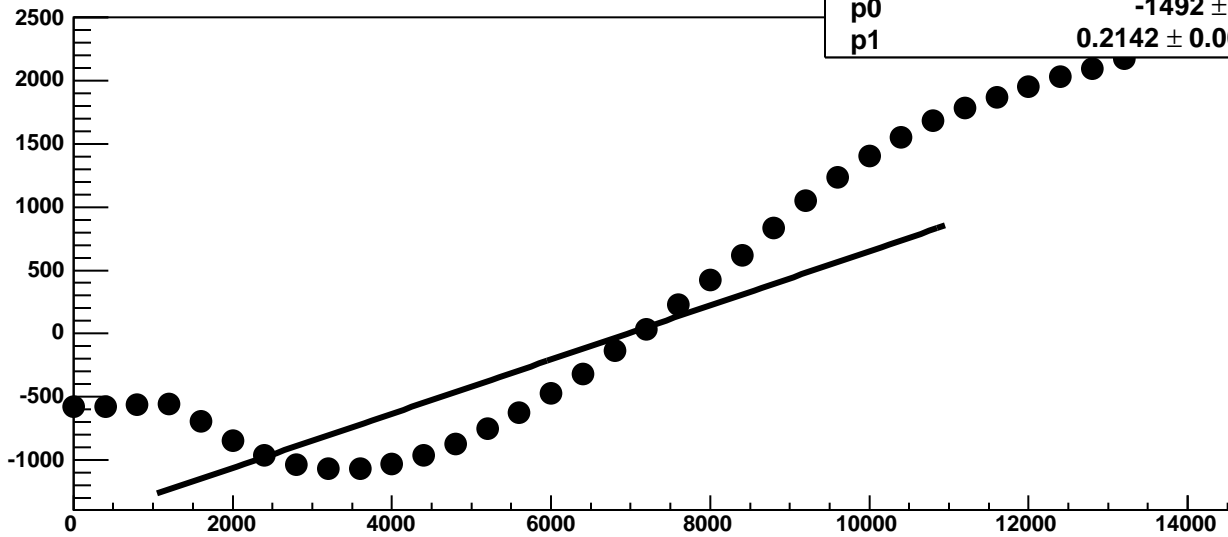
Chip 1, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

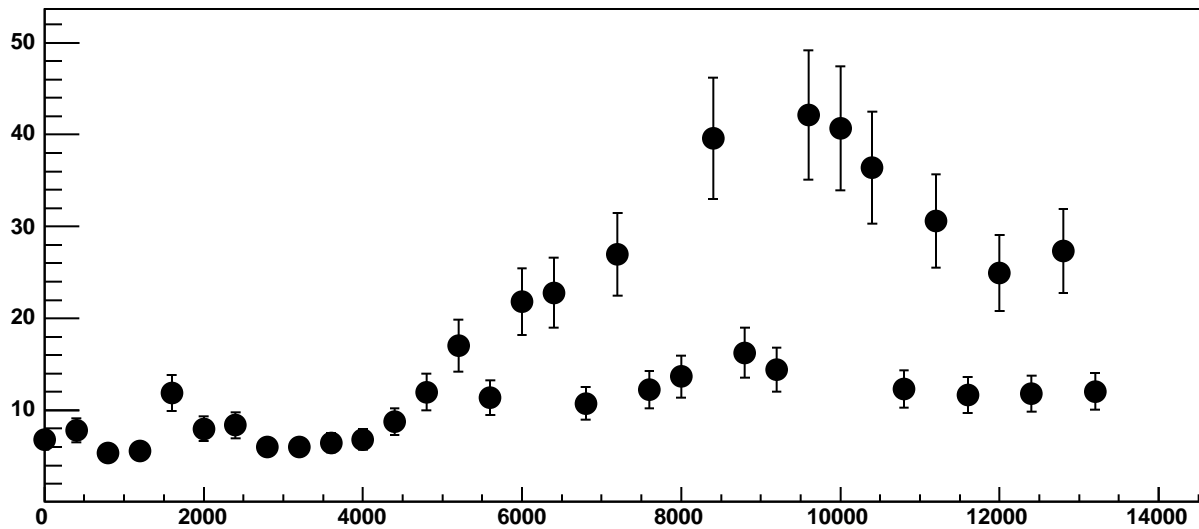


Chip 1, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC

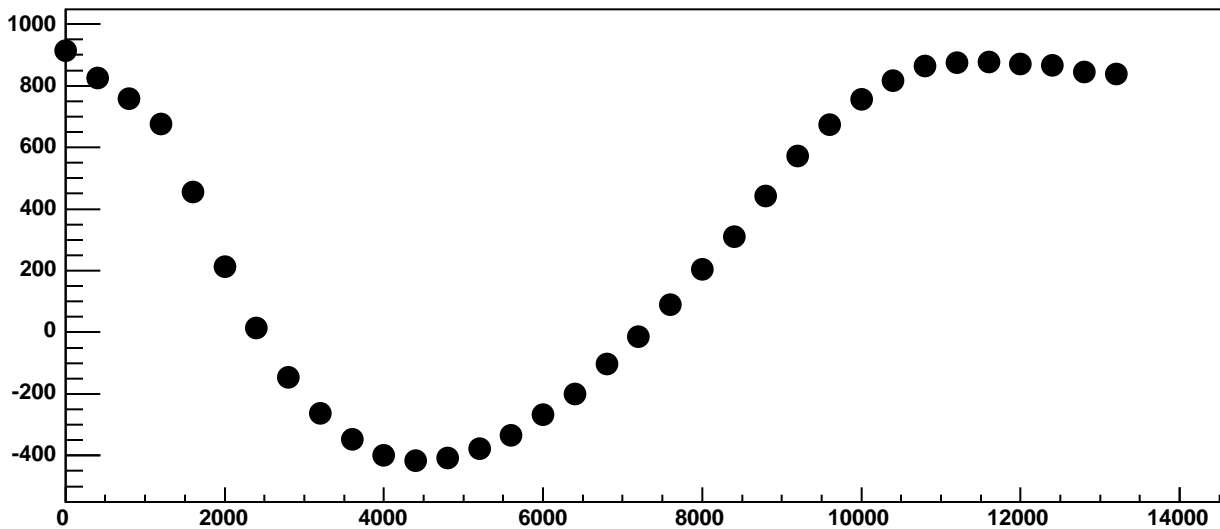


χ^2 / ndf 7.541e+05 / 23
p0 -1492 ± 0.9015
p1 0.2142 ± 0.0001963

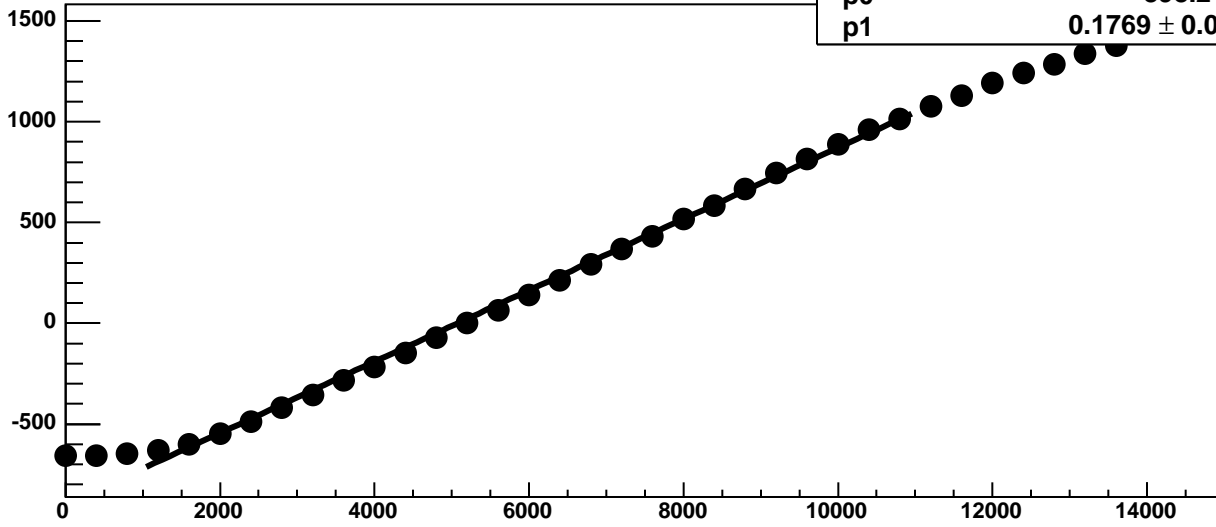
Chip 1, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC

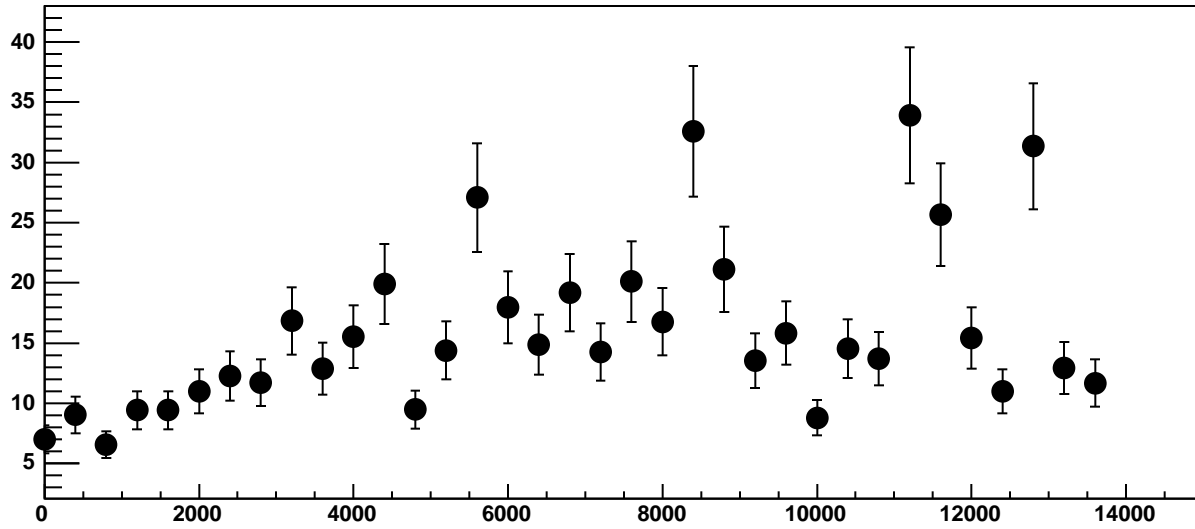


Chip 1, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

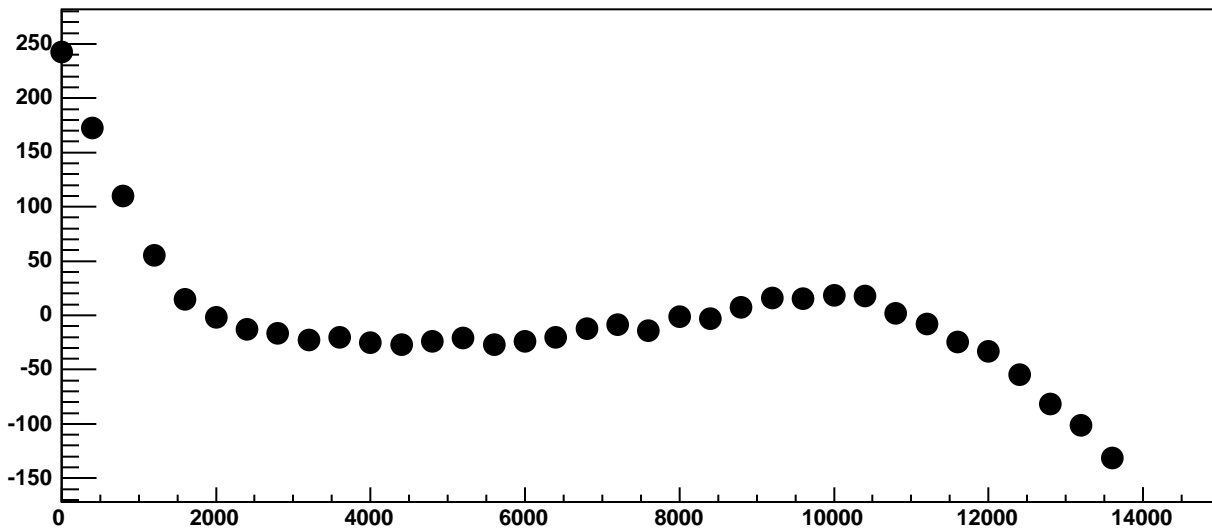


χ^2 / ndf 1355 / 23
p0 -898.2 ± 1.217
p1 0.1769 ± 0.0001927

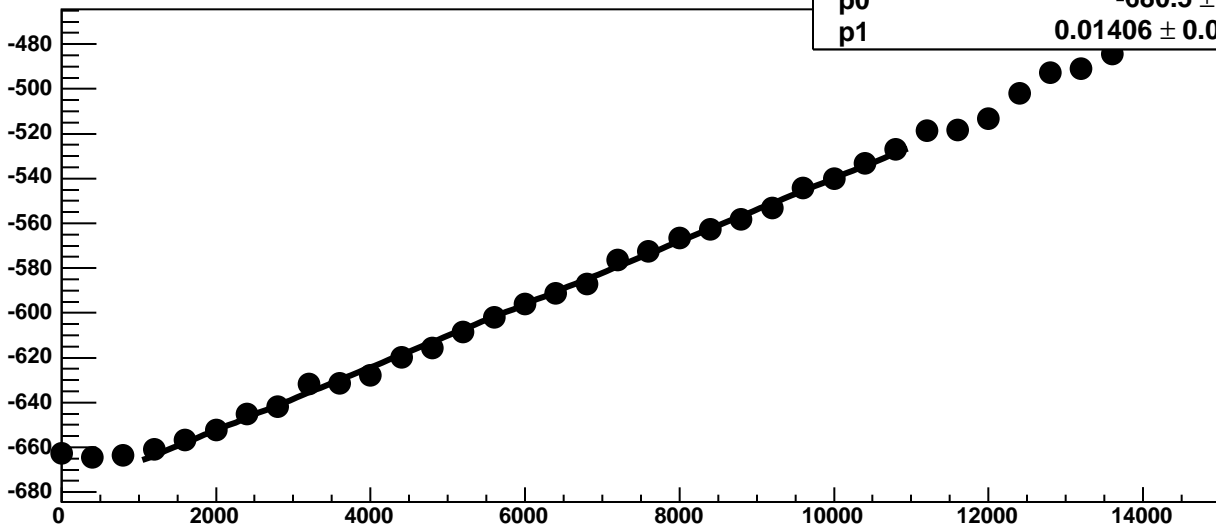
Chip 1, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

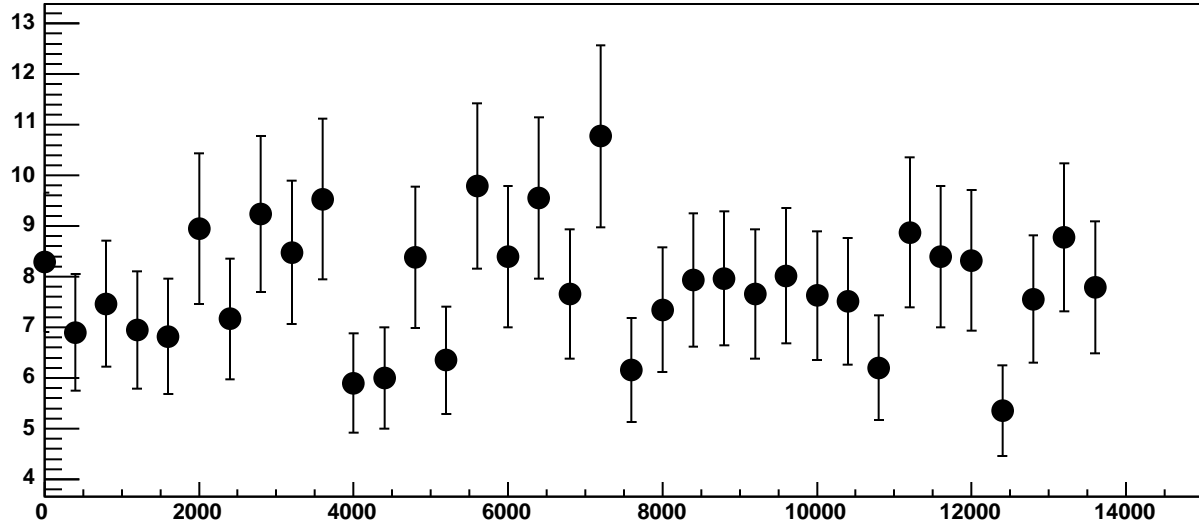


Chip 1, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

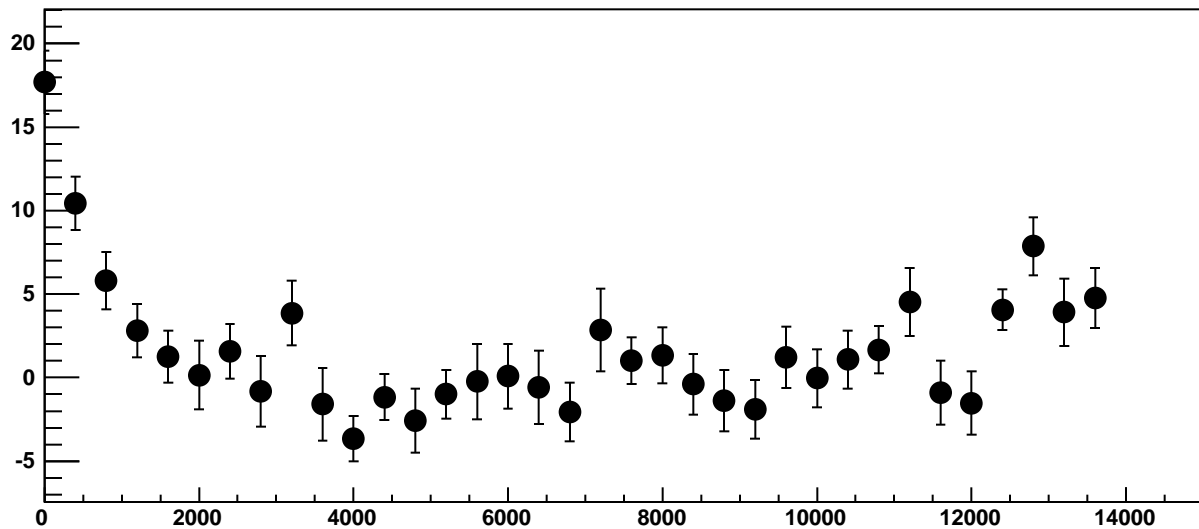


χ^2 / ndf 27.46 / 23
p0 -680.5 ± 0.7865
p1 0.01406 ± 0.0001171

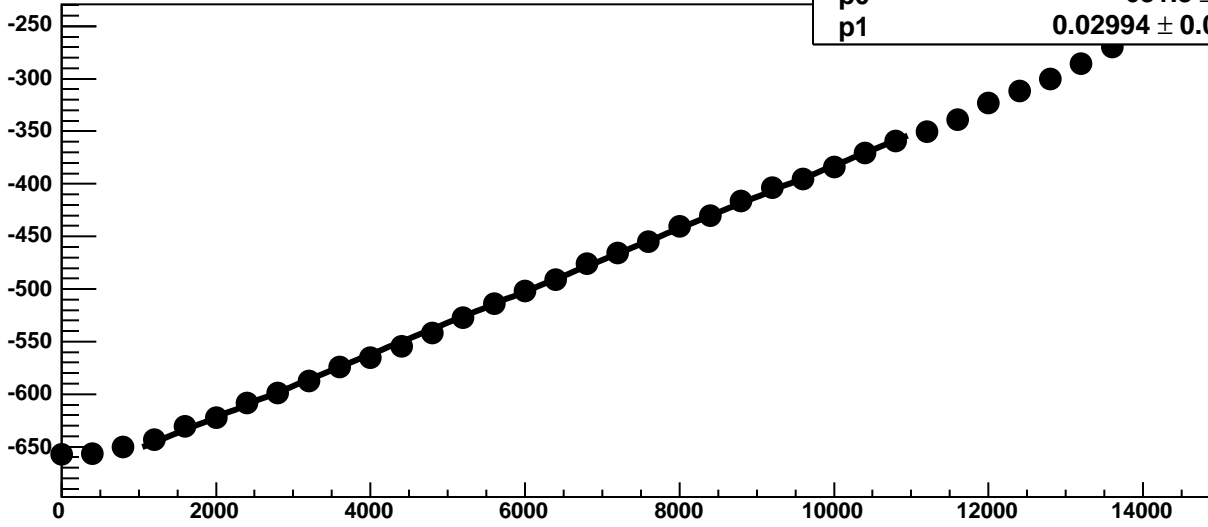
Chip 1, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

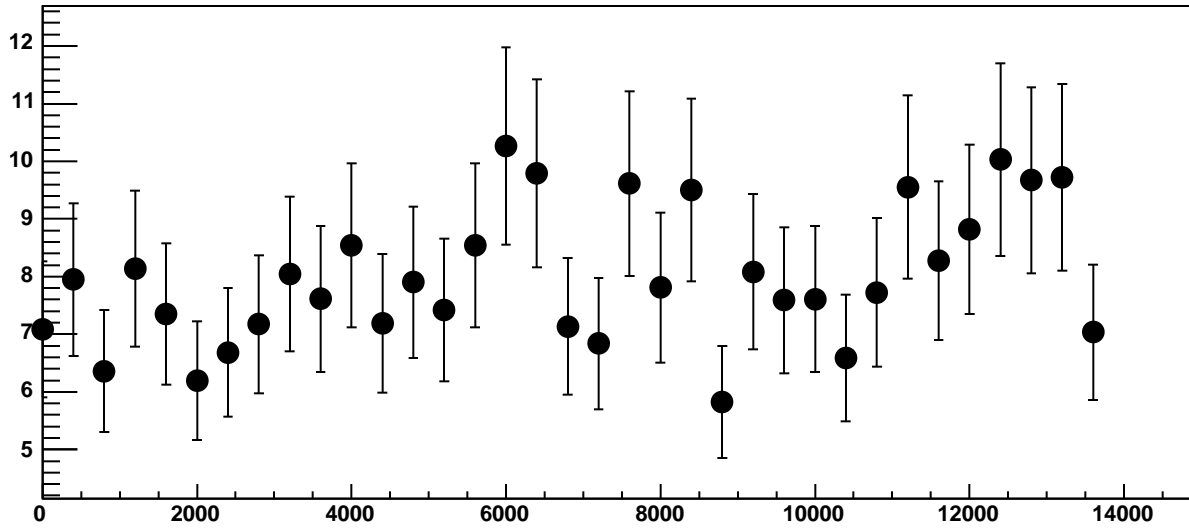


Chip 1, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC

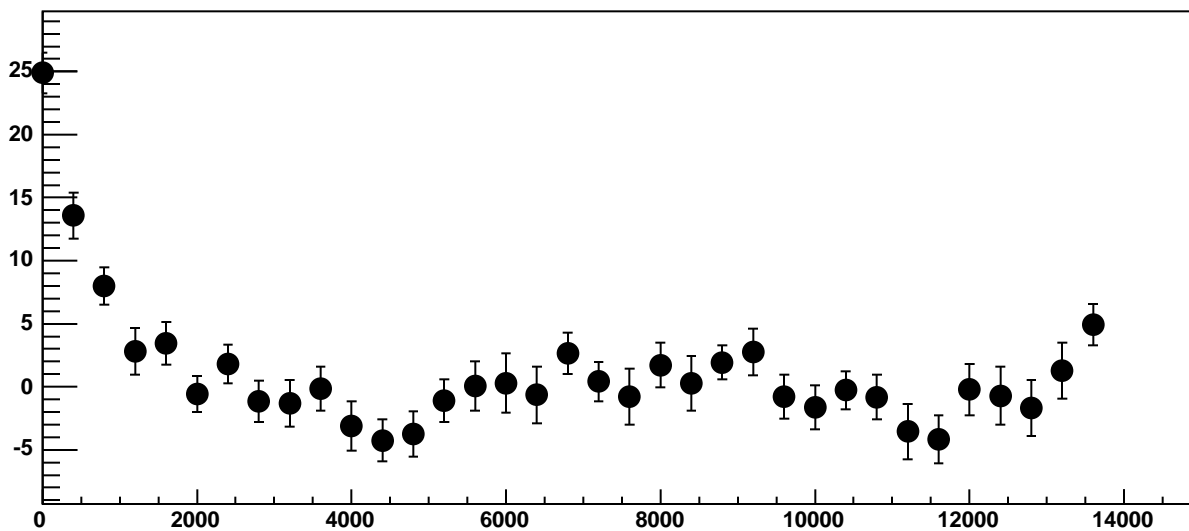


χ^2 / ndf 32.37 / 23
p0 -681.8 ± 0.7757
p1 0.02994 ± 0.0001162

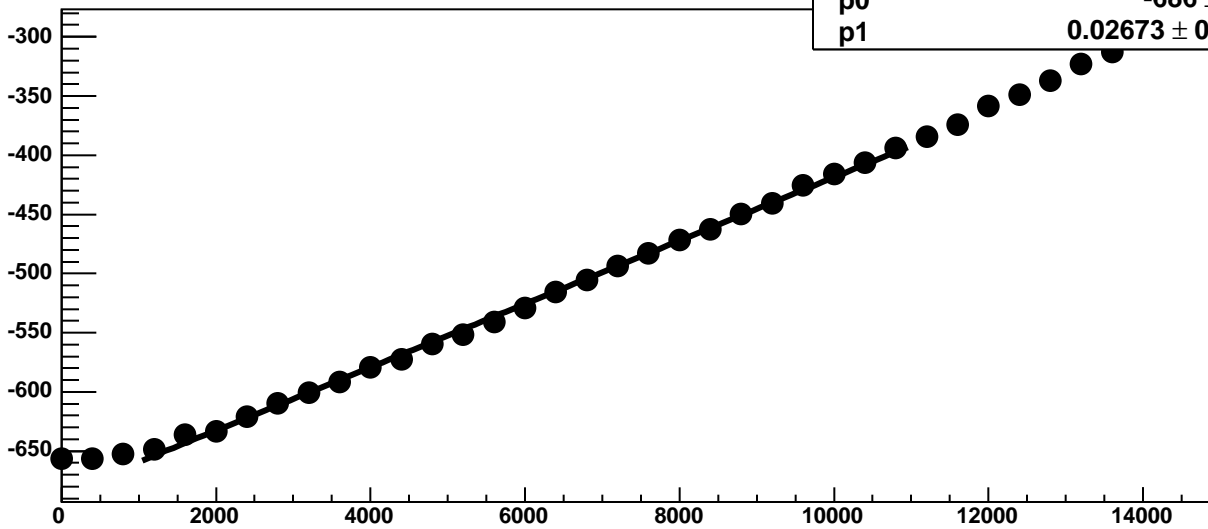
Chip 1, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



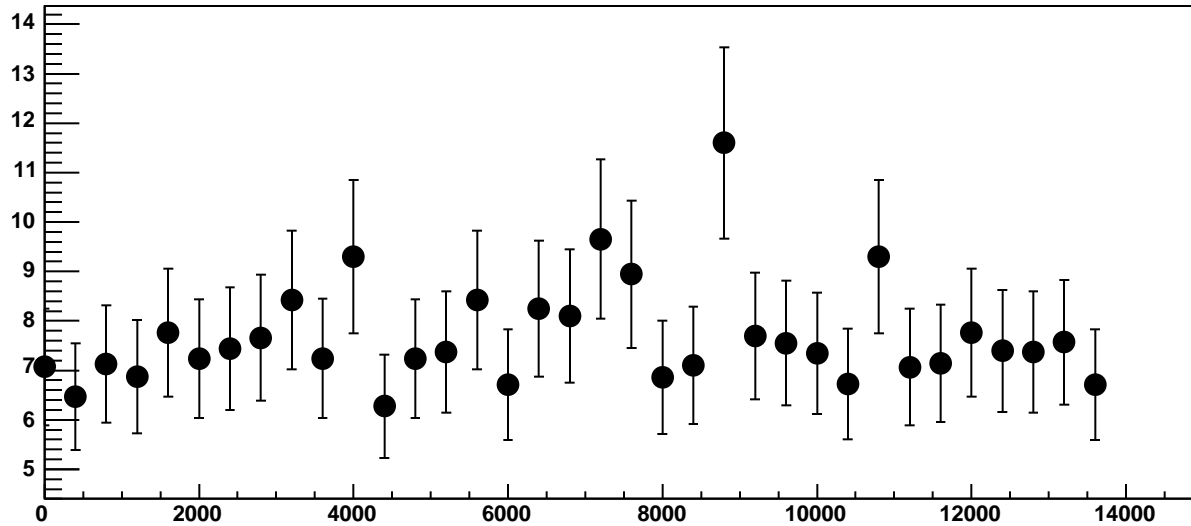
Chip 1, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC



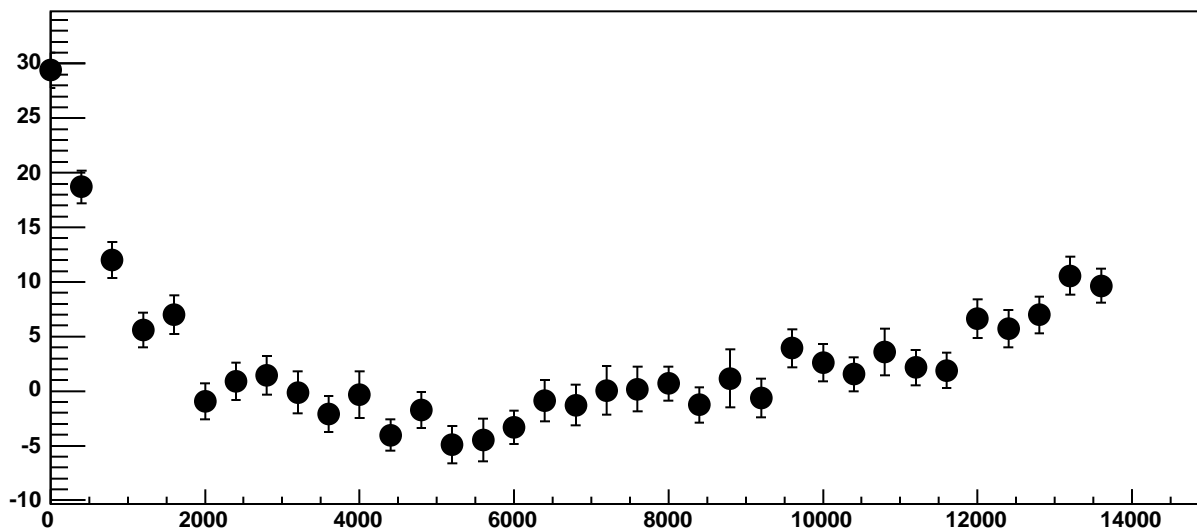
Chip 1, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



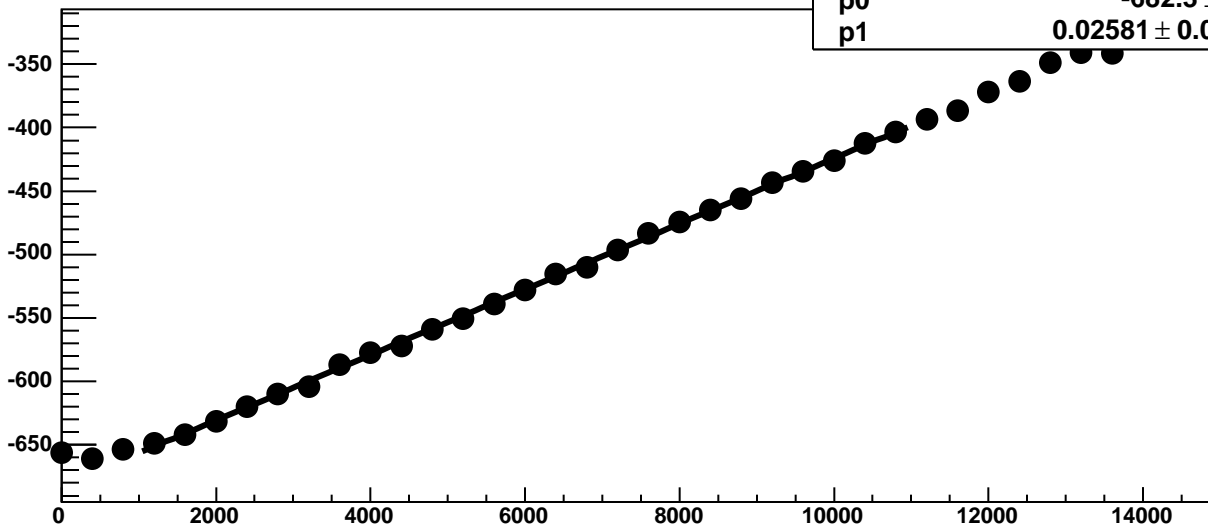
Chip 1, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



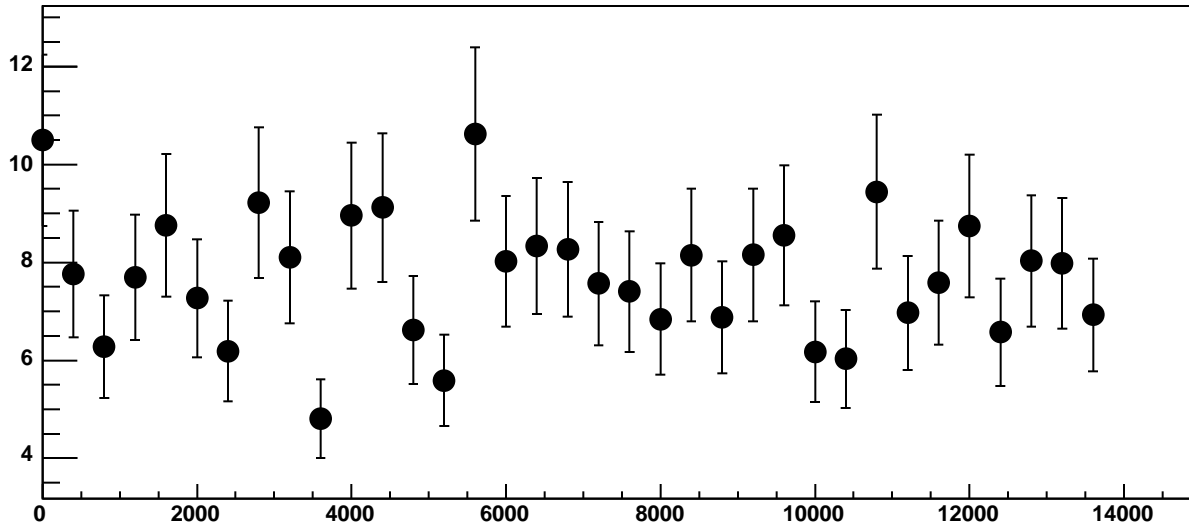
Chip 1, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



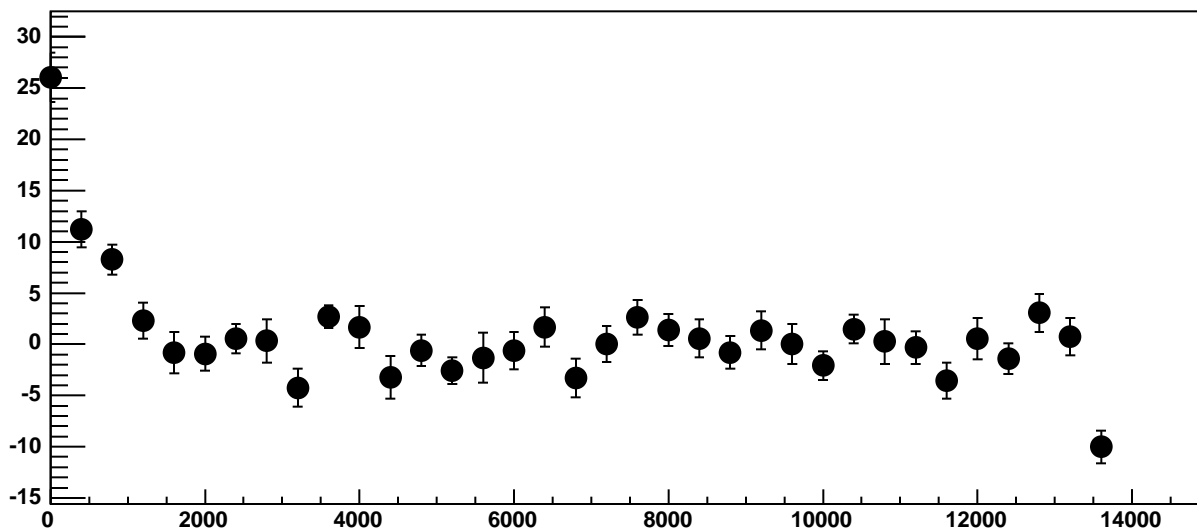
Chip 1, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



Chip 1, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

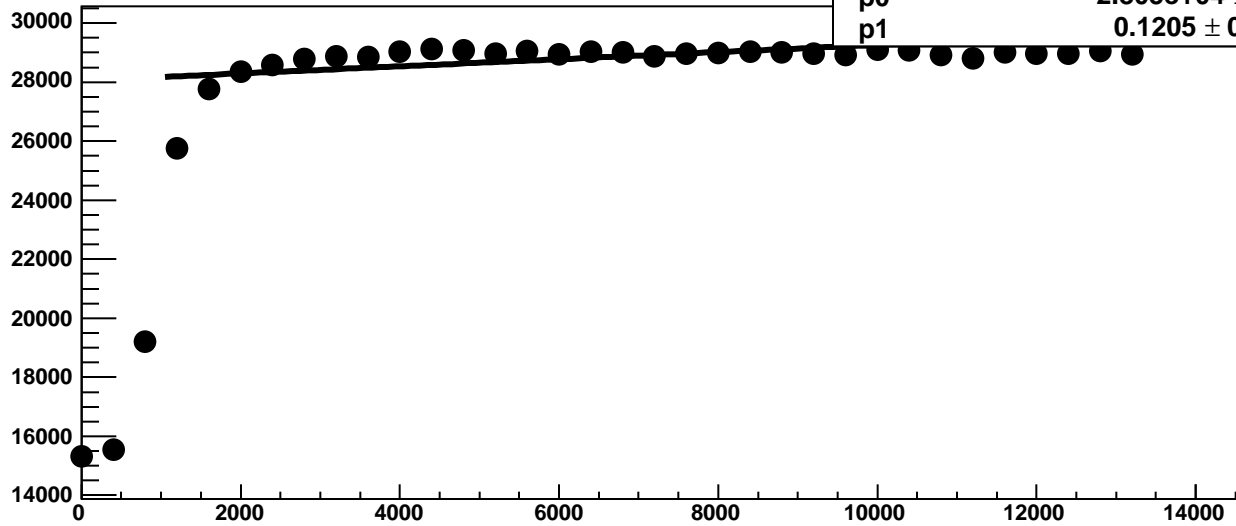
2475 / 23

p0

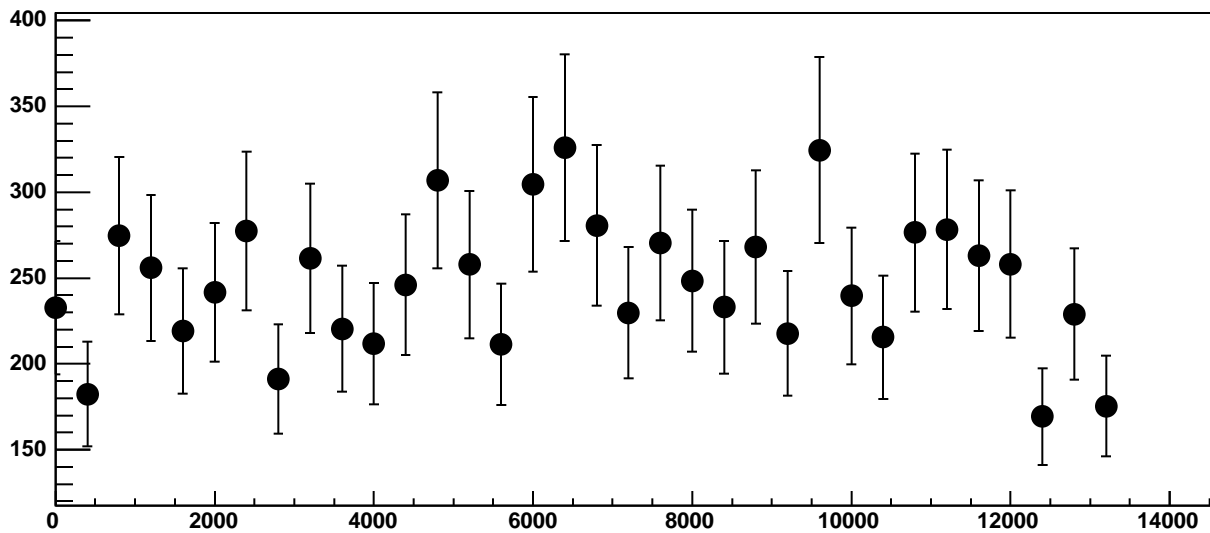
$2.805\text{e}+04 \pm 25.22$

p1

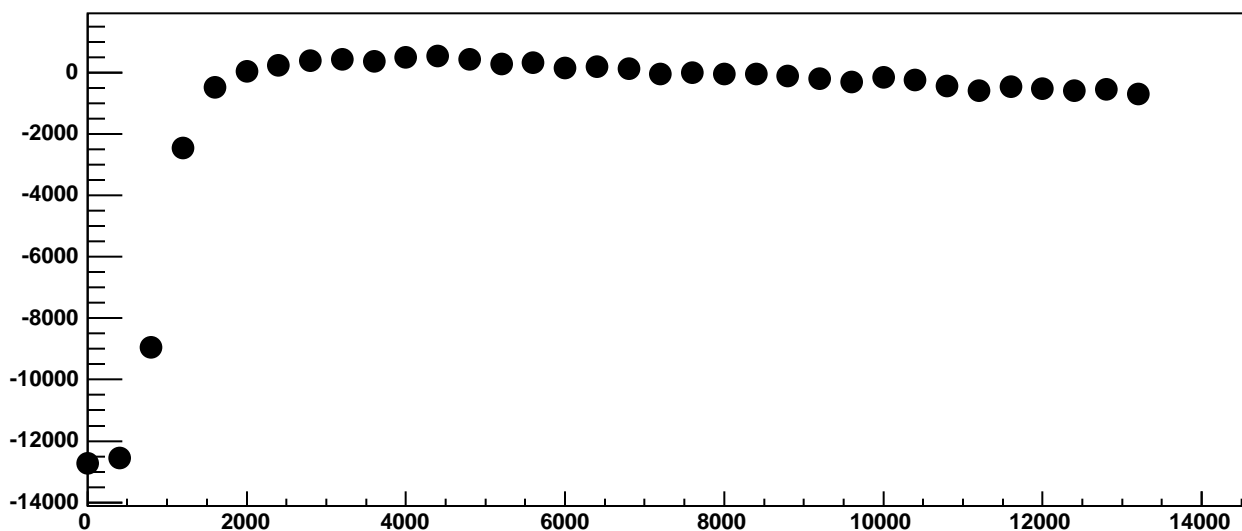
0.1205 ± 0.00385



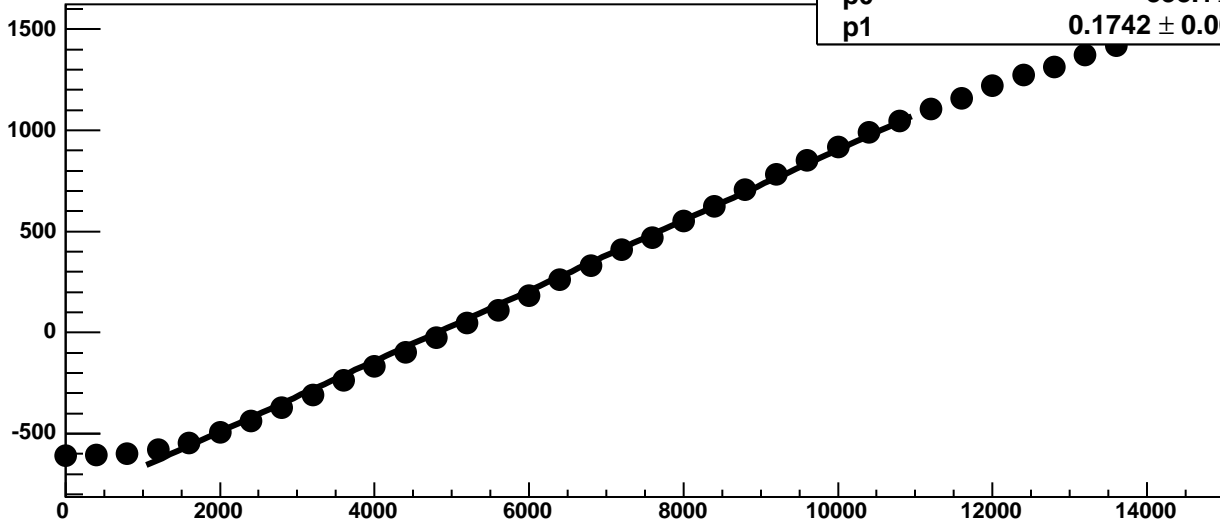
Chip 1, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC

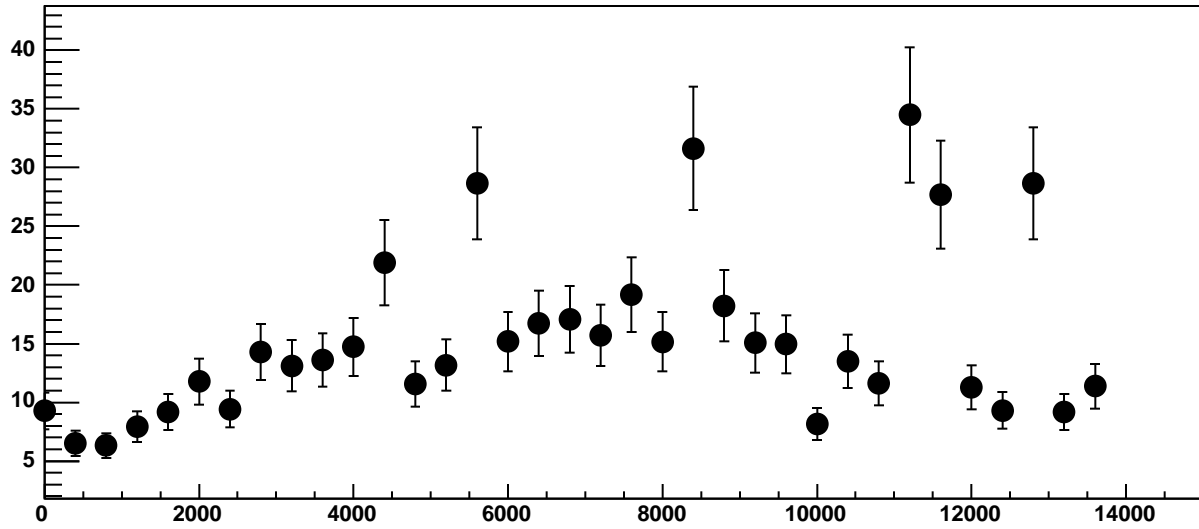


Chip 1, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

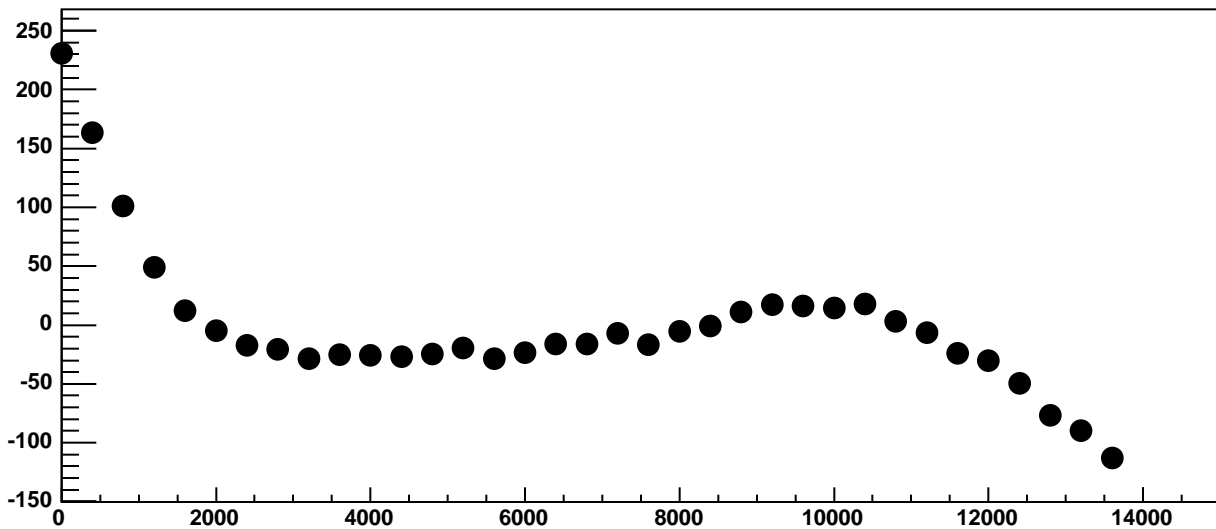


χ^2 / ndf 1502 / 23
p0 -838.1 ± 1.135
p1 0.1742 ± 0.0001778

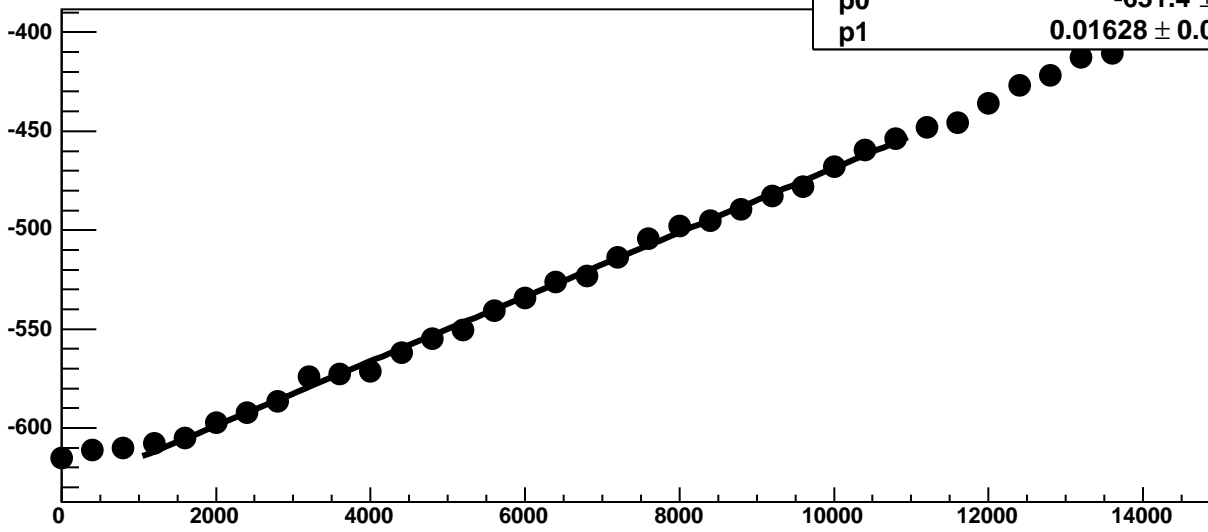
Chip 1, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

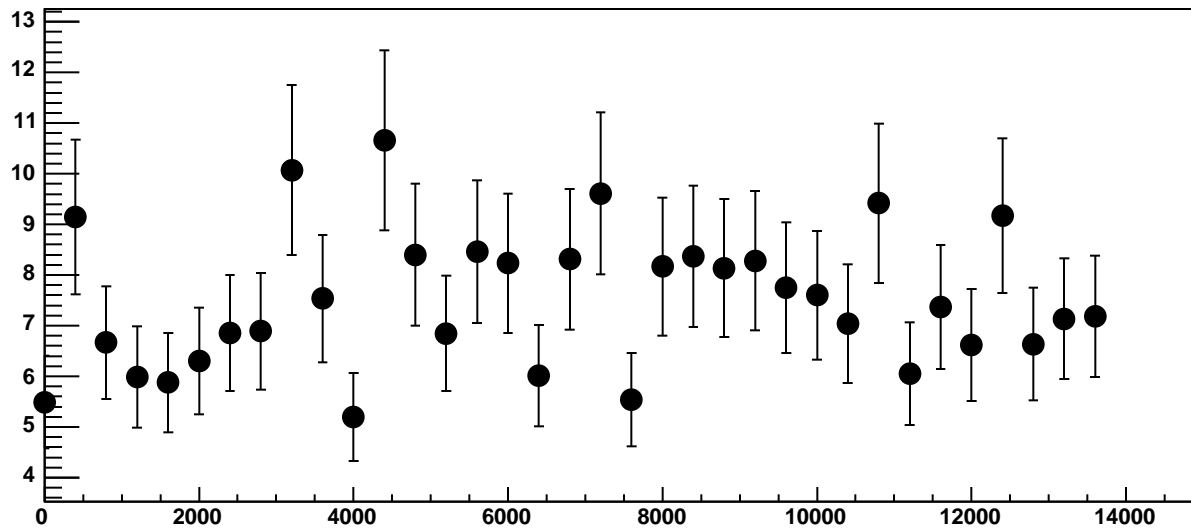


Chip 1, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

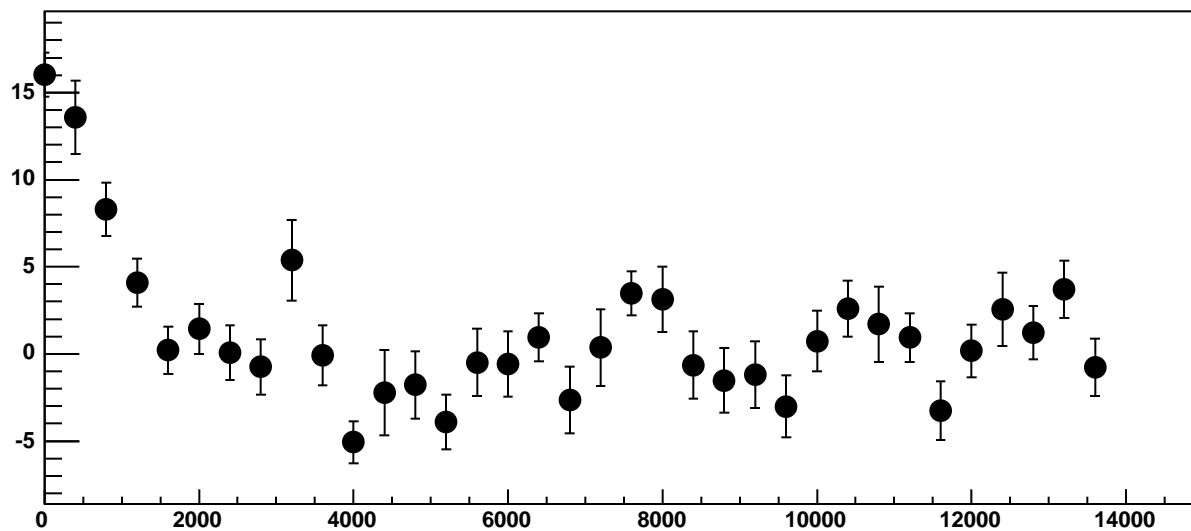


χ^2 / ndf 61.82 / 23
p0 -631.4 ± 0.7187
p1 0.01628 ± 0.0001137

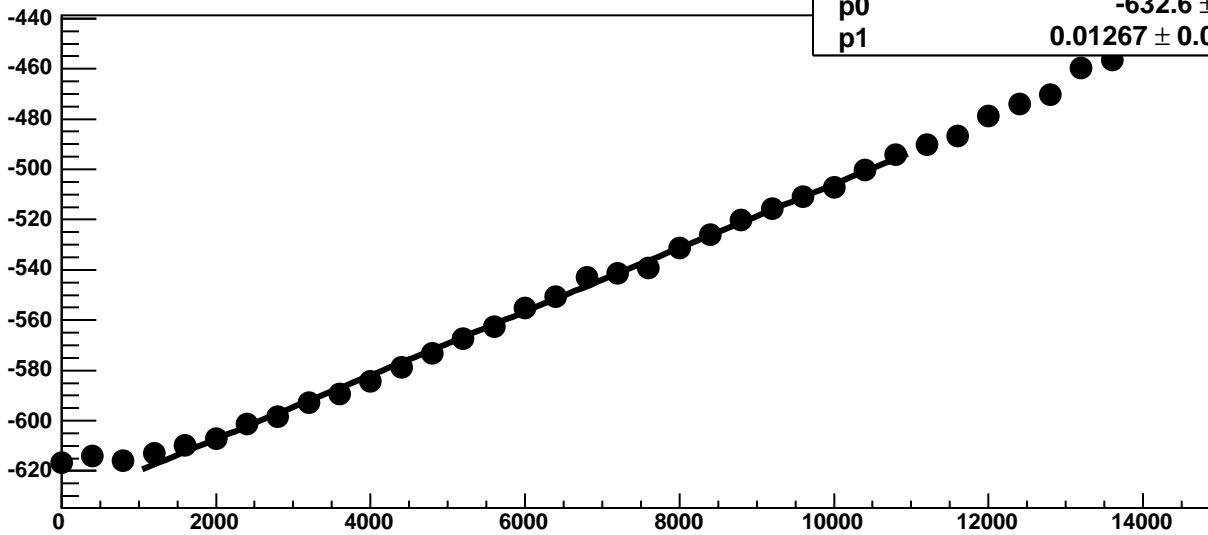
Chip 1, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

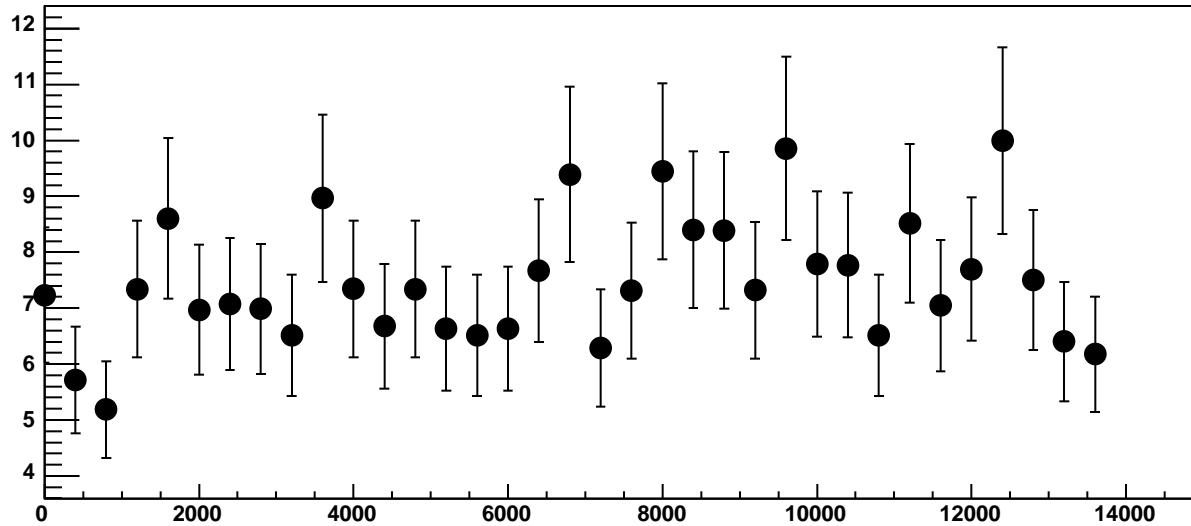


Chip 1, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

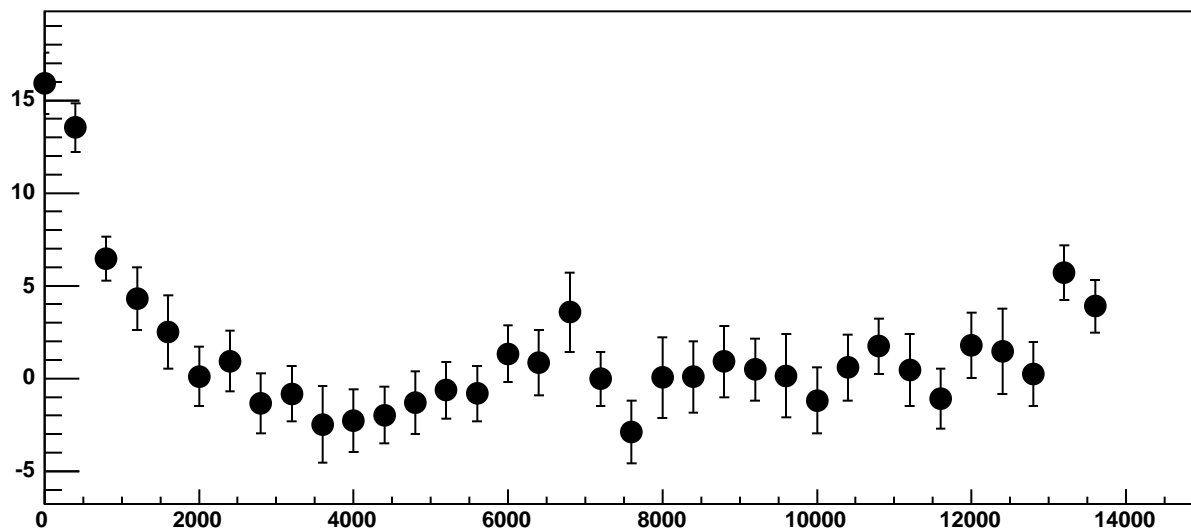


χ^2 / ndf 24.37 / 23
p0 -632.6 ± 0.7772
p1 0.01267 ± 0.0001193

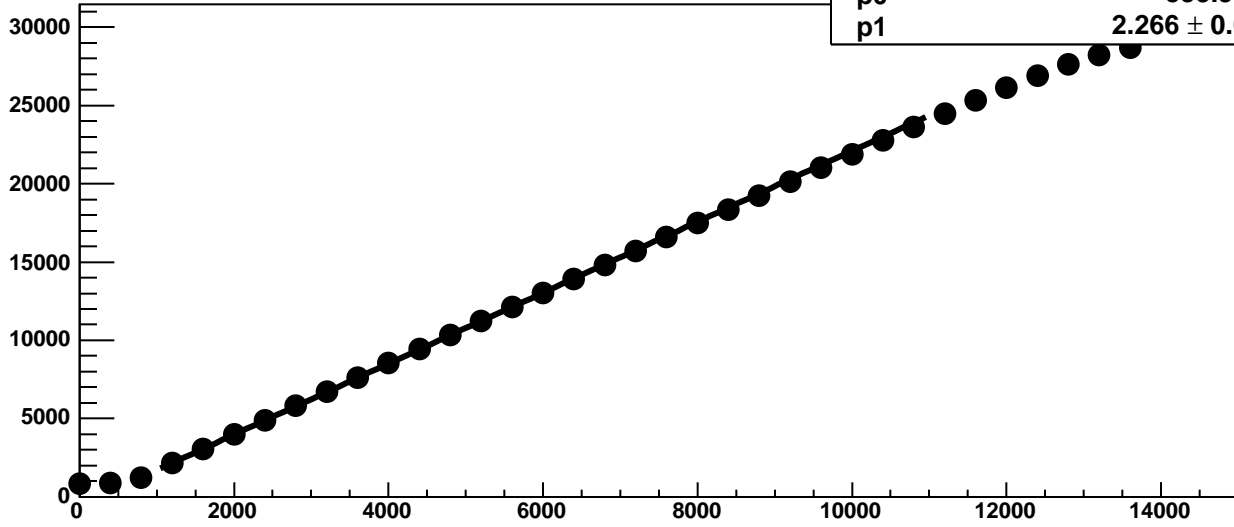
Chip 1, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



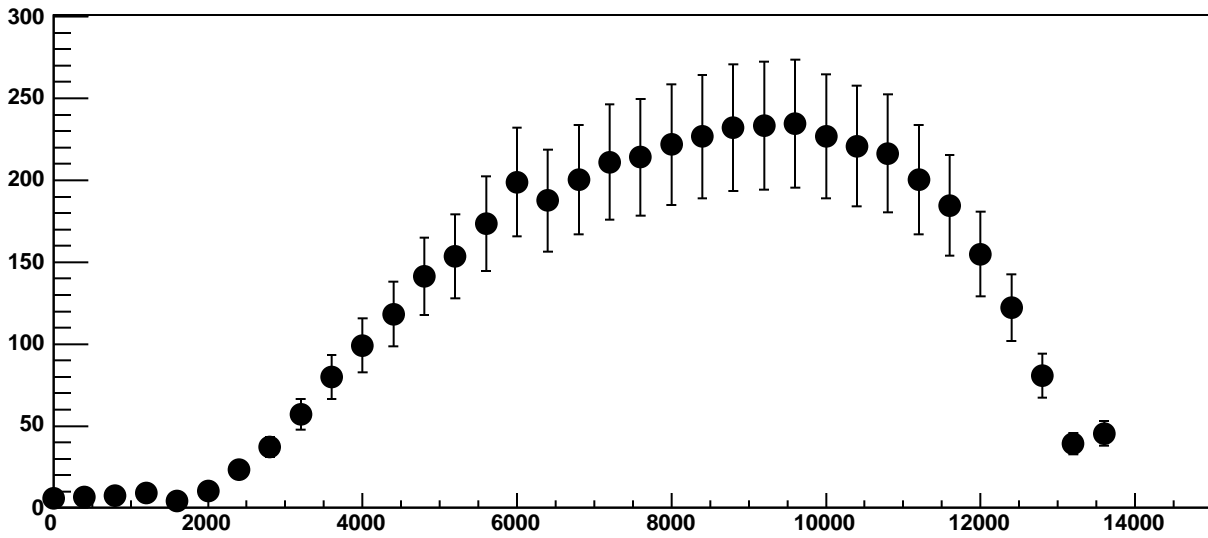
Chip 1, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



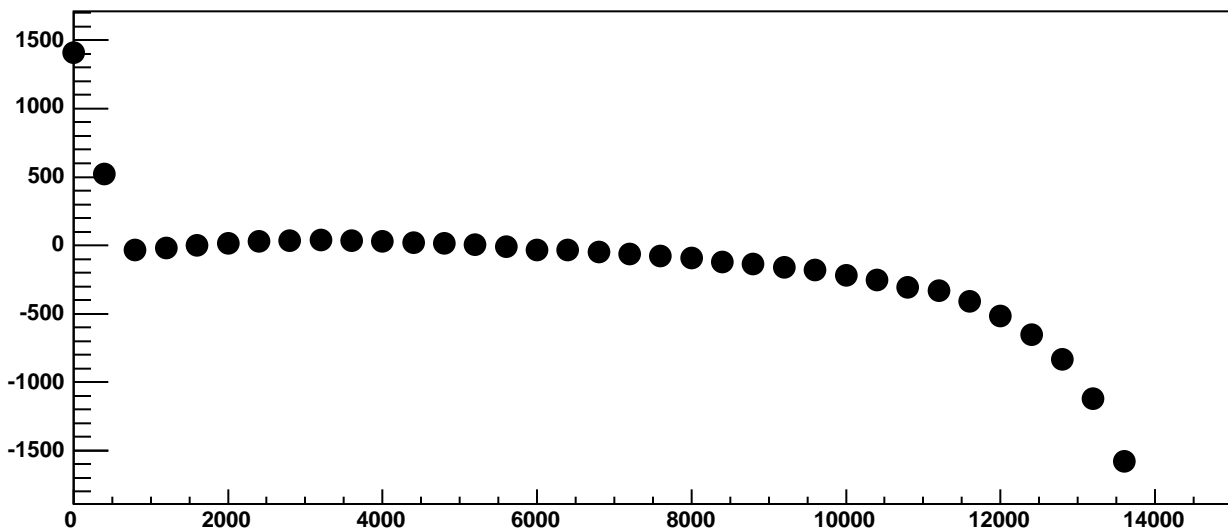
Chip 1, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC



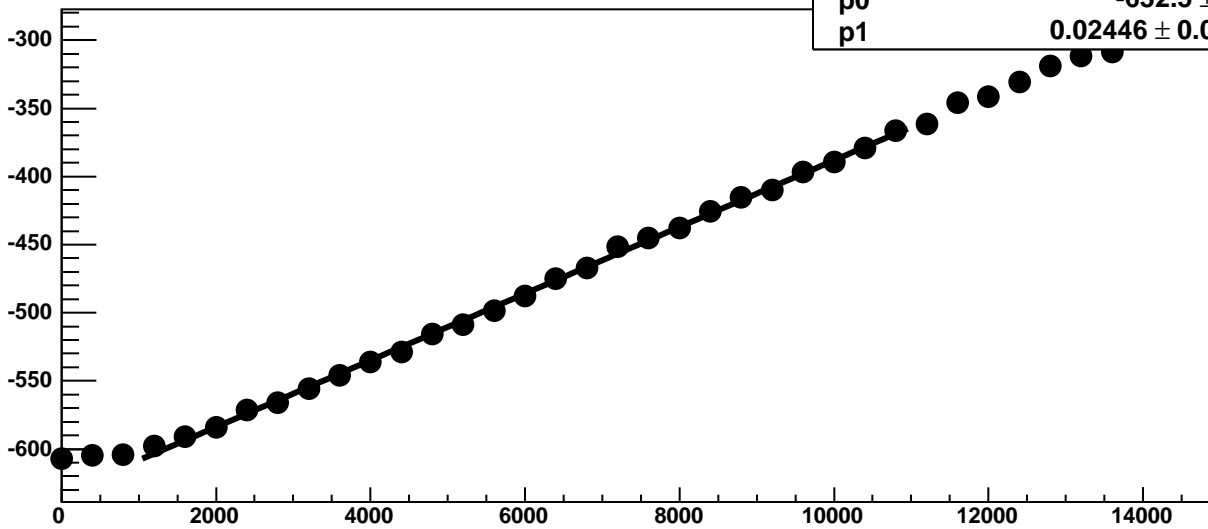
Chip 1, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC

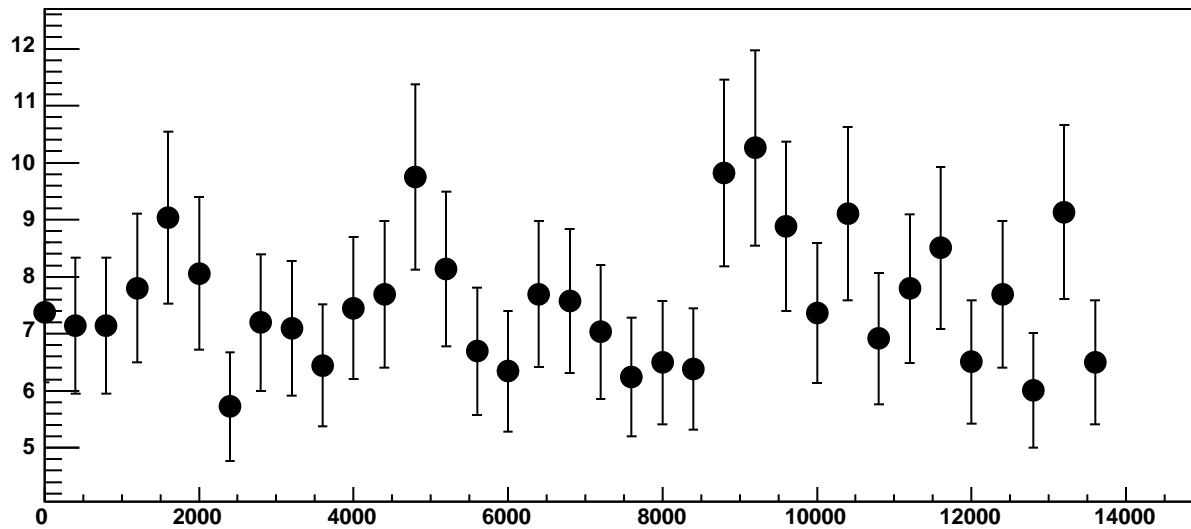


Chip 1, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

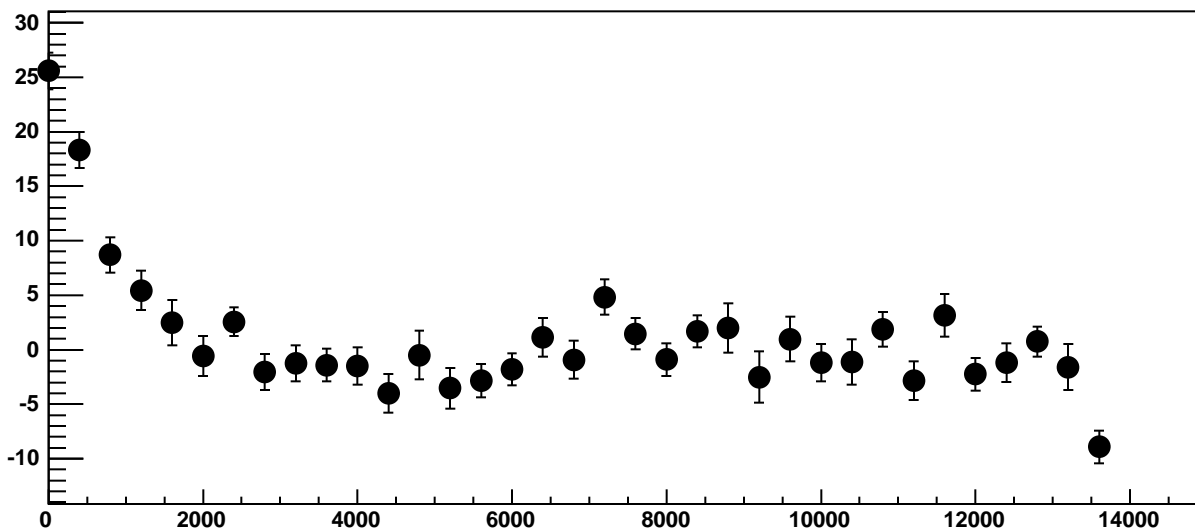


χ^2 / ndf 48.9 / 23
p0 -632.5 ± 0.7898
p1 0.02446 ± 0.0001212

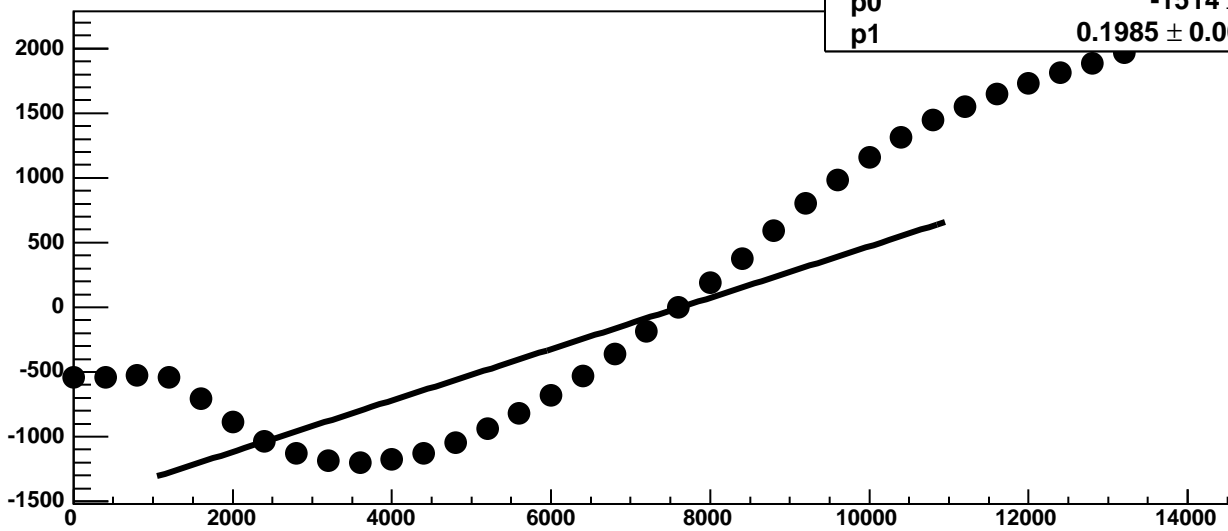
Chip 1, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



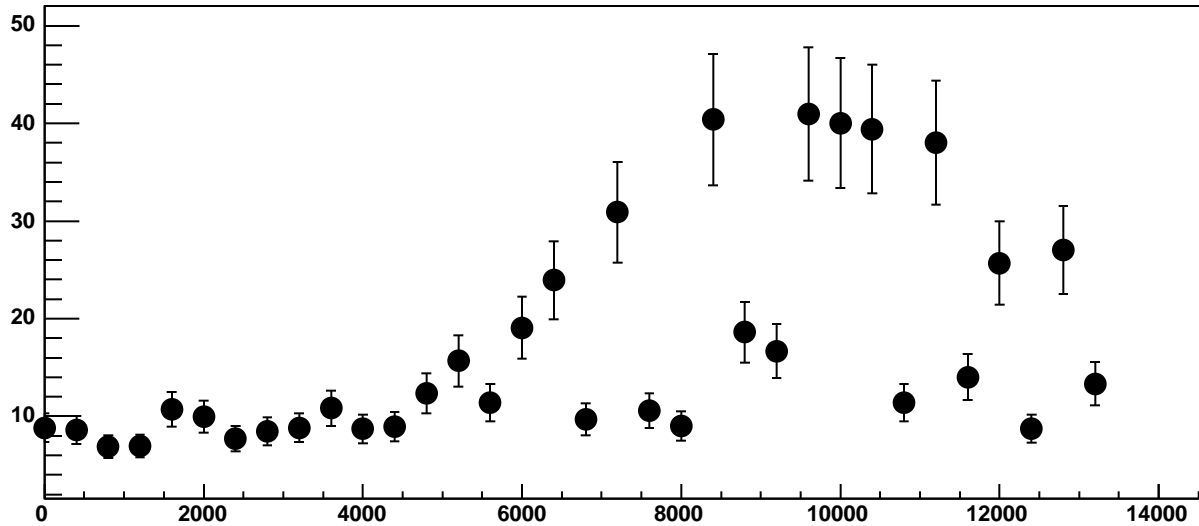
Chip 1, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



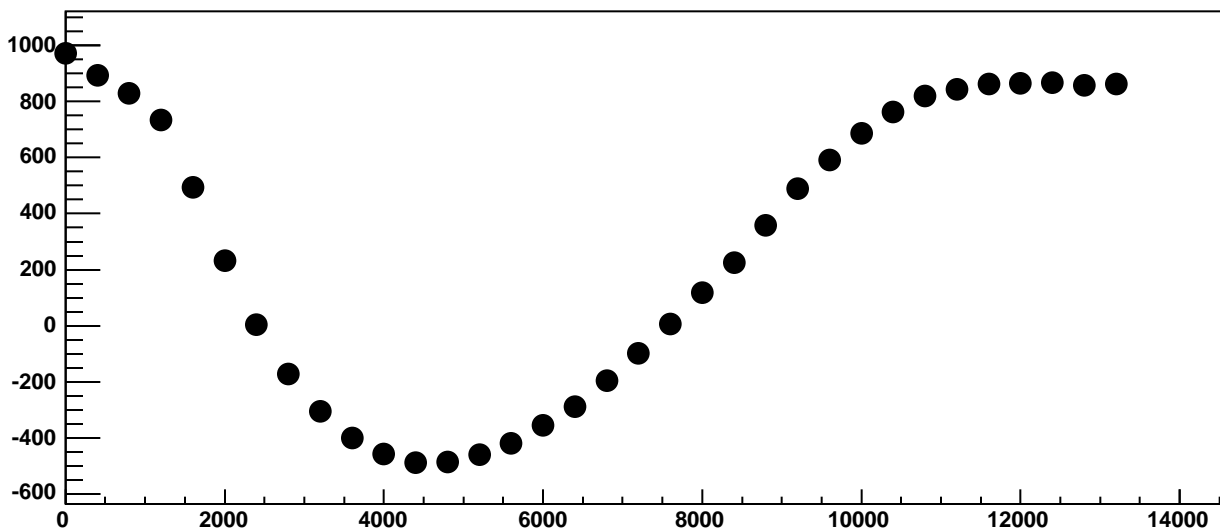
Chip 1, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



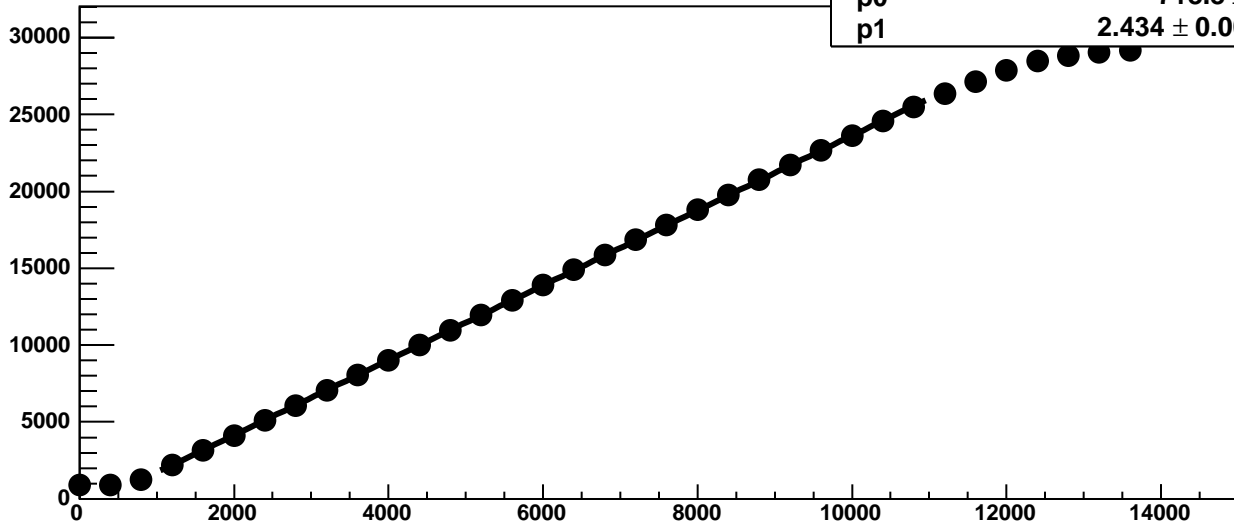
Chip 1, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

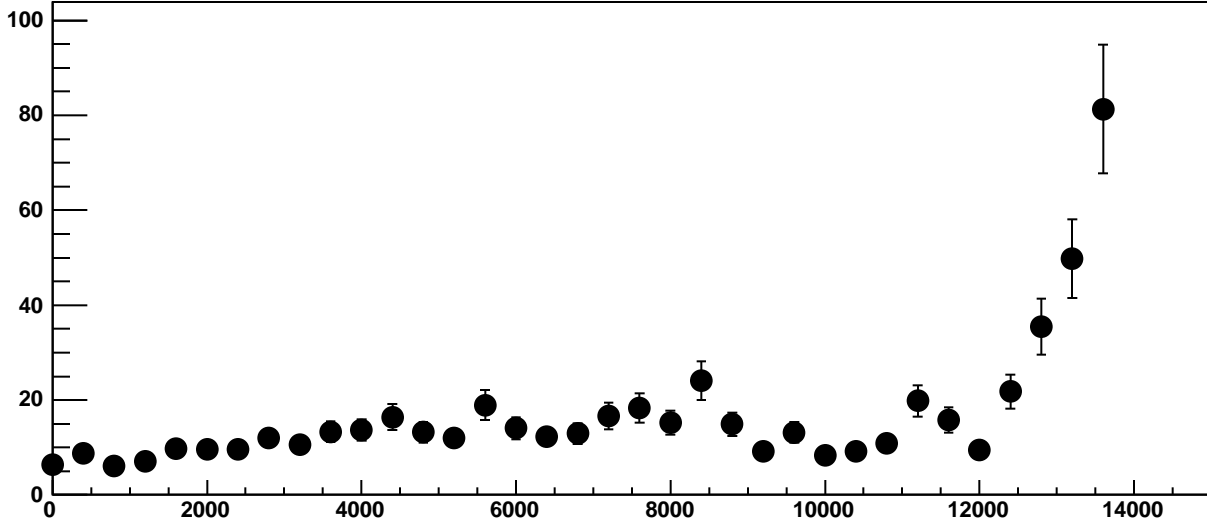


Chip 1, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC

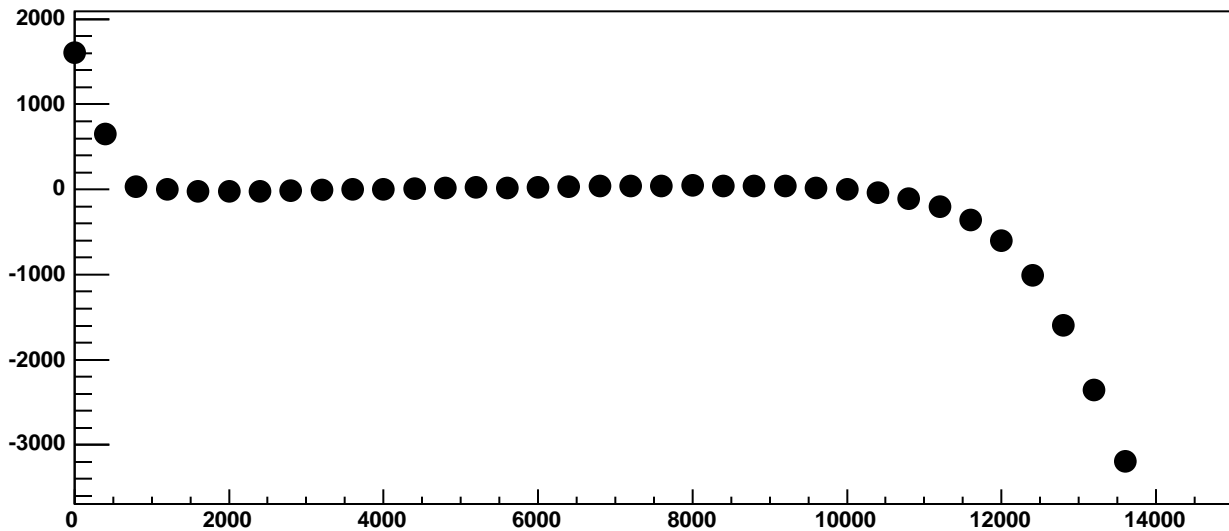


χ^2 / ndf 3892 / 23
p0 -718.8 ± 1.043
p1 2.434 ± 0.0001582

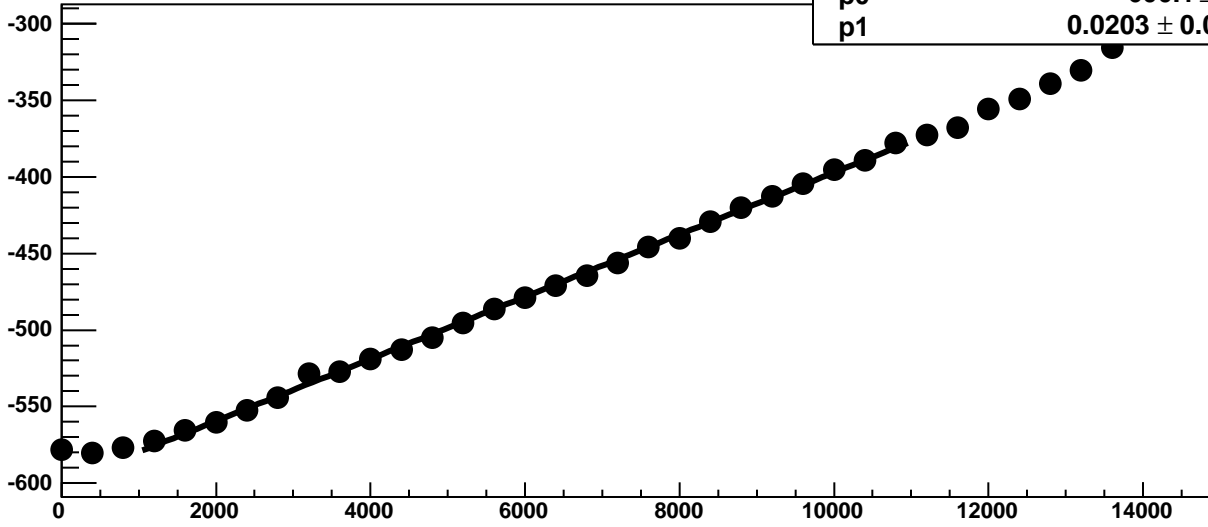
Chip 1, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

26 / 23

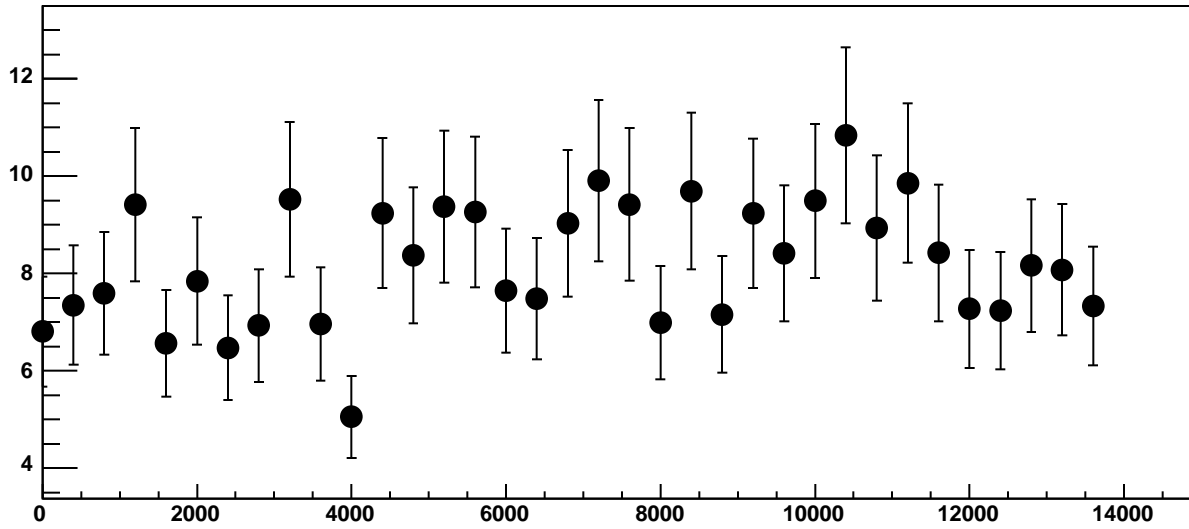
p0

-600.1 ± 0.8075

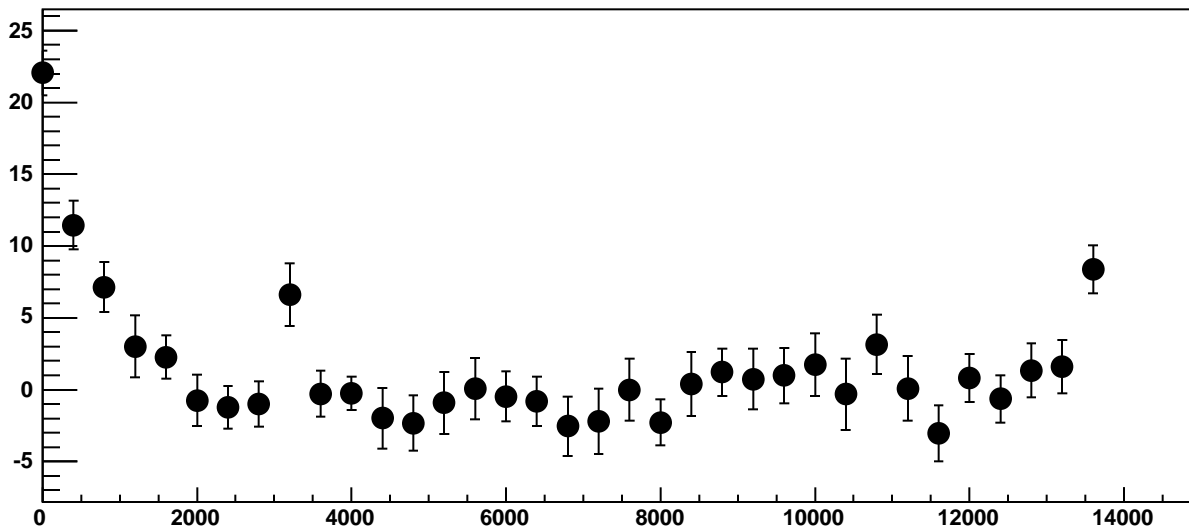
p1

0.0203 ± 0.0001301

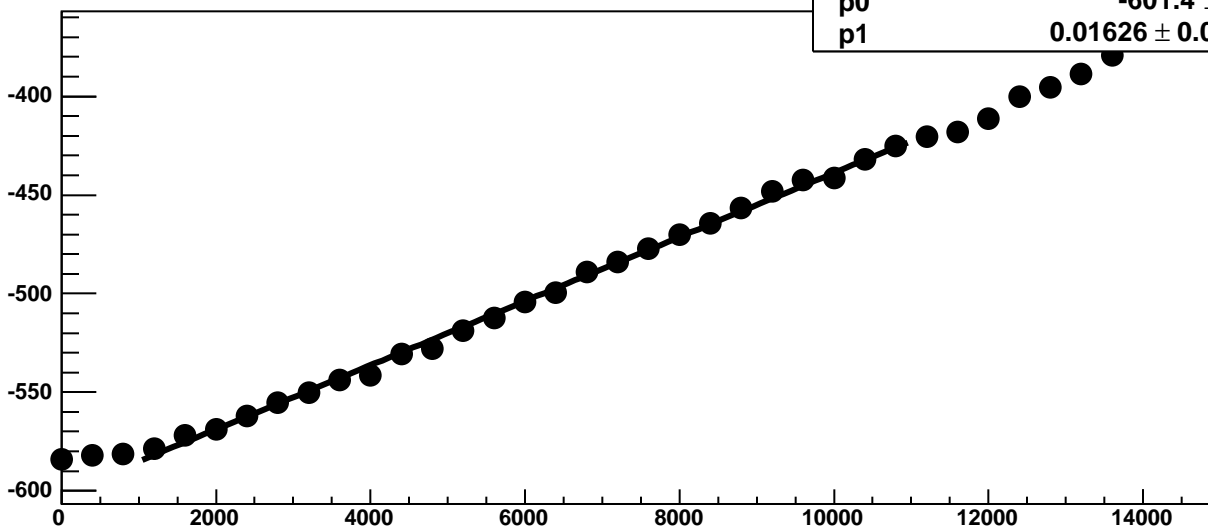
Chip 1, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



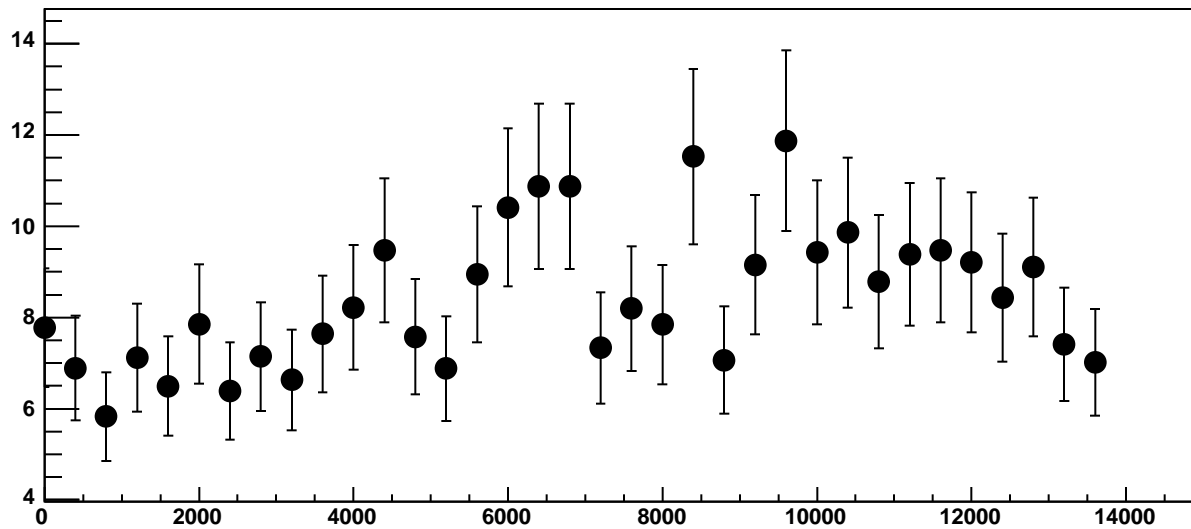
Chip 1, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



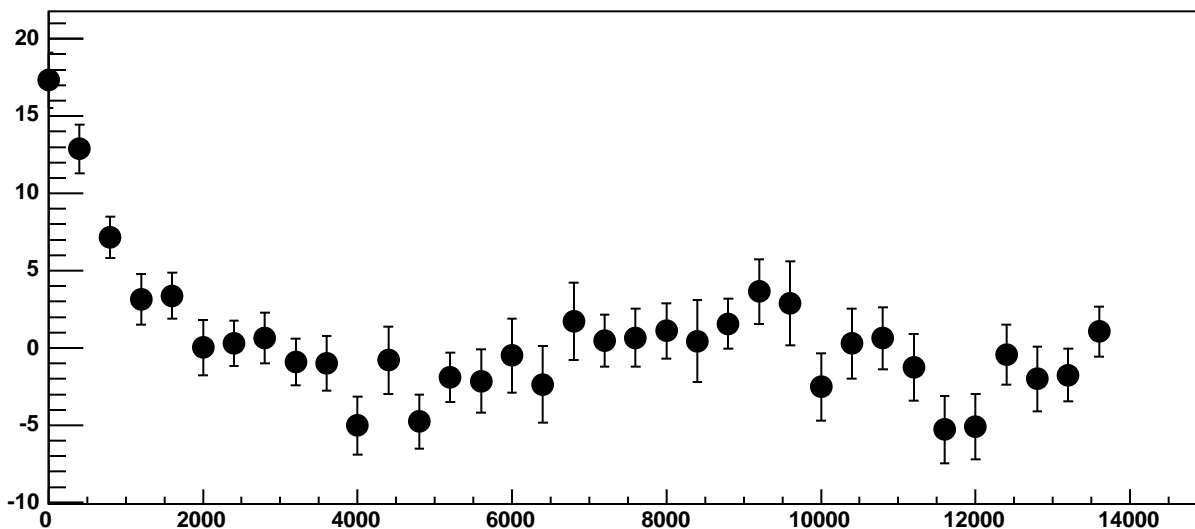
Chip 1, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



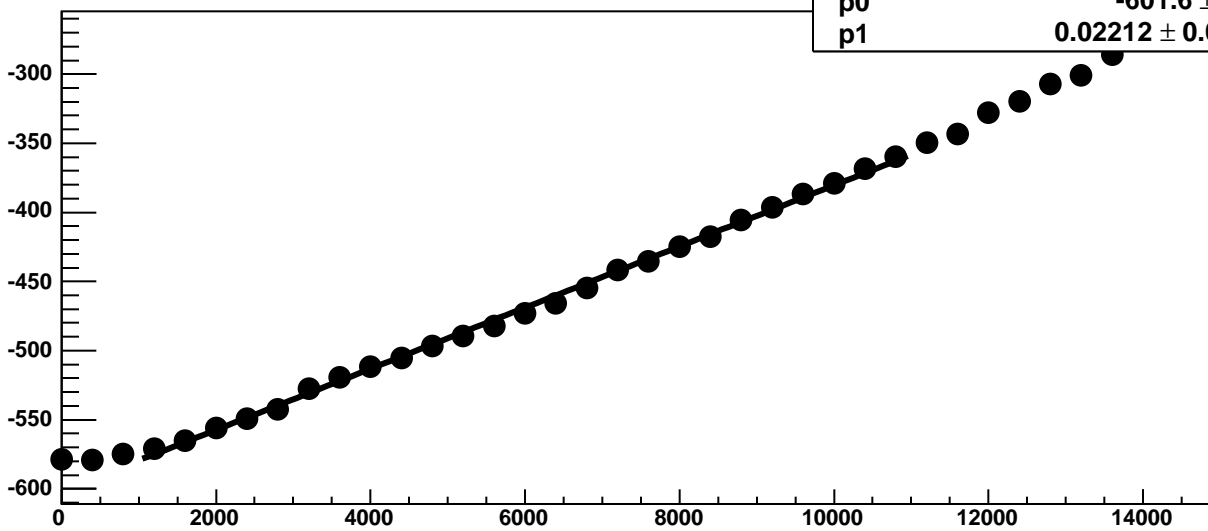
Chip 1, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

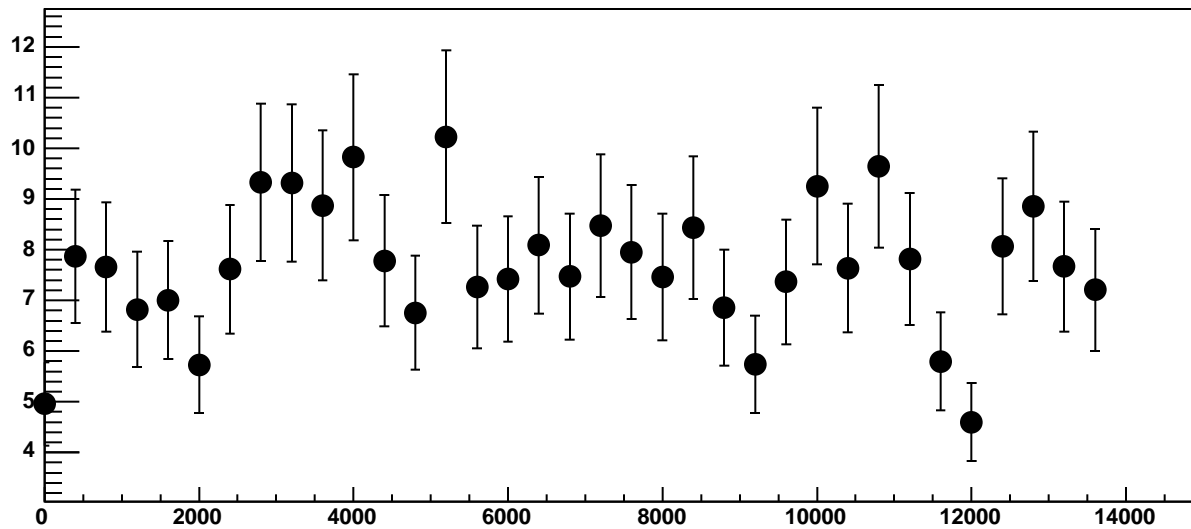


Chip 1, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC

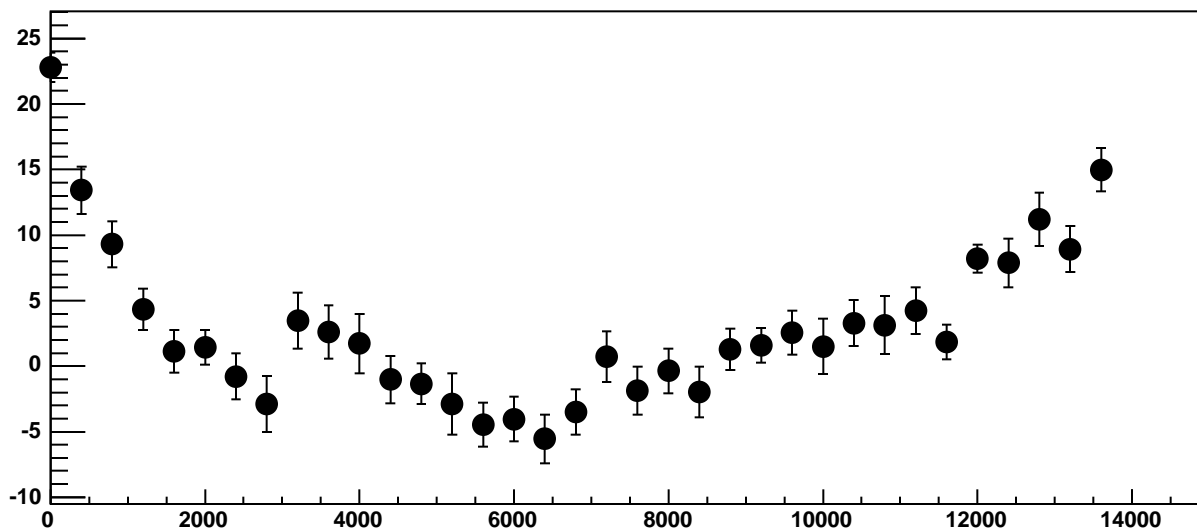


χ^2 / ndf 57.48 / 23
p0 -601.6 ± 0.7872
p1 0.02212 ± 0.0001191

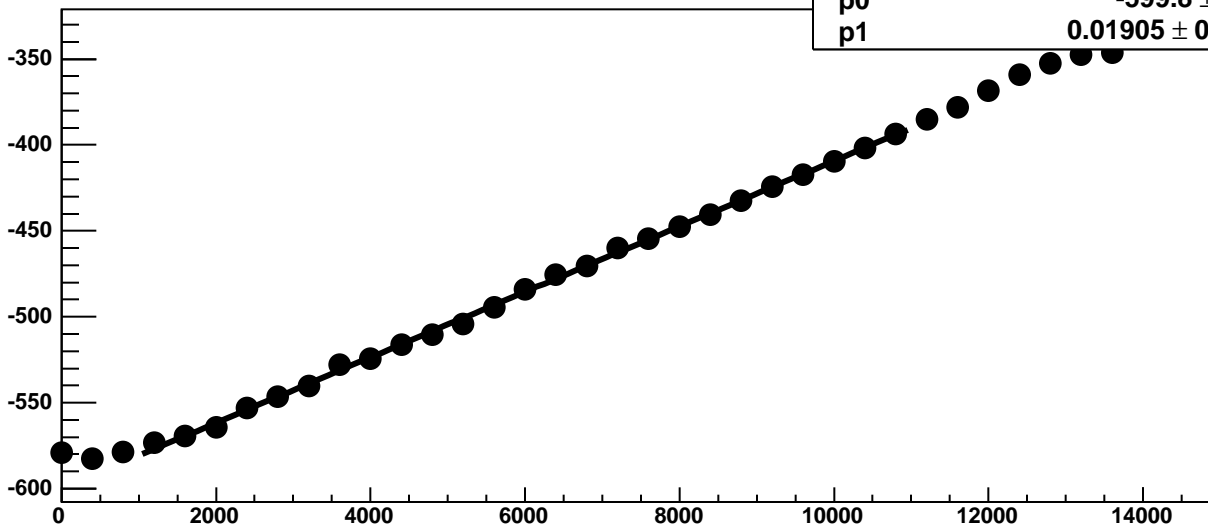
Chip 1, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



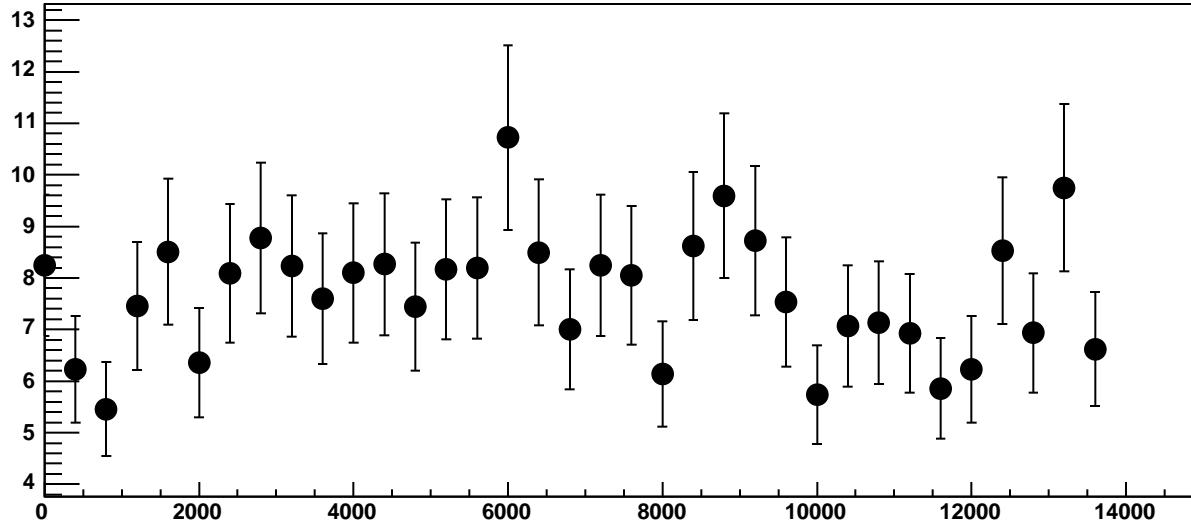
Chip 1, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC



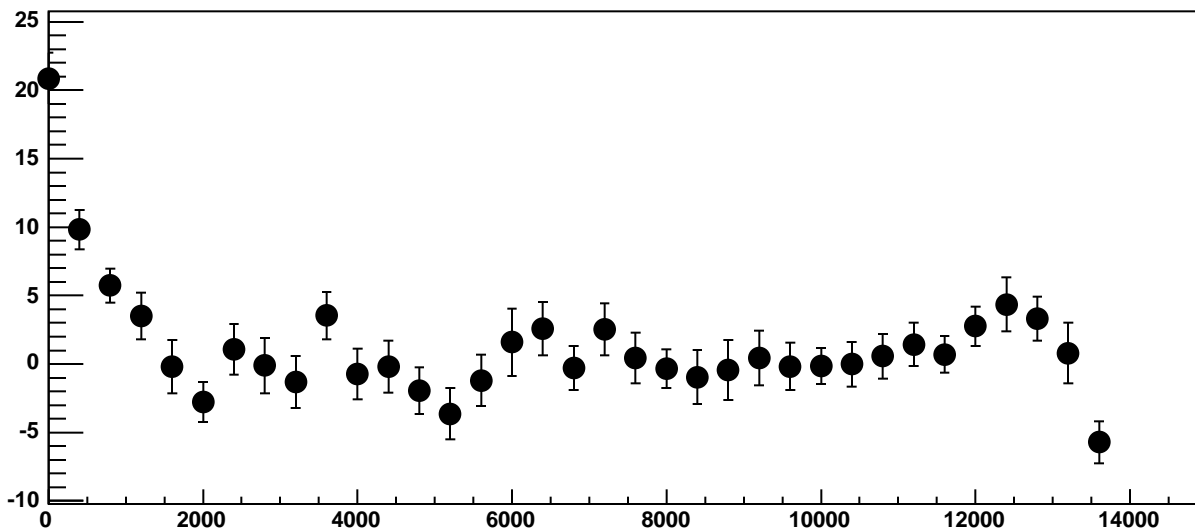
Chip 1, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



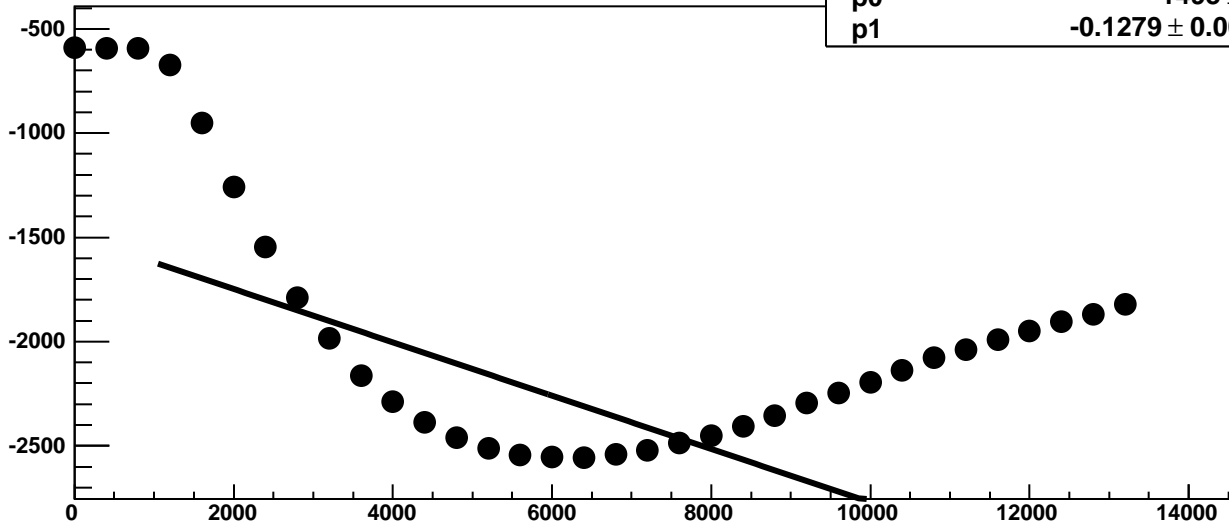
Chip 1, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

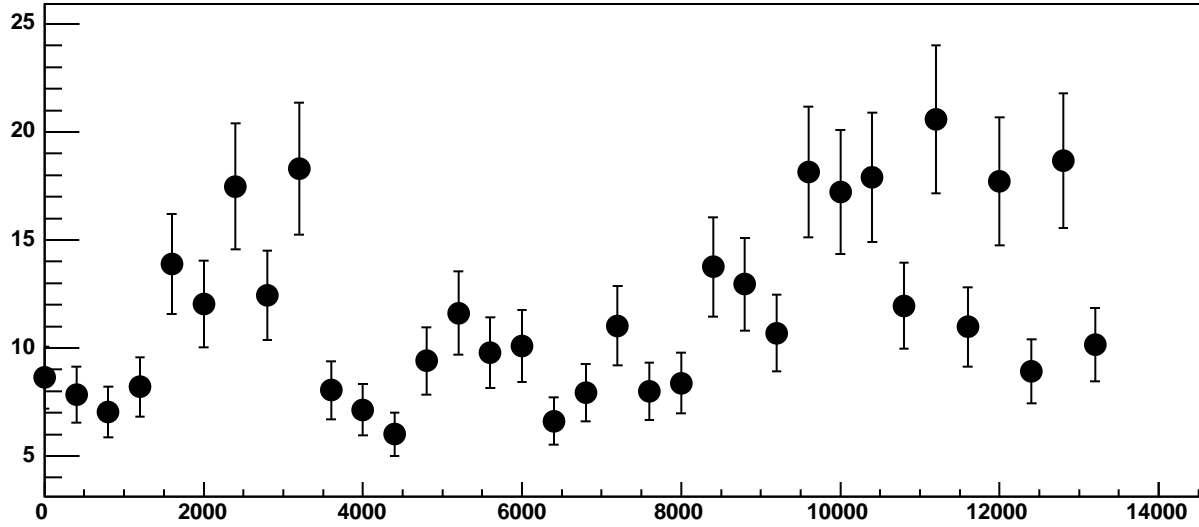


Chip 1, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

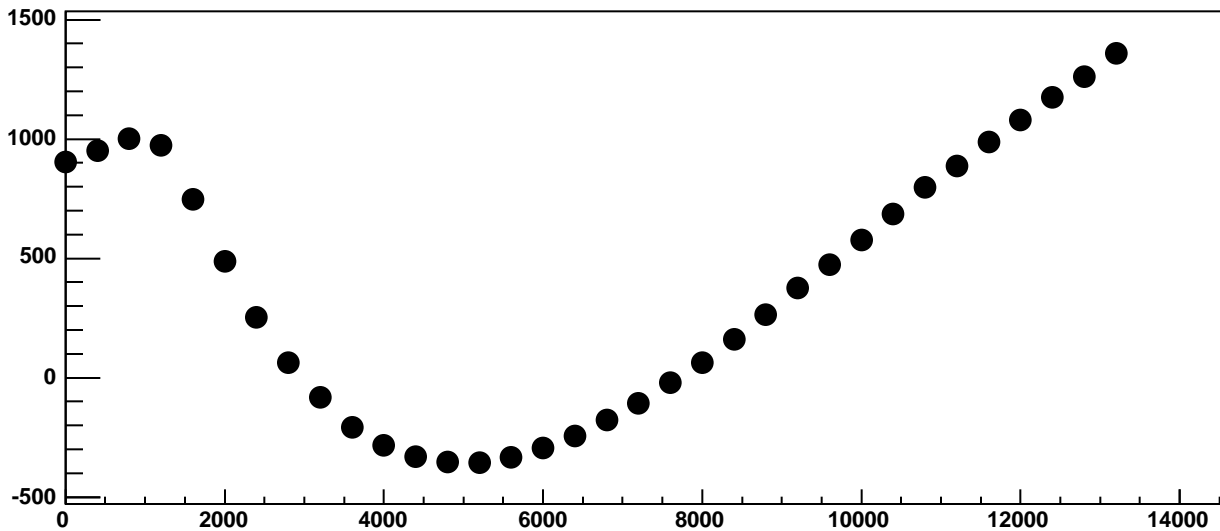


χ^2 / ndf	7.615e+05 / 23
p0	-1493 ± 1.144
p1	-0.1279 ± 0.0001862

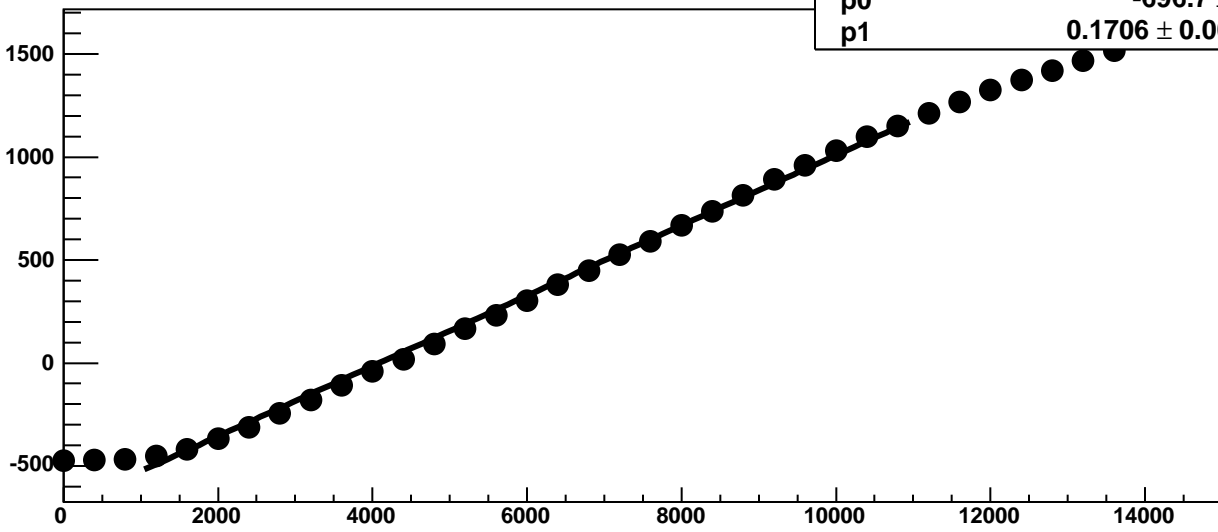
Chip 1, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



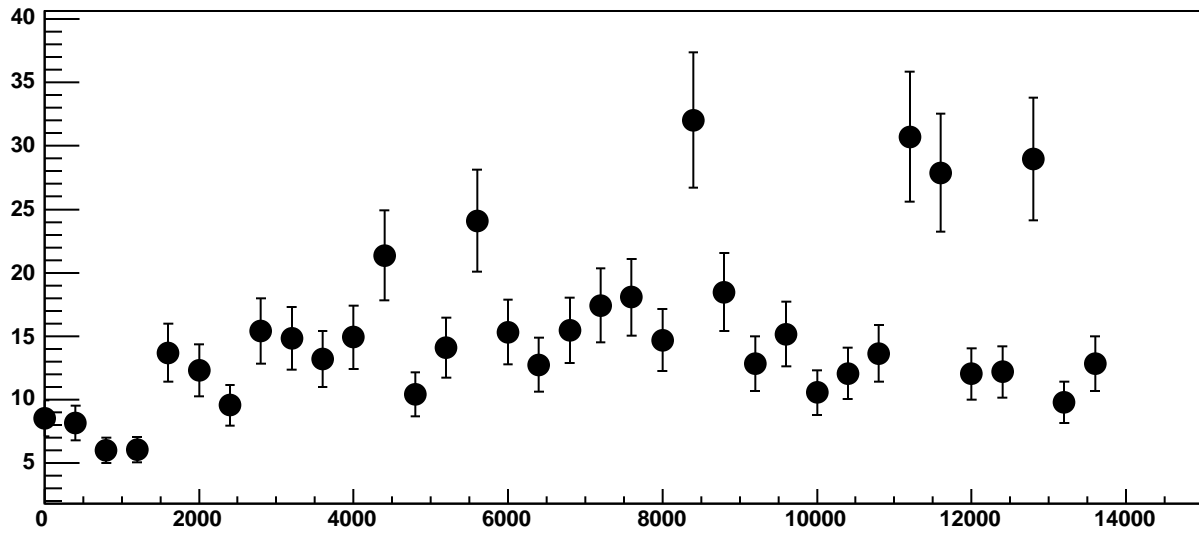
Chip 1, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



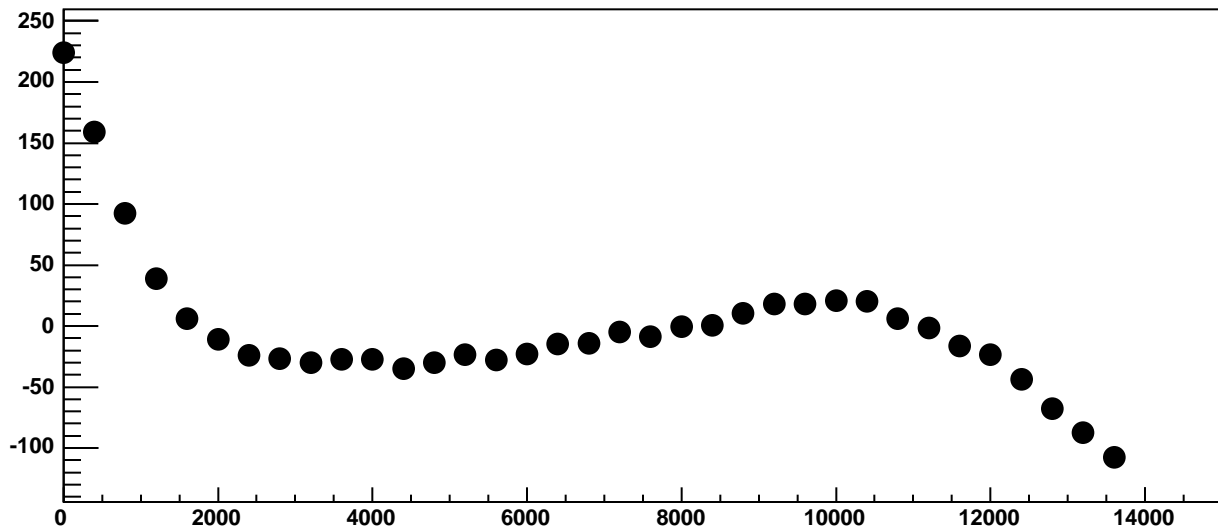
Chip 1, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC



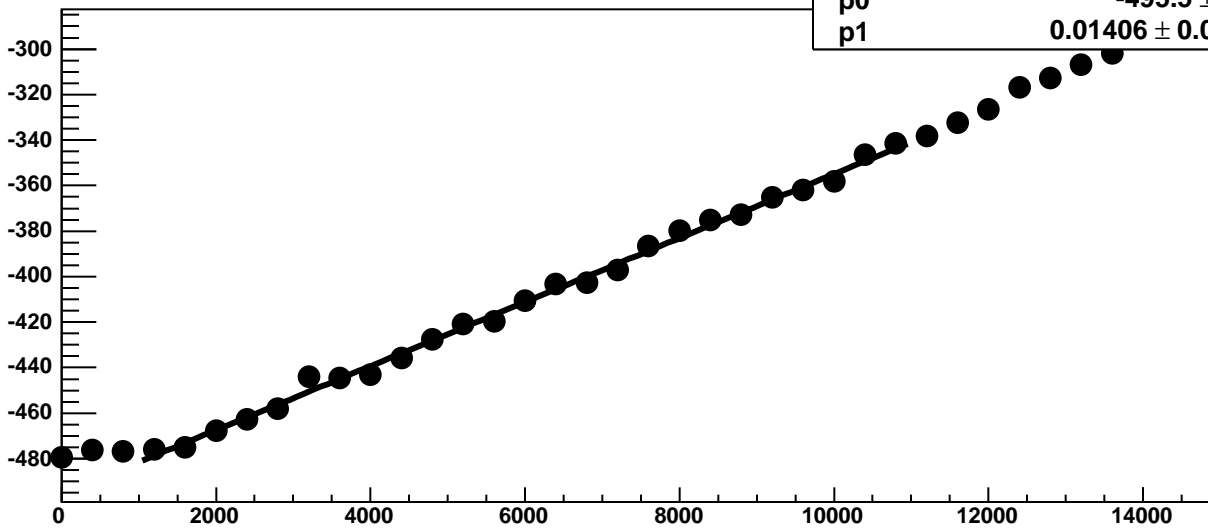
Chip 1, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC

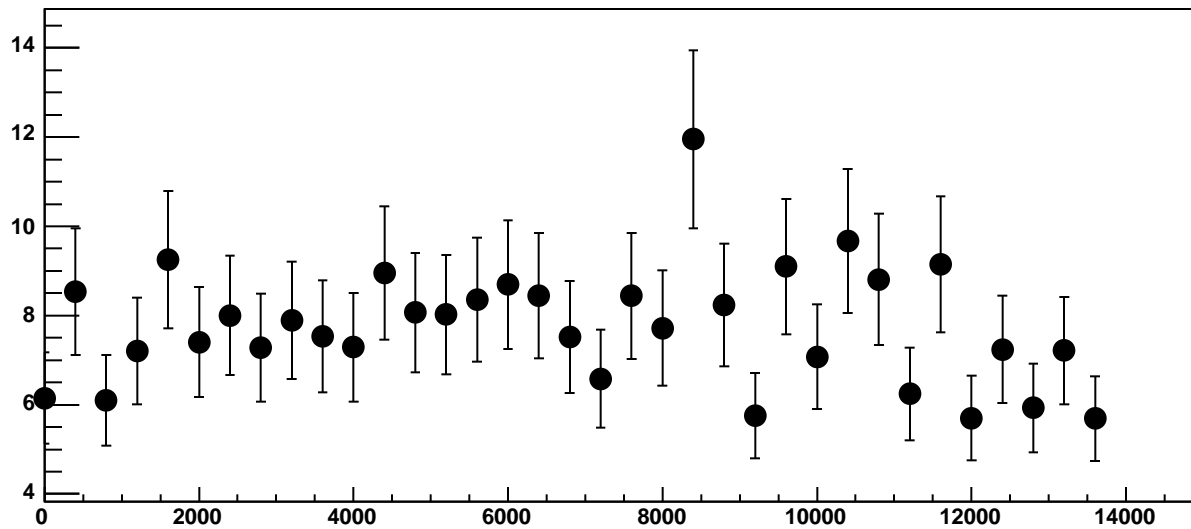


Chip 1, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

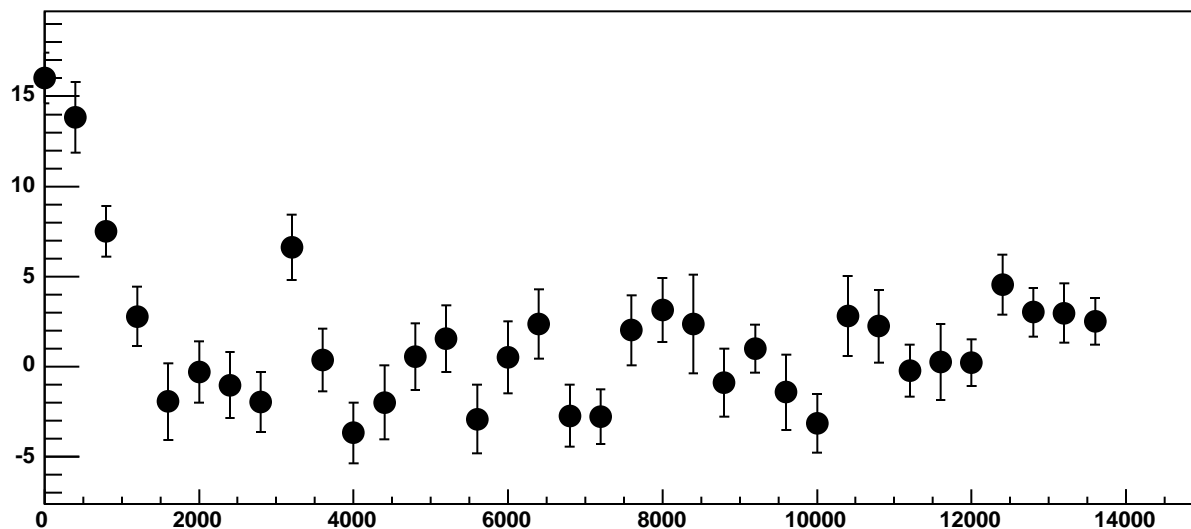


χ^2 / ndf 47.95 / 23
p0 -495.5 ± 0.8309
p1 0.01406 ± 0.0001259

Chip 1, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

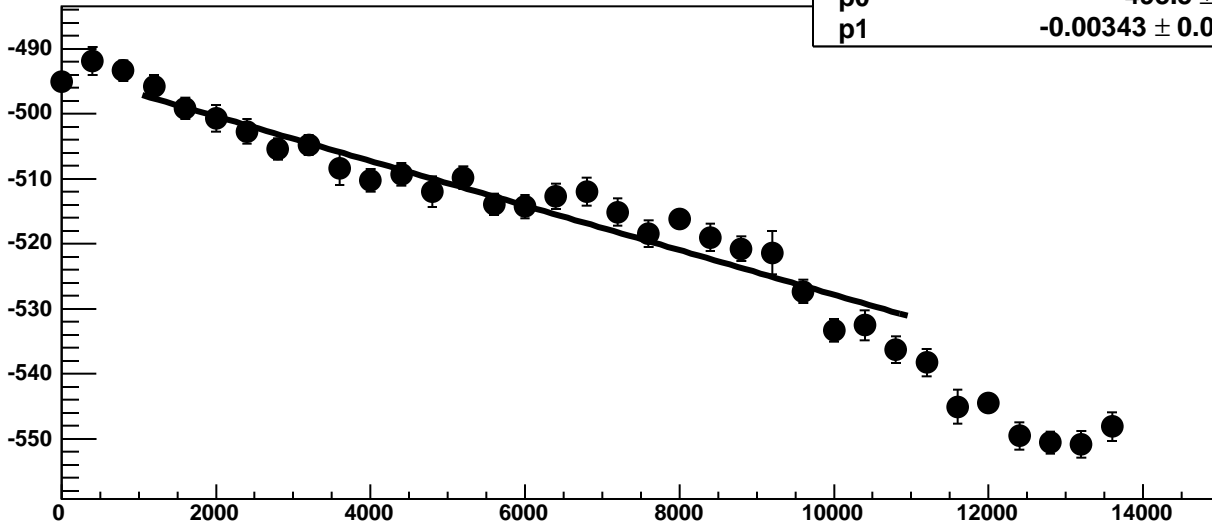


Chip 1, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

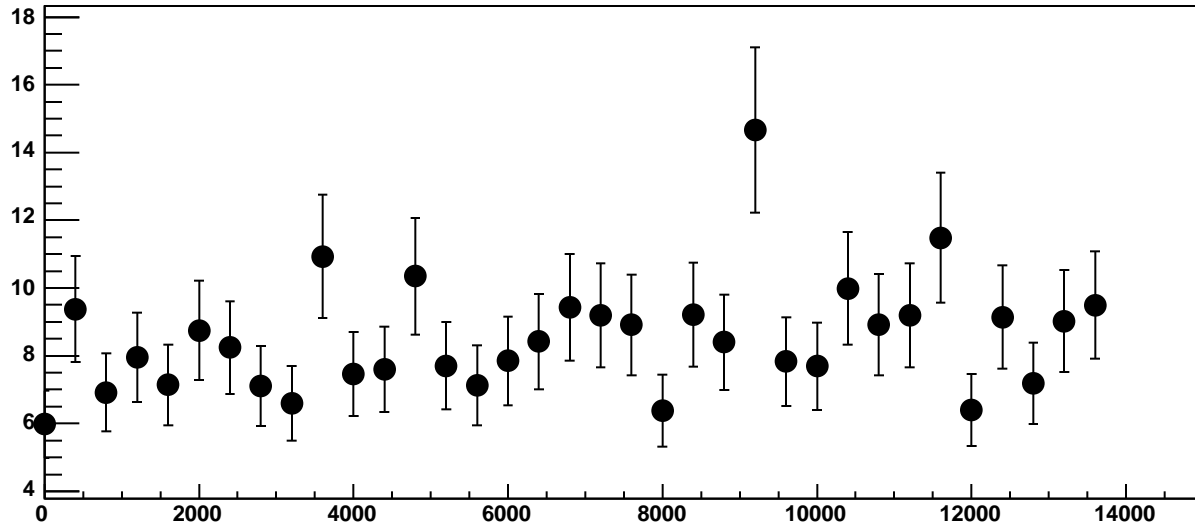


Chip 1, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

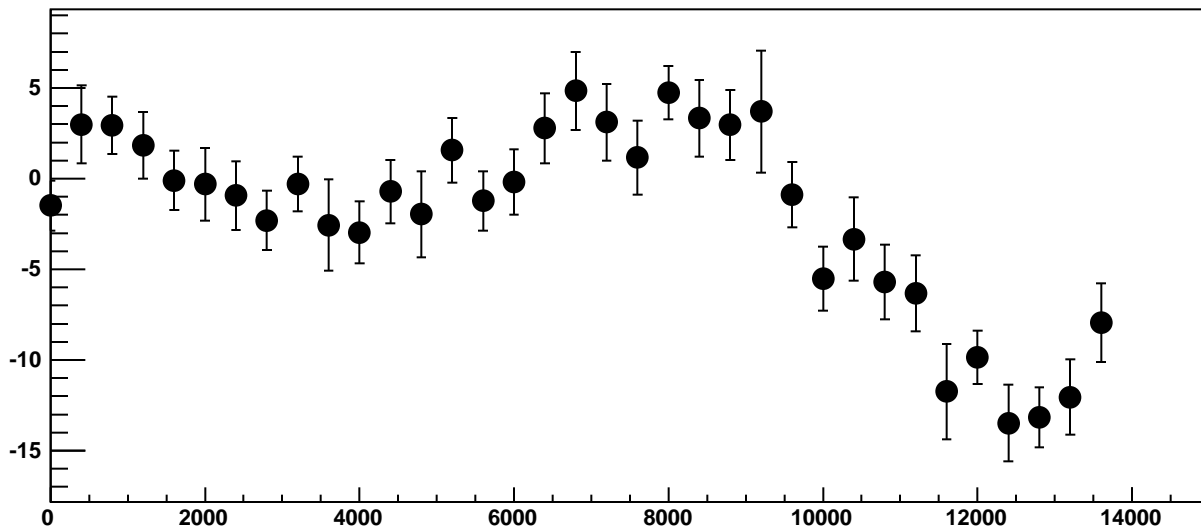
χ^2 / ndf 55.57 / 23
p0 -493.5 ± 0.8428
p1 -0.00343 ± 0.0001316



Chip 1, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

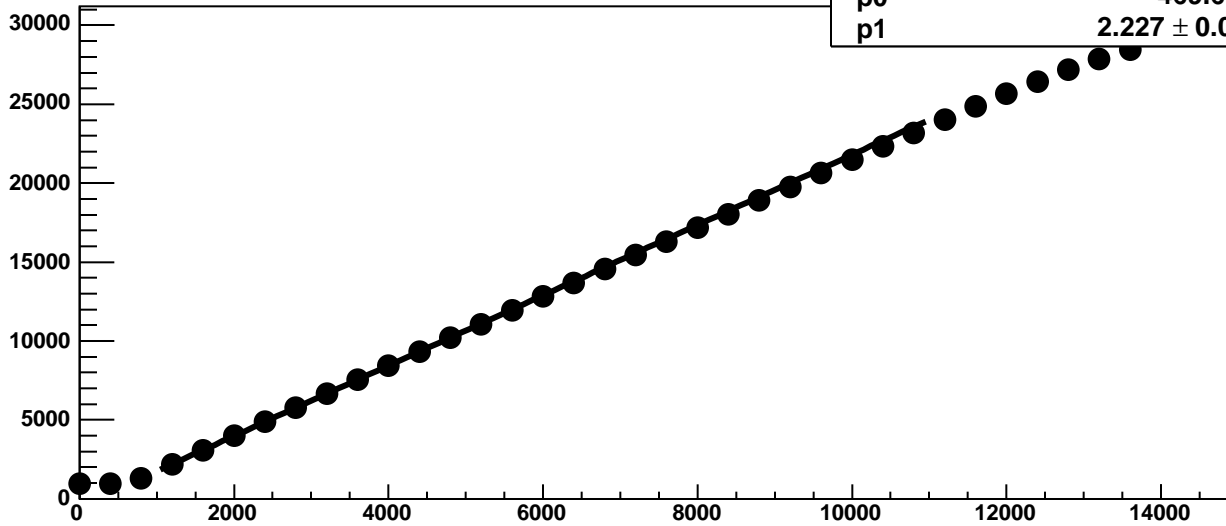


Chip 1, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

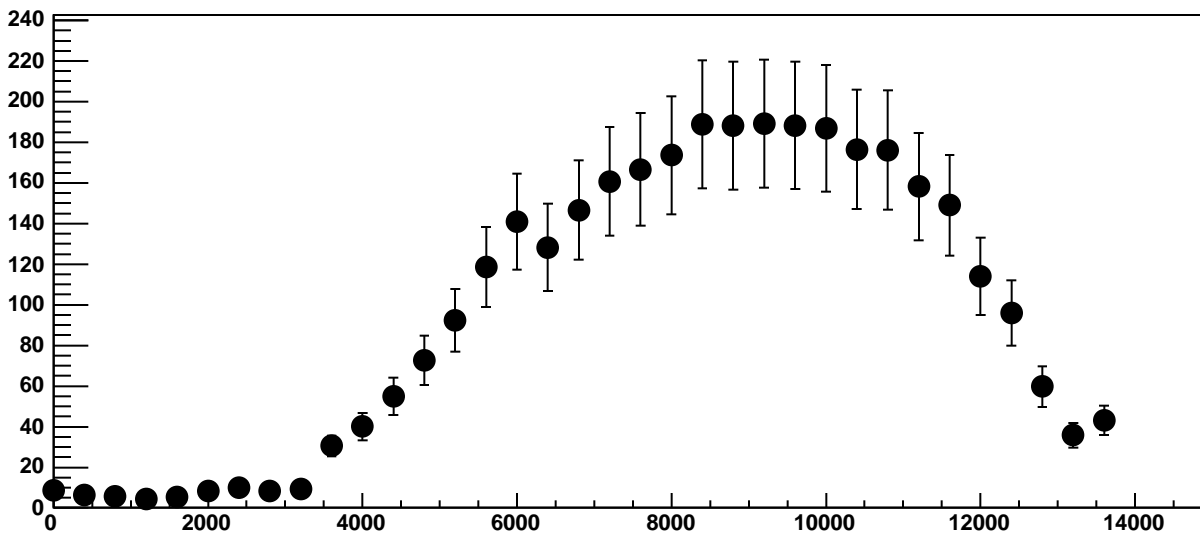


Chip 1, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC

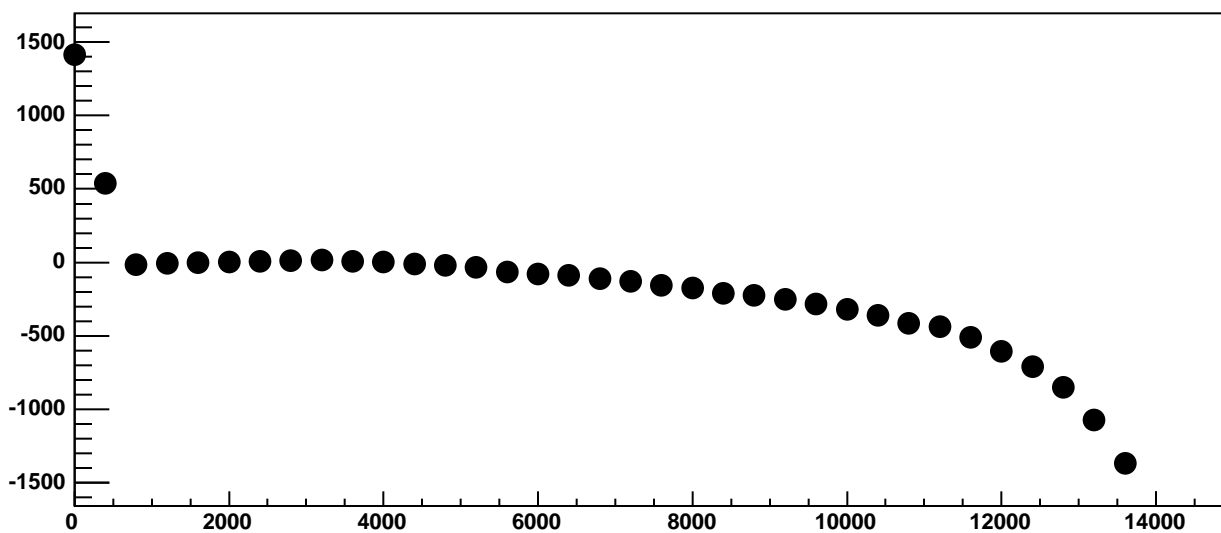
χ^2 / ndf 606.9 / 23
p0 -469.6 ± 1.535
p1 2.227 ± 0.0007543



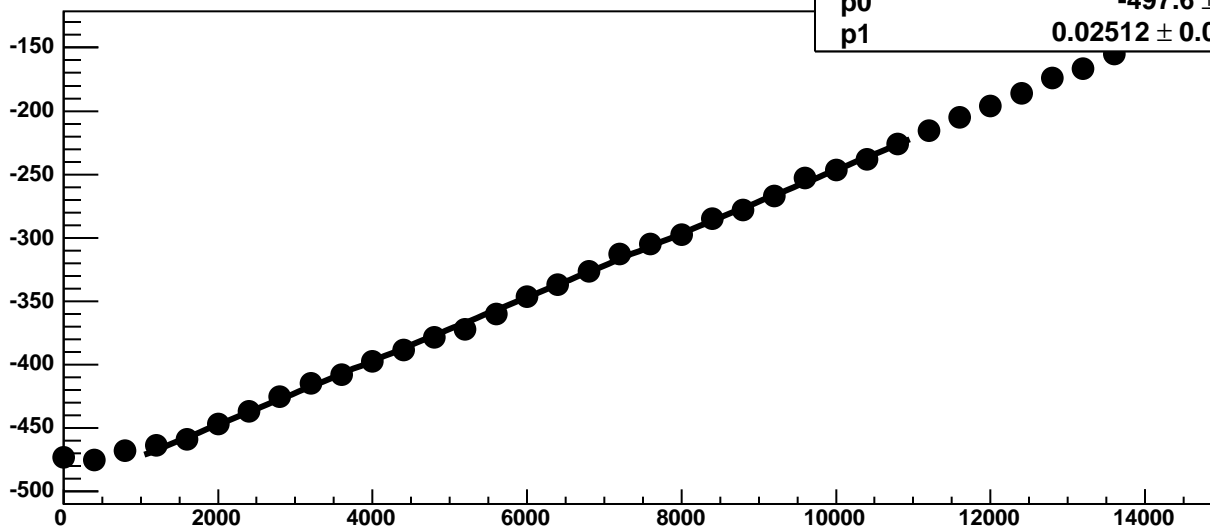
Chip 1, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC

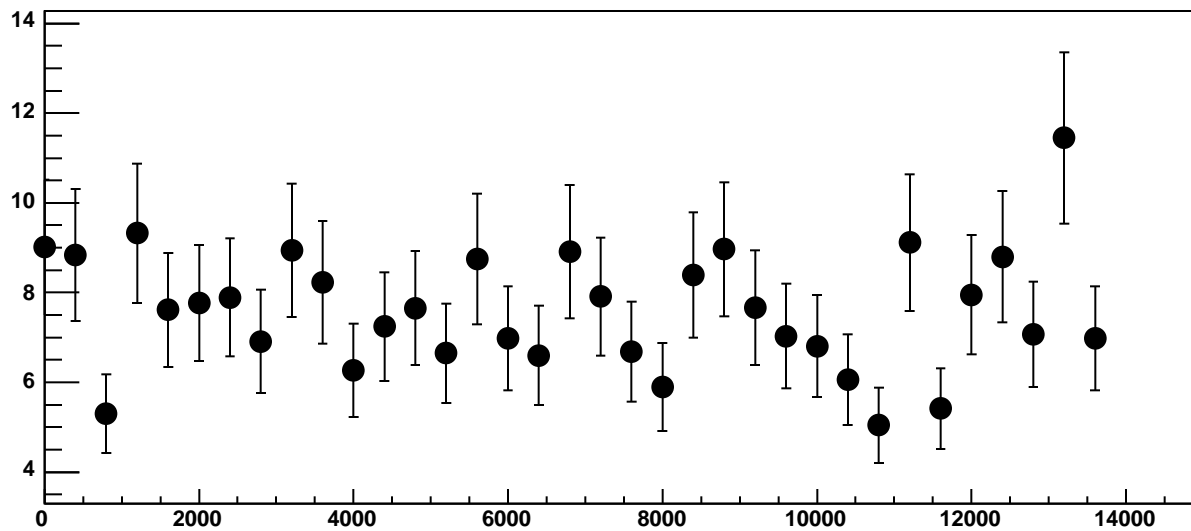


Chip 1, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

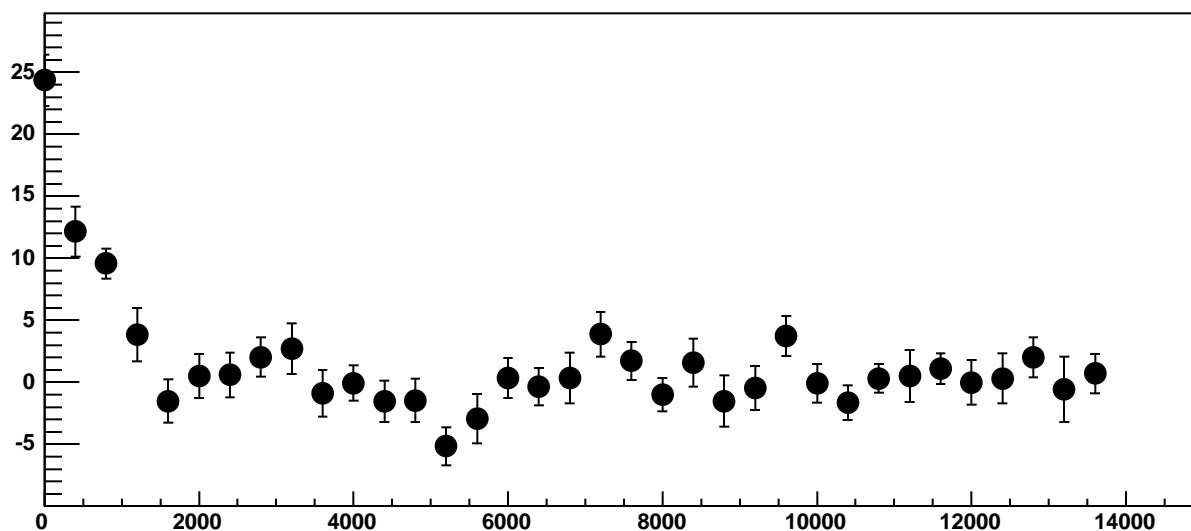


χ^2 / ndf 37.58 / 23
p0 -497.6 ± 0.7934
p1 0.02512 ± 0.0001123

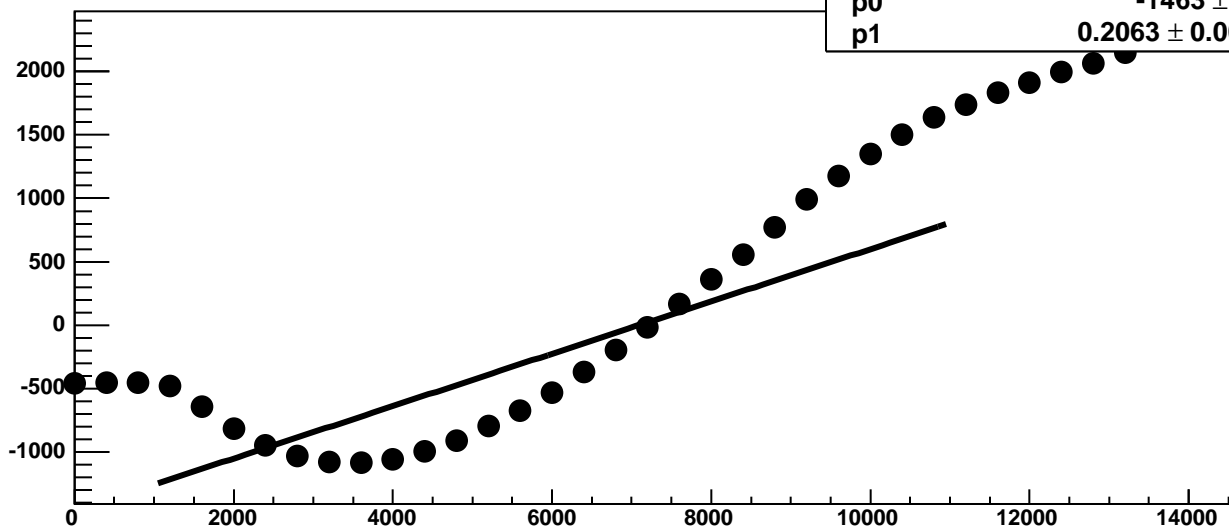
Chip 1, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

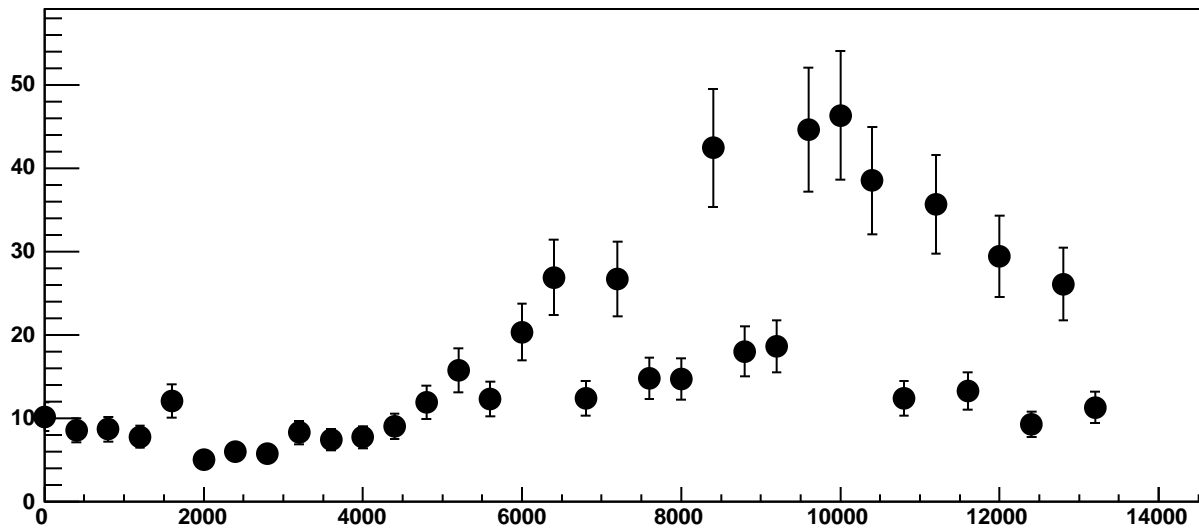


Chip 1, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

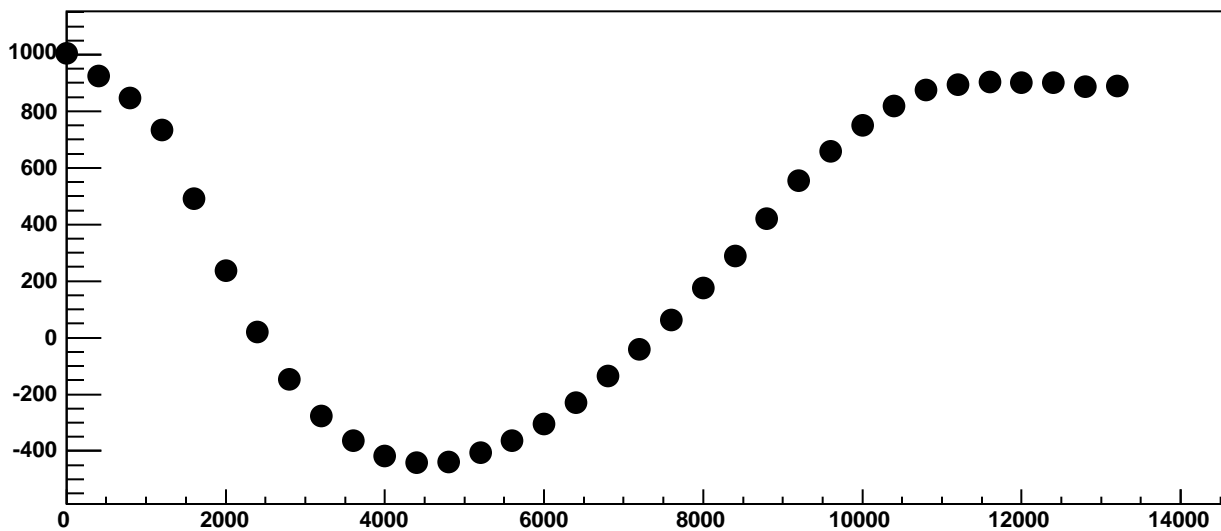


χ^2 / ndf 6.242e+05 / 23
p0 -1463 ± 0.9163
p1 0.2063 ± 0.0002098

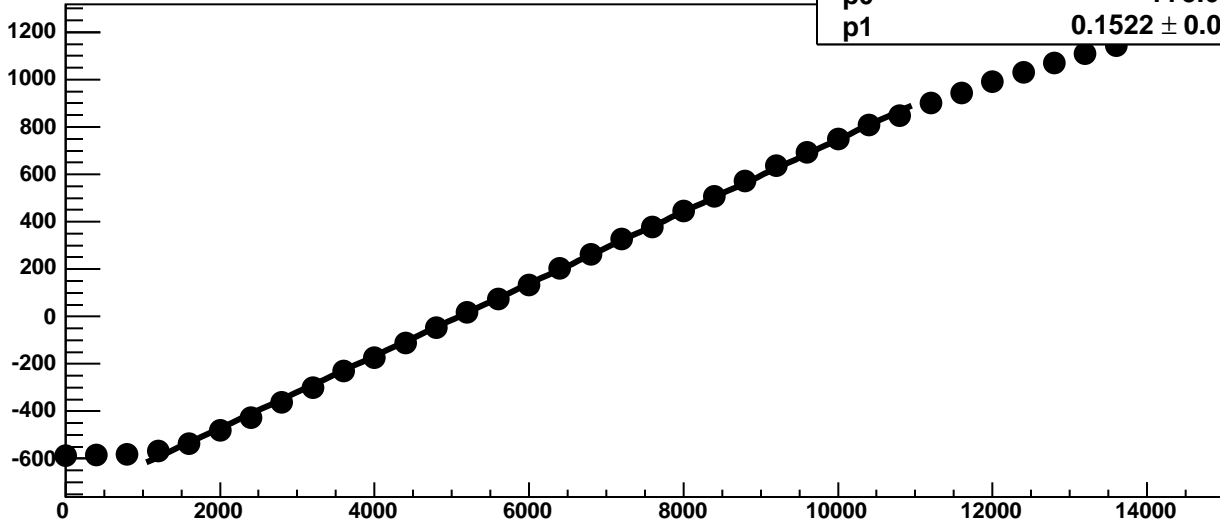
Chip 1, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



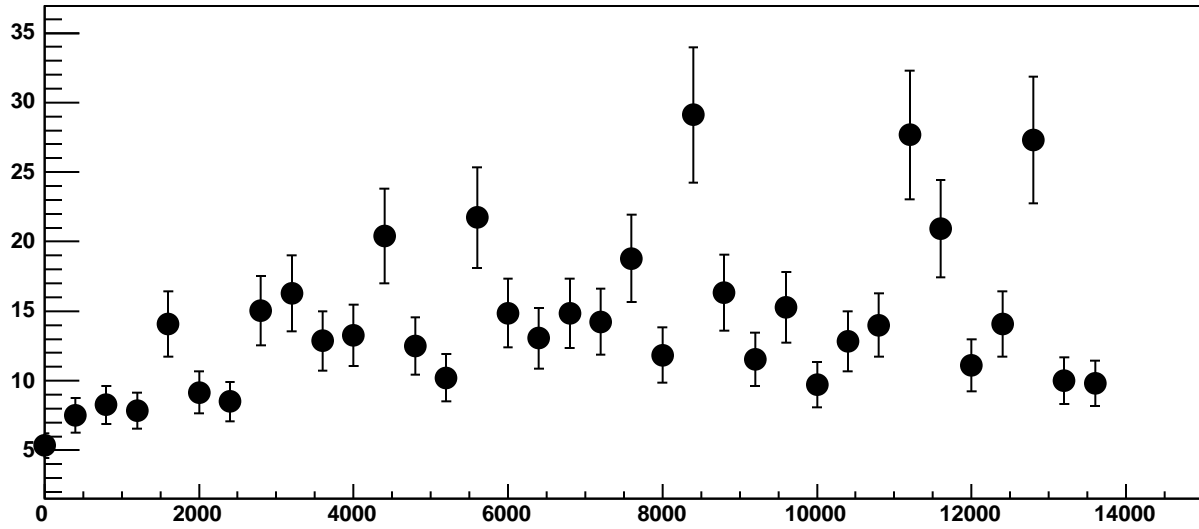
Chip 1, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



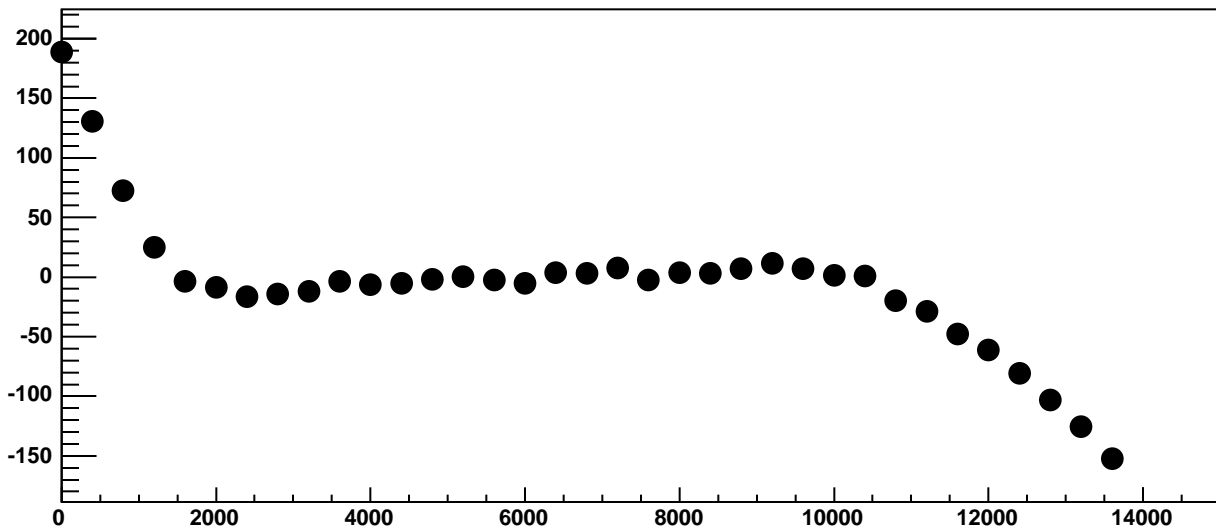
Chip 1, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



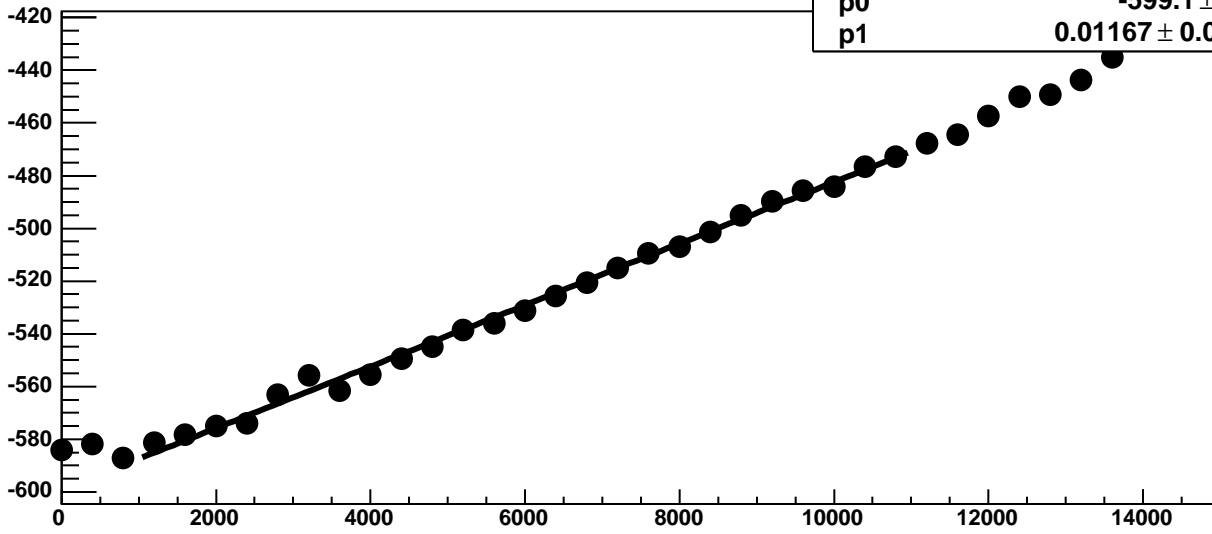
Chip 1, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

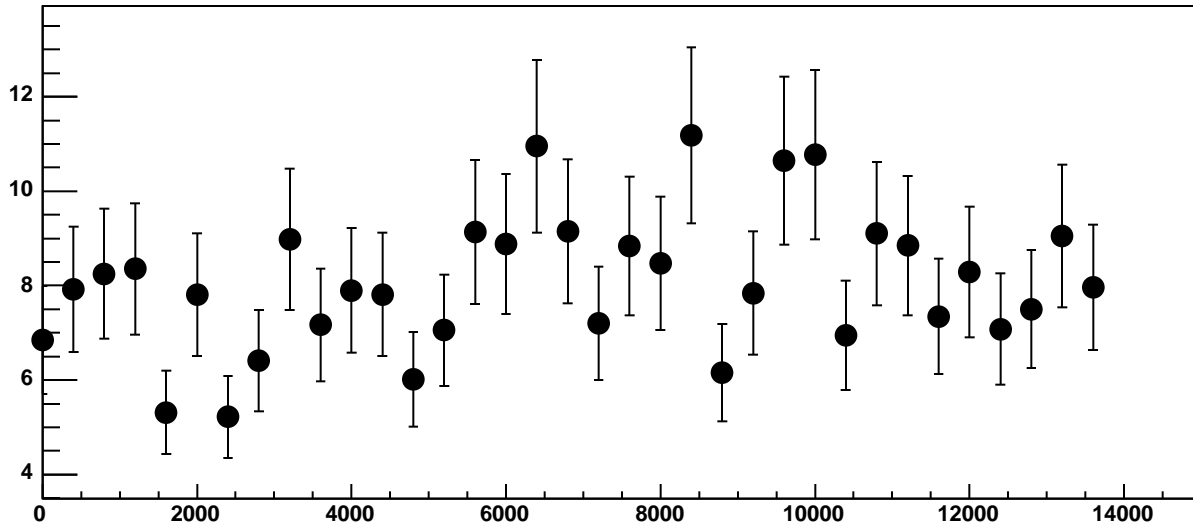


Chip 1, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

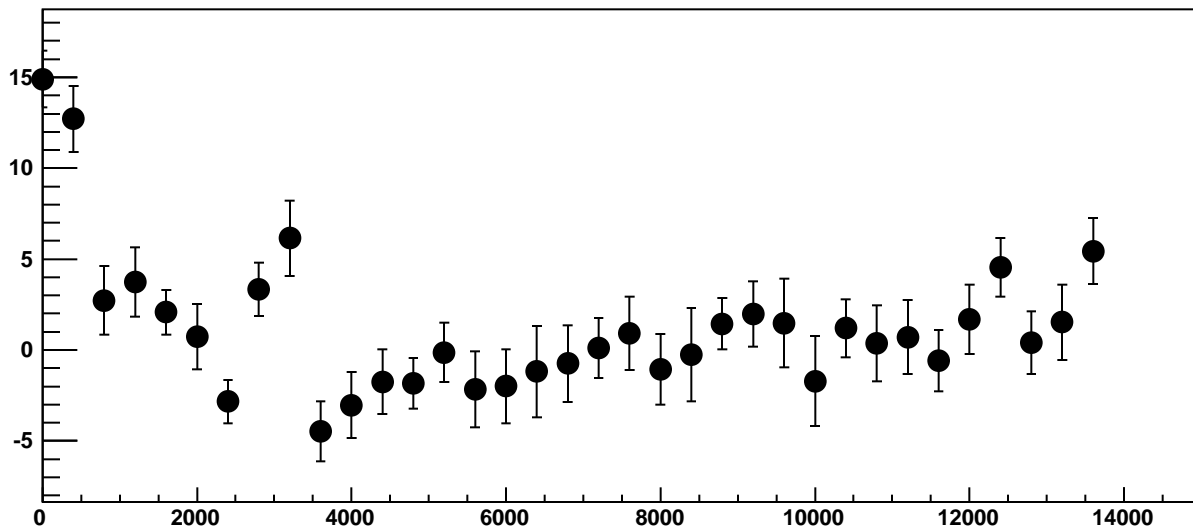


χ^2 / ndf 46.05 / 23
p0 -599.1 ± 0.7347
p1 0.01167 ± 0.0001192

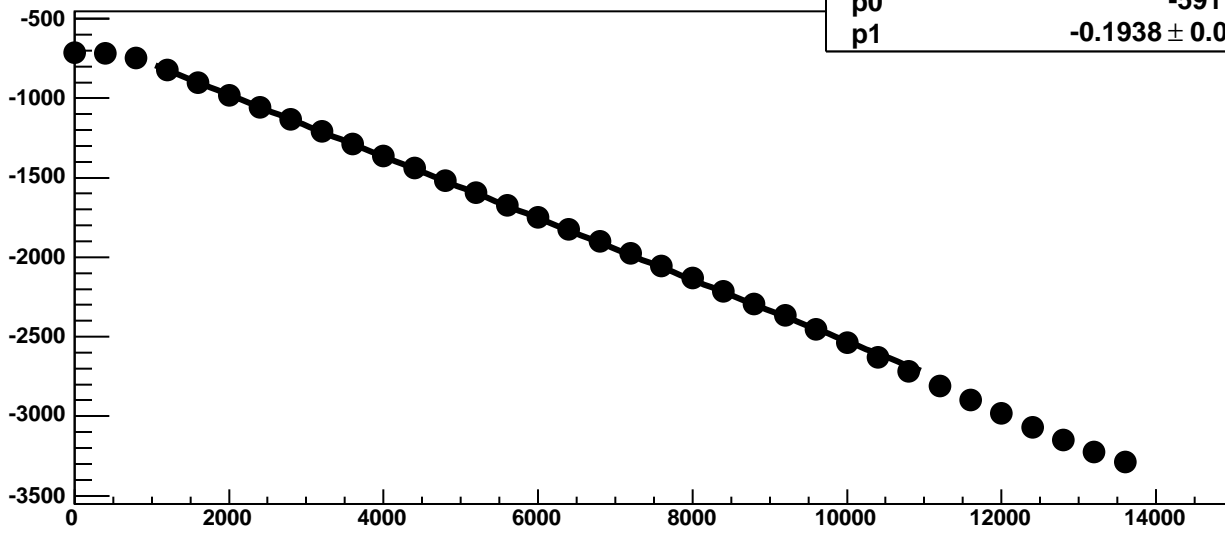
Chip 1, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC

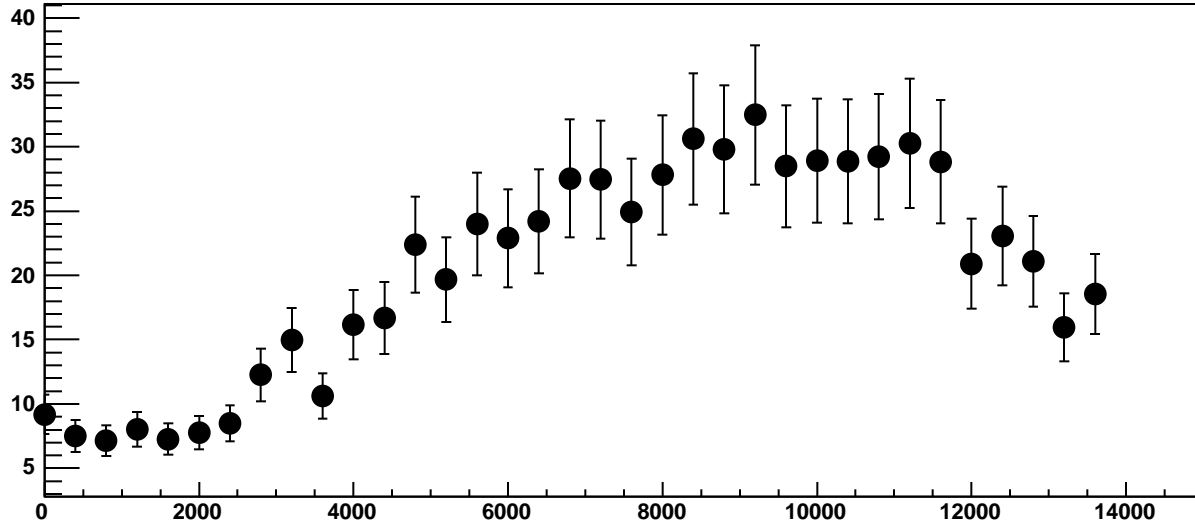


Chip 1, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

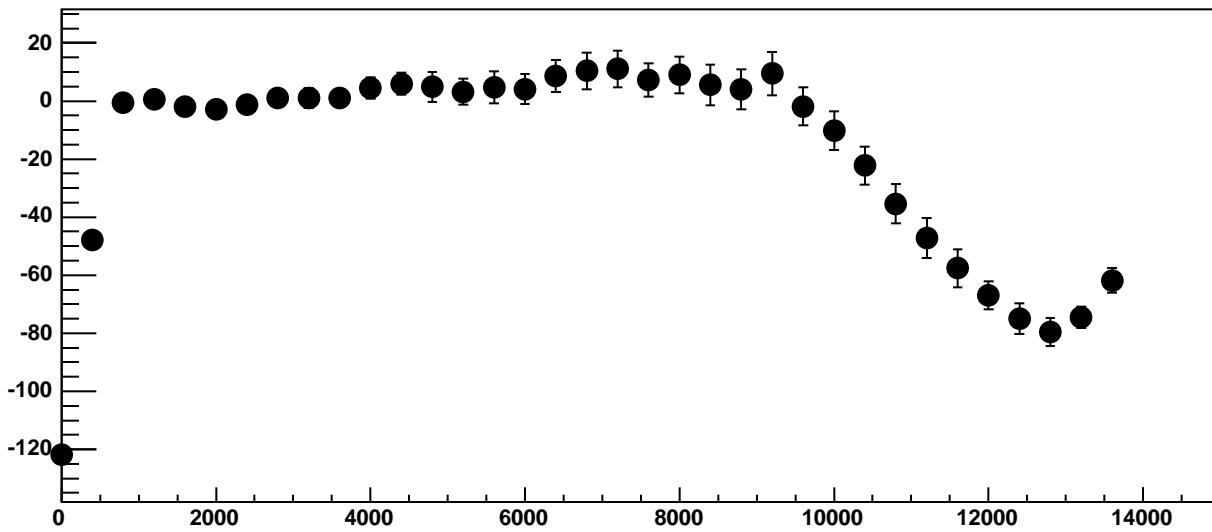


χ^2 / ndf	67.65 / 23
p0	-591 ± 1.165
p1	-0.1938 ± 0.0002862

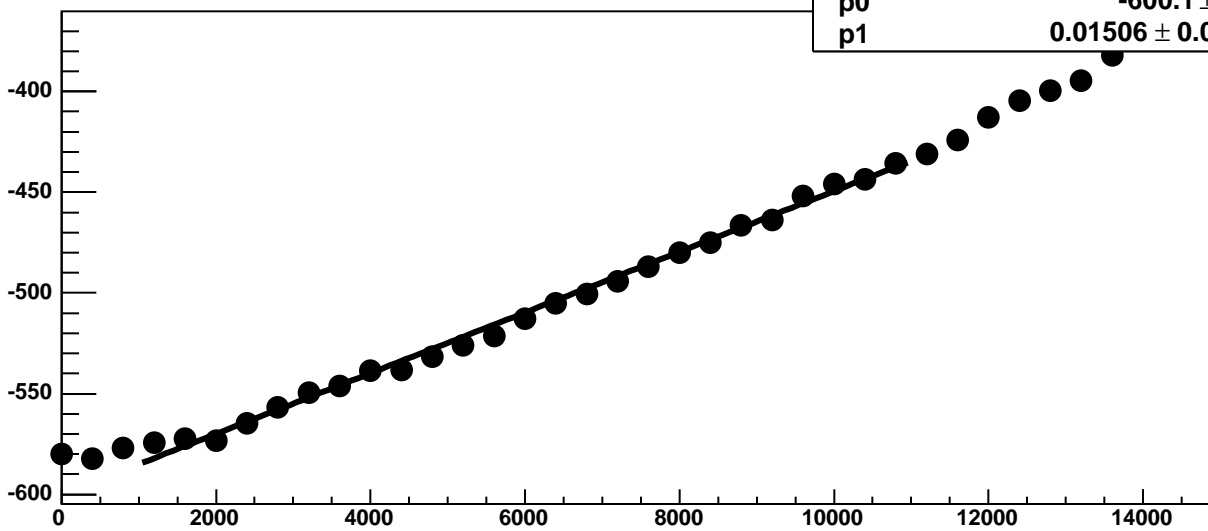
Chip 1, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

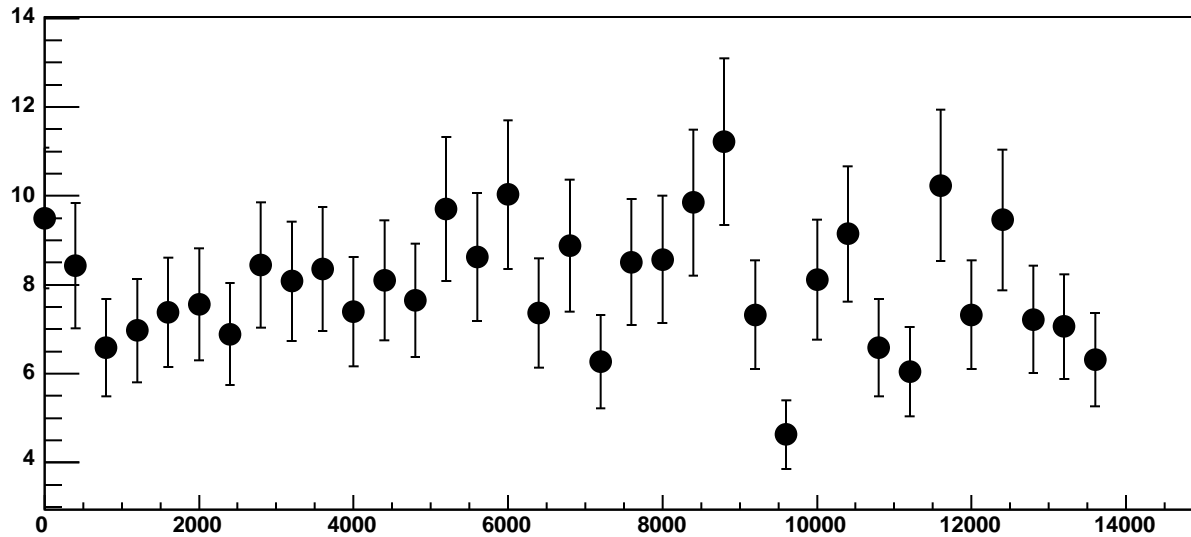


Chip 1, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

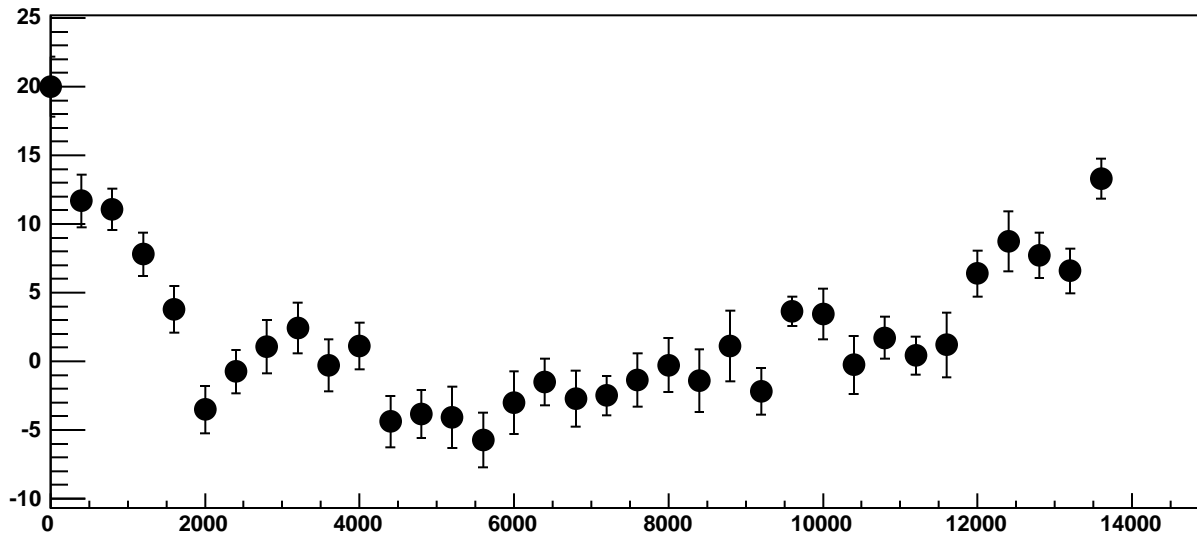


χ^2 / ndf 84.04 / 23
p0 -600.1 ± 0.7918
p1 0.01506 ± 0.0001157

Chip 1, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC

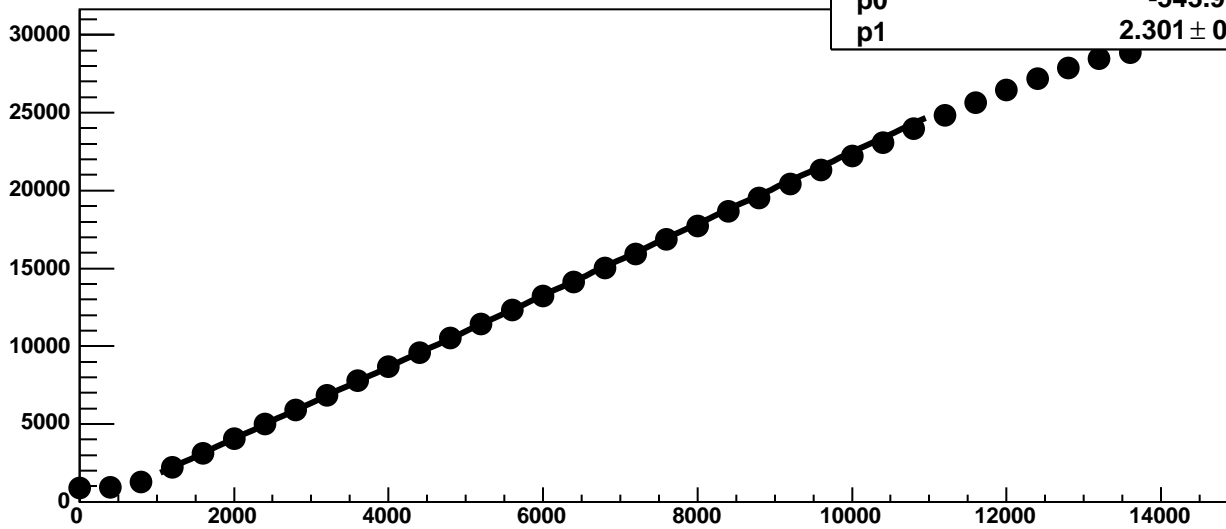


Chip 1, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

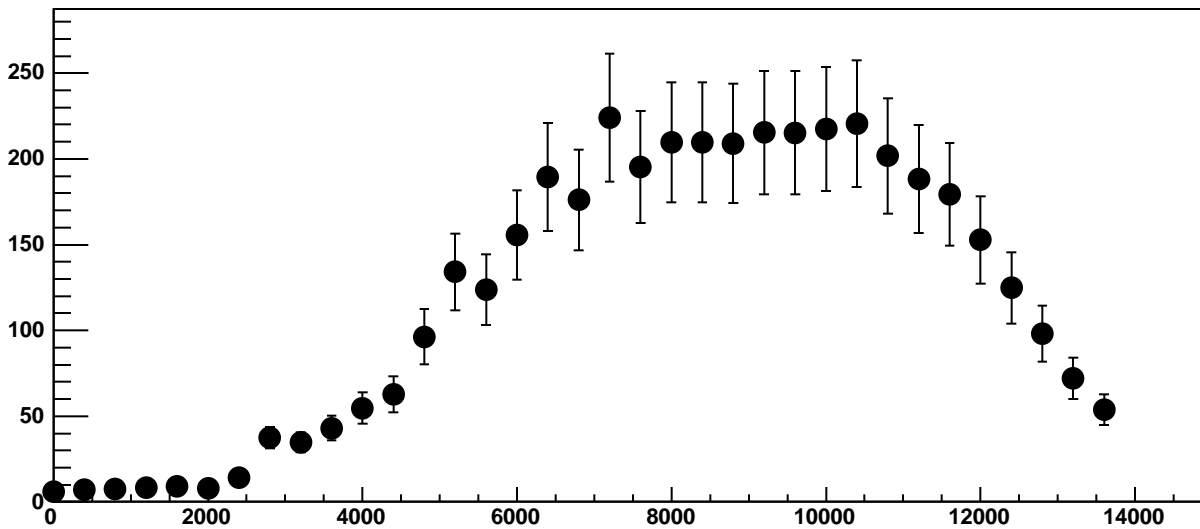


Chip 1, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC

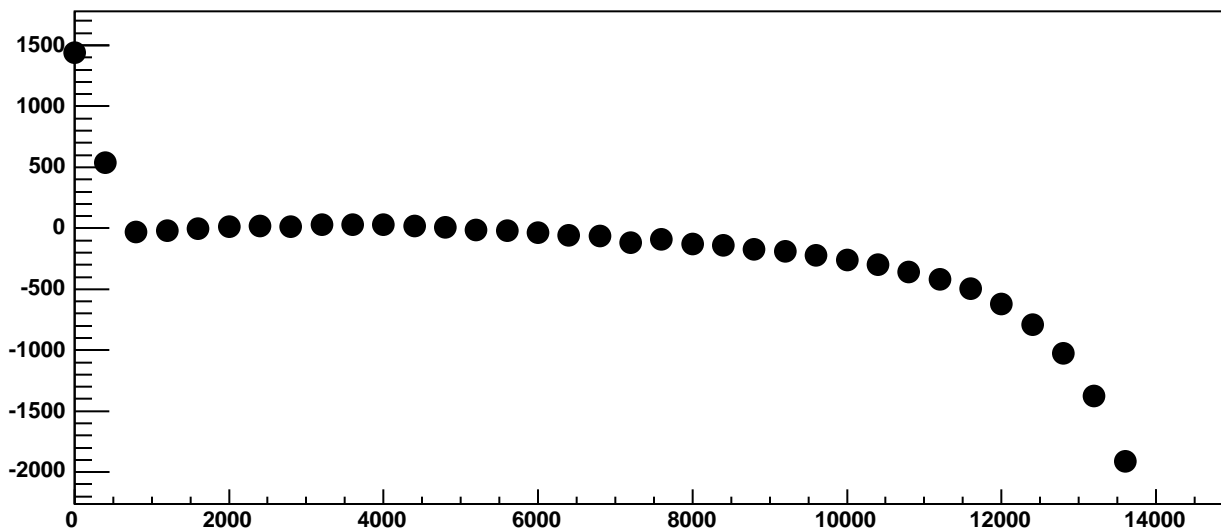
χ^2 / ndf 405.6 / 23
p0 -543.9 ± 2.566
p1 2.301 ± 0.001281



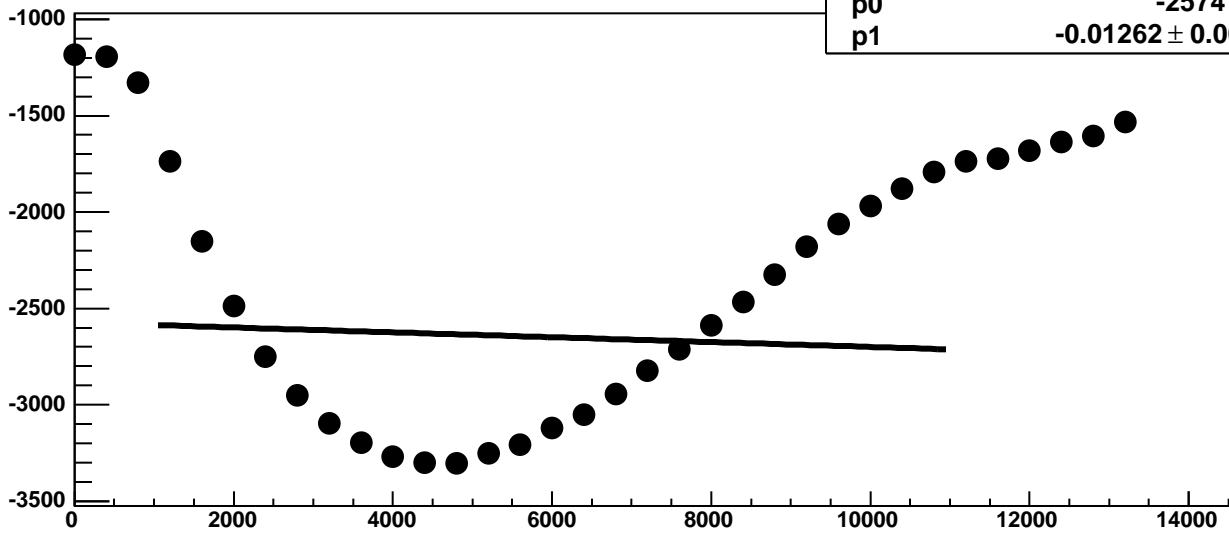
Chip 1, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC

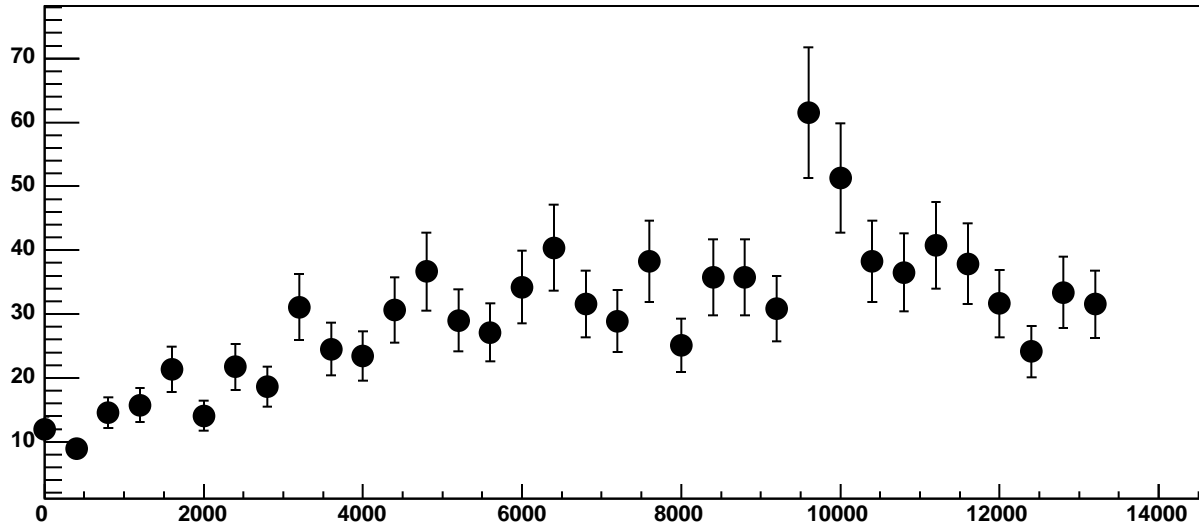


Chip 1, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

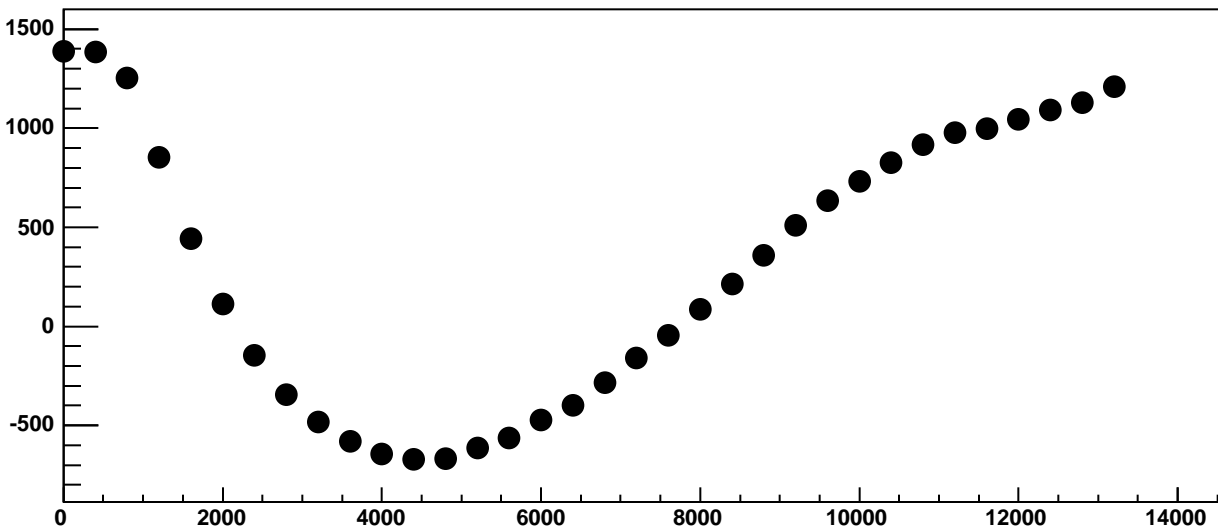


χ^2 / ndf 1.767e+05 / 23
p0 -2574 ± 2.251
p1 -0.01262 ± 0.0004315

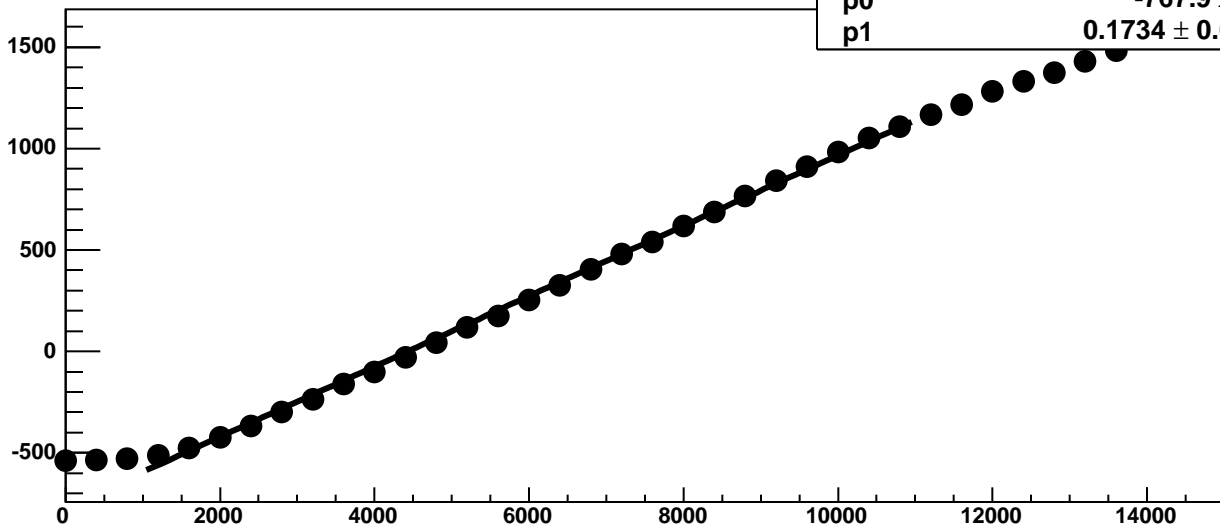
Chip 1, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



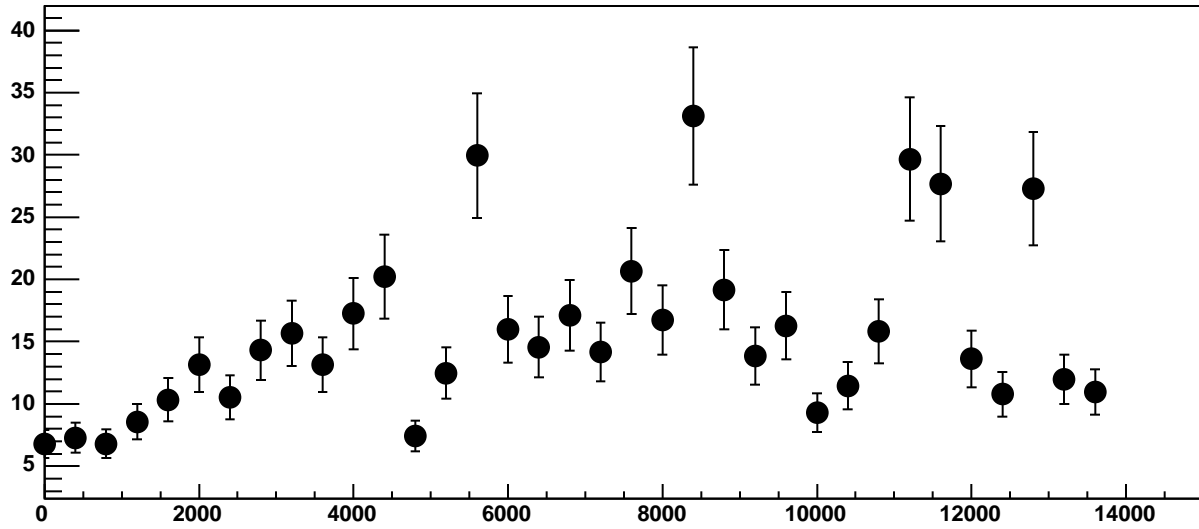
Chip 1, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC



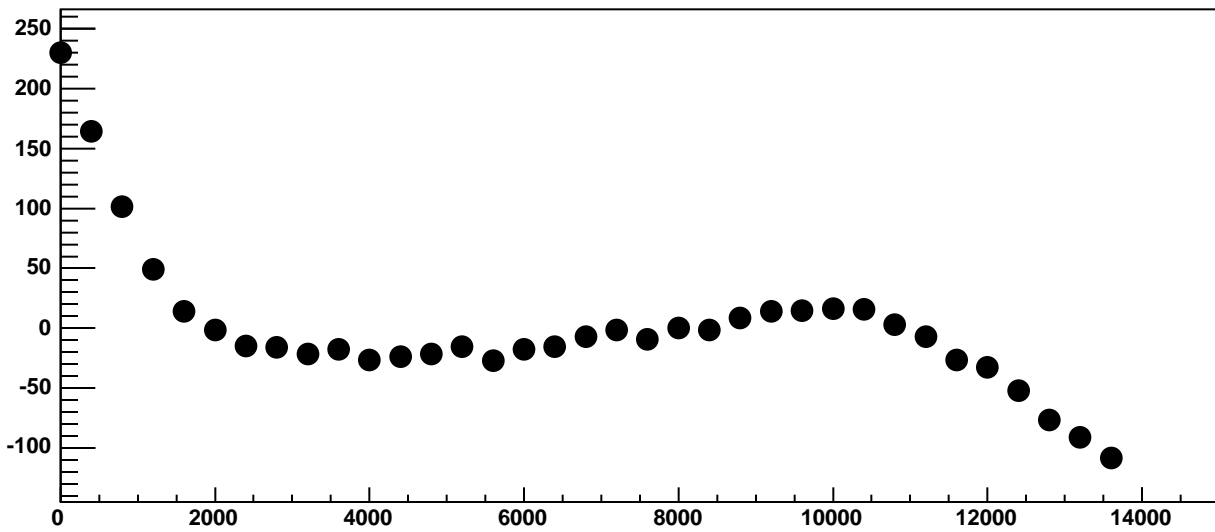
Chip 1, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



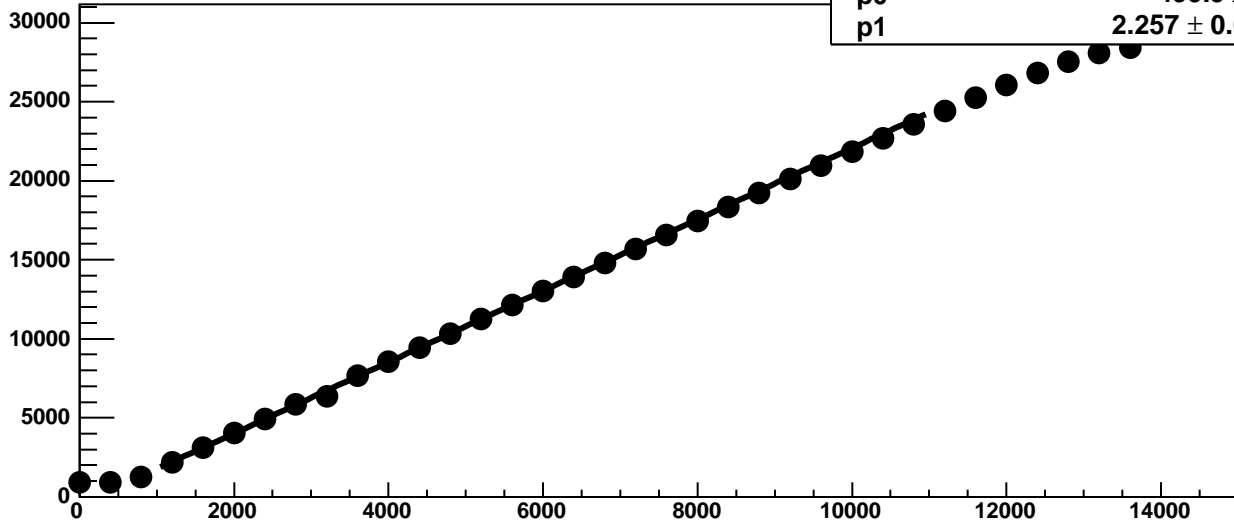
Chip 1, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



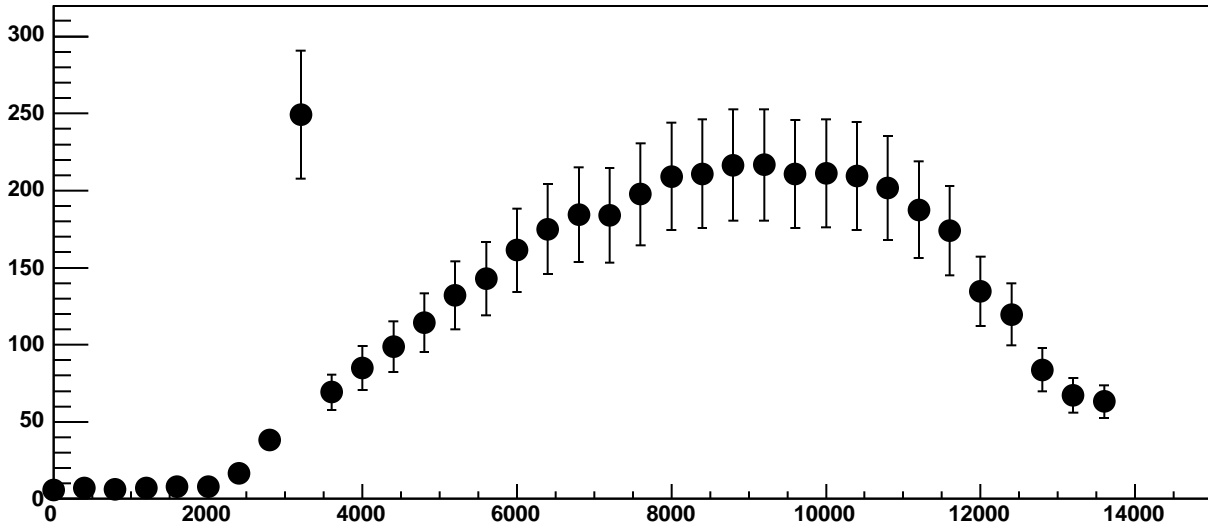
Chip 1, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



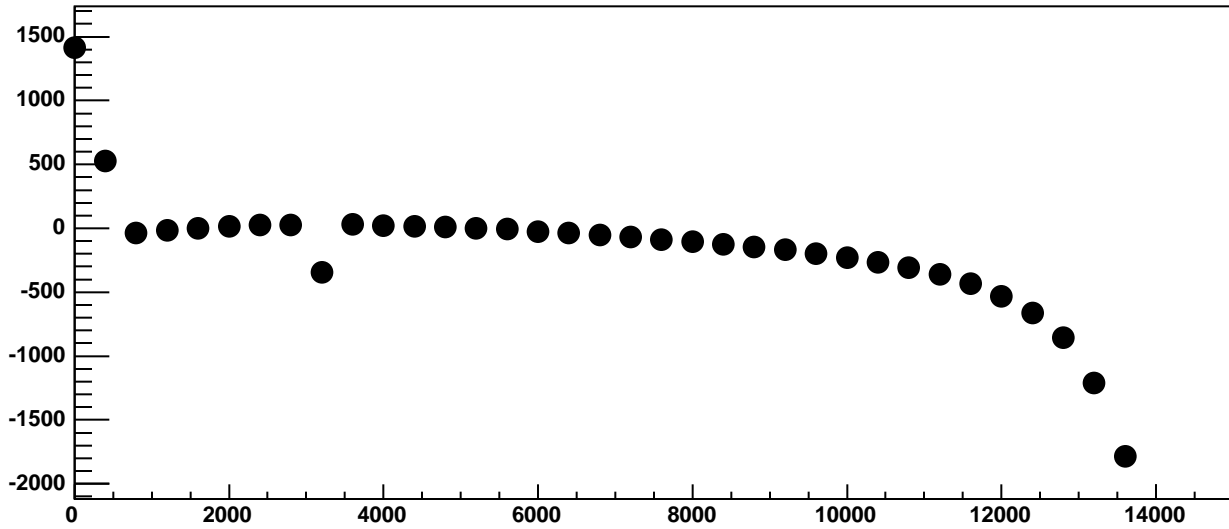
Chip 1, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC



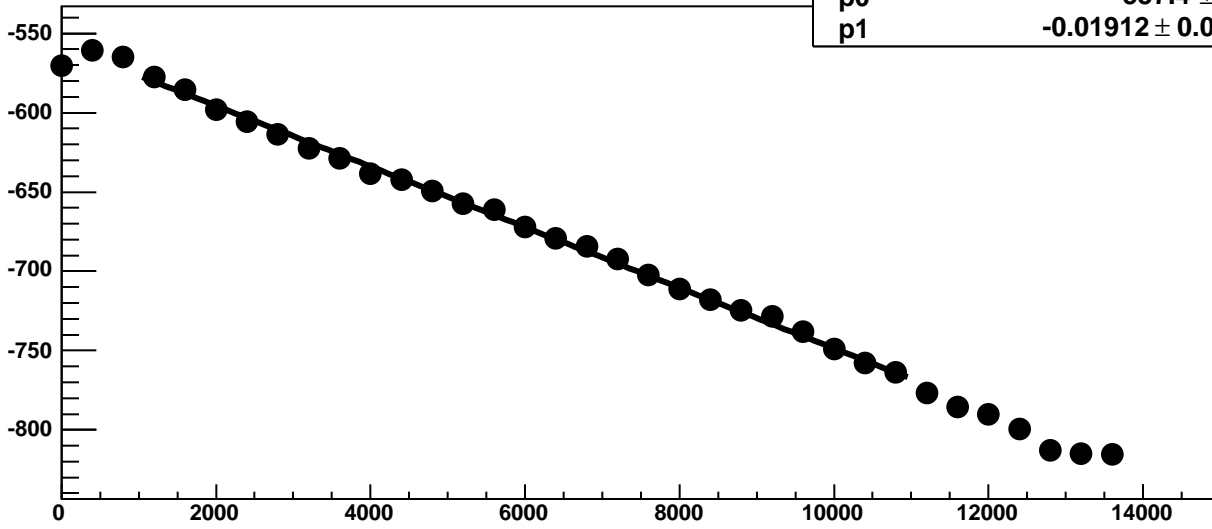
Chip 1, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC

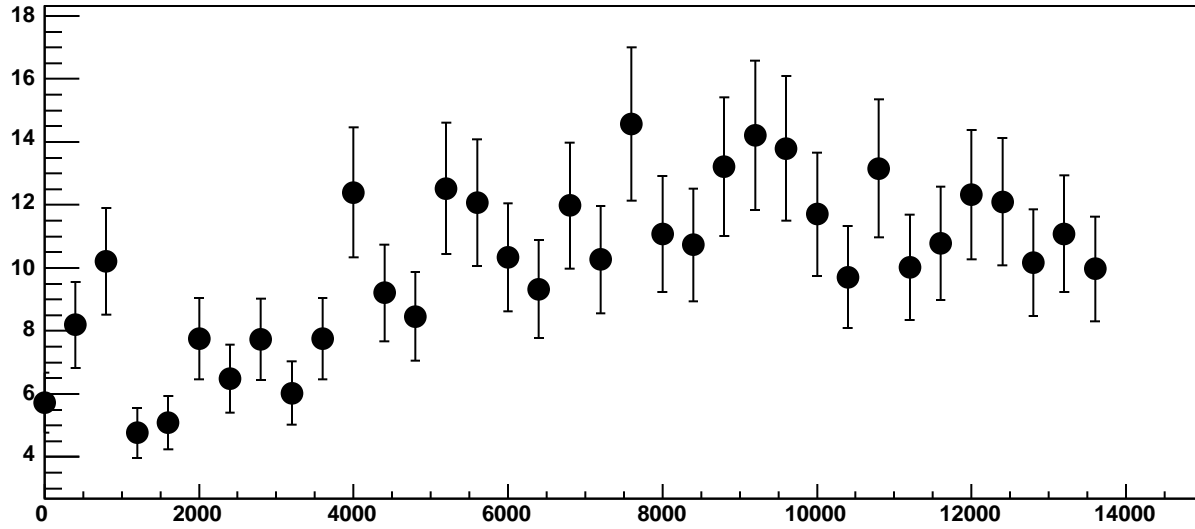


Chip 1, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

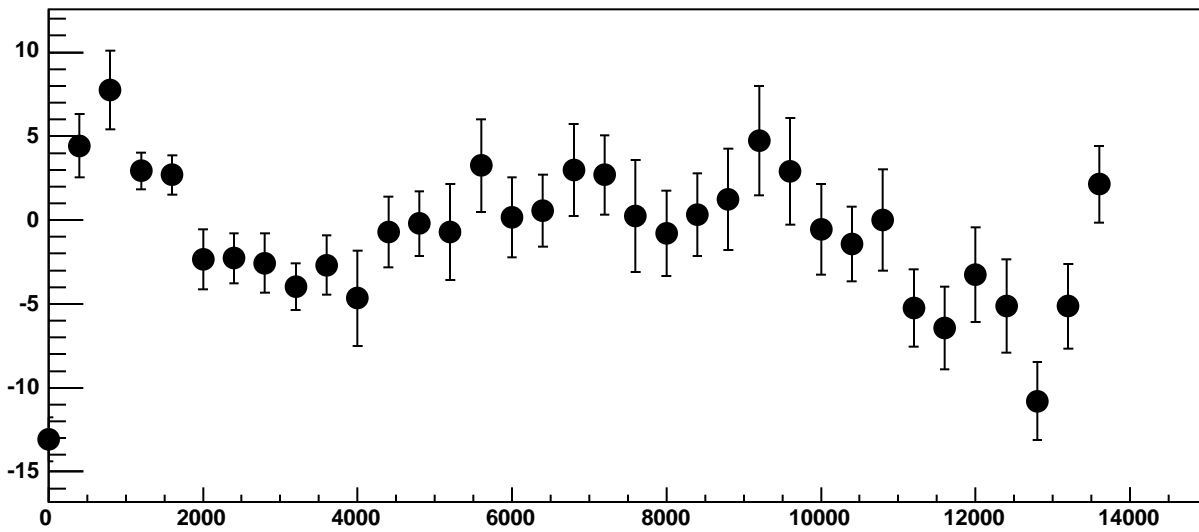


χ^2 / ndf 39.73 / 23
p0 -557.4 ± 0.7237
p1 -0.01912 ± 0.0001384

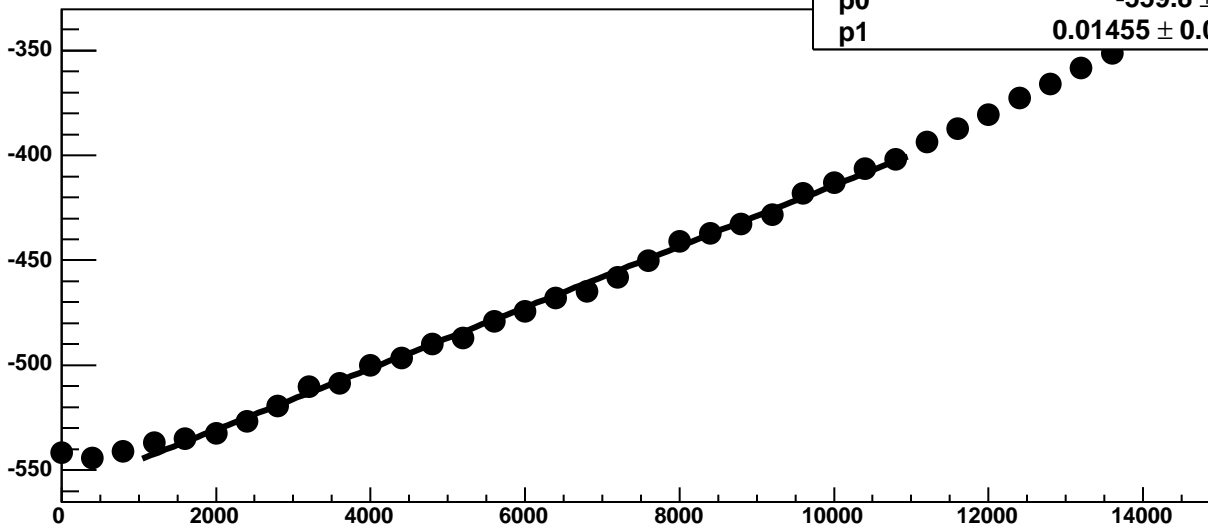
Chip 1, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

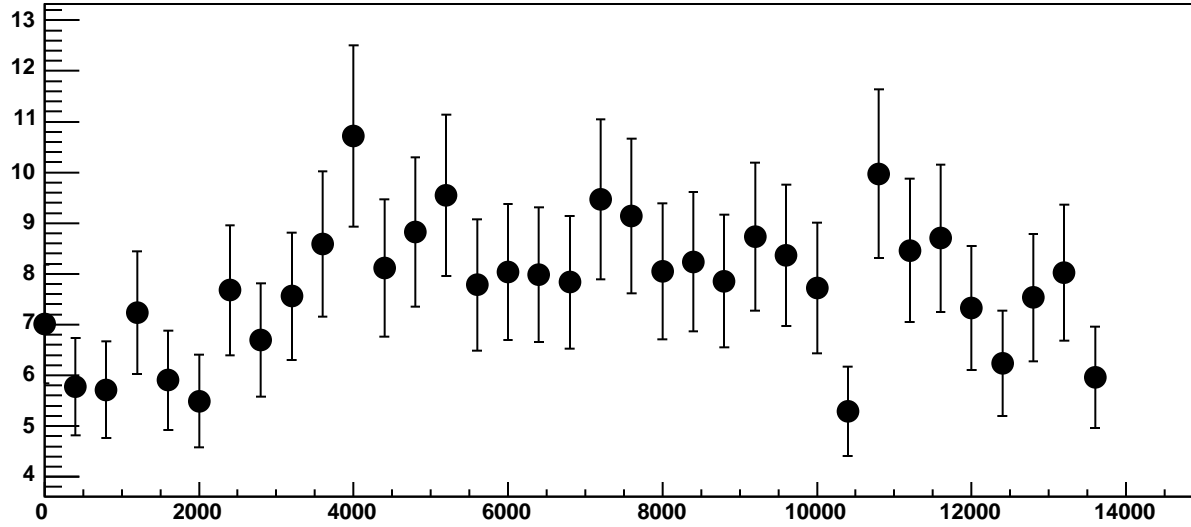


Chip 1, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

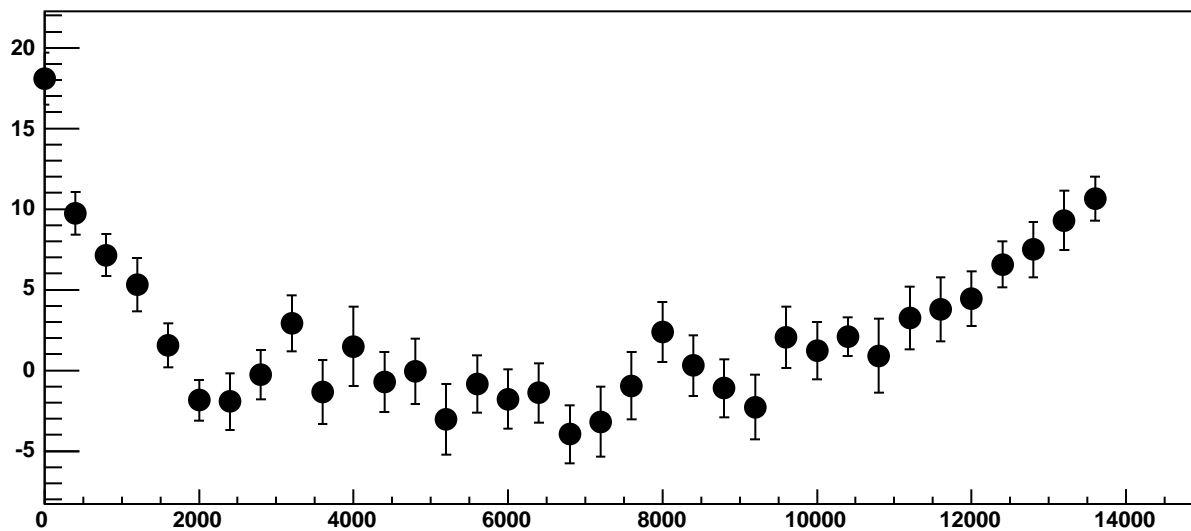


χ^2 / ndf 37.71 / 23
p0 -559.8 ± 0.7398
p1 0.01455 ± 0.0001132

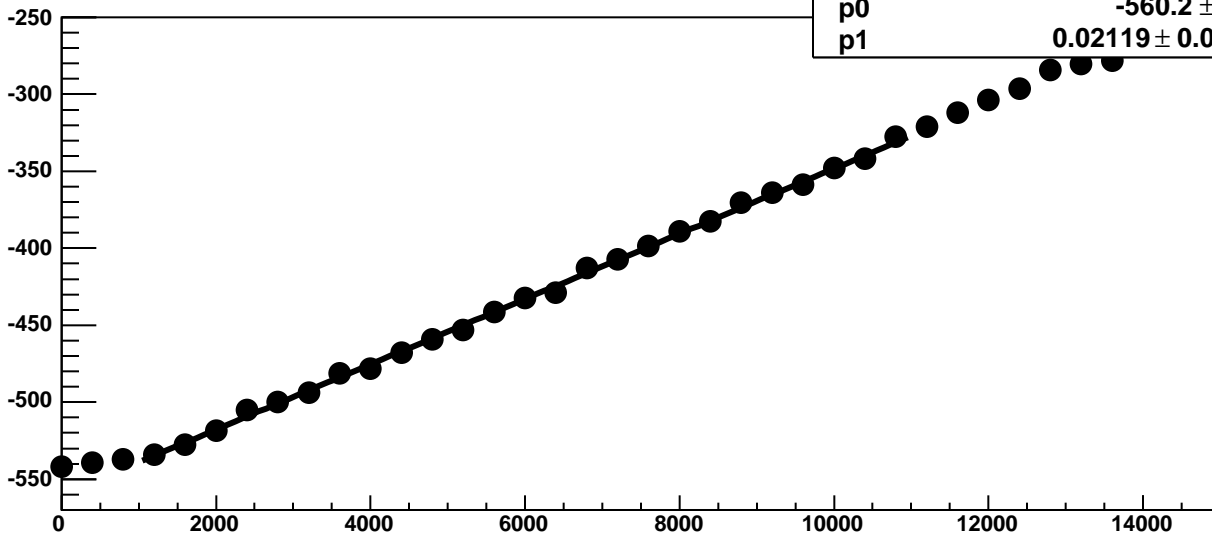
Chip 1, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

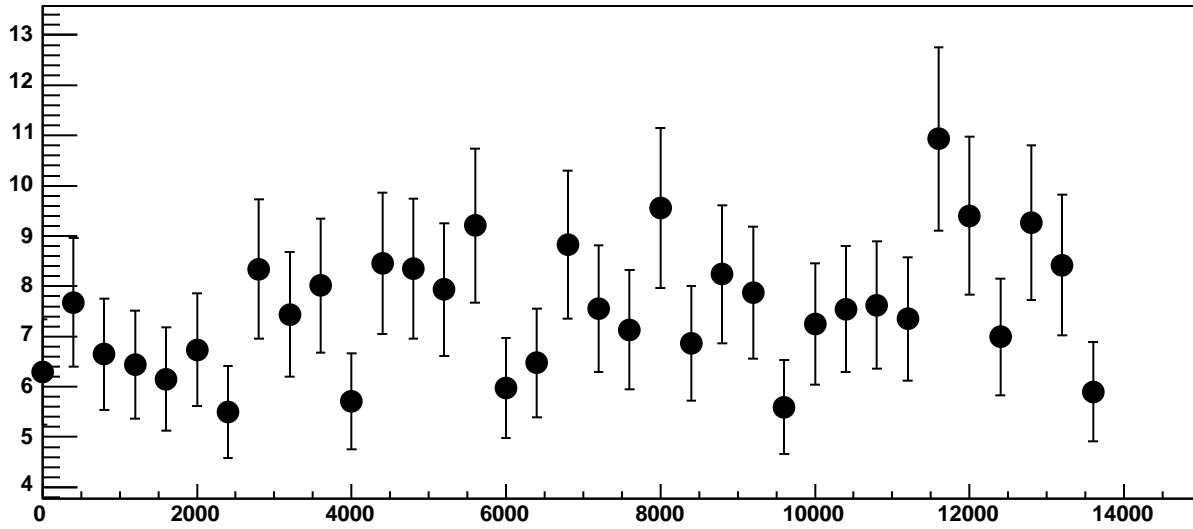


Chip 1, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC

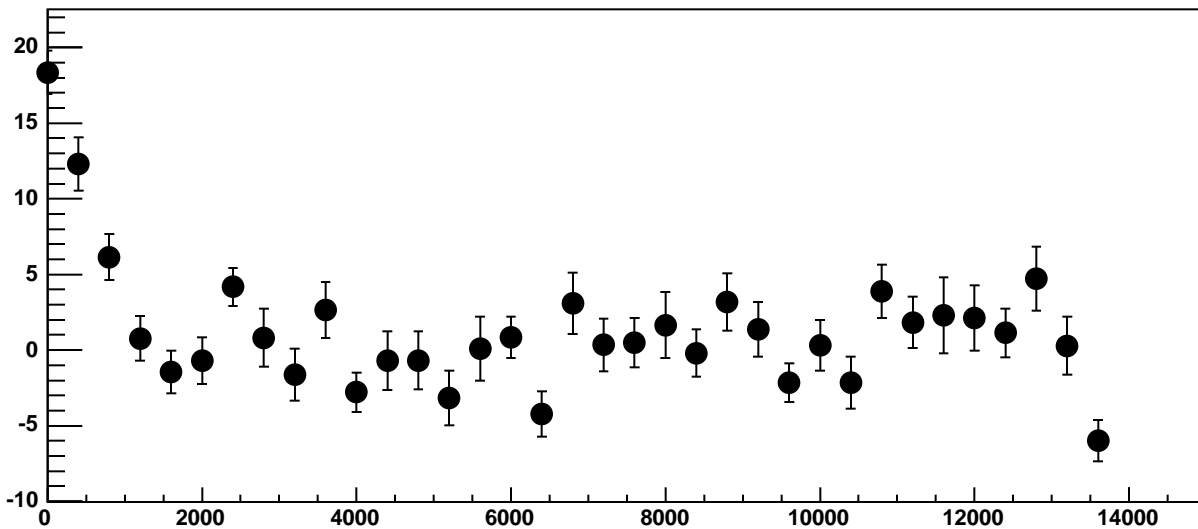


χ^2 / ndf 47.66 / 23
p0 -560.2 ± 0.7139
p1 0.02119 ± 0.0001095

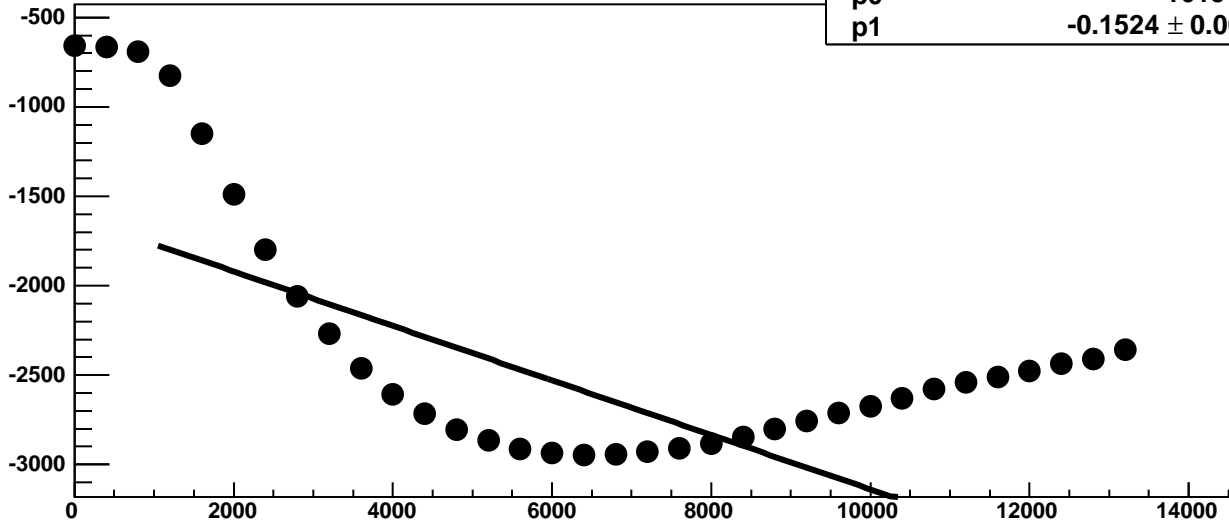
Chip 1, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

6.141e+05 / 23

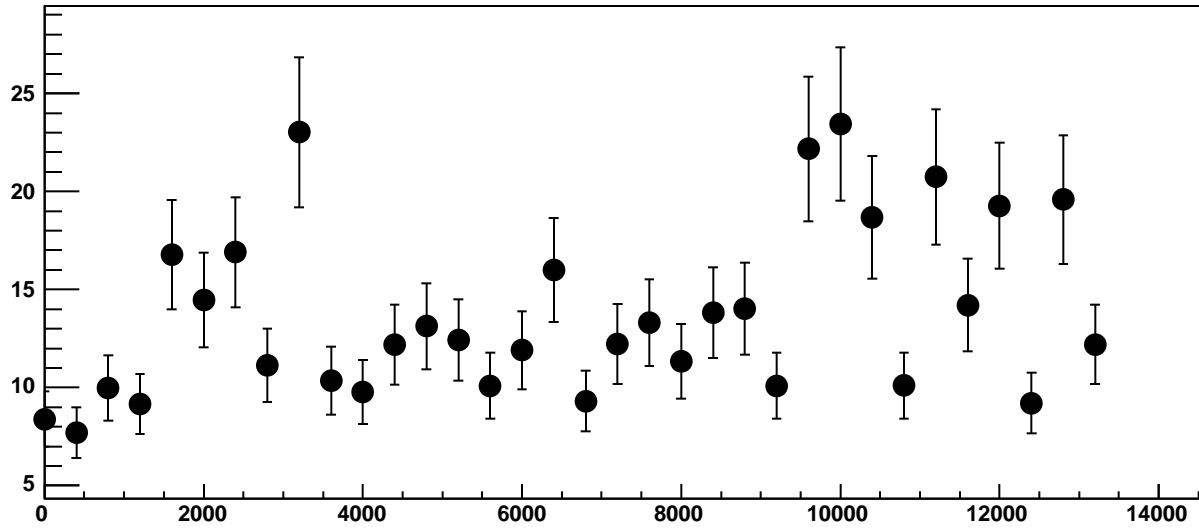
p0

-1615 ± 1.321

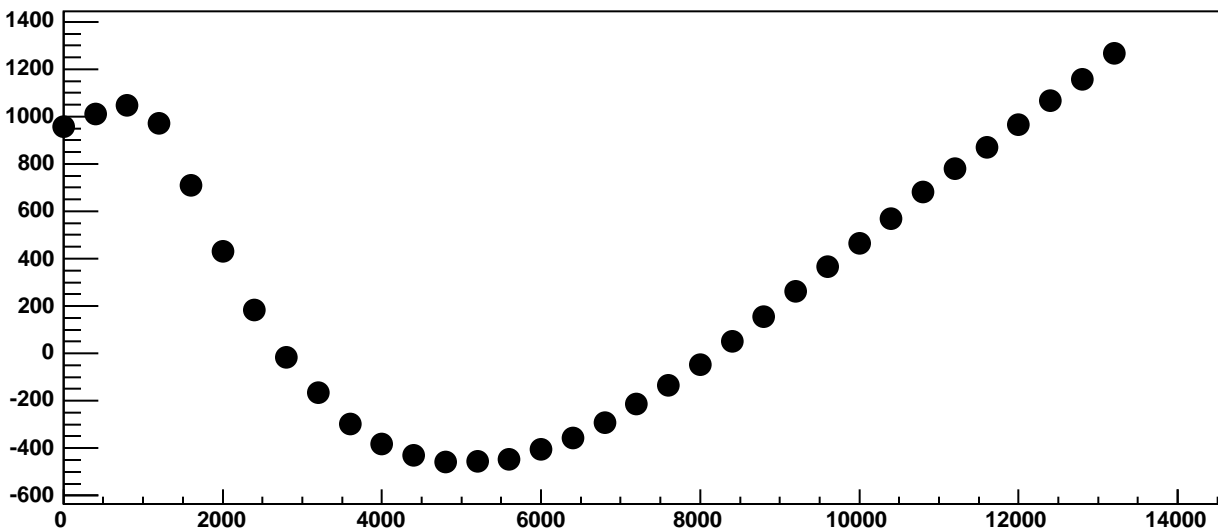
p1

-0.1524 ± 0.0002048

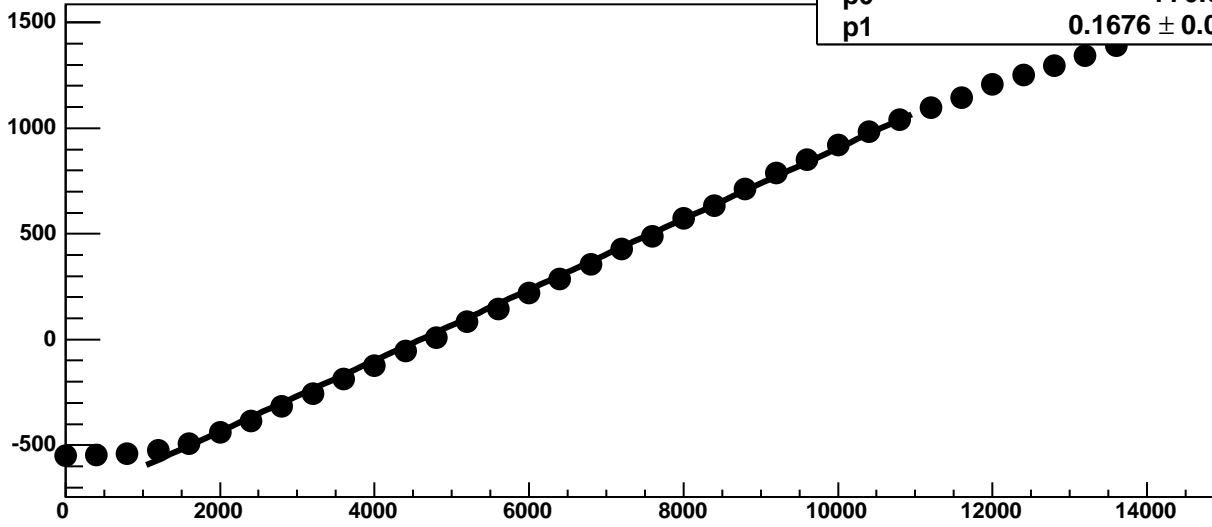
Chip 1, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



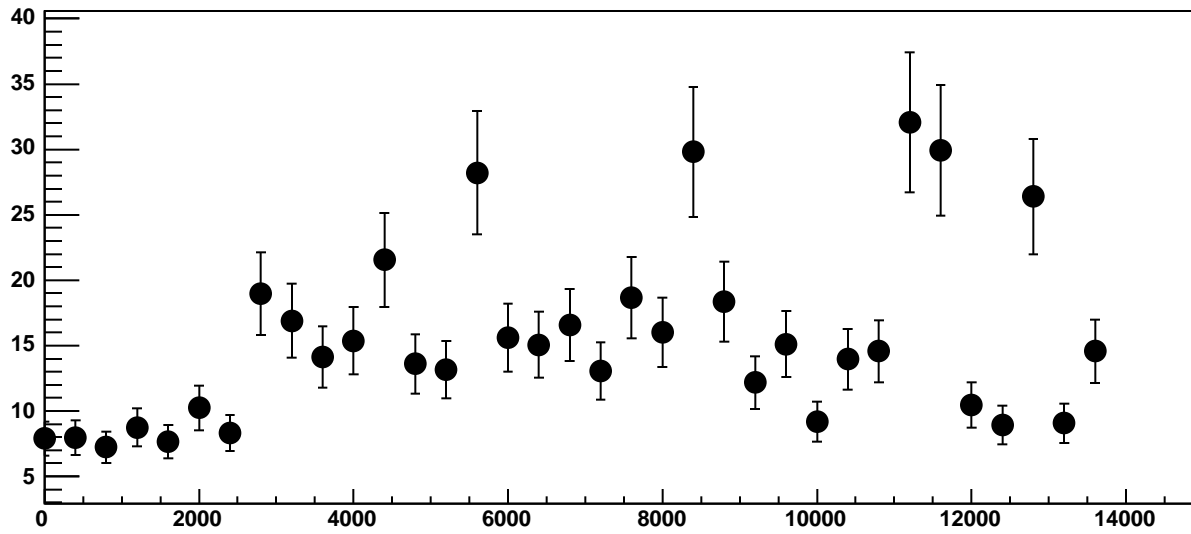
Chip 1, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



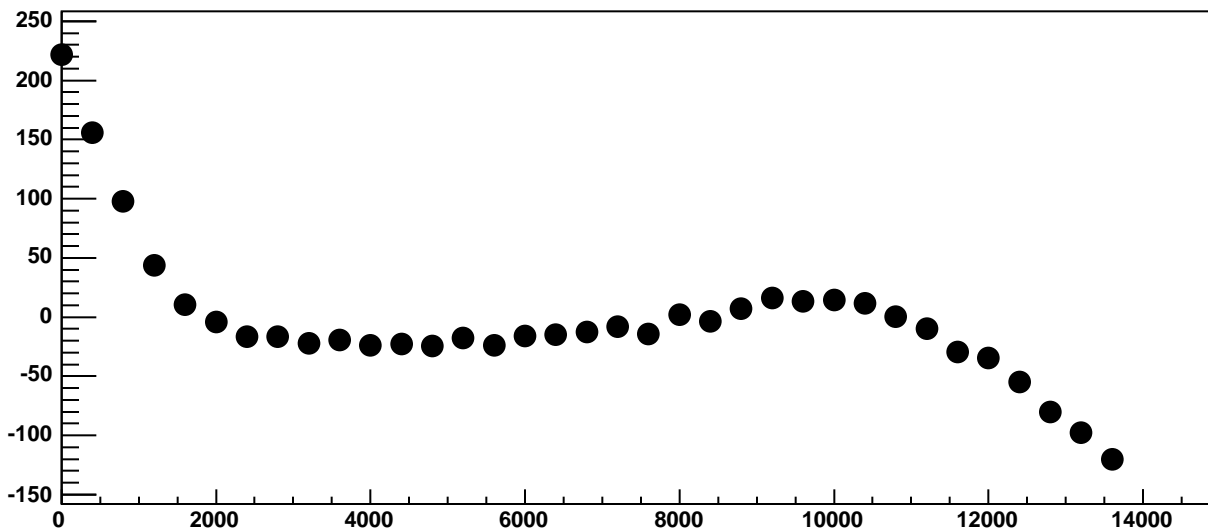
Chip 1, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



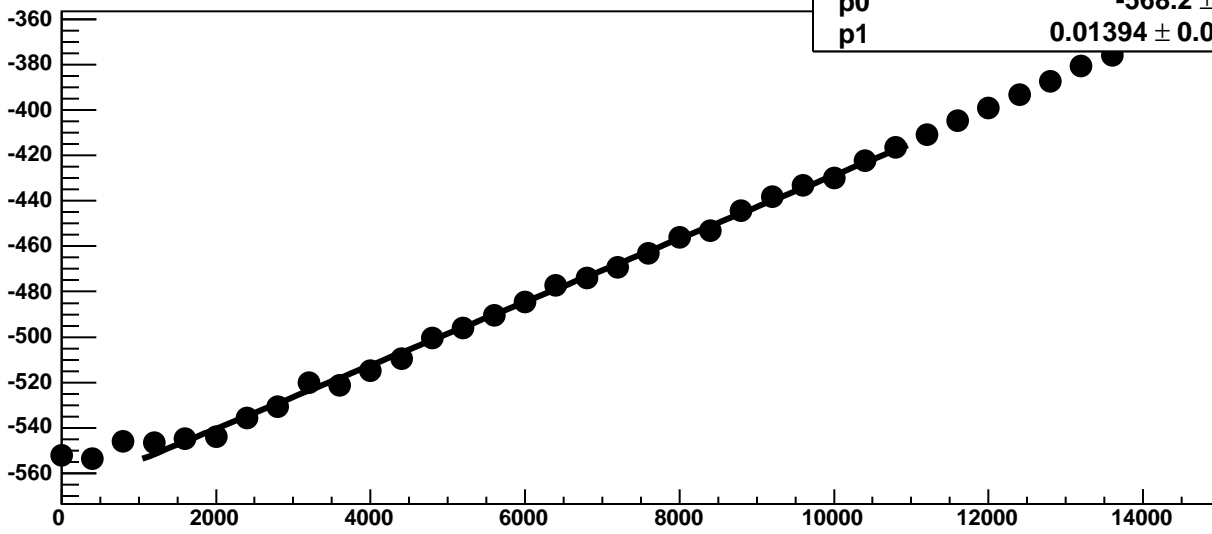
Chip 1, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



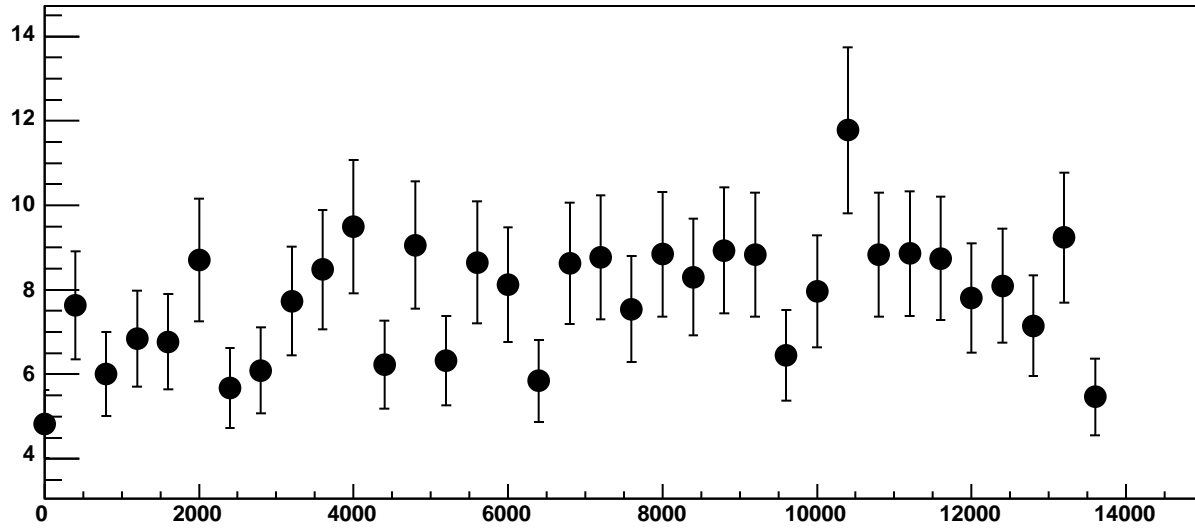
Chip 1, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



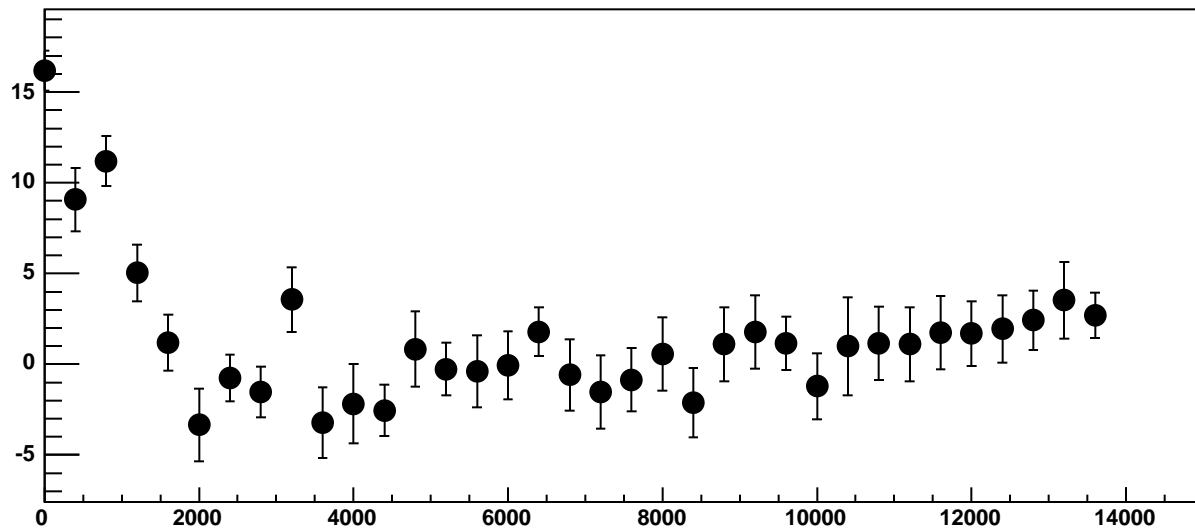
Chip 1, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC



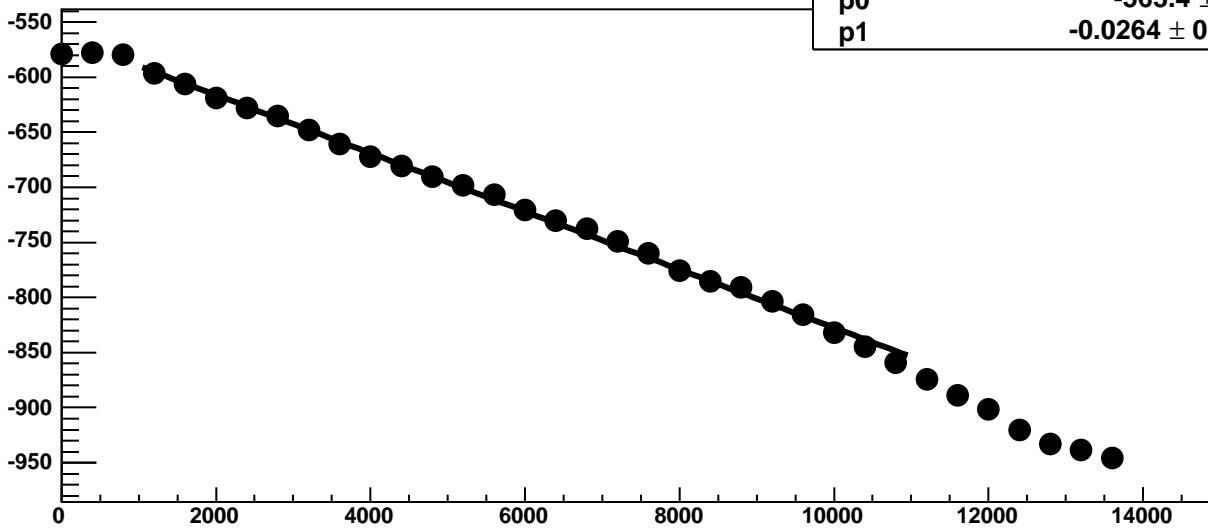
Chip 1, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

65.4 / 23

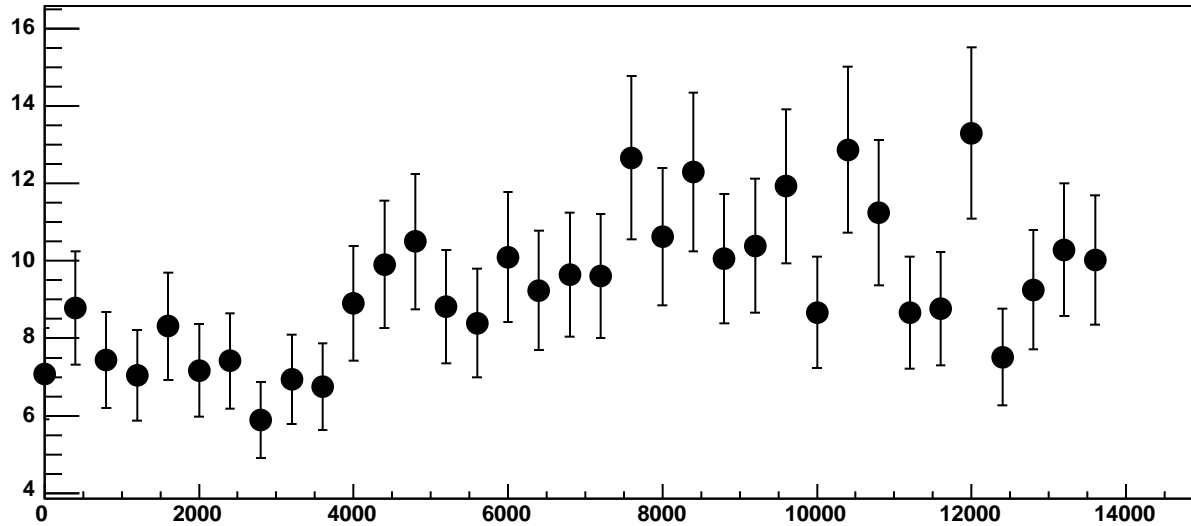
p0

-563.4 ± 0.8278

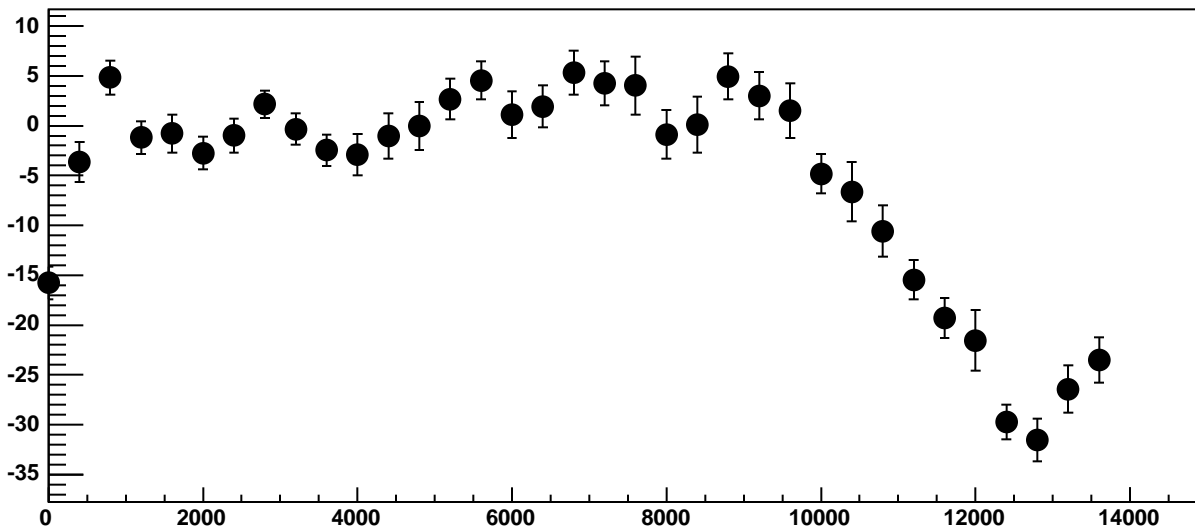
p1

-0.0264 ± 0.000143

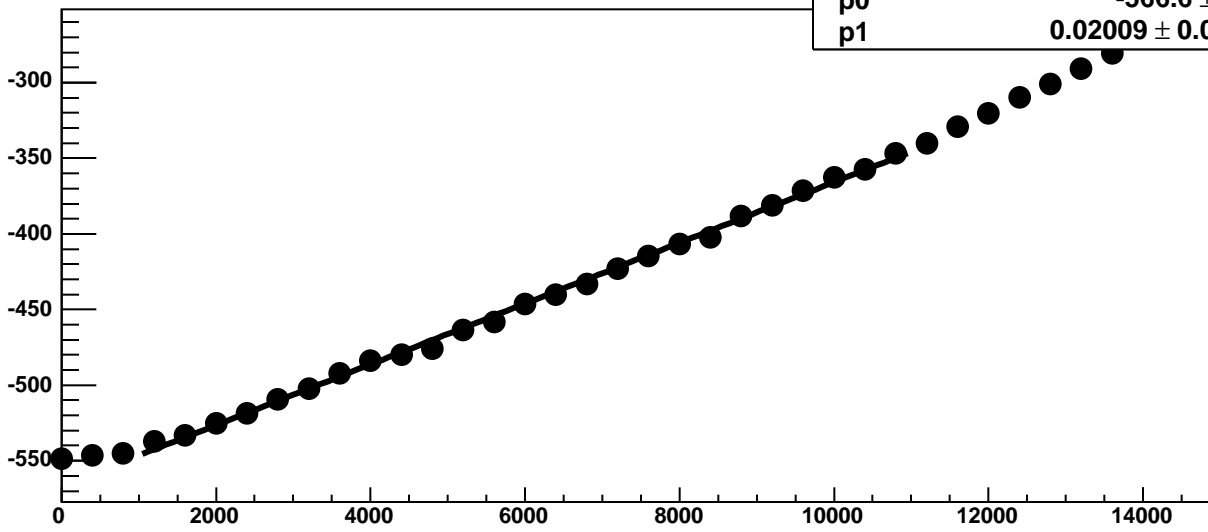
Chip 1, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

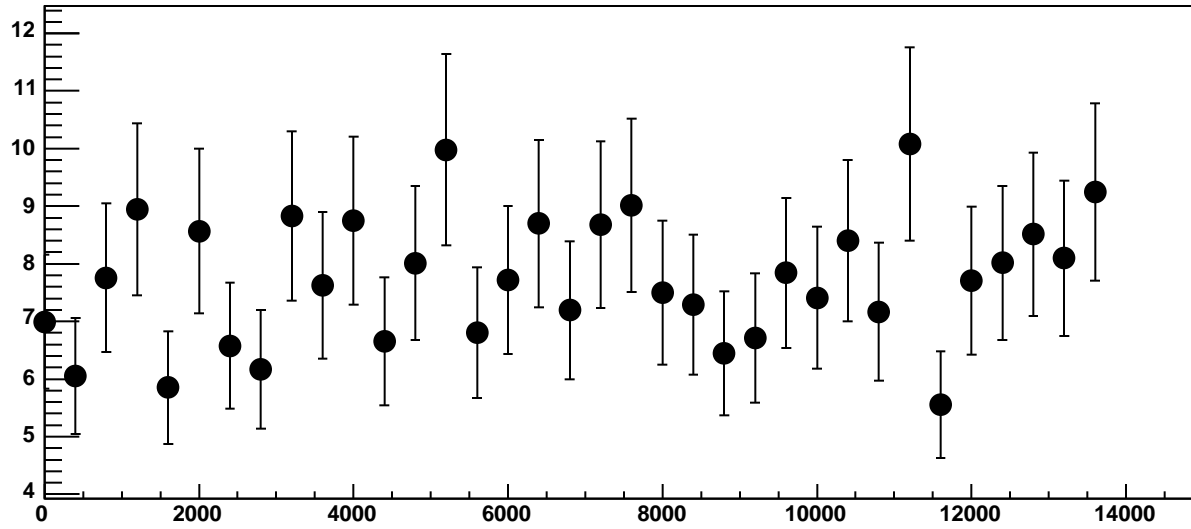


Chip 1, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

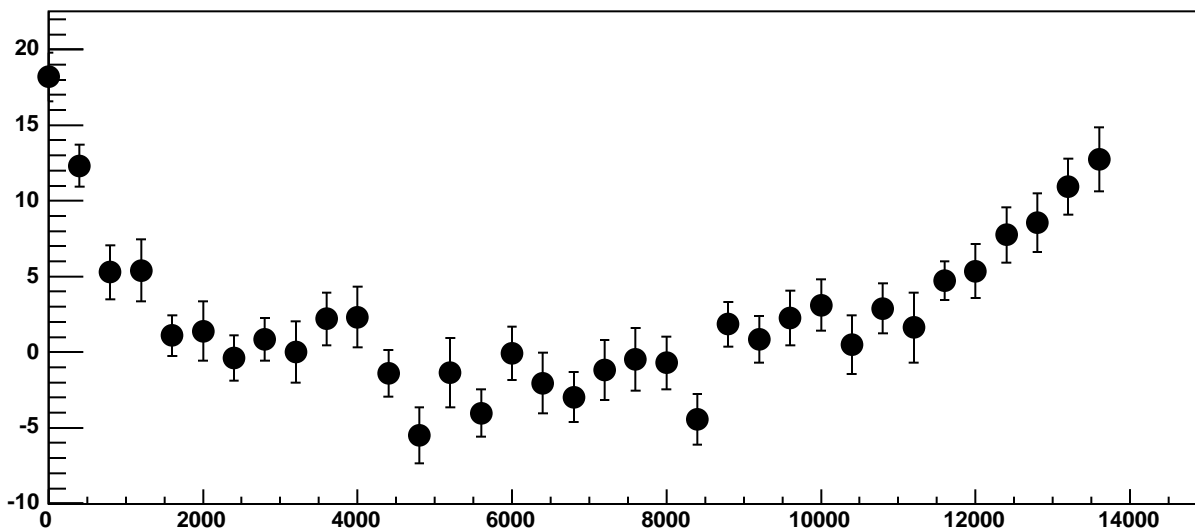


χ^2 / ndf 50.06 / 23
p0 -566.6 ± 0.7749
p1 0.02009 ± 0.0001167

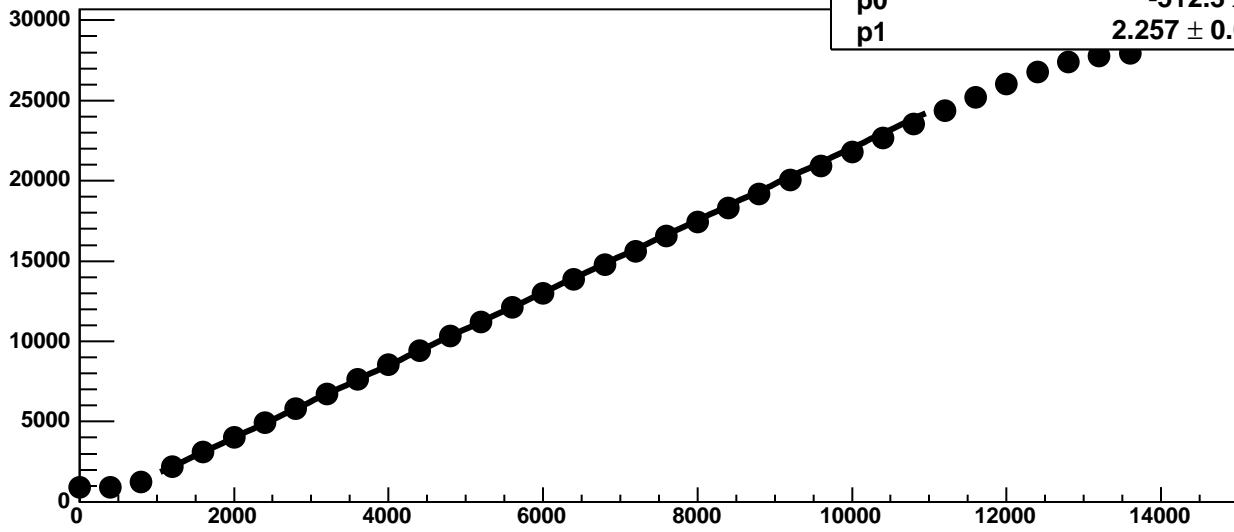
Chip 1, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



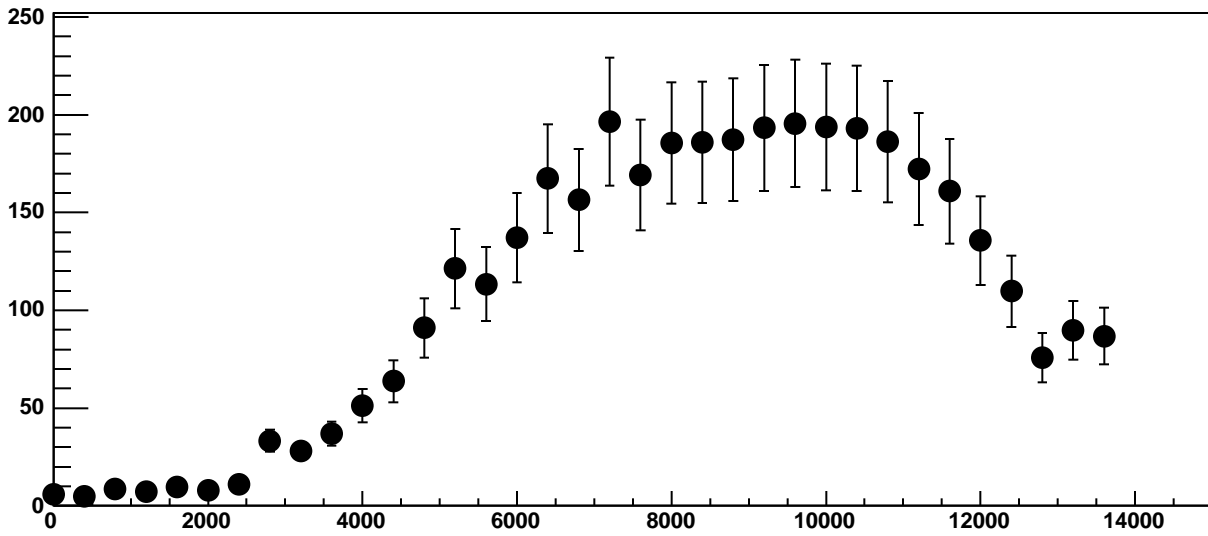
Chip 1, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



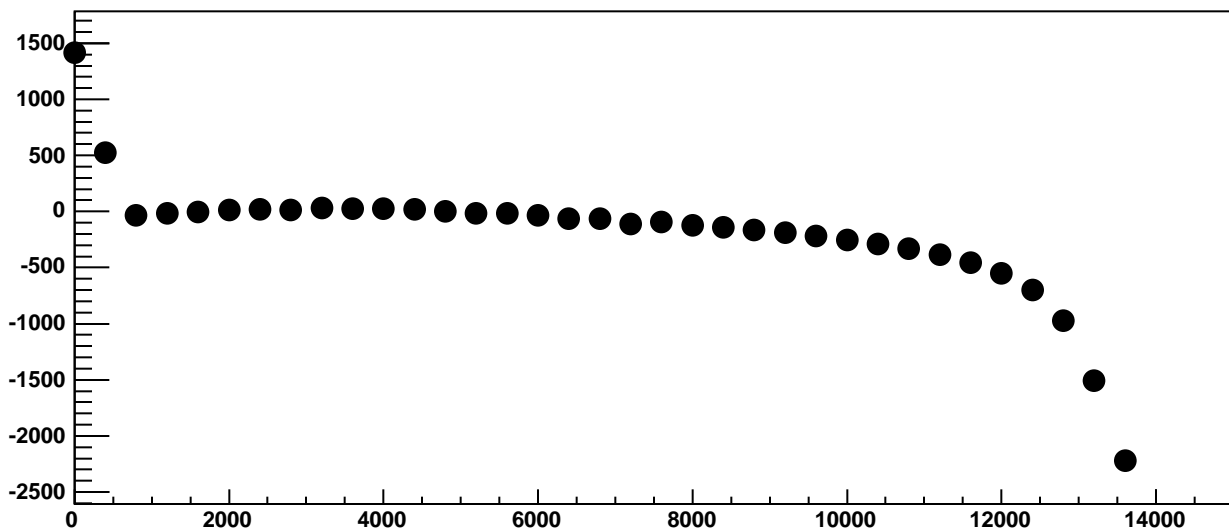
Chip 1, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC



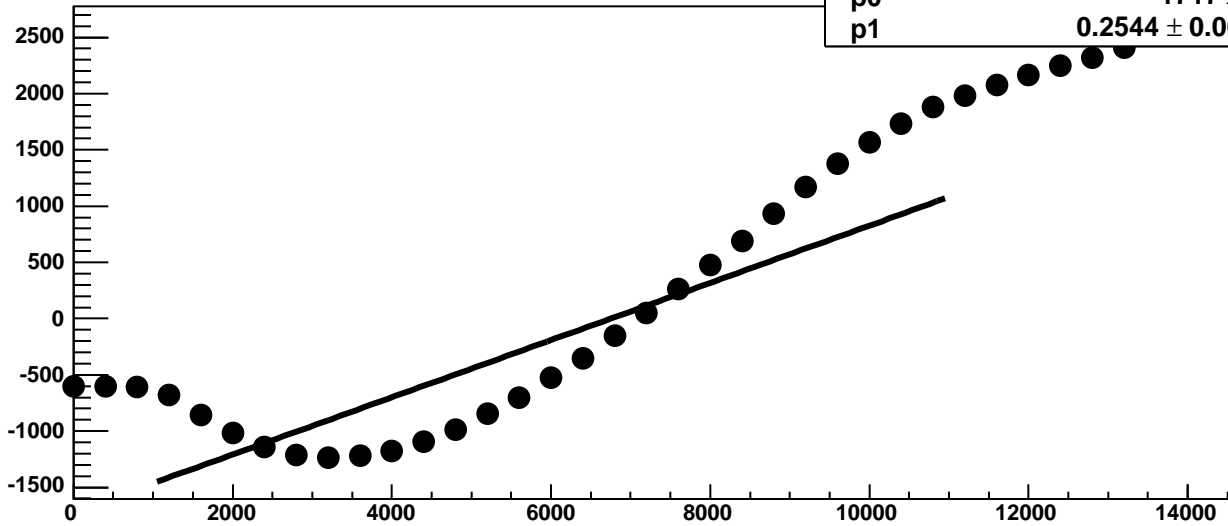
Chip 1, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC

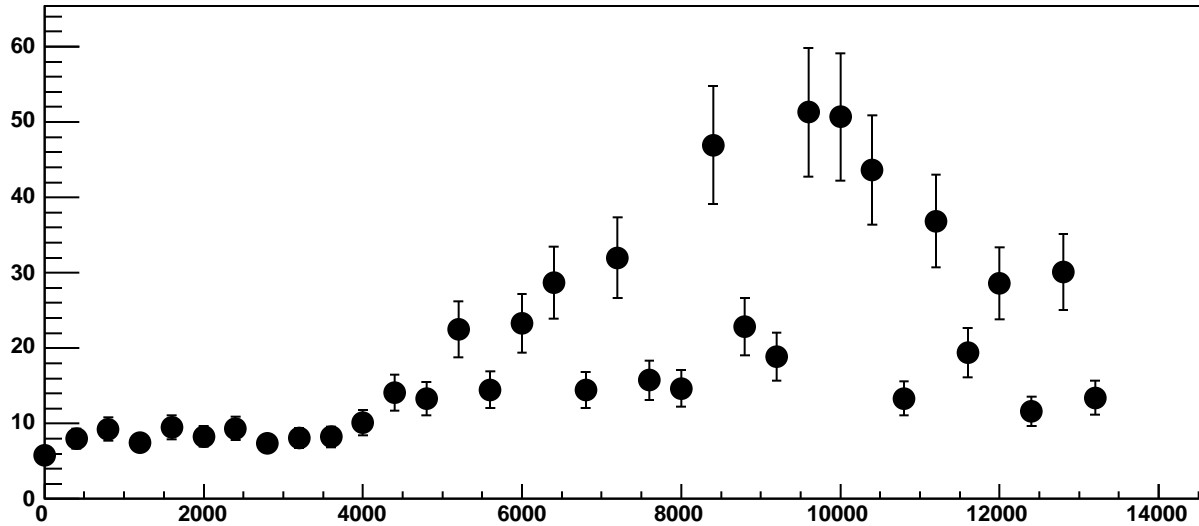


Chip 1, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

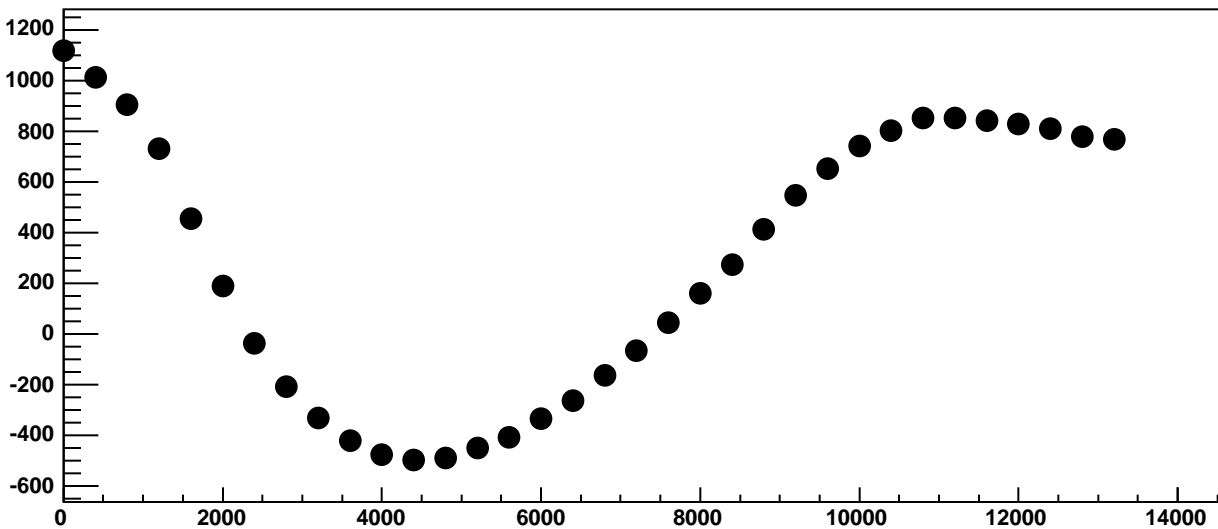


χ^2 / ndf 5.707e+05 / 23
p0 -1717 ± 1.069
p1 0.2544 ± 0.0002327

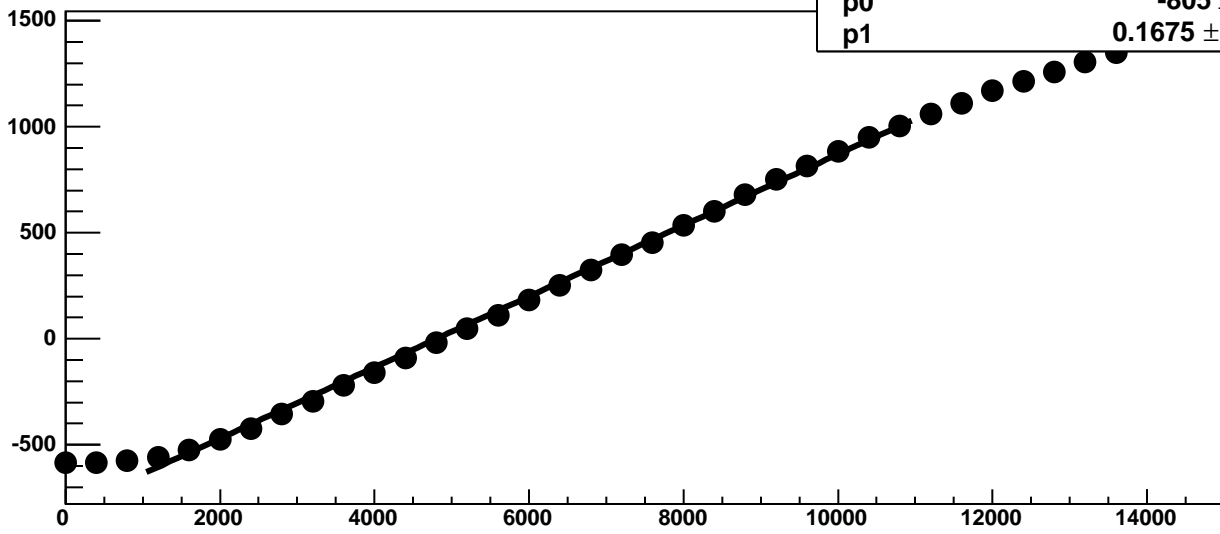
Chip 1, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



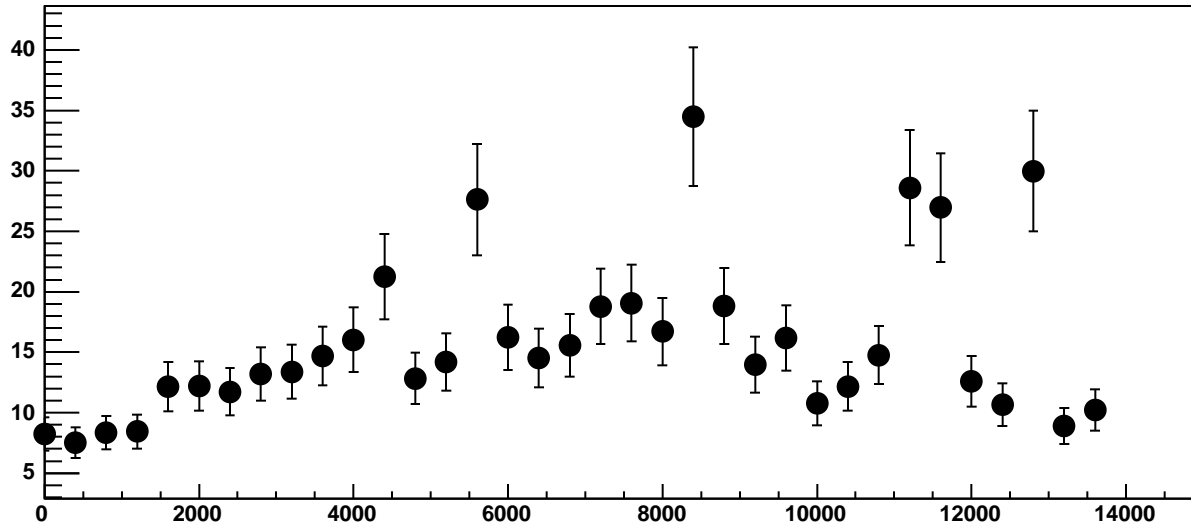
Chip 1, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



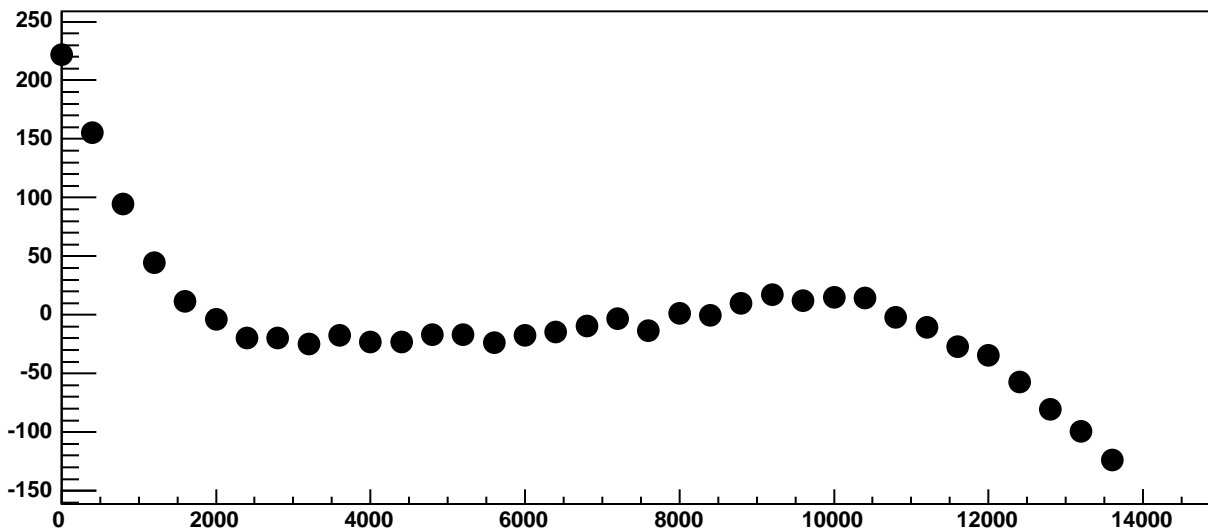
Chip 1, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



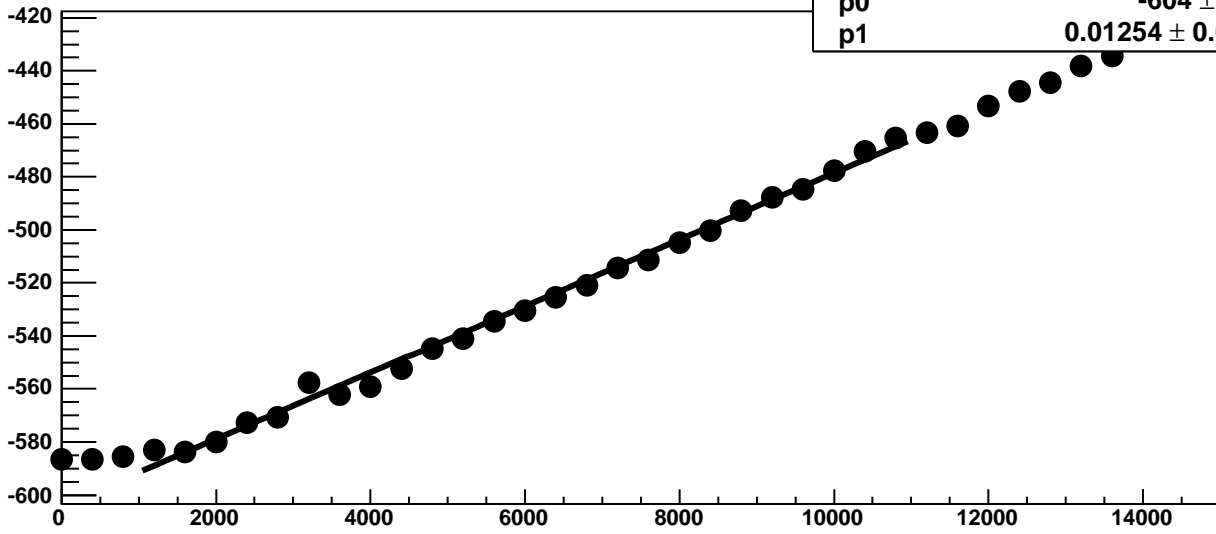
Chip 1, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



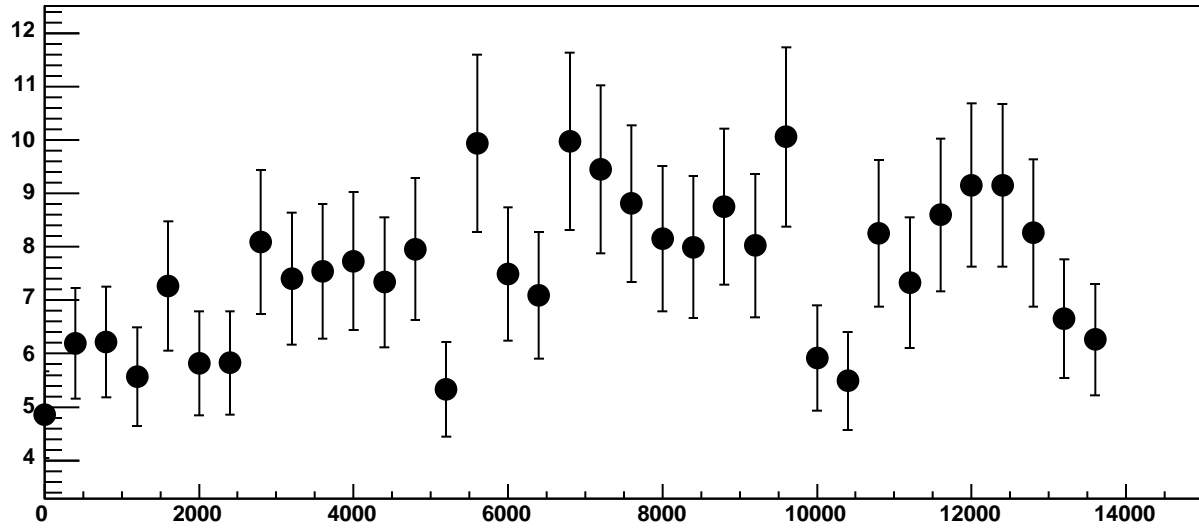
Chip 1, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



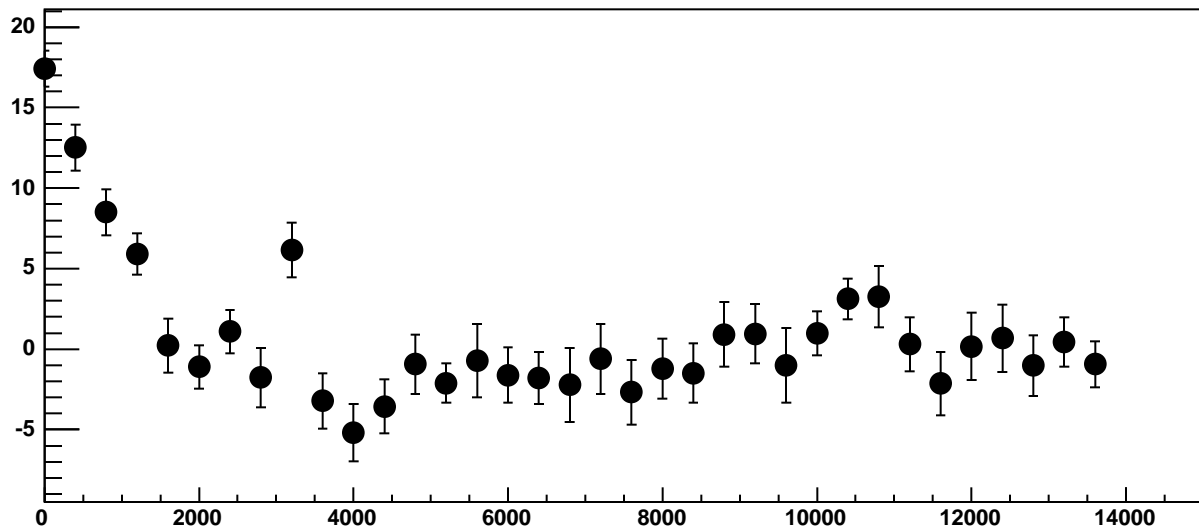
Chip 1, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



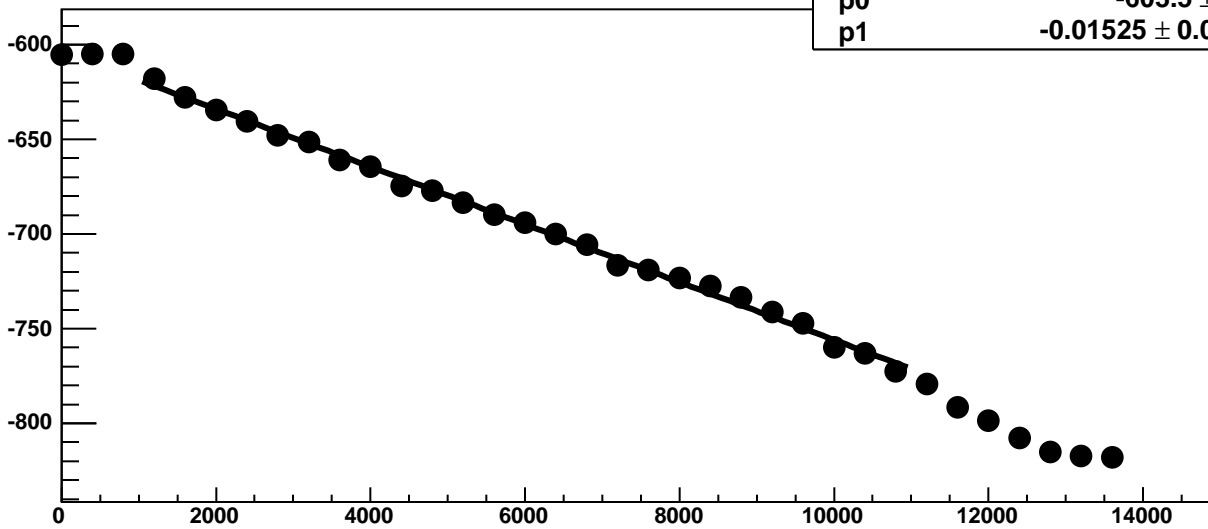
Chip 1, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

20.92 / 23

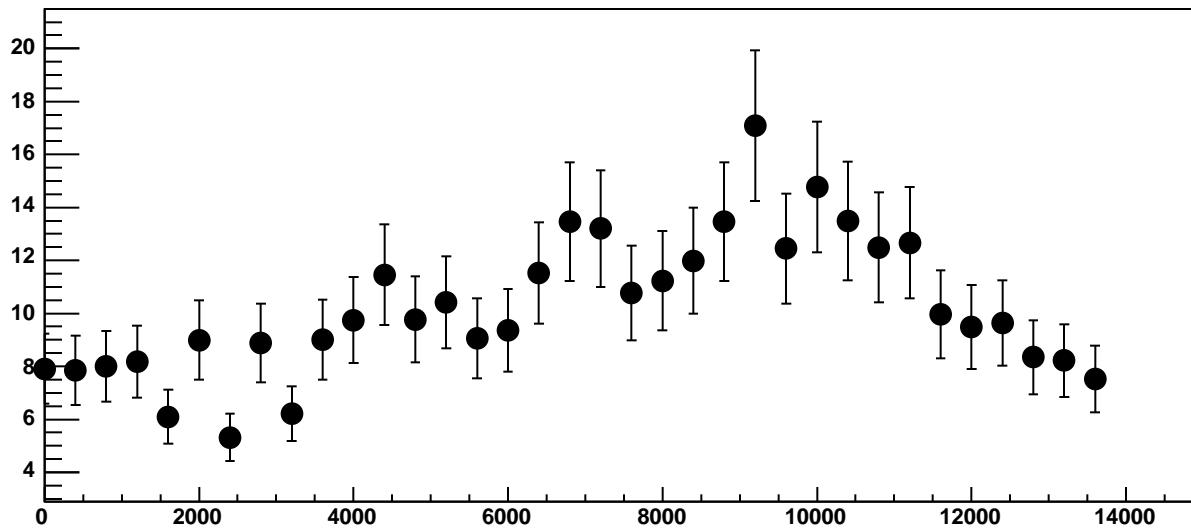
p0

-603.5 ± 0.8544

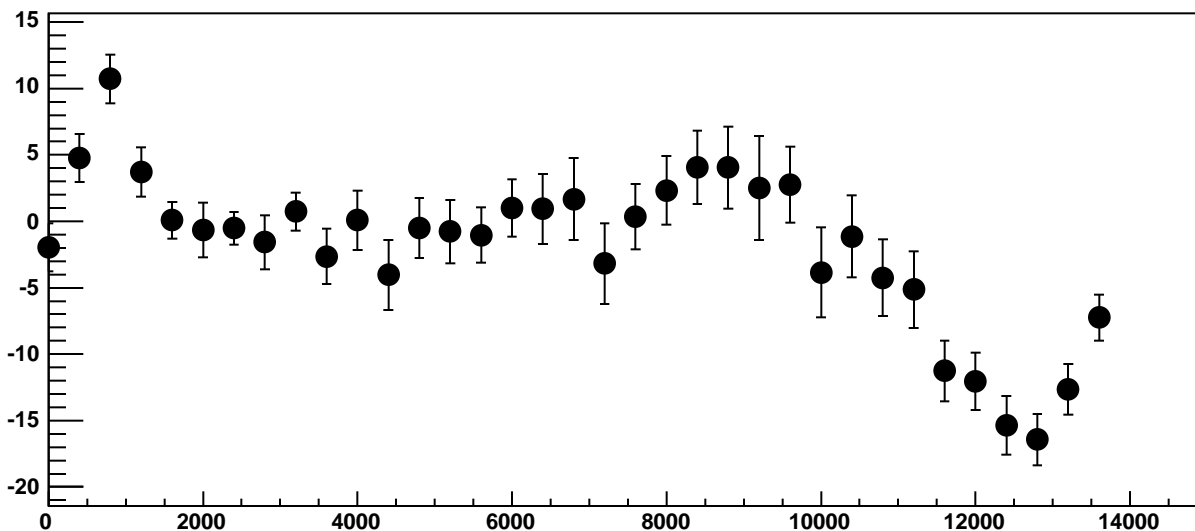
p1

-0.01525 ± 0.0001594

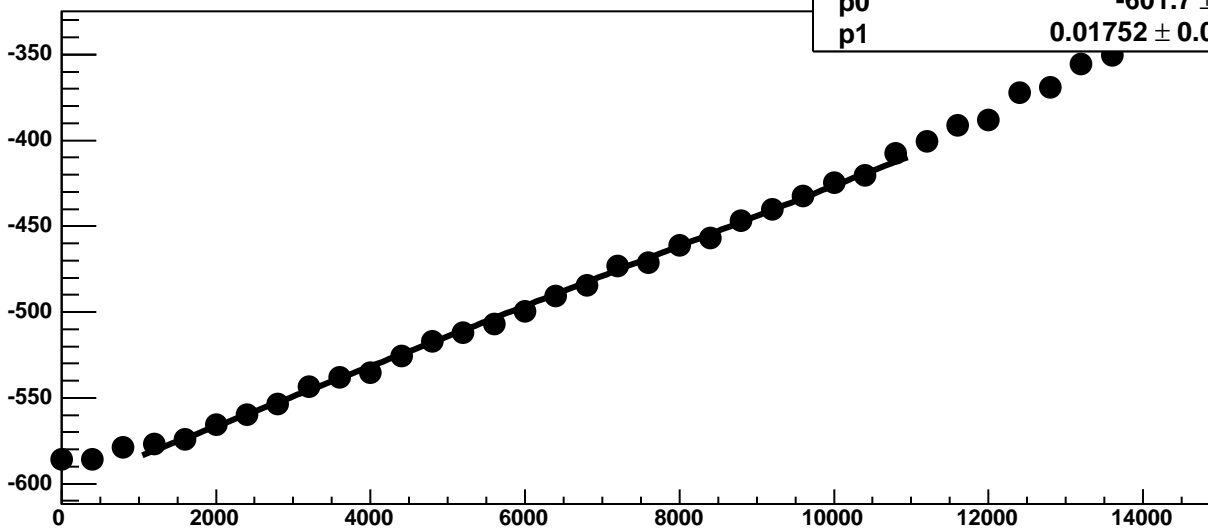
Chip 1, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC

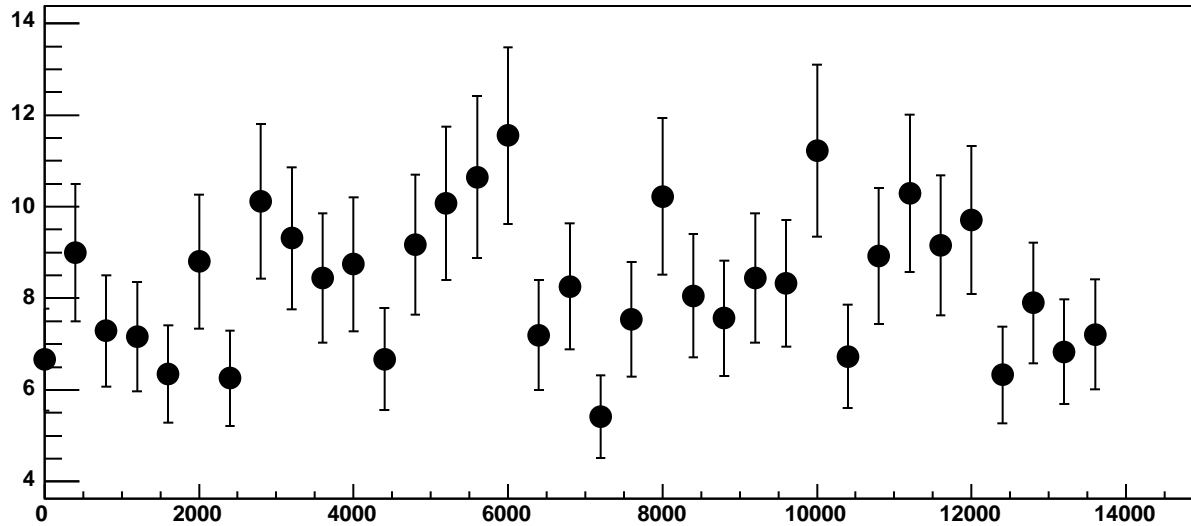


Chip 1, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

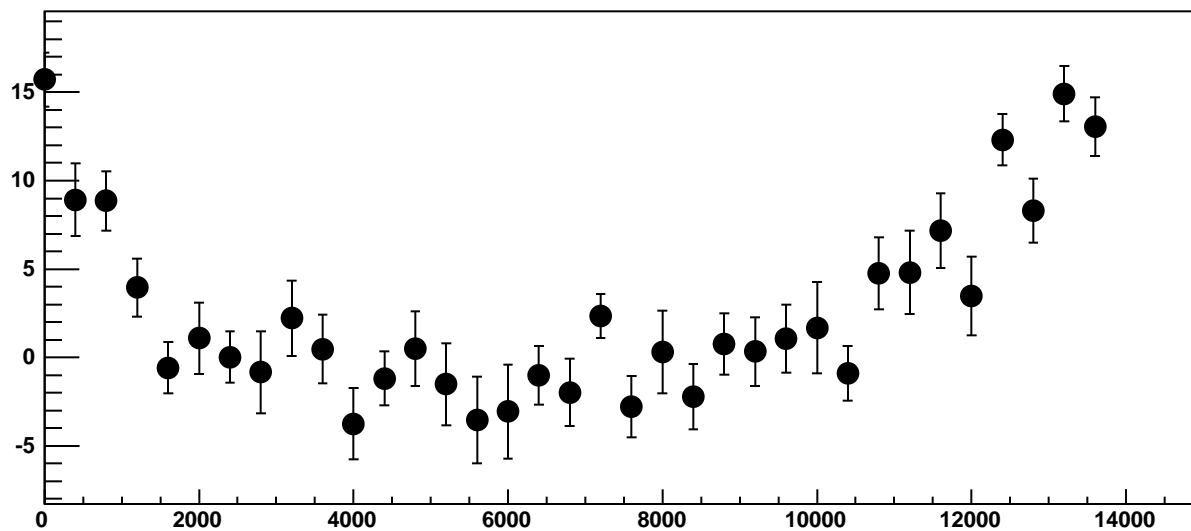


χ^2 / ndf 31.27 / 23
p0 -601.7 ± 0.8159
p1 0.01752 ± 0.0001242

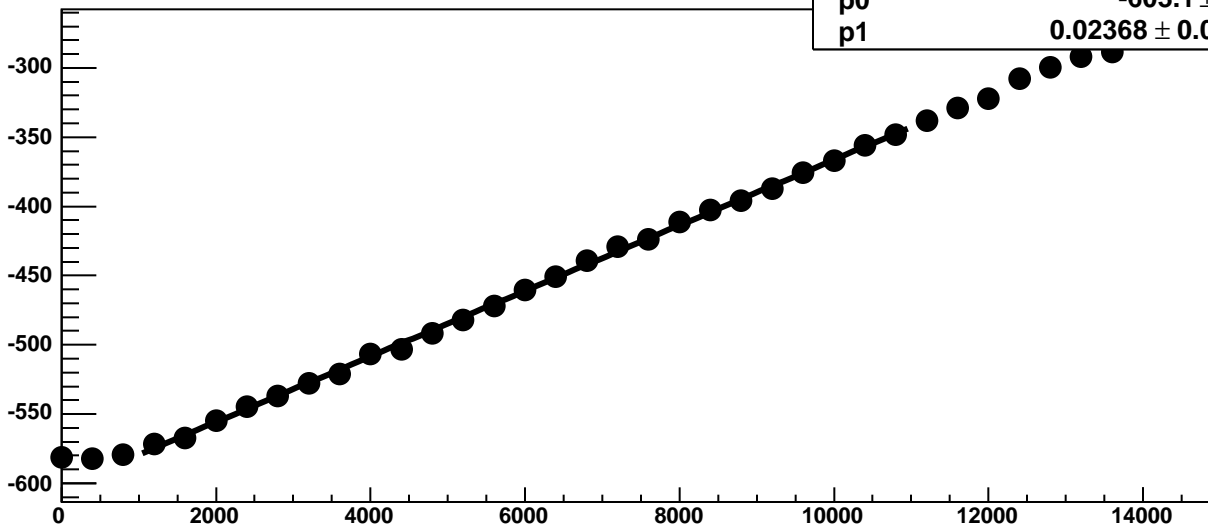
Chip 1, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

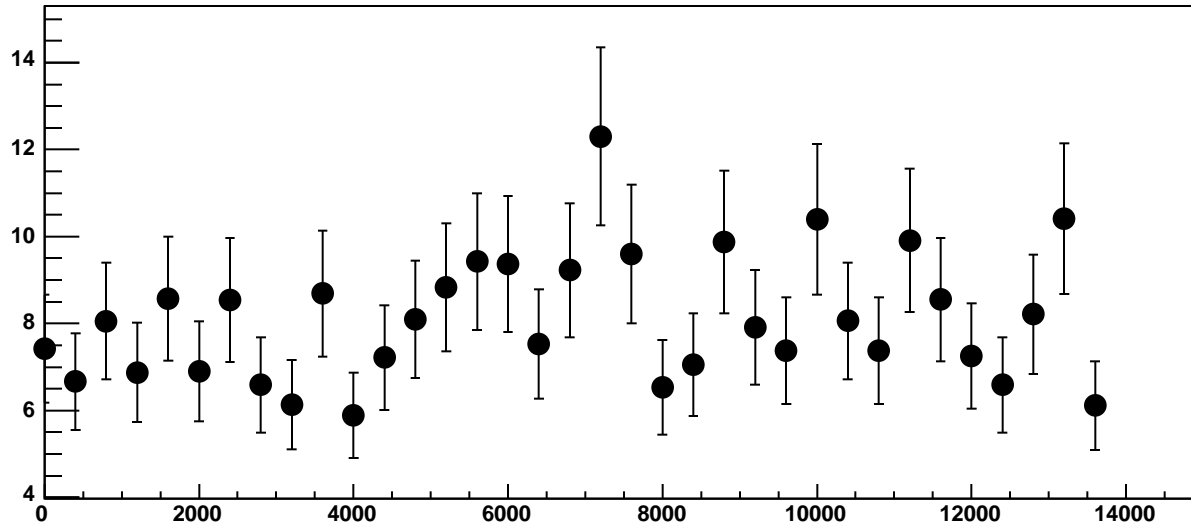


Chip 1, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

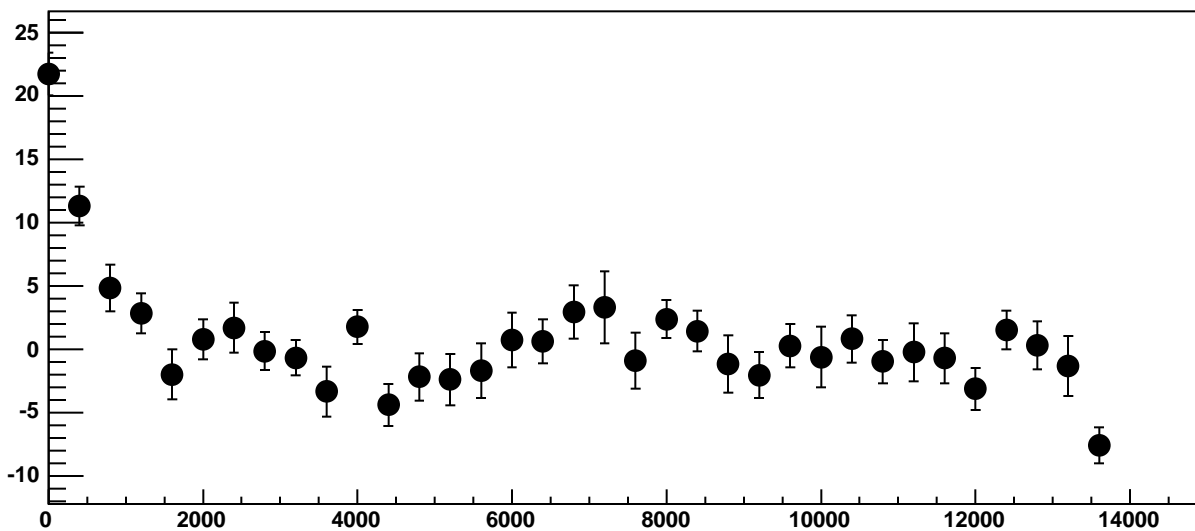


χ^2 / ndf 29.6 / 23
p0 -603.1 ± 0.7814
p1 0.02368 ± 0.0001217

Chip 1, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

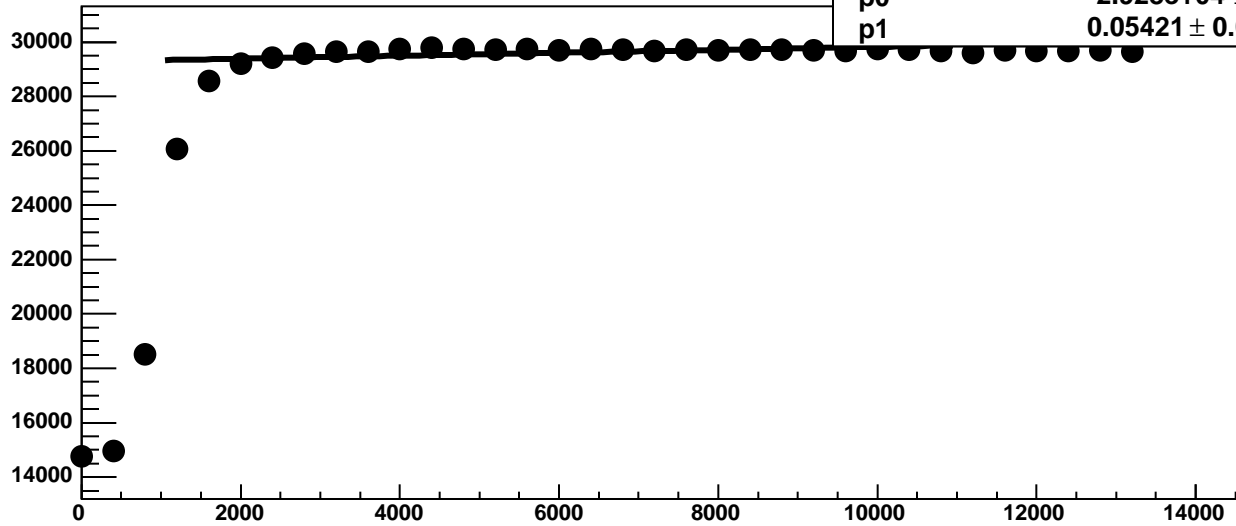
4737 / 23

p0

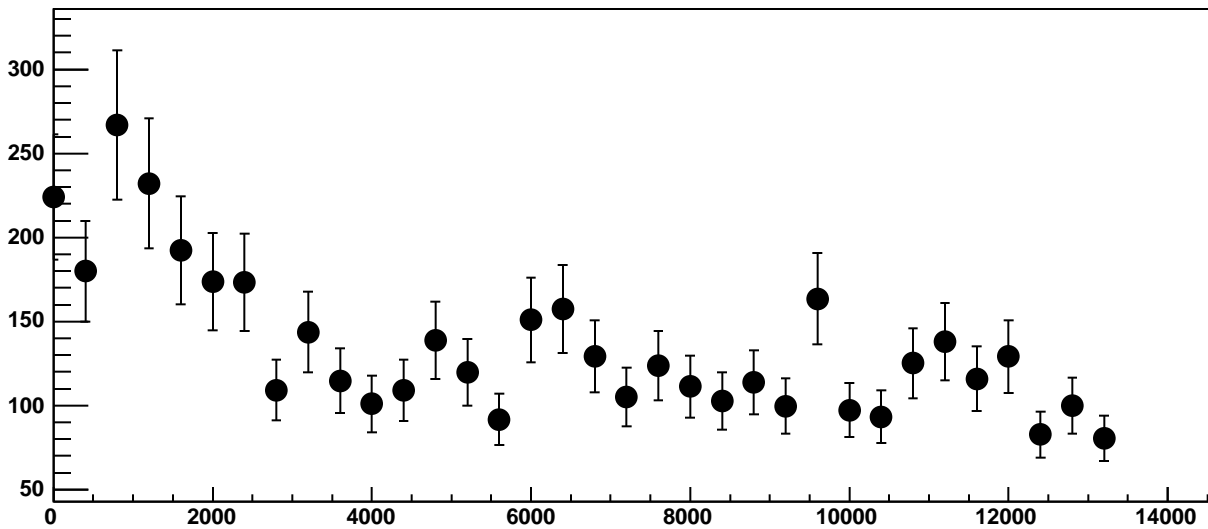
$2.928e+04 \pm 14.67$

p1

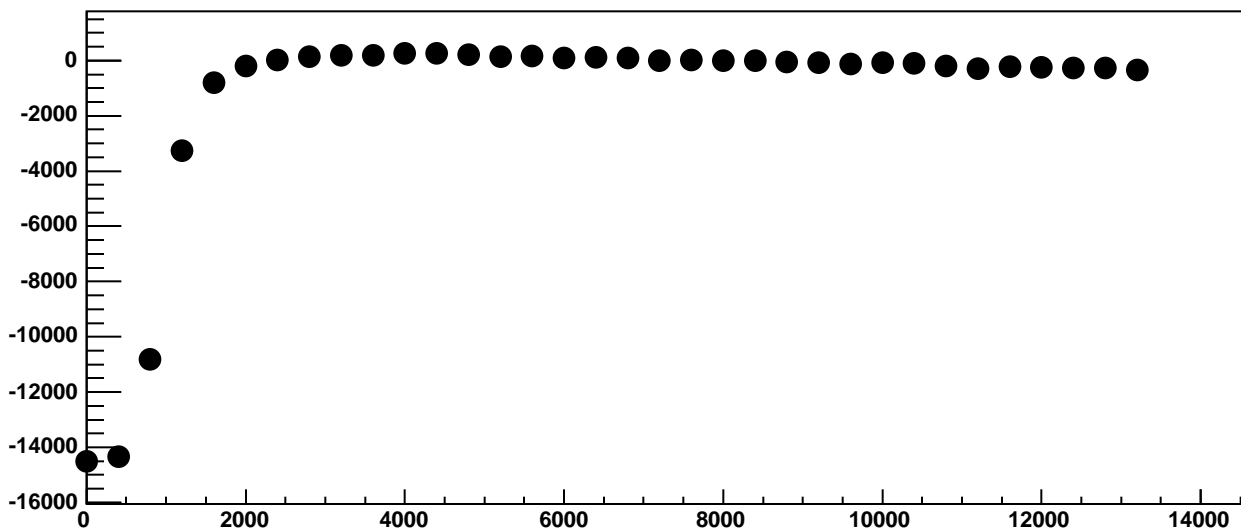
0.05421 ± 0.002058



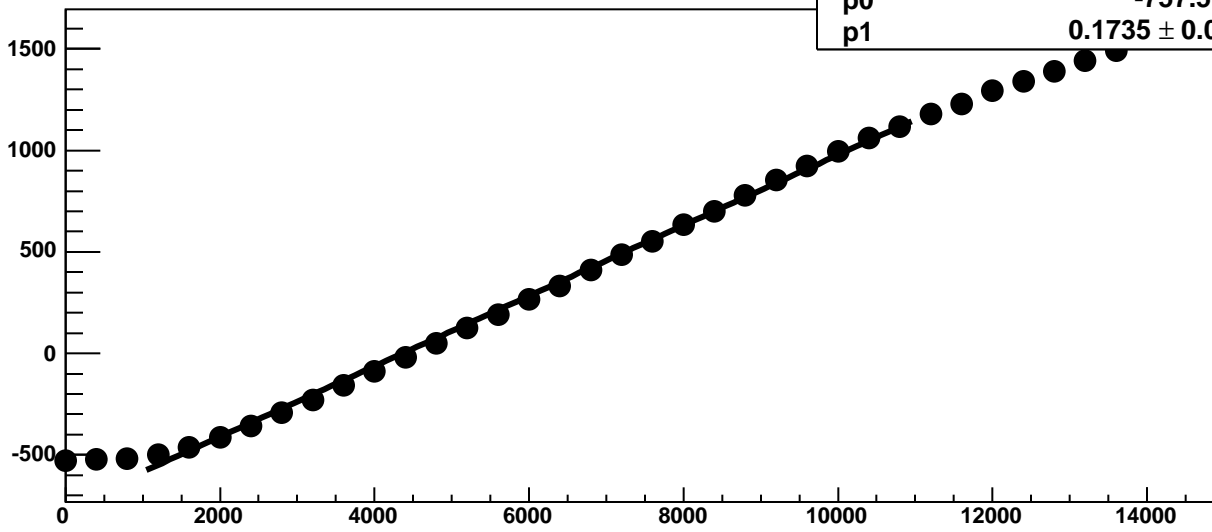
Chip 1, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC



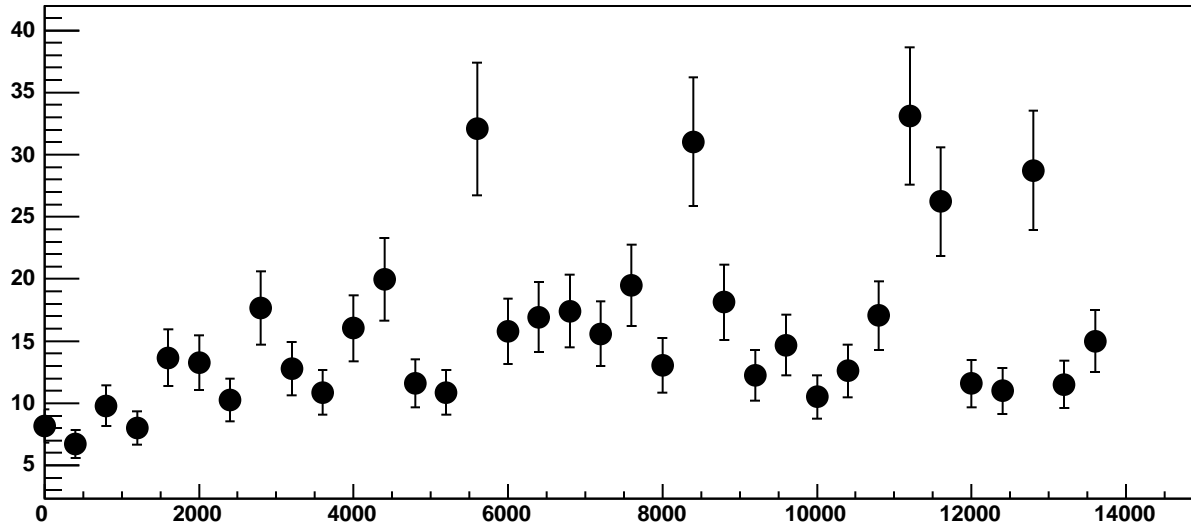
Chip 1, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC



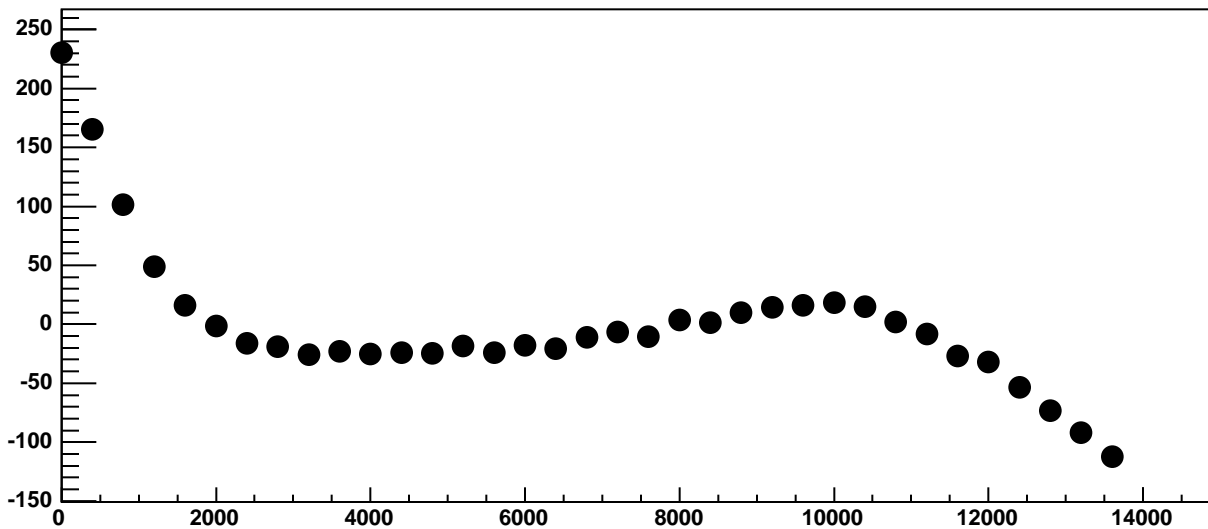
Chip 1, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



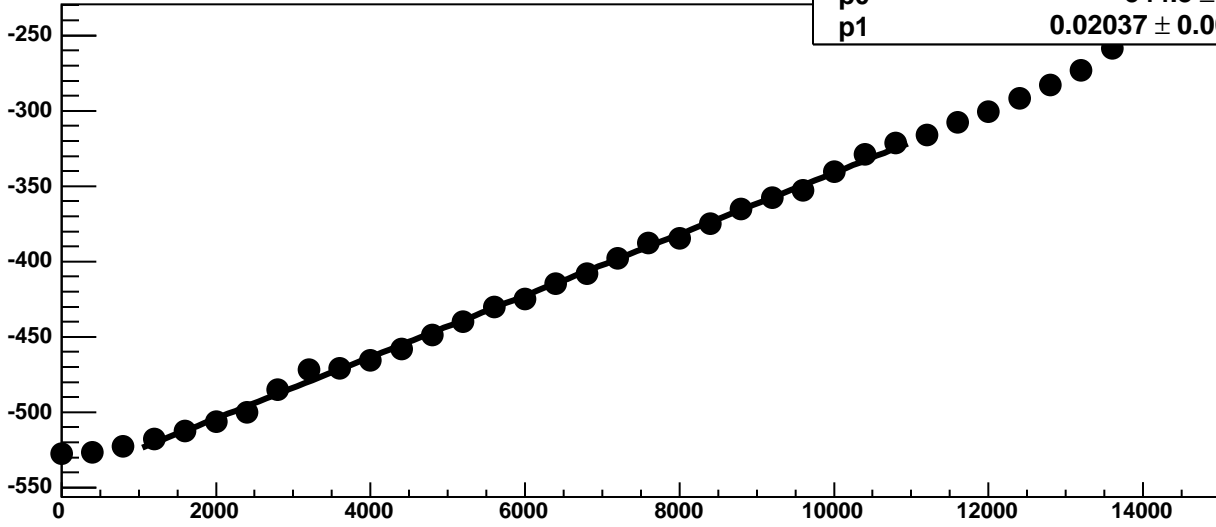
Chip 1, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

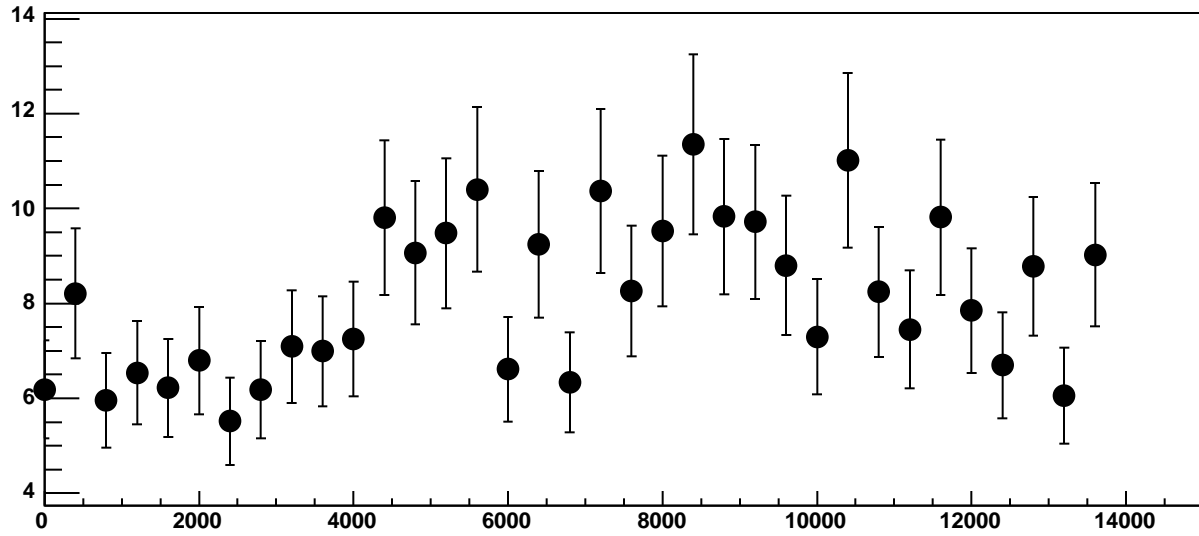


Chip 1, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

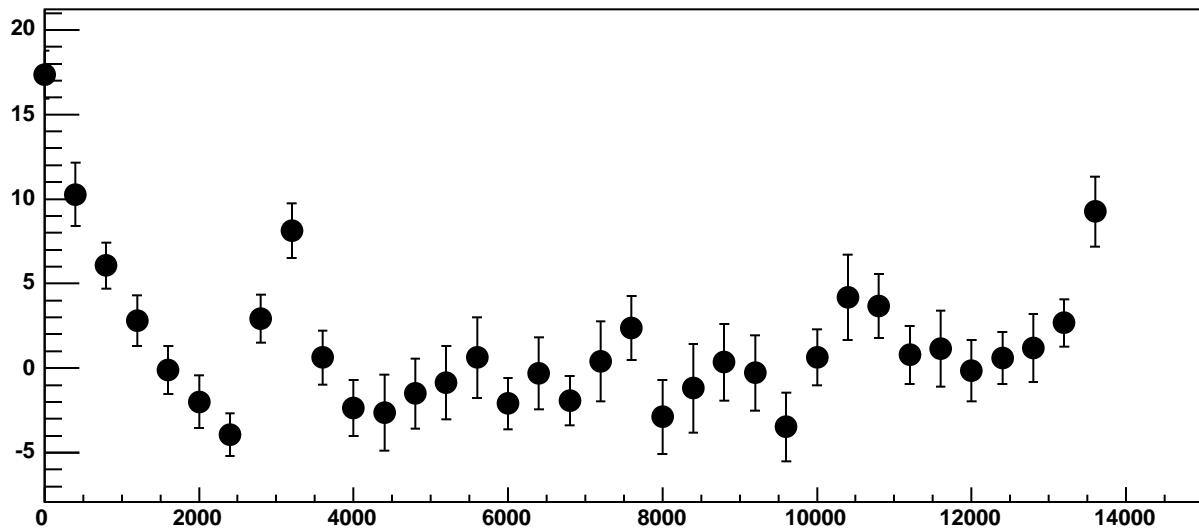


χ^2 / ndf 65.26 / 23
p0 -544.8 ± 0.7312
p1 0.02037 ± 0.0001216

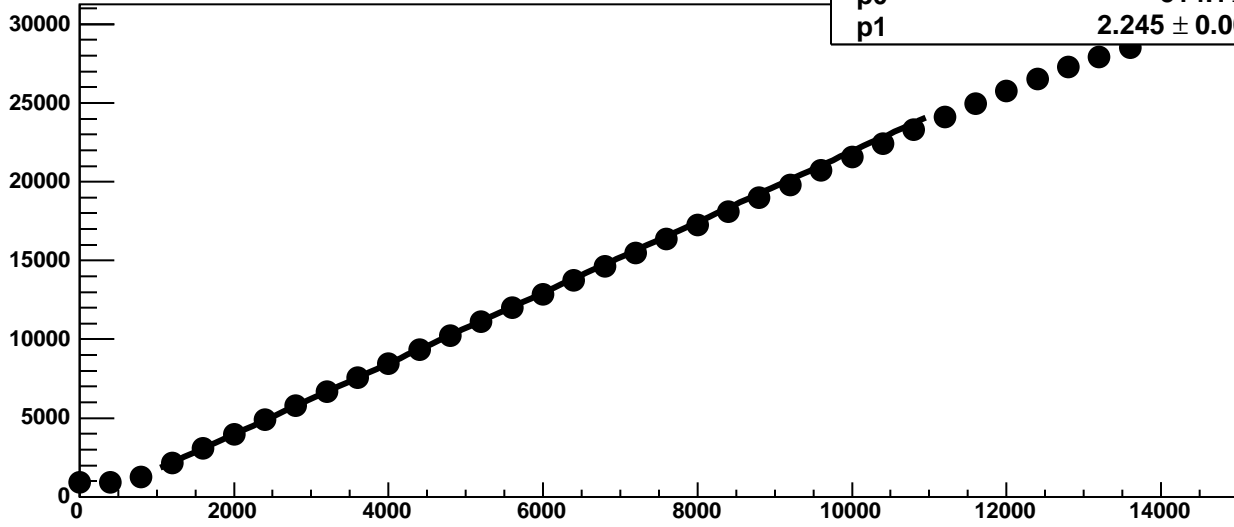
Chip 1, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



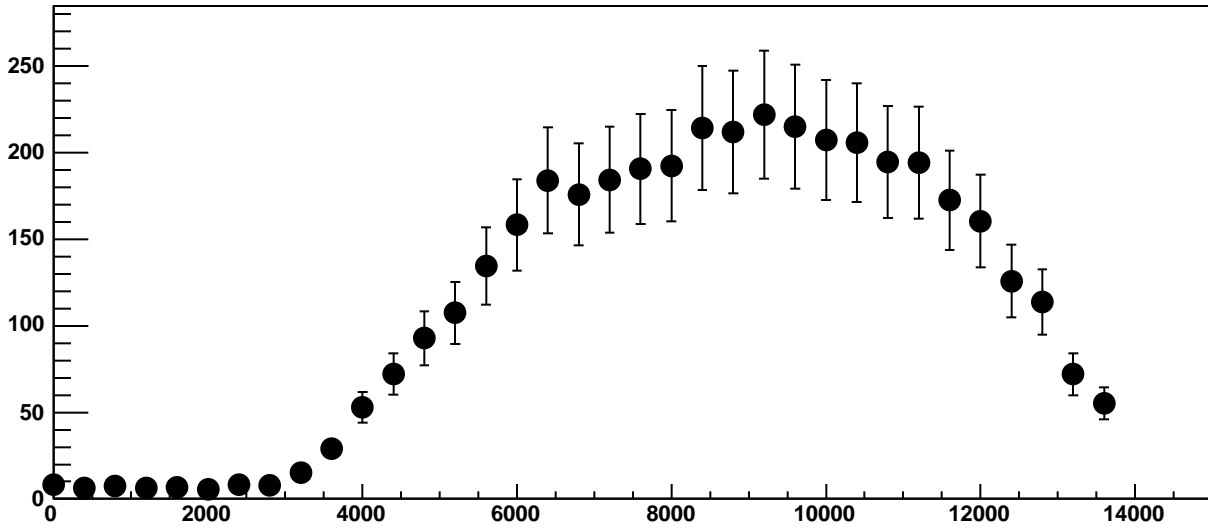
Chip 1, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



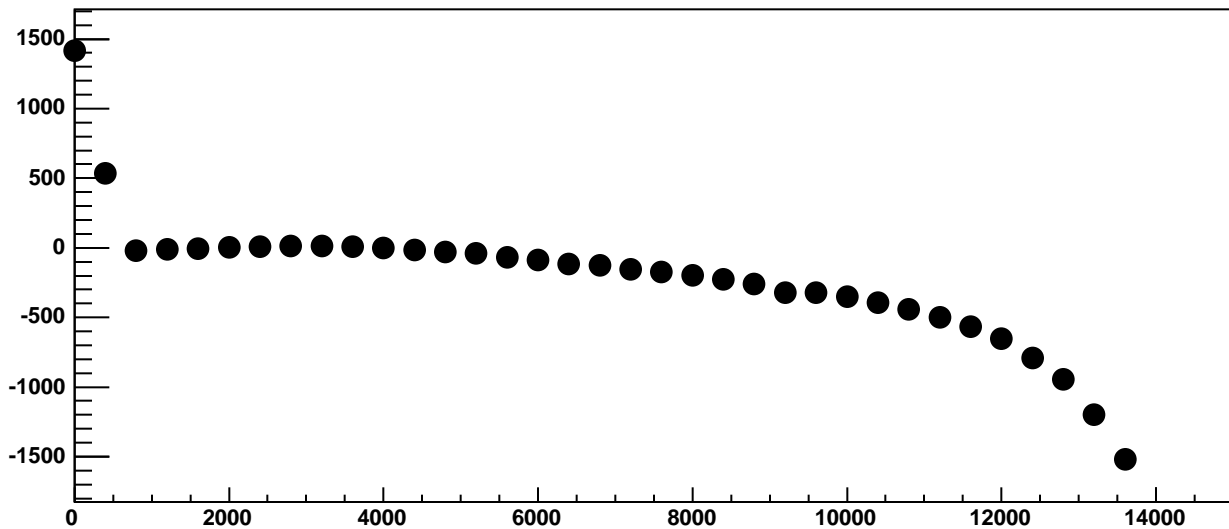
Chip 1, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC



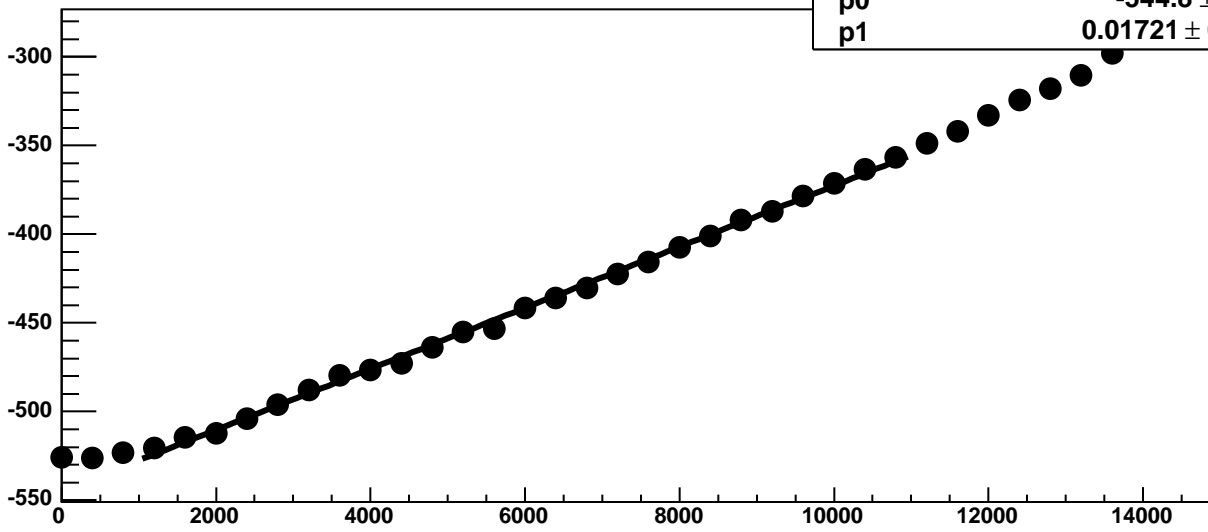
Chip 1, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

35.15 / 23

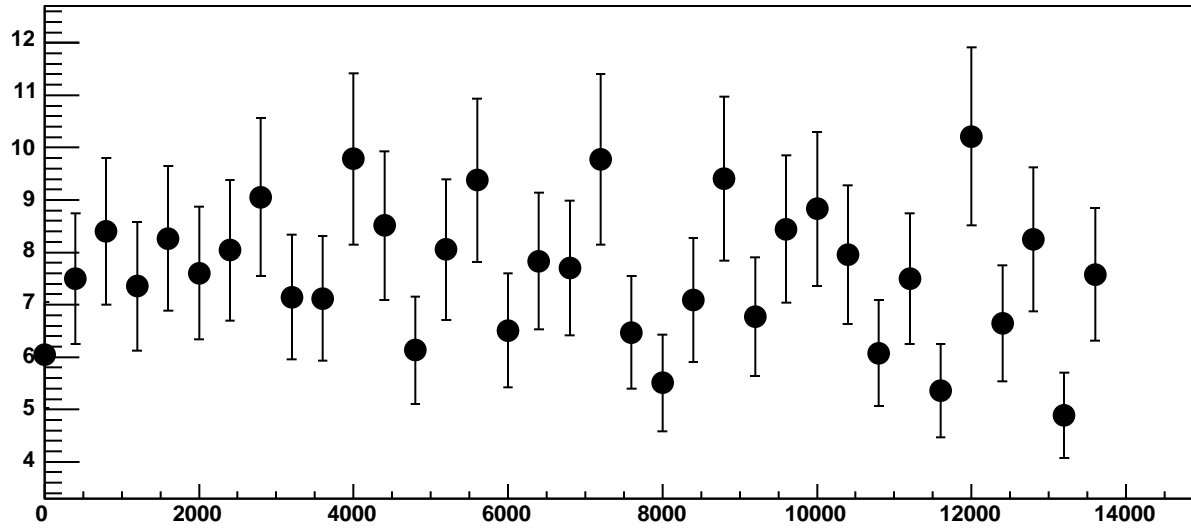
p0

-544.8 ± 0.8169

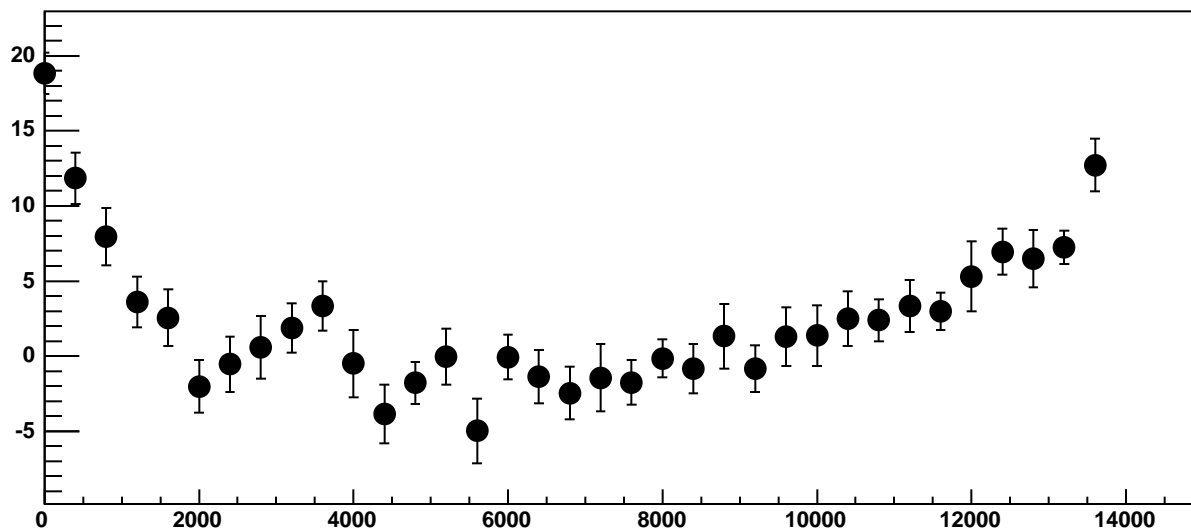
p1

0.01721 ± 0.00012

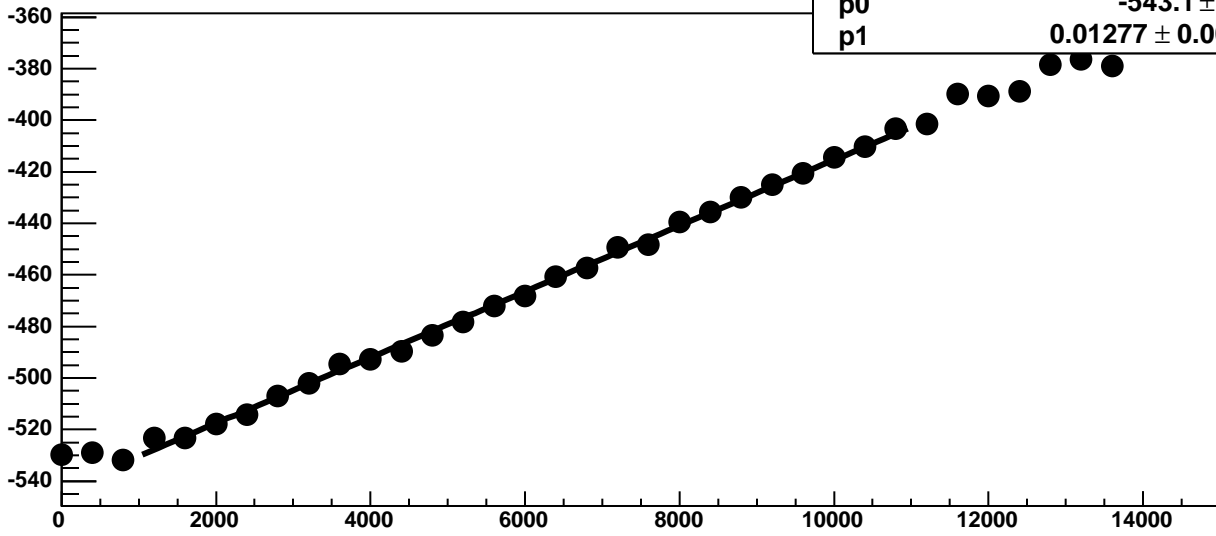
Chip 1, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

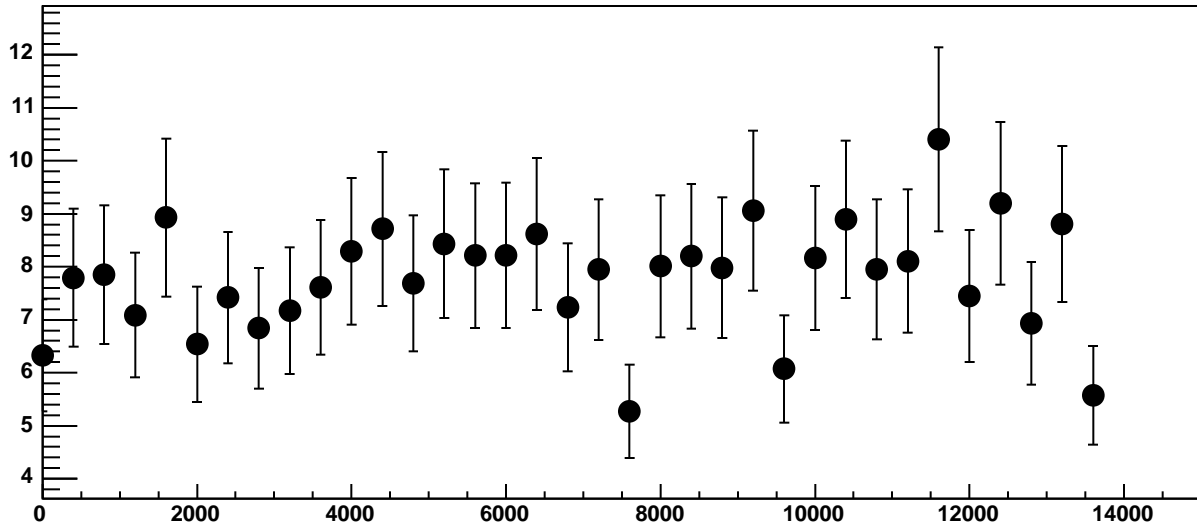


Chip 1, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

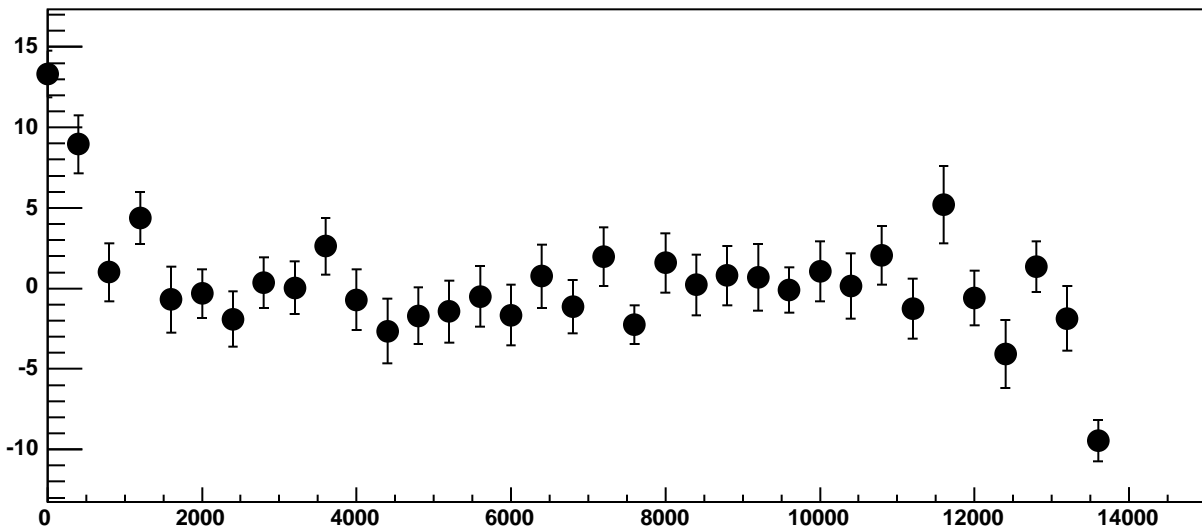


χ^2 / ndf 23.04 / 23
p0 -543.1 ± 0.7954
p1 0.01277 ± 0.0001204

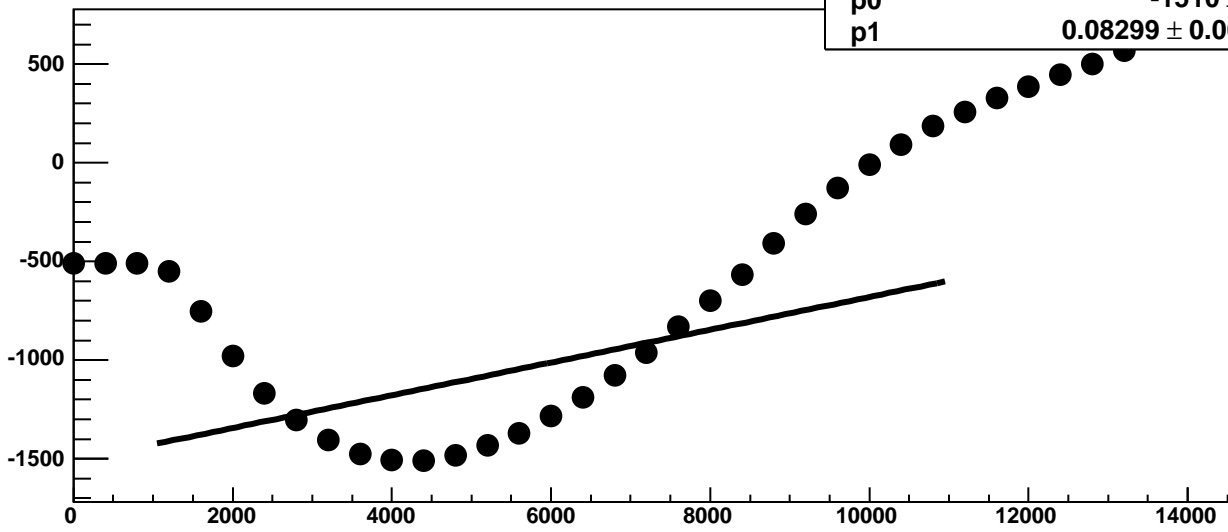
Chip 1, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



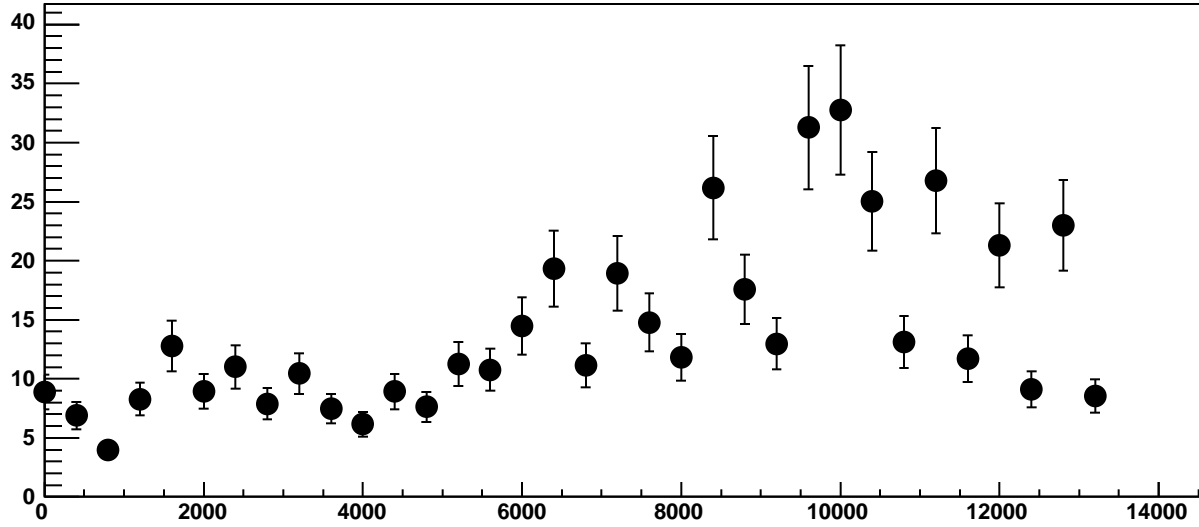
Chip 1, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



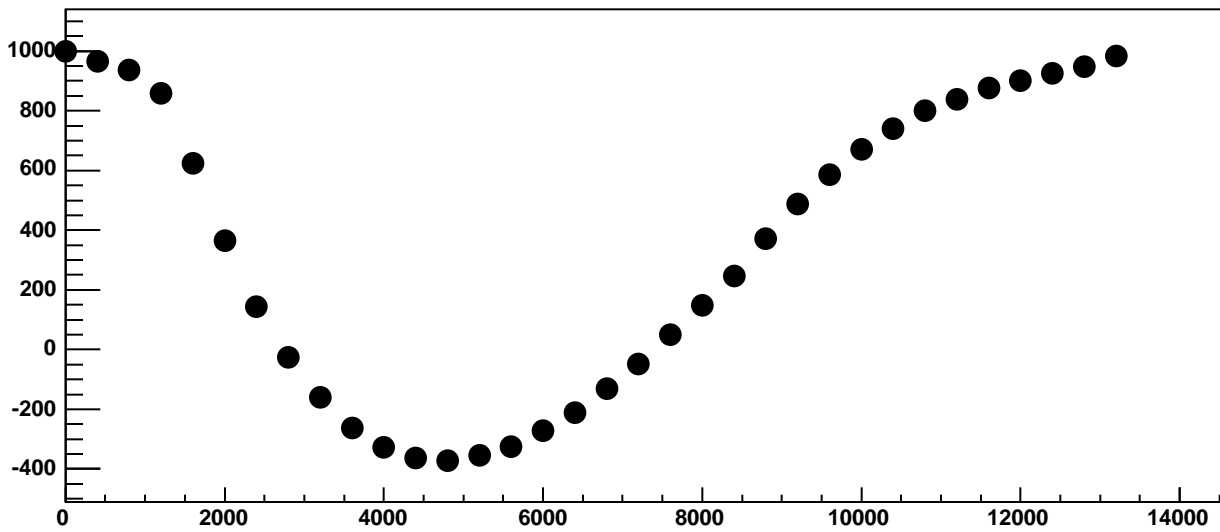
Chip 1, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC



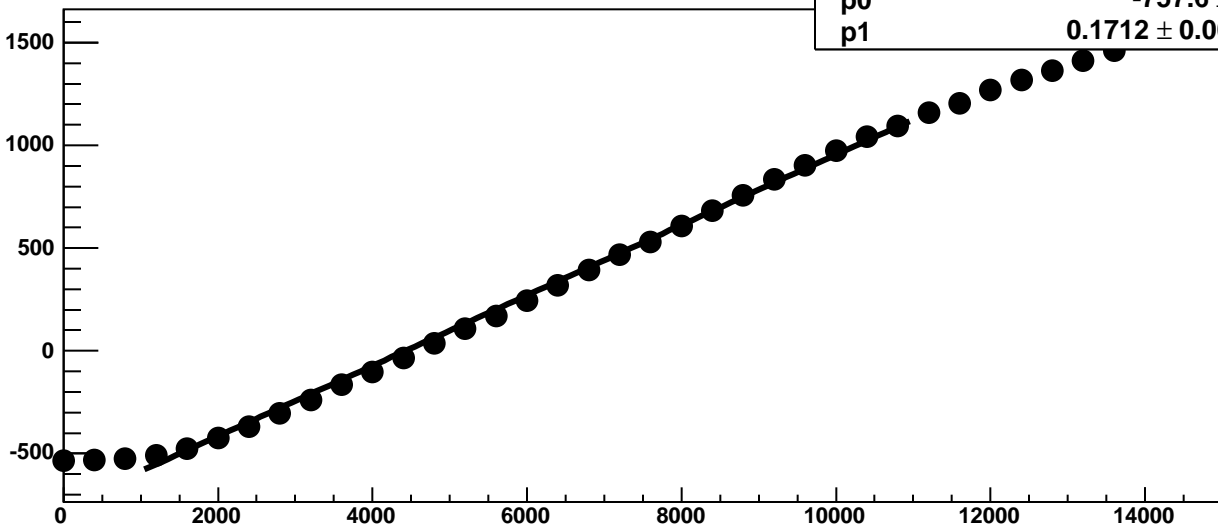
Chip 1, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC

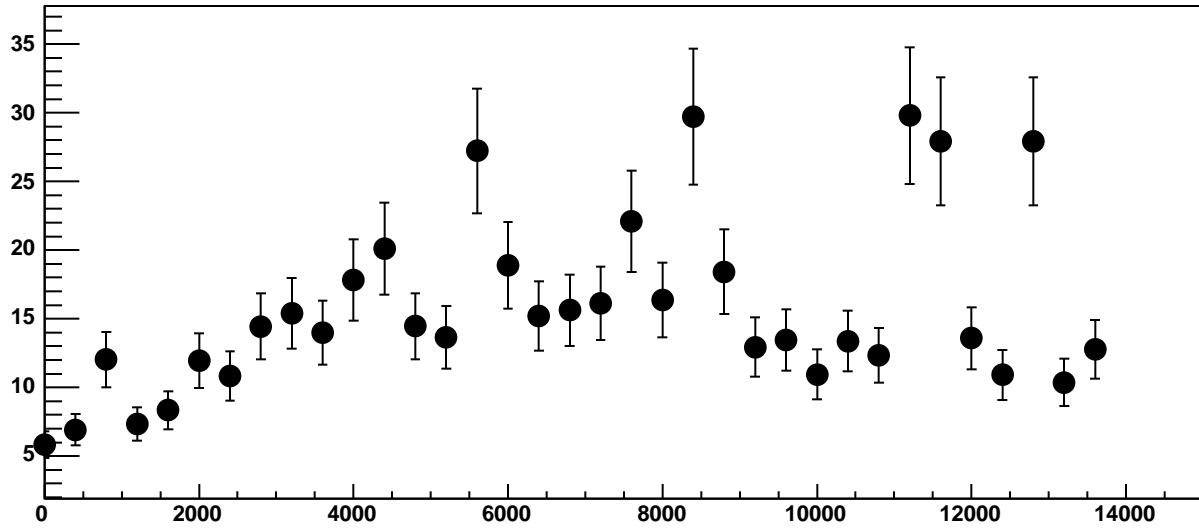


Chip 1, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

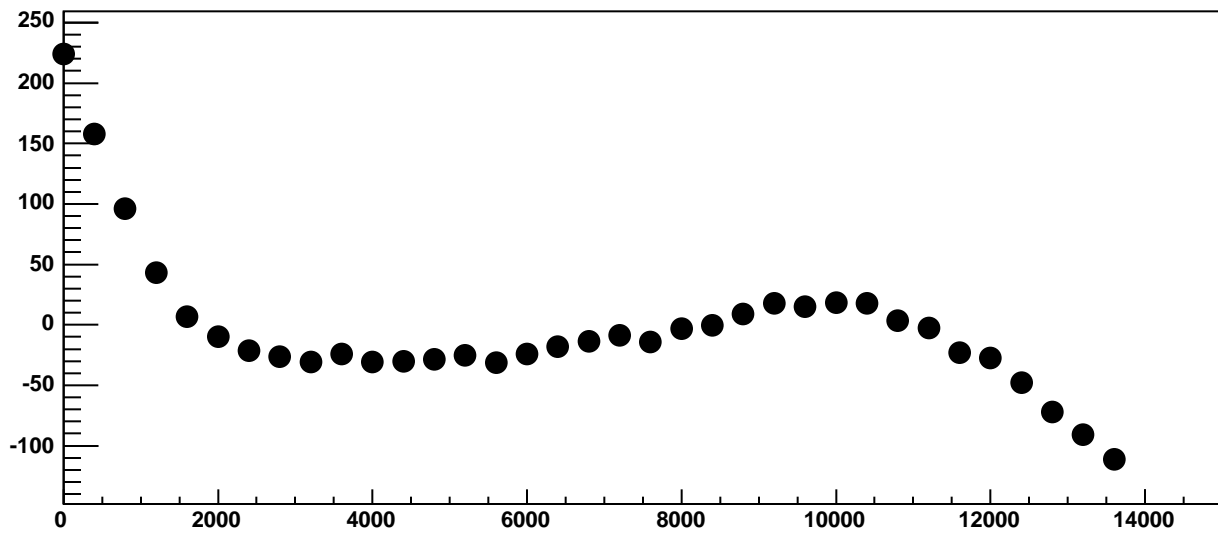


χ^2 / ndf 1453 / 23
p0 -757.6 ± 1.134
p1 0.1712 ± 0.0001828

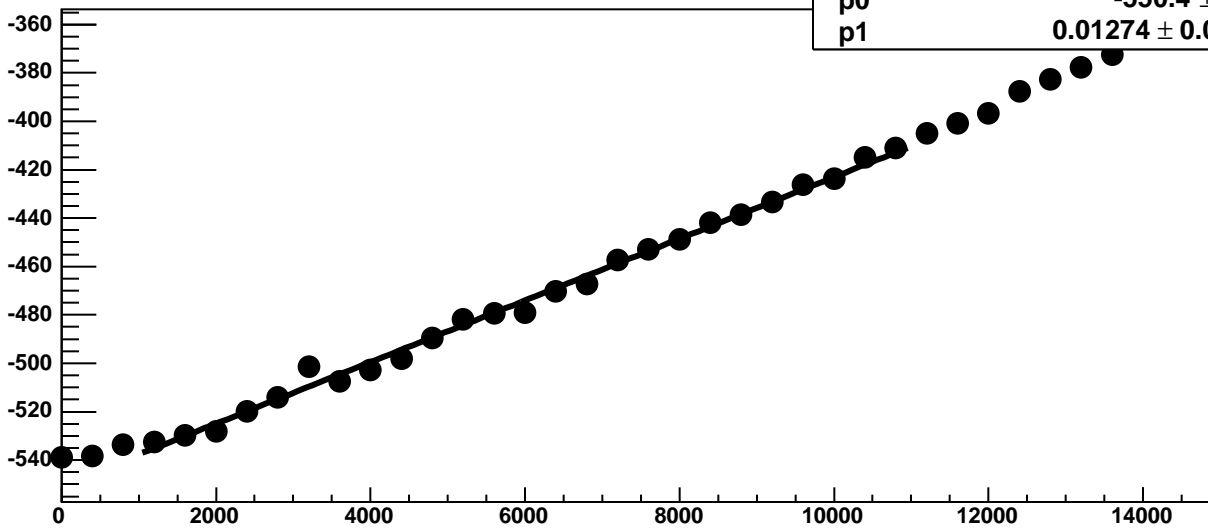
Chip 1, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

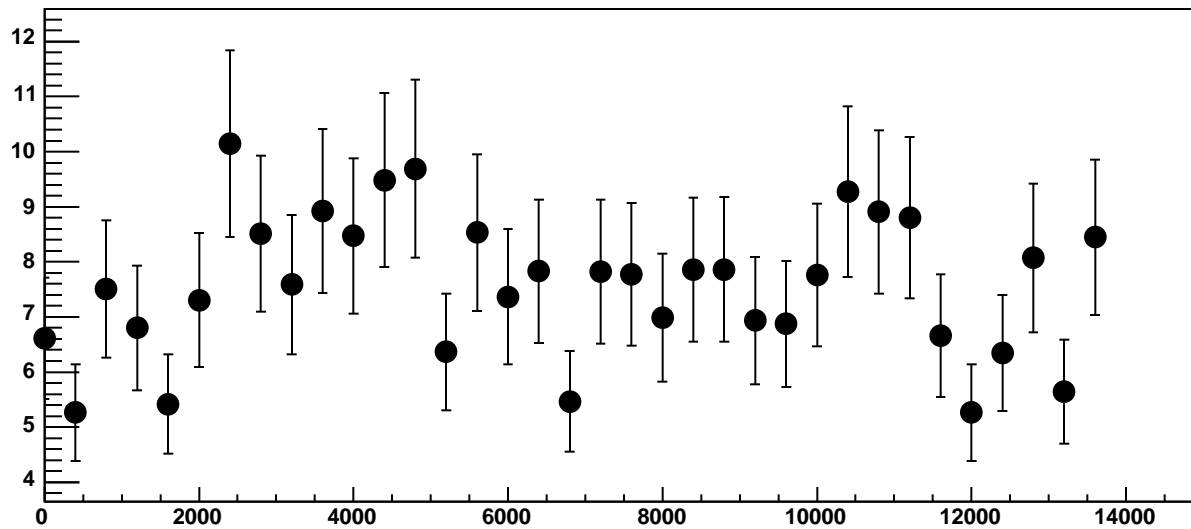


Chip 1, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

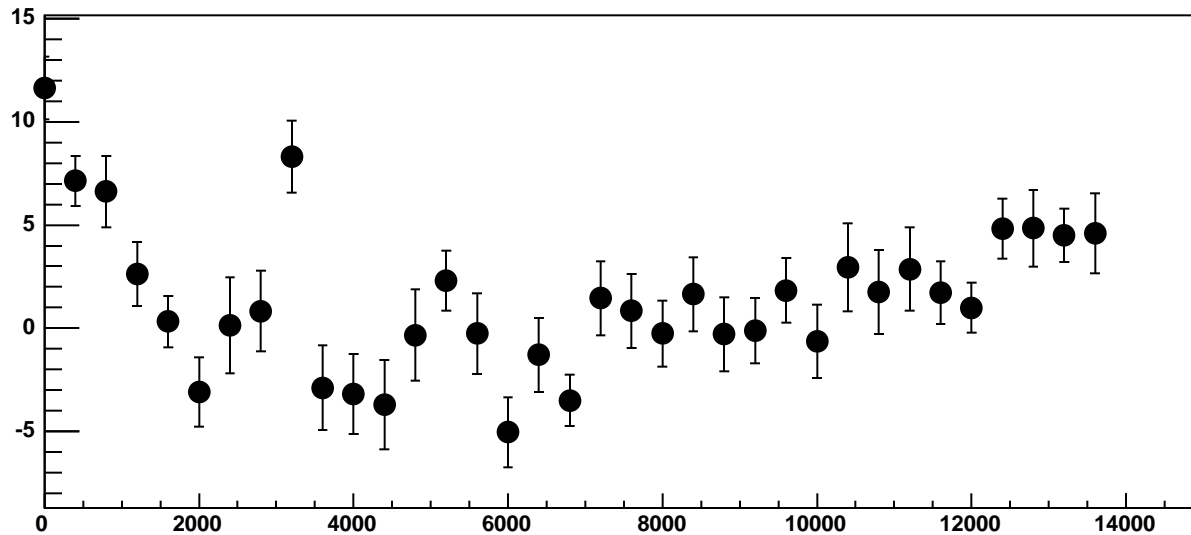


χ^2 / ndf 62.54 / 23
p0 -550.4 ± 0.7834
p1 0.01274 ± 0.0001192

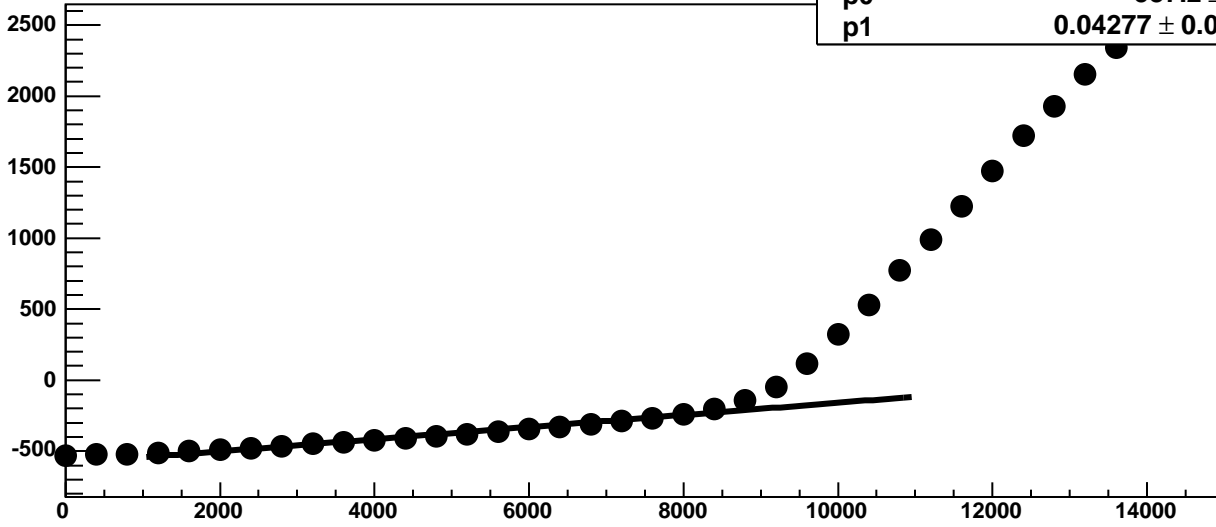
Chip 1, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

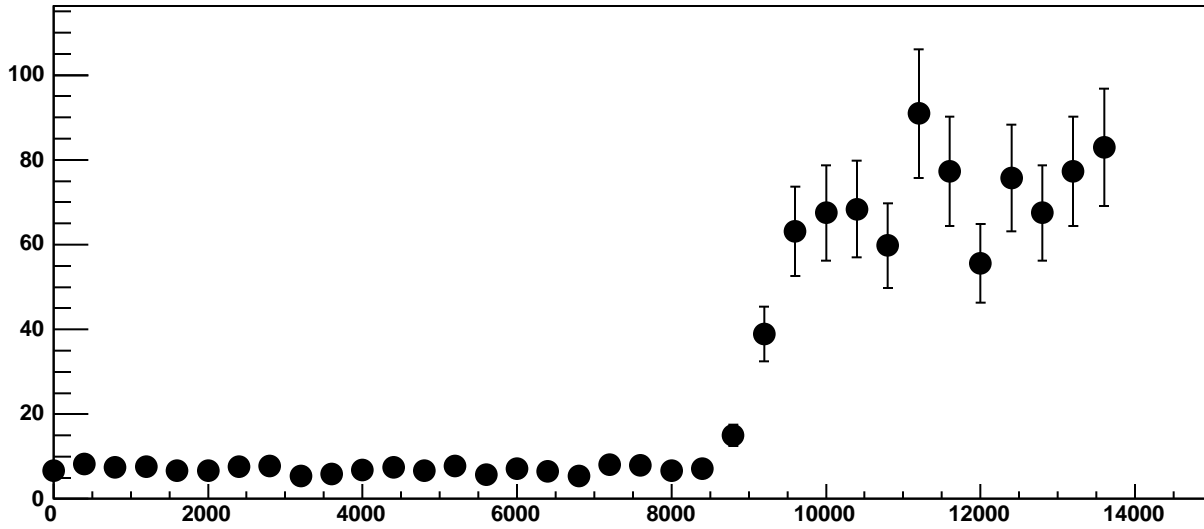


Chip 1, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC

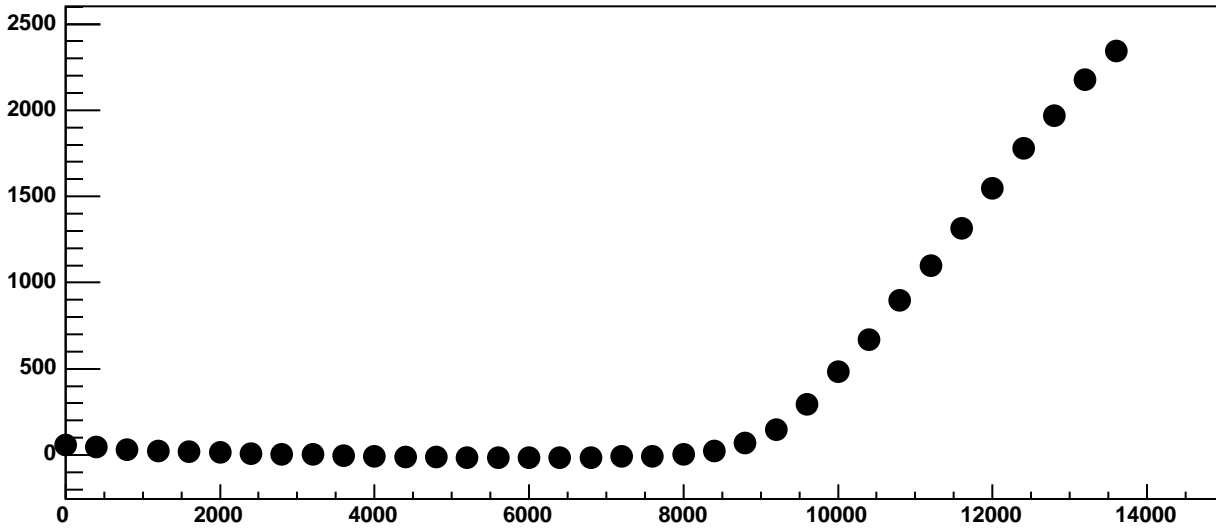


χ^2 / ndf 9519 / 23
p0 -587.2 ± 0.8661
p1 0.04277 ± 0.0001628

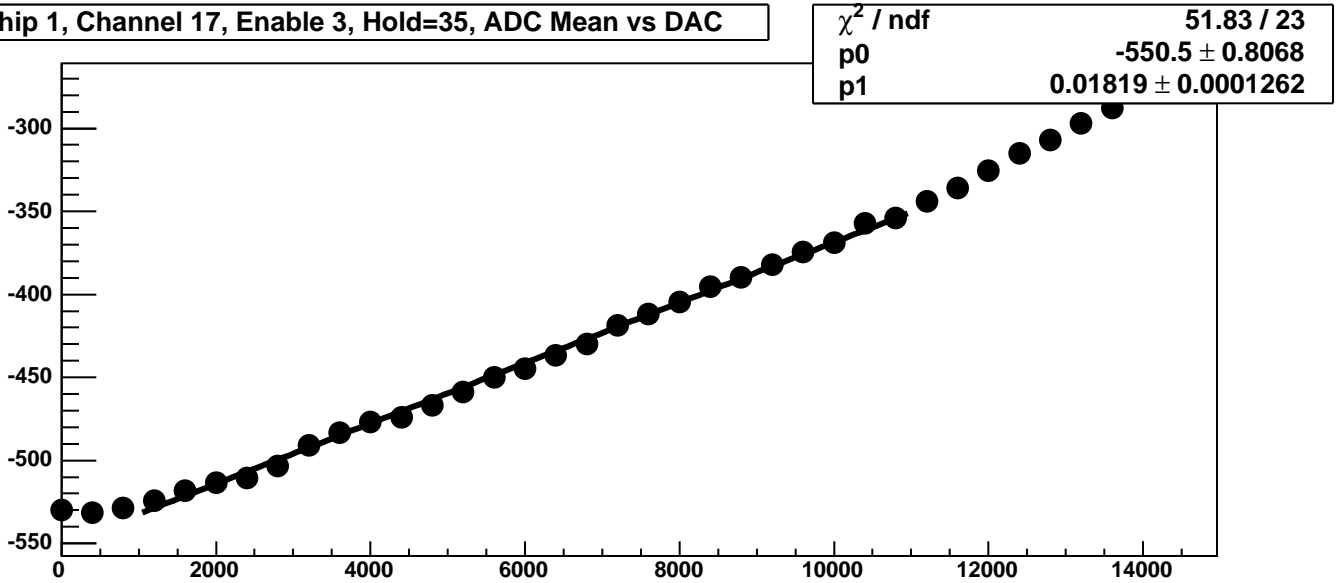
Chip 1, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



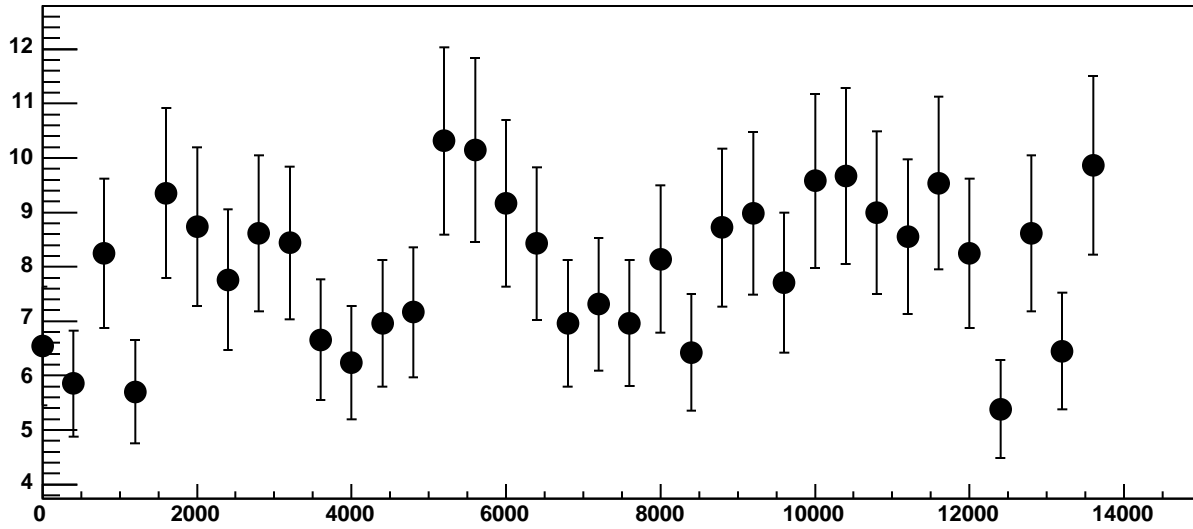
Chip 1, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC



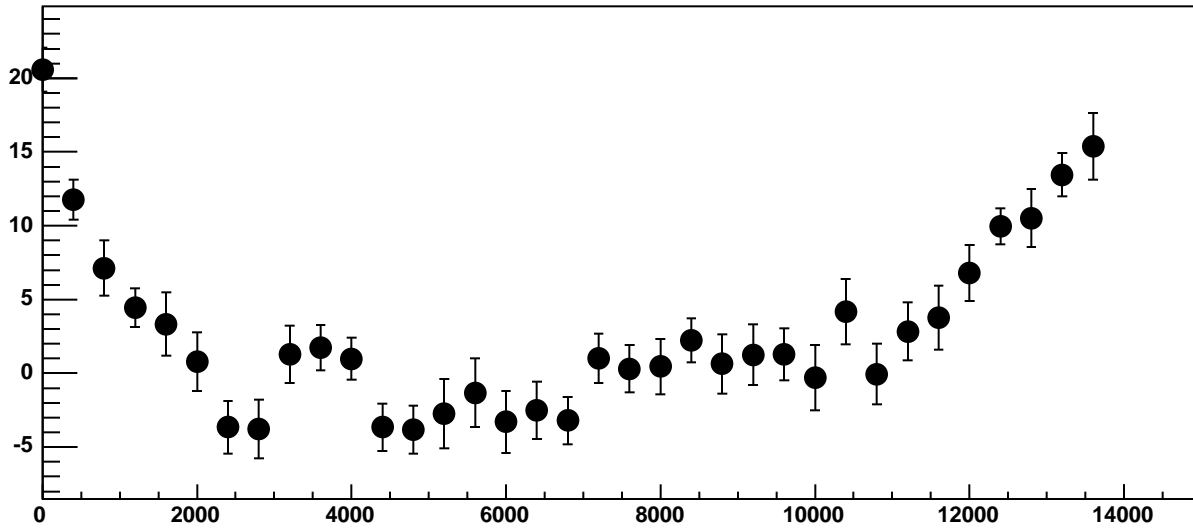
Chip 1, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



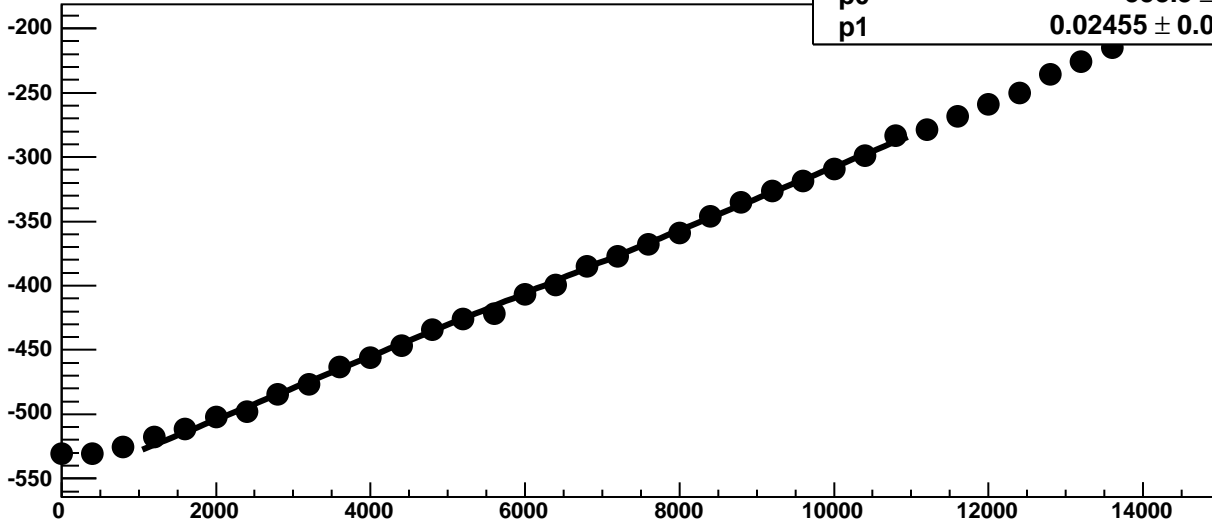
Chip 1, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



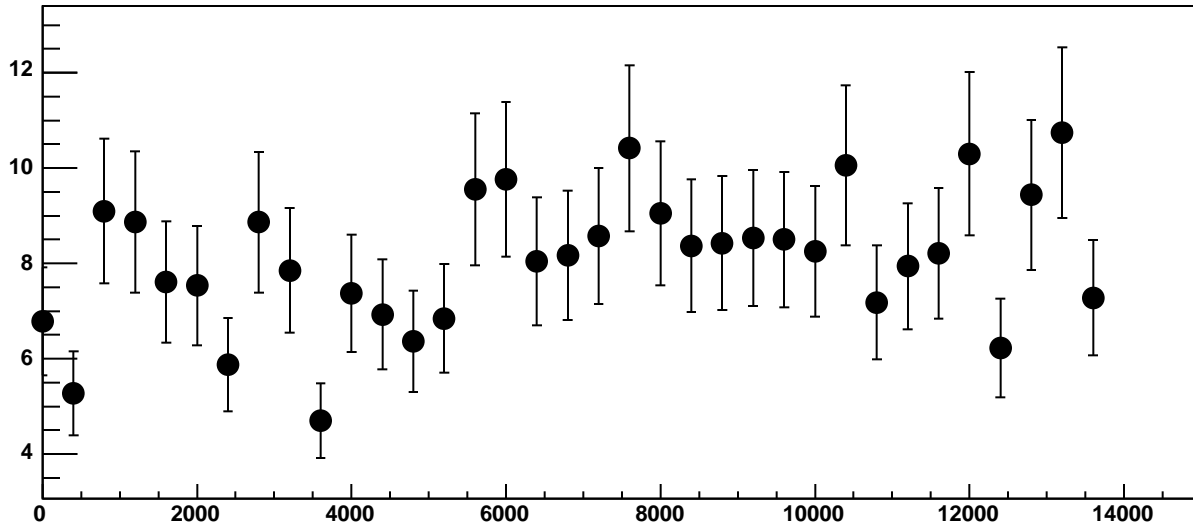
Chip 1, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



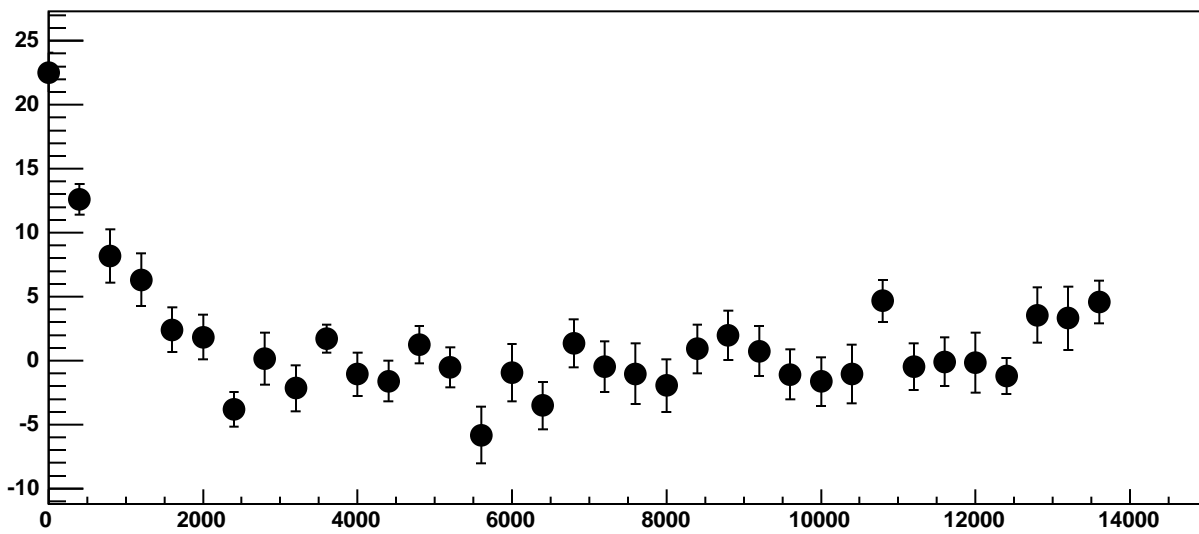
Chip 1, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



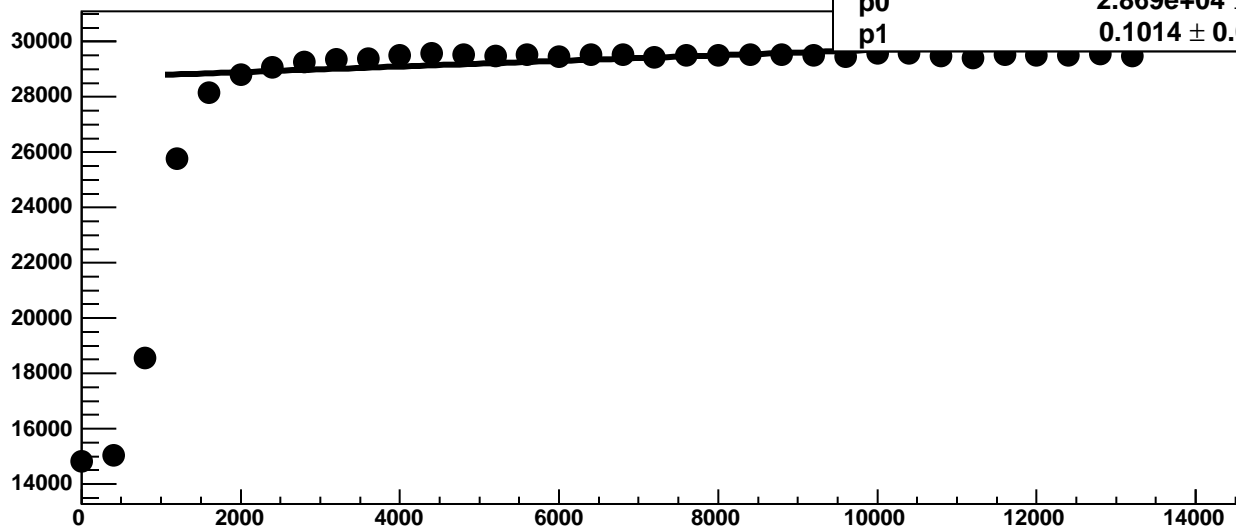
Chip 1, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



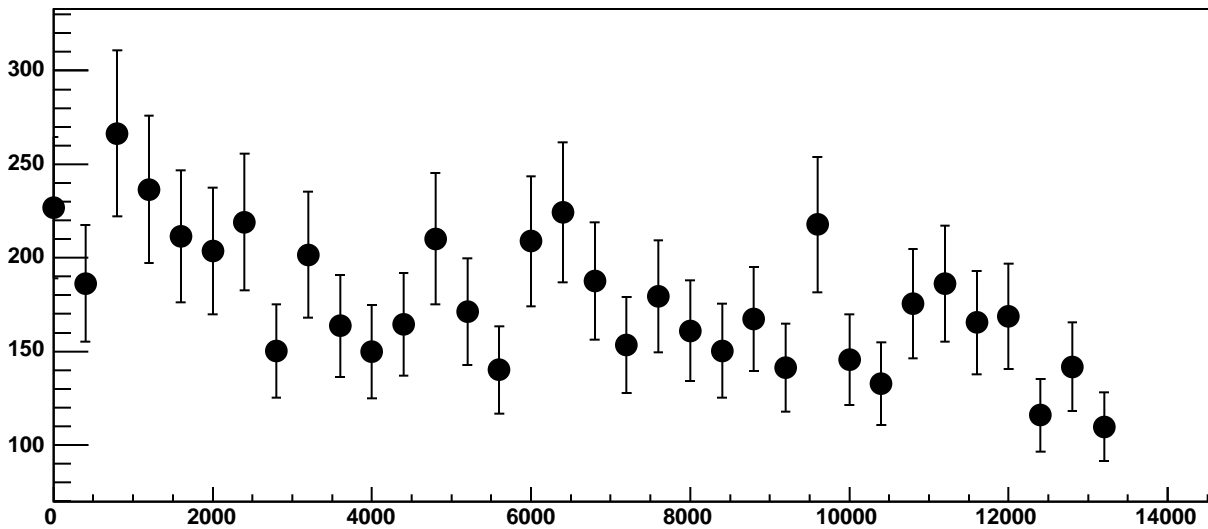
Chip 1, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



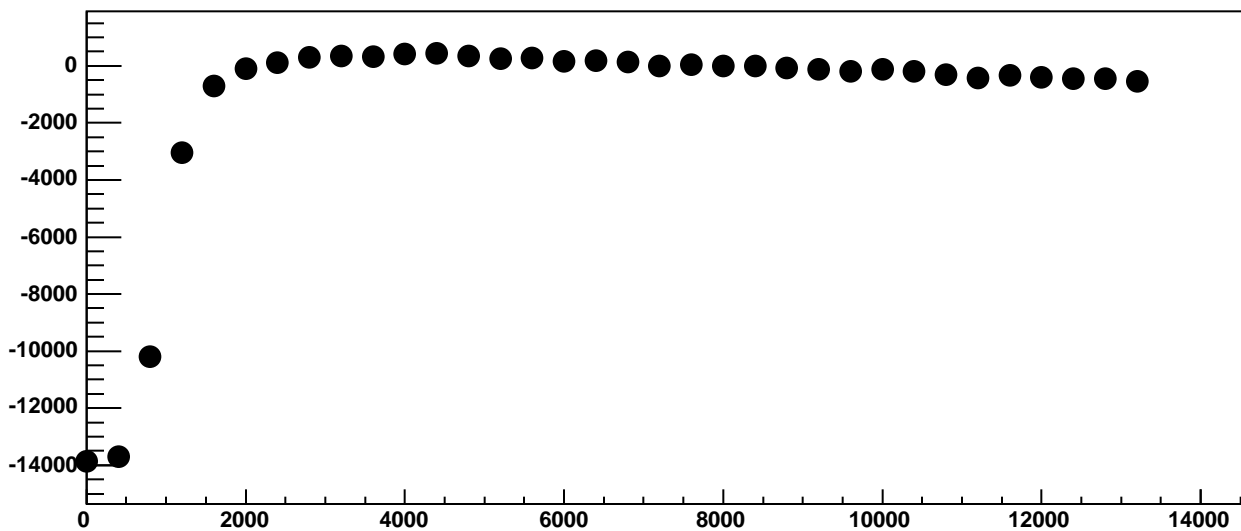
Chip 1, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC



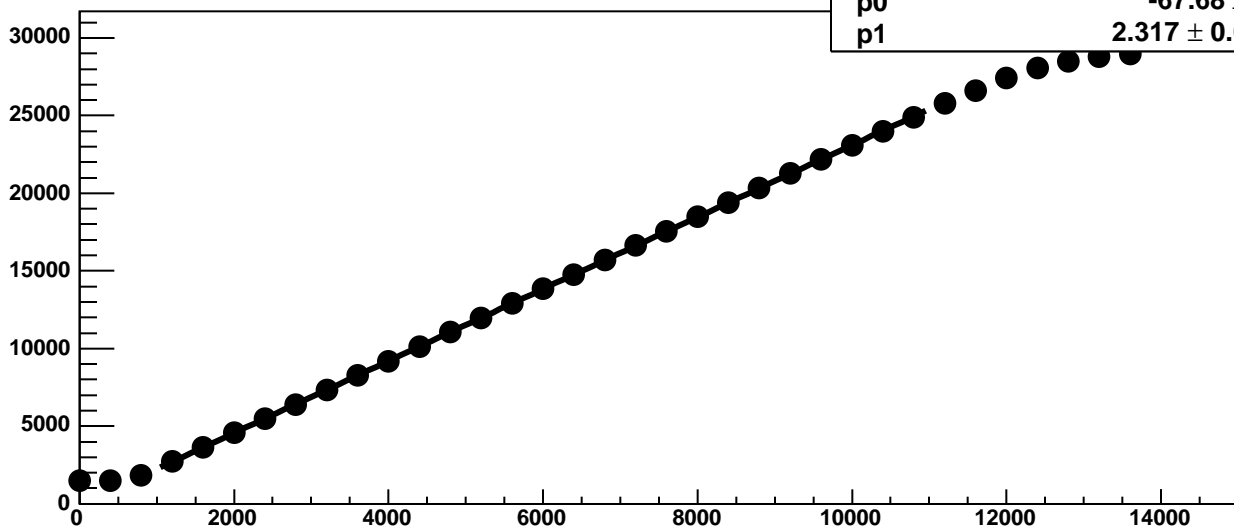
Chip 1, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC



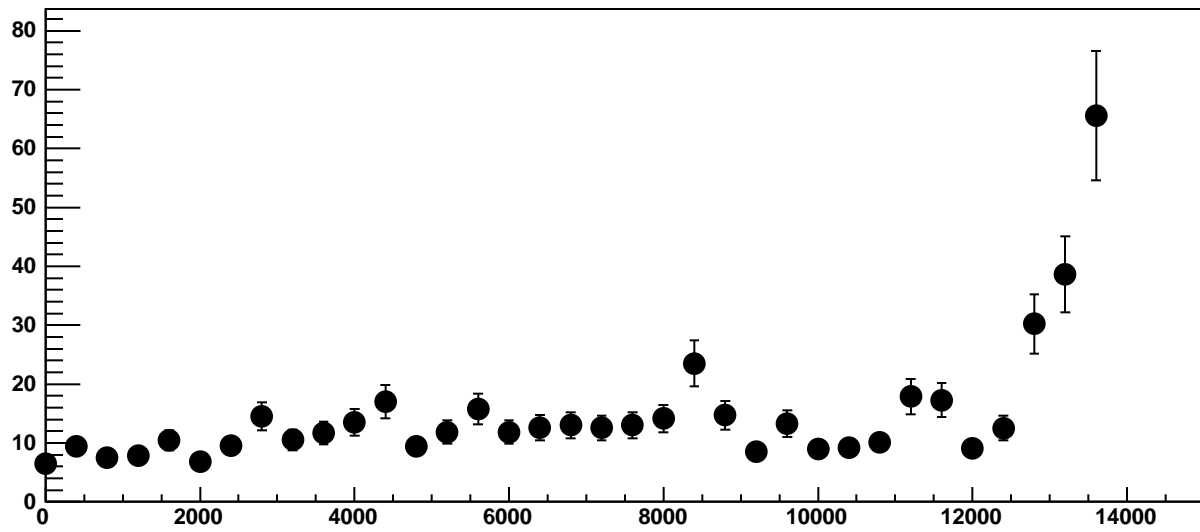
Chip 1, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC



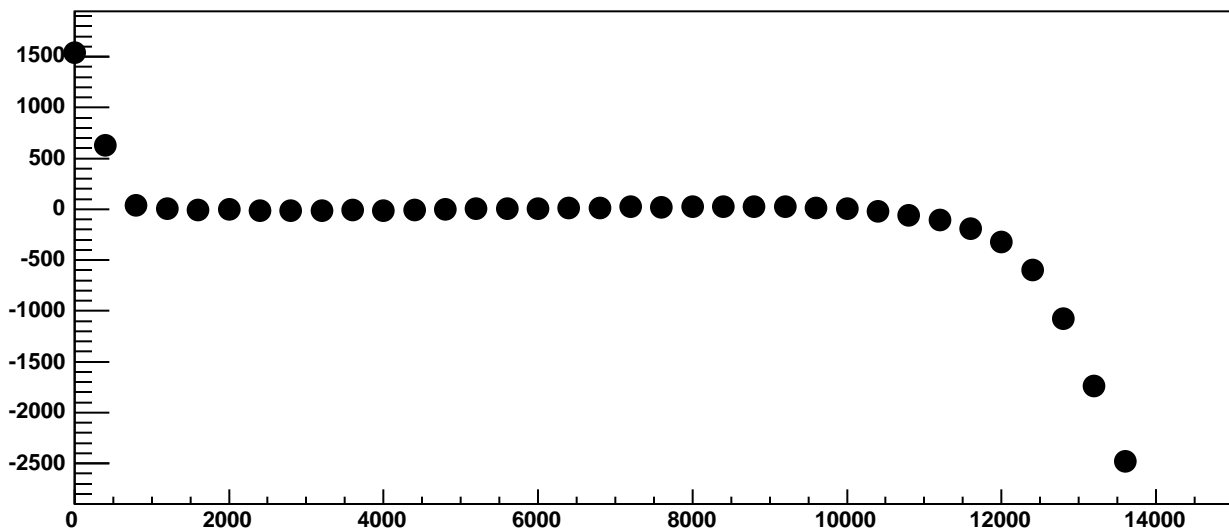
Chip 2, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



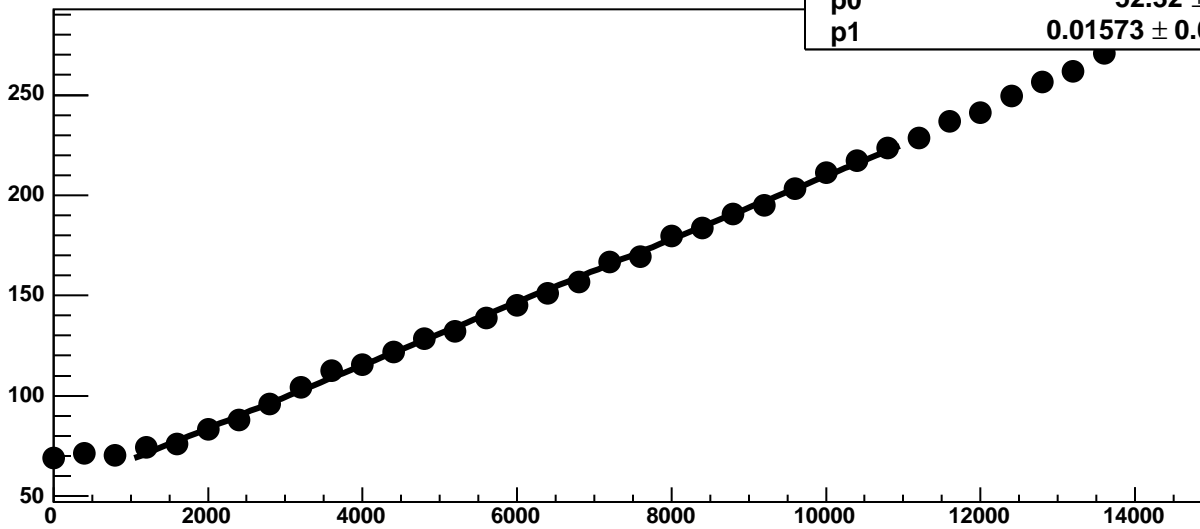
Chip 2, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC

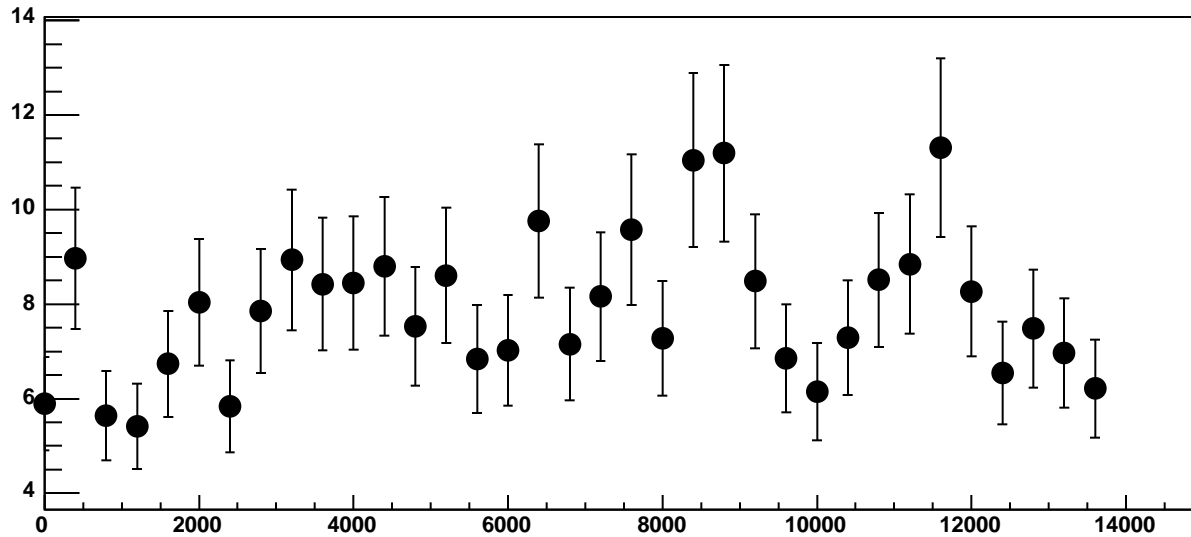


Chip 2, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

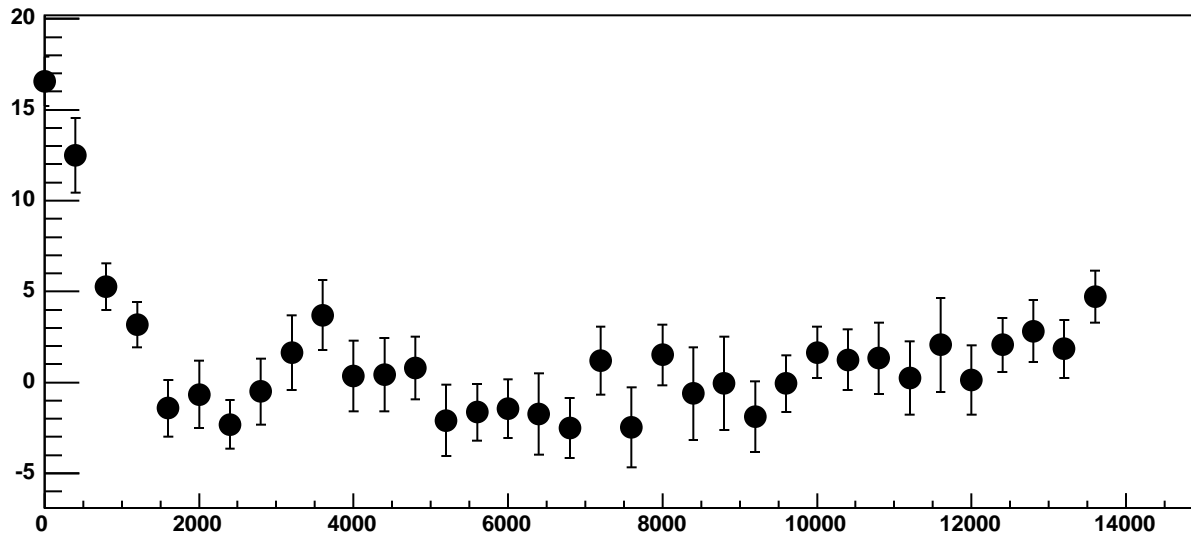


χ^2 / ndf 27.03 / 23
p0 52.32 ± 0.7389
p1 0.01573 ± 0.0001141

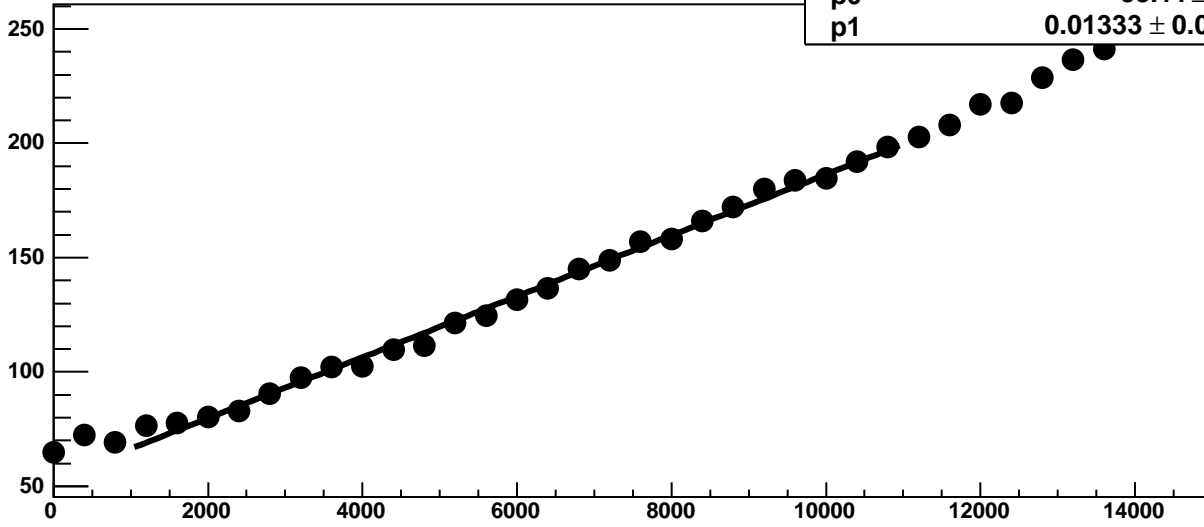
Chip 2, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



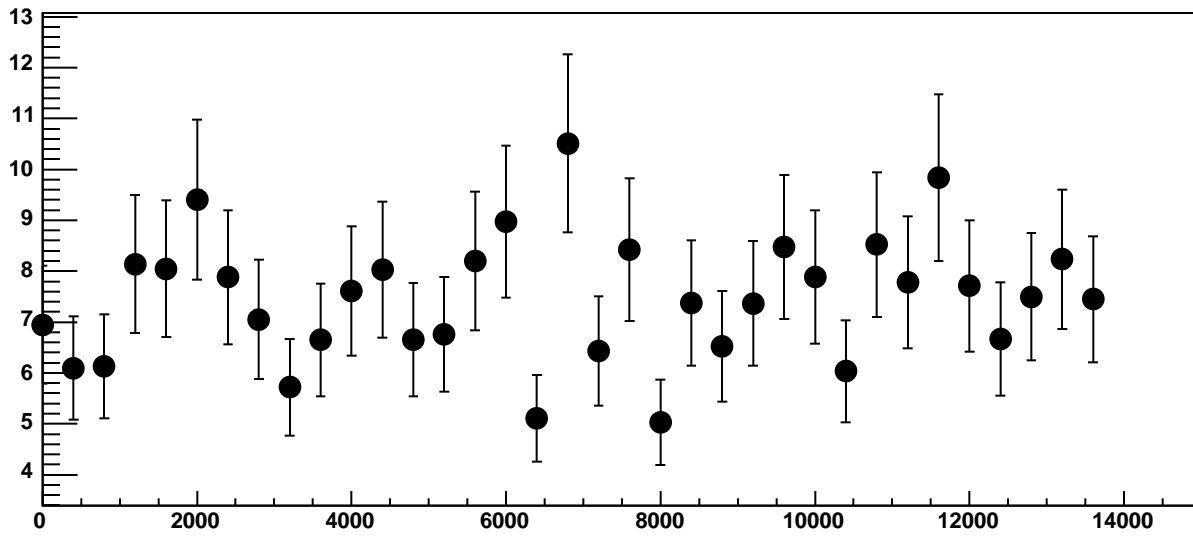
Chip 2, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



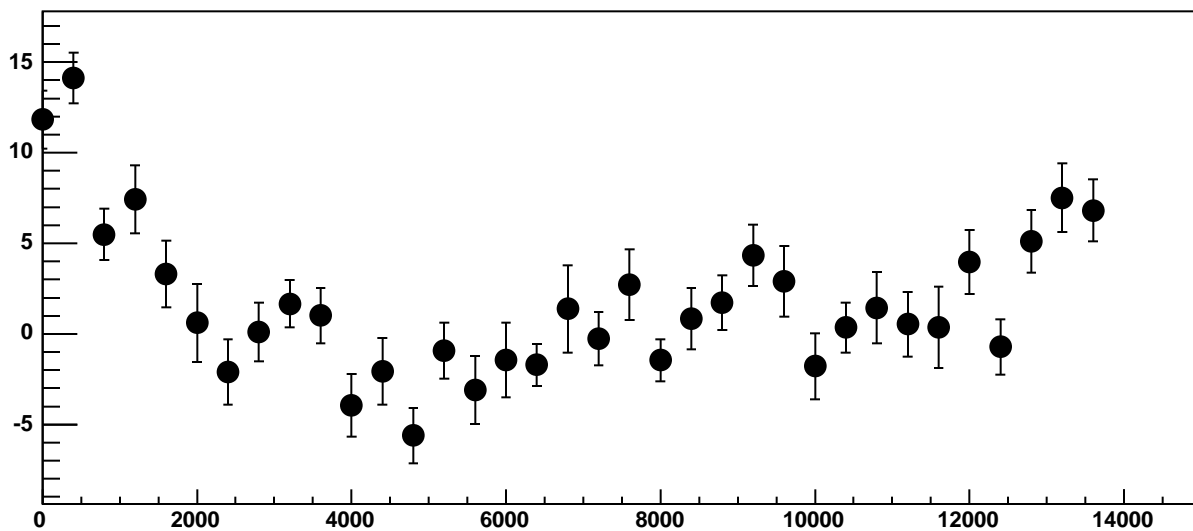
Chip 2, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



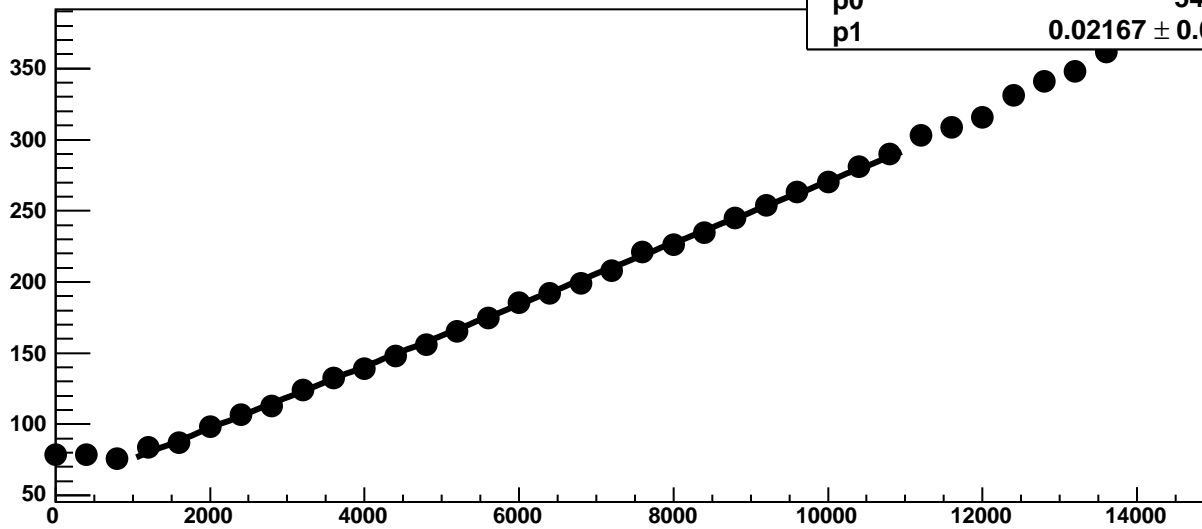
Chip 2, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

22.35 / 23

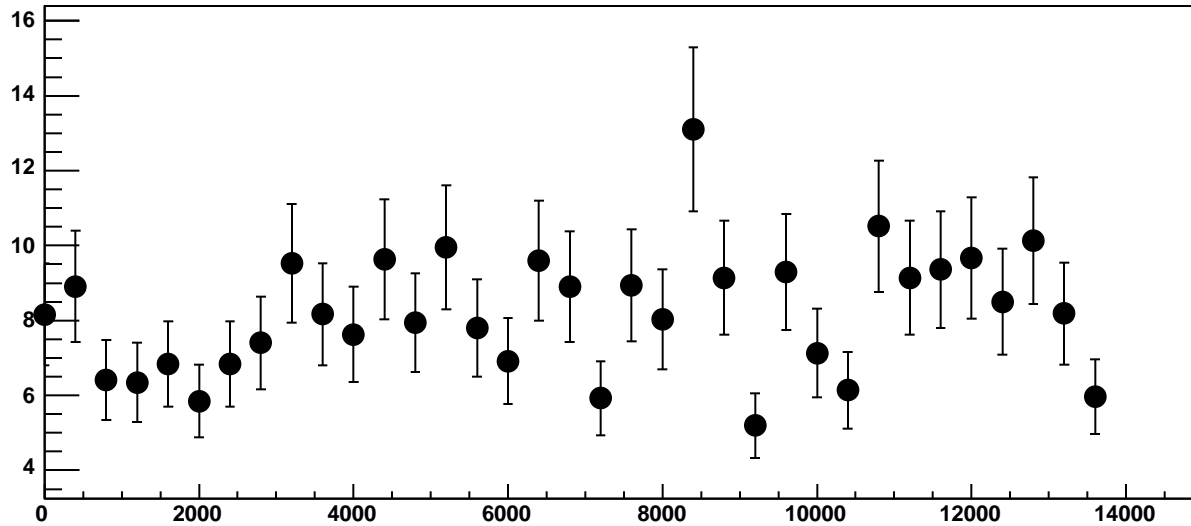
p0

54 ± 0.751

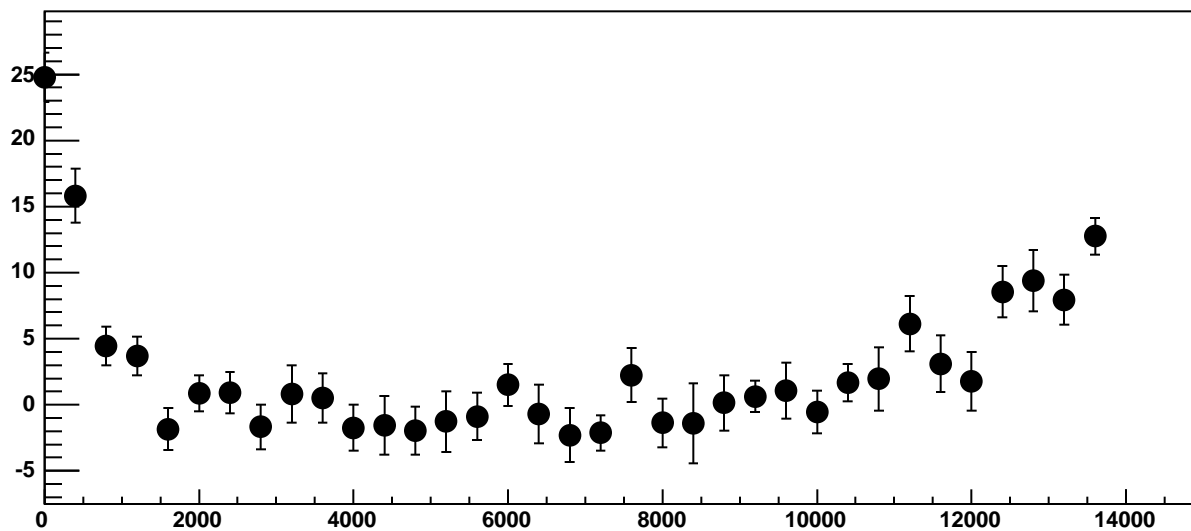
p1

0.02167 ± 0.0001141

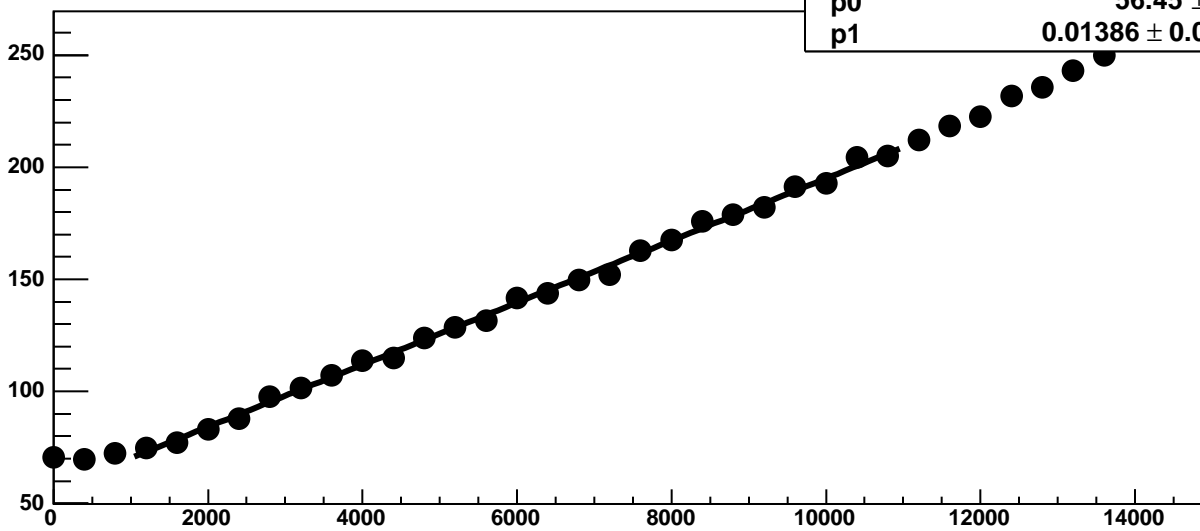
Chip 2, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC

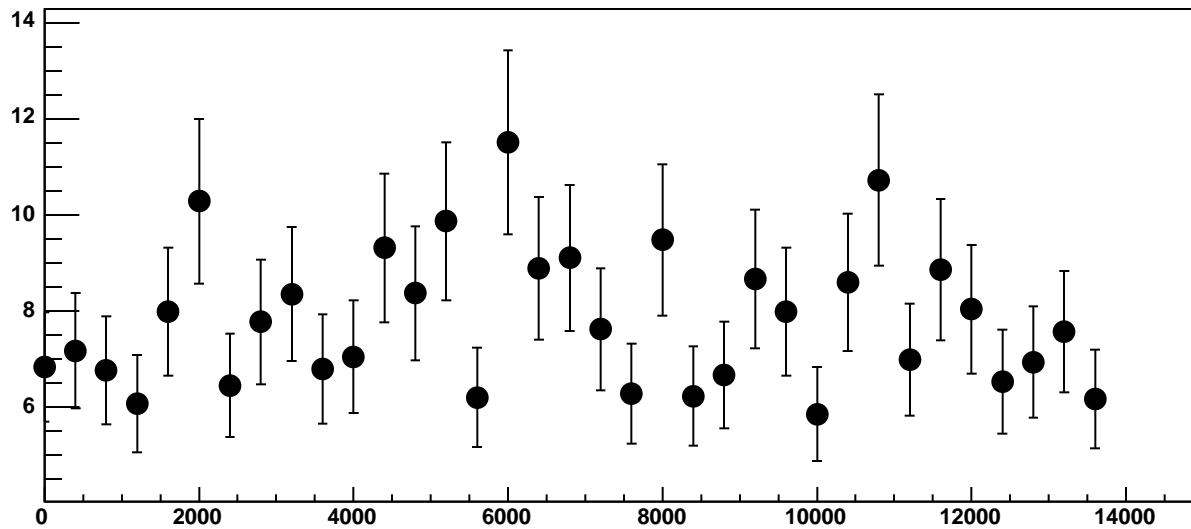


Chip 2, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

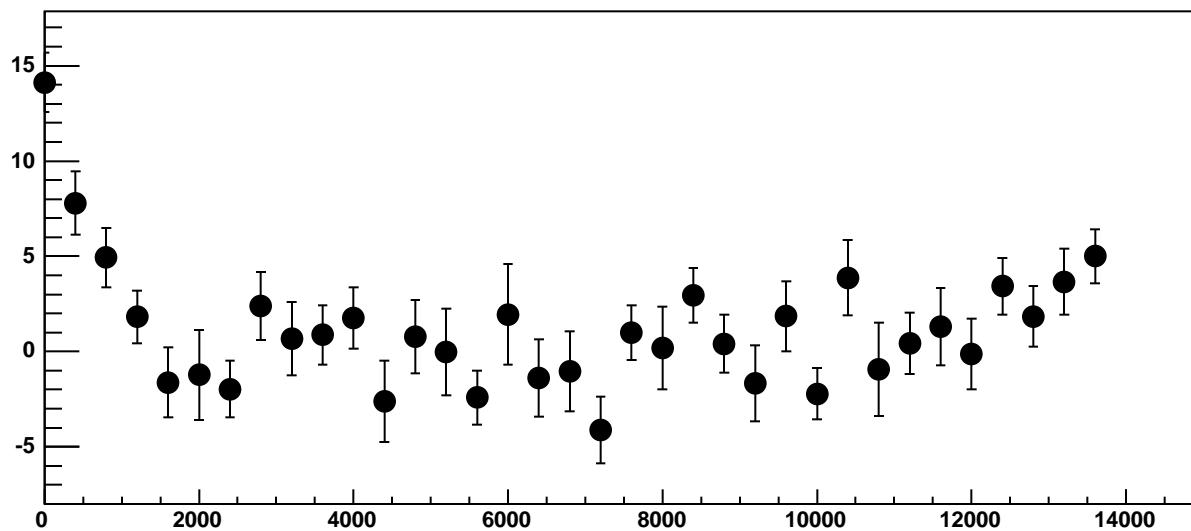


χ^2 / ndf 32.48 / 23
p0 56.45 ± 0.7939
p1 0.01386 ± 0.0001197

Chip 2, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

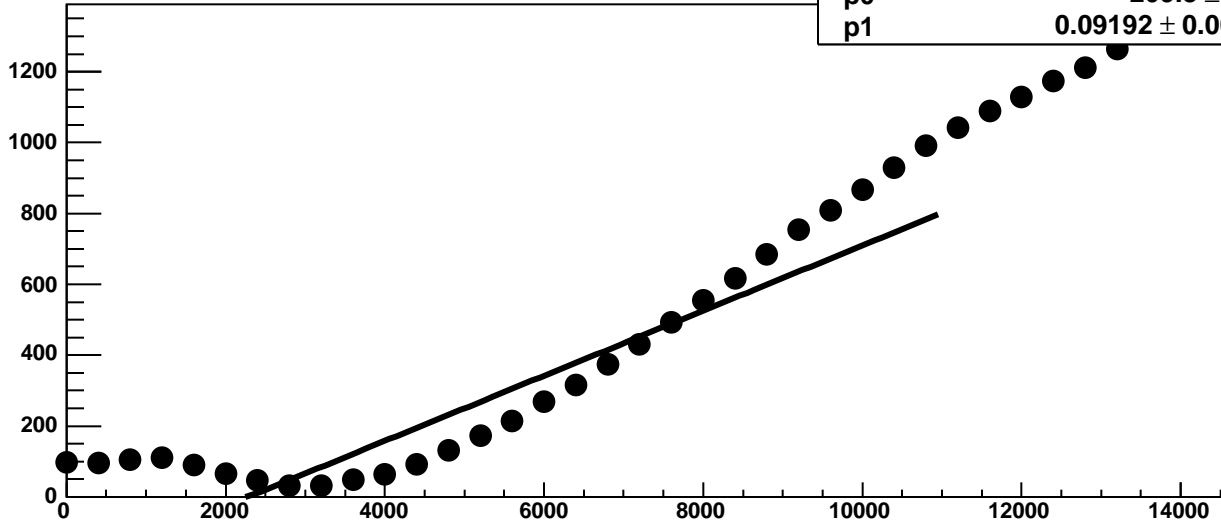


Chip 2, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

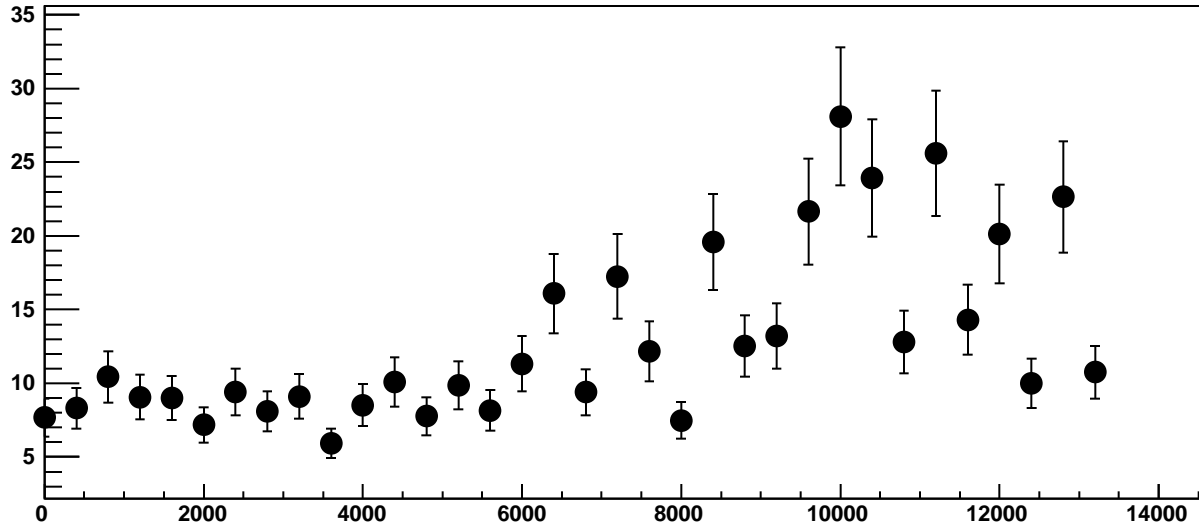


Chip 2, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

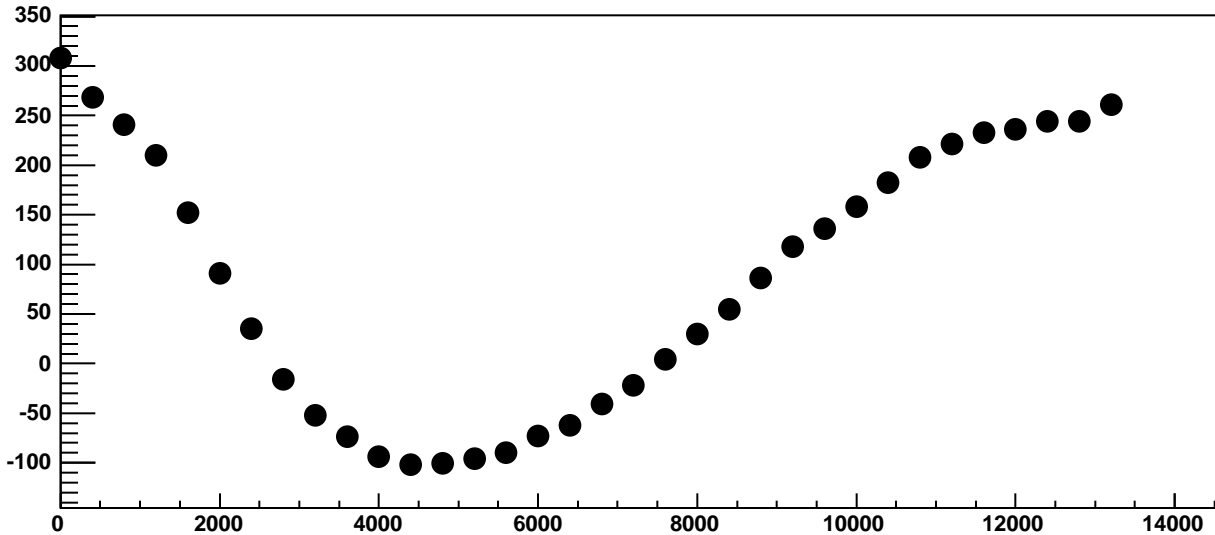
χ^2 / ndf 4.591e+04 / 23
p0 -209.8 ± 0.9852
p1 0.09192 ± 0.0001823



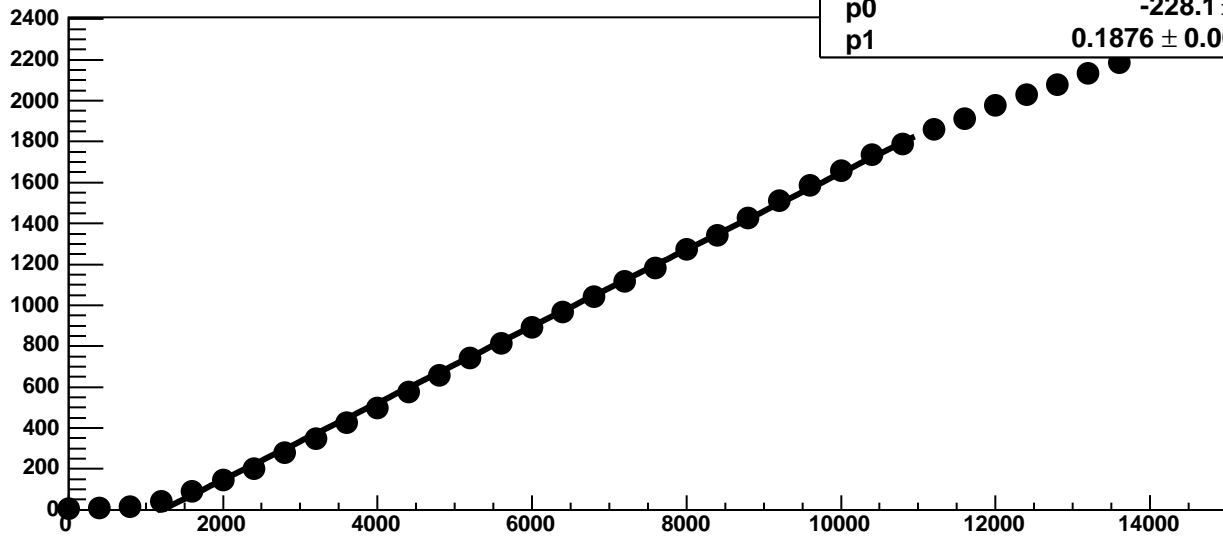
Chip 2, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

935 / 23

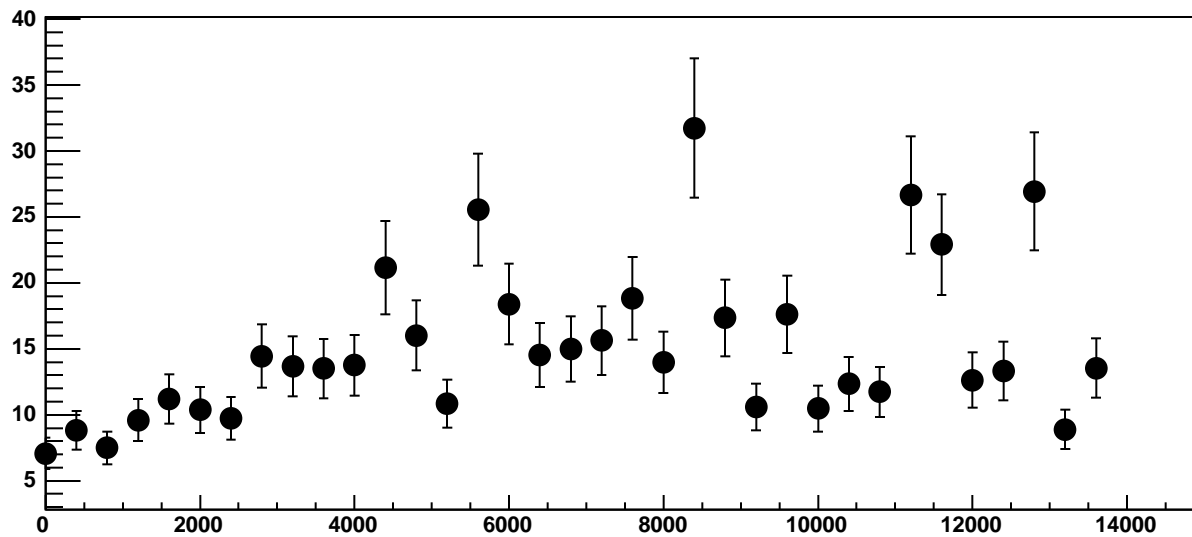
p0

-228.1 ± 1.223

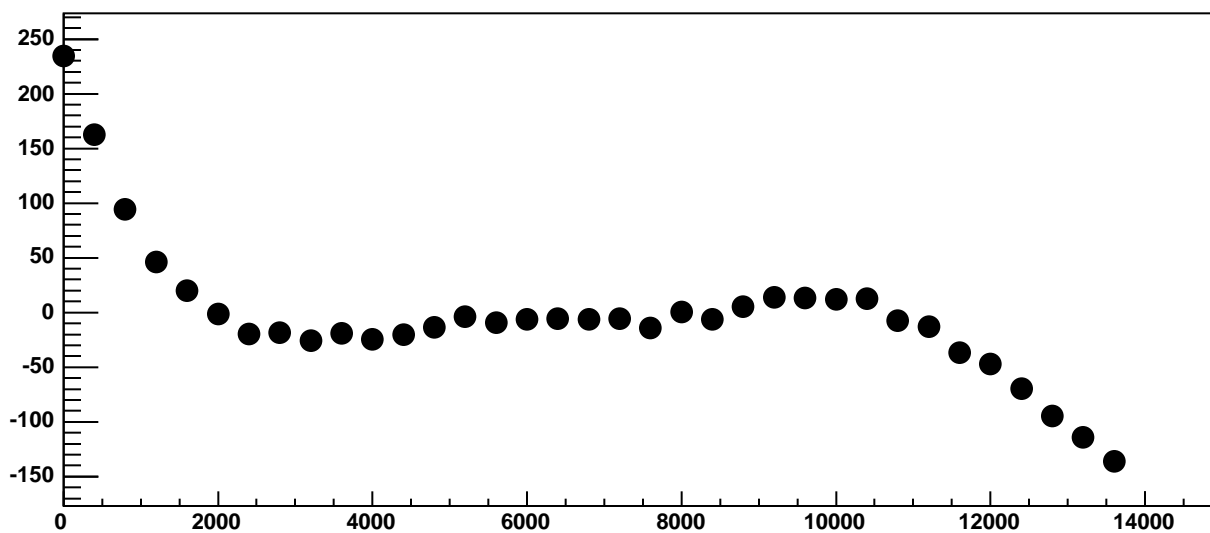
p1

0.1876 ± 0.0001884

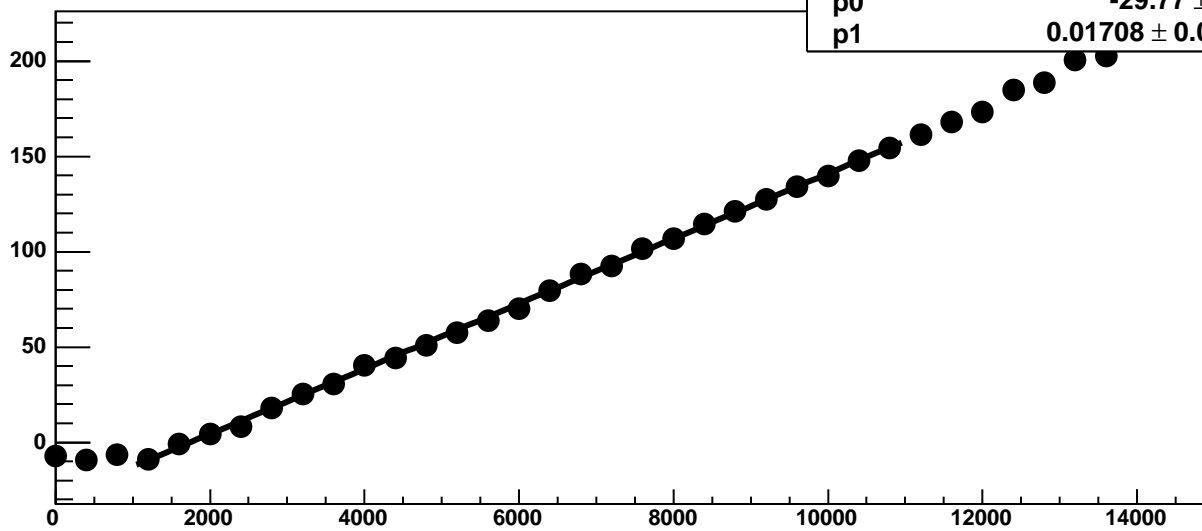
Chip 2, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC

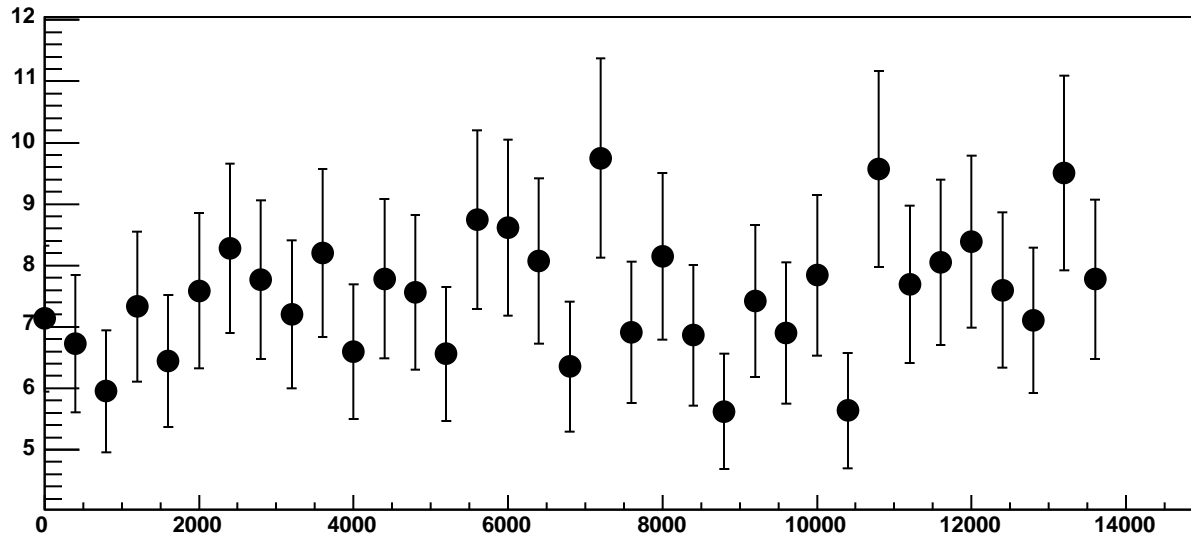


Chip 2, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

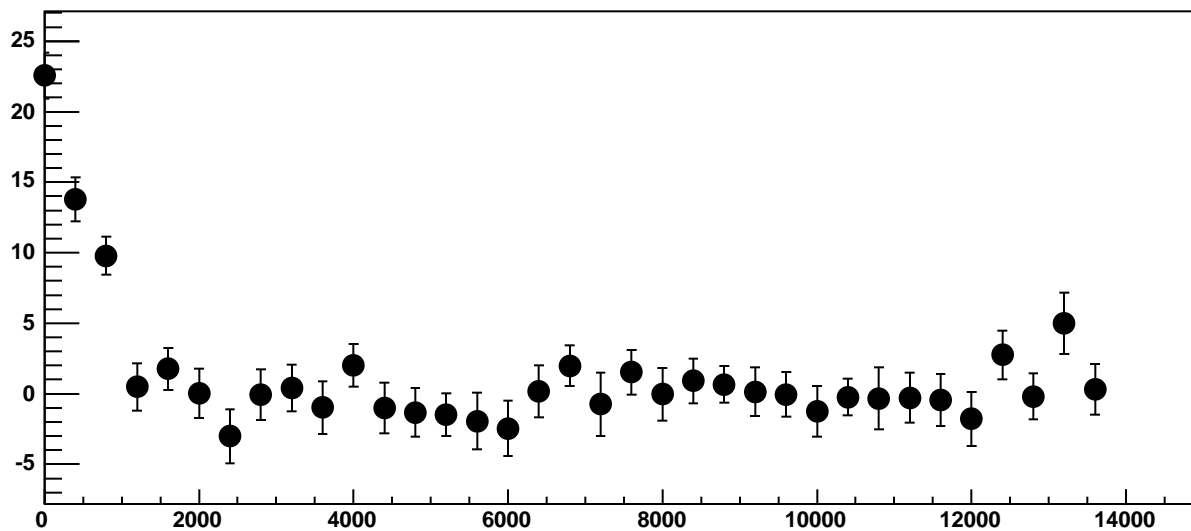


χ^2 / ndf 14.56 / 23
p0 -29.77 ± 0.7746
p1 0.01708 ± 0.0001142

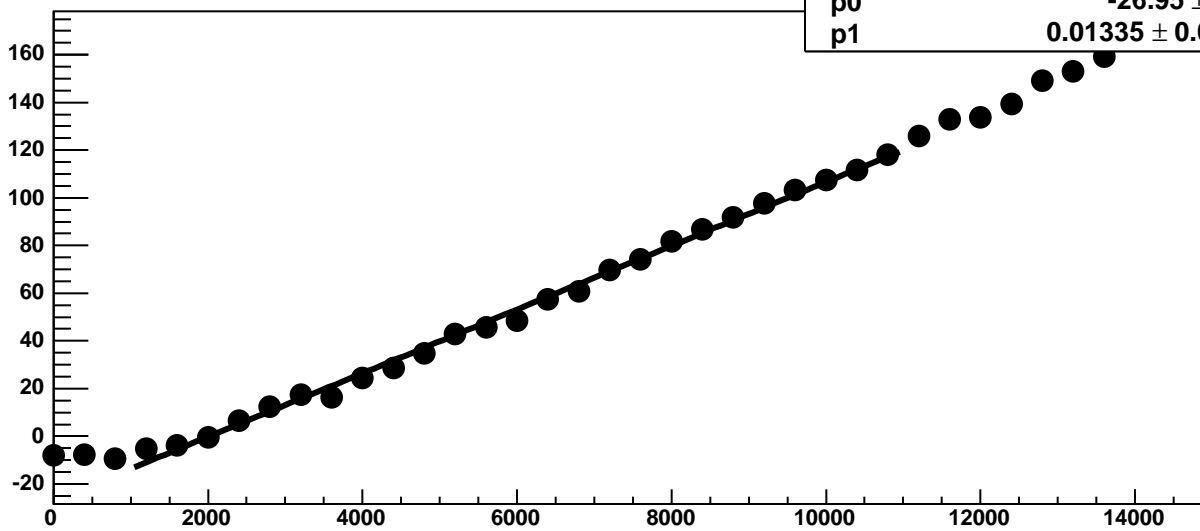
Chip 2, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

49.81 / 23

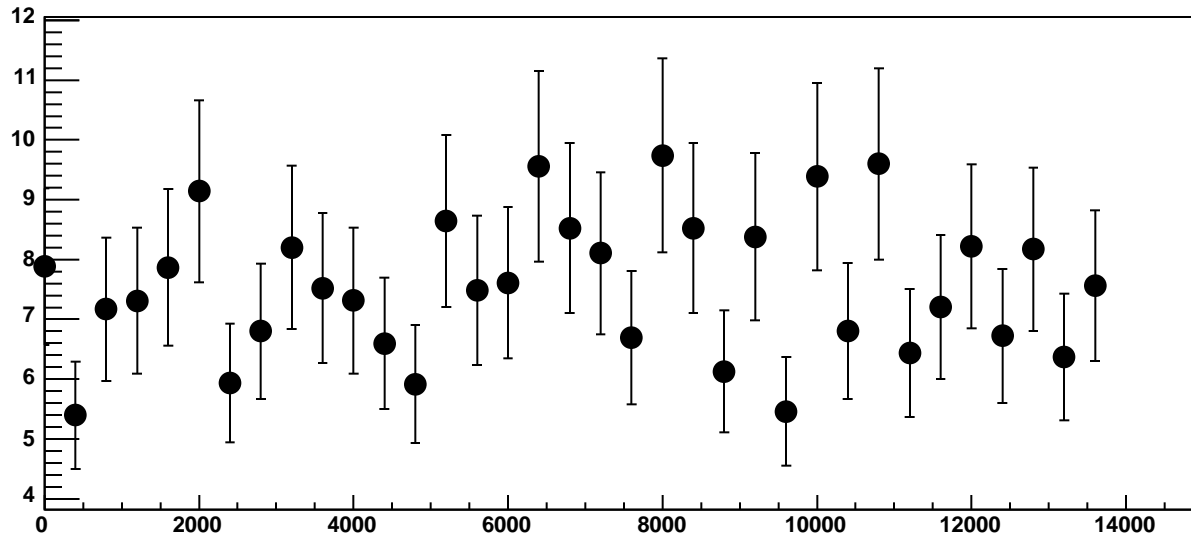
p0

-26.95 ± 0.7782

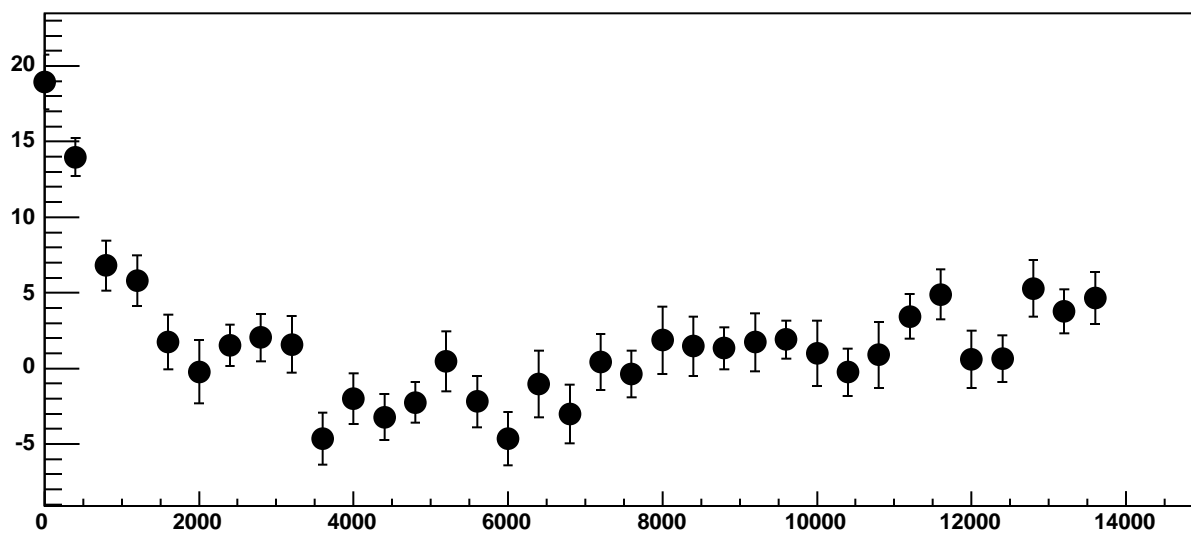
p1

0.01335 ± 0.0001181

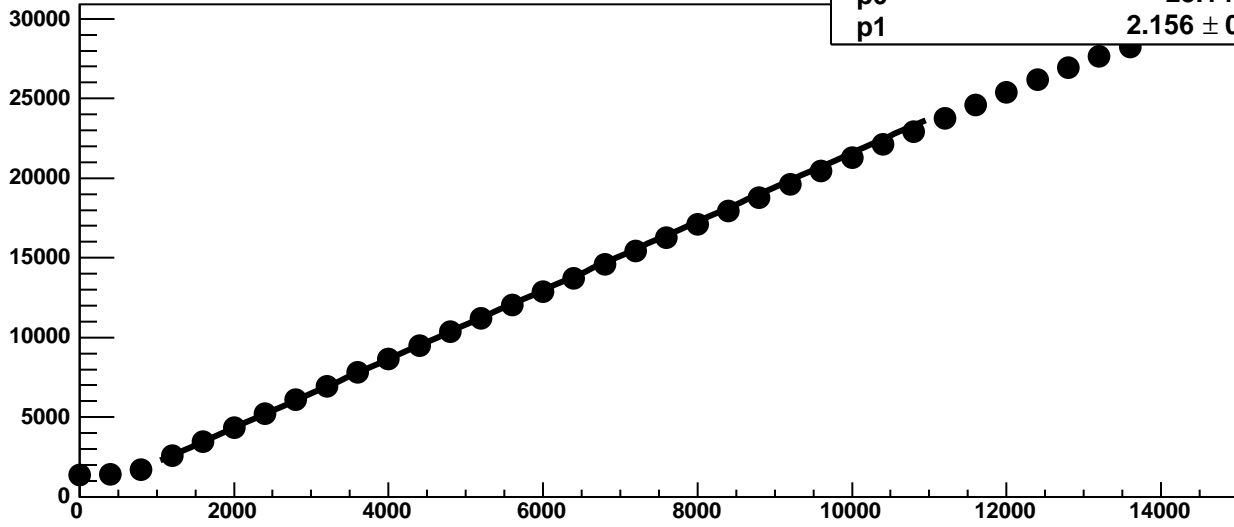
Chip 2, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

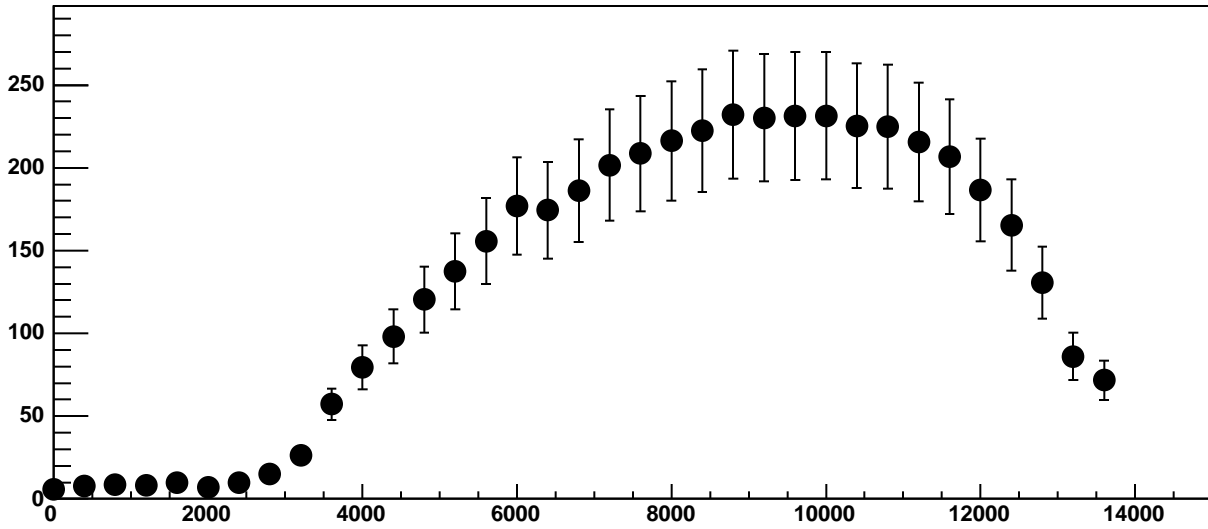


Chip 2, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC

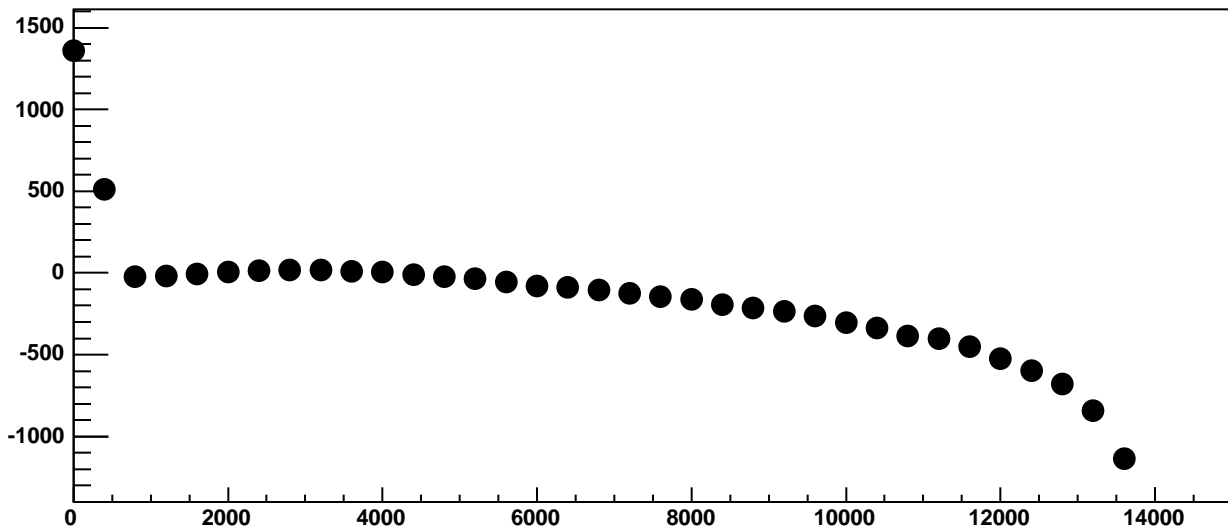


χ^2 / ndf	440.8 / 23
p0	23.14 ± 2.62
p1	2.156 ± 0.00126

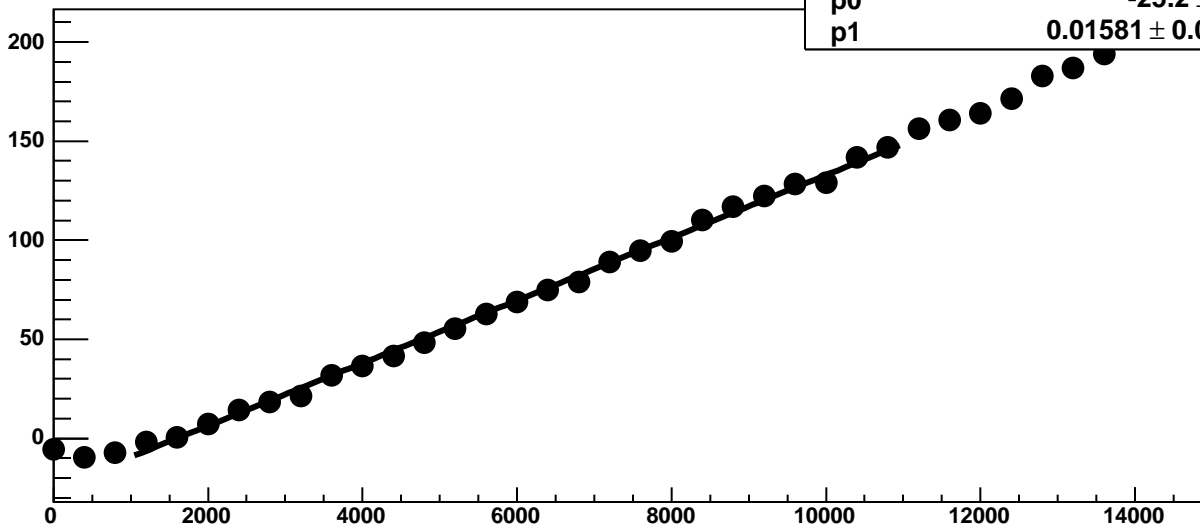
Chip 2, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



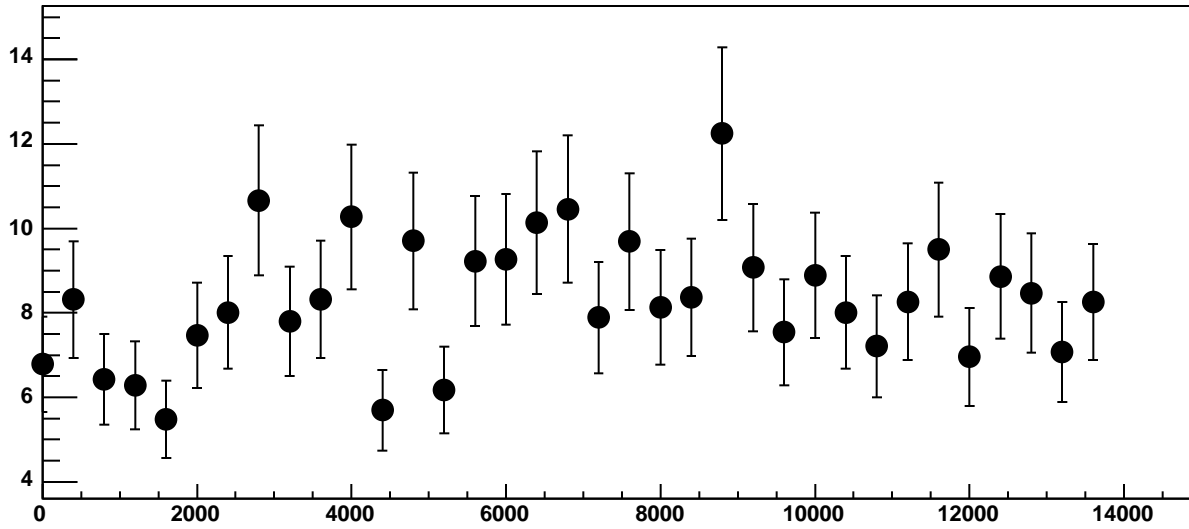
Chip 2, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC



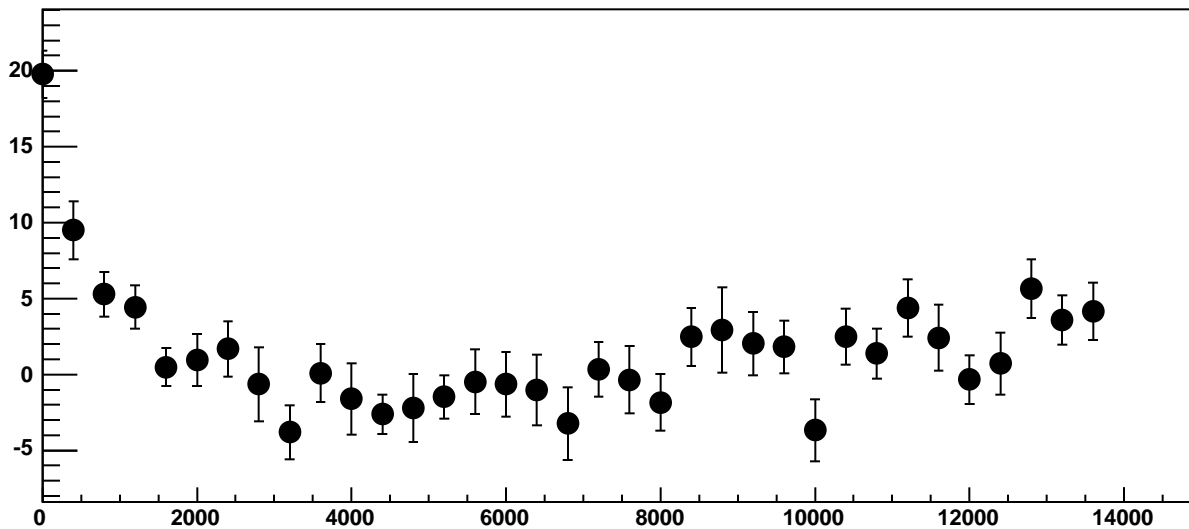
Chip 2, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



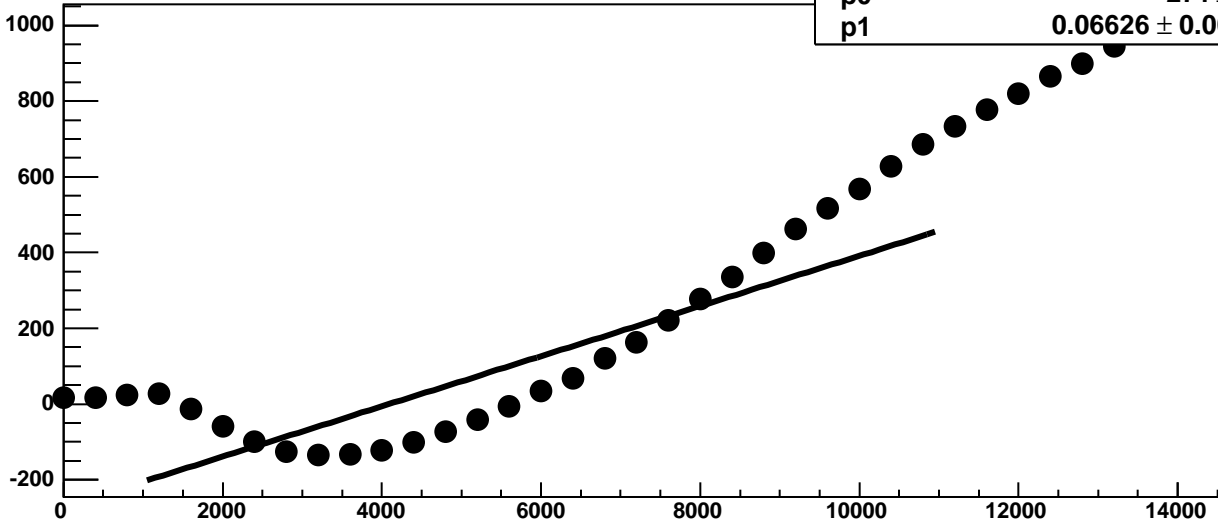
Chip 2, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



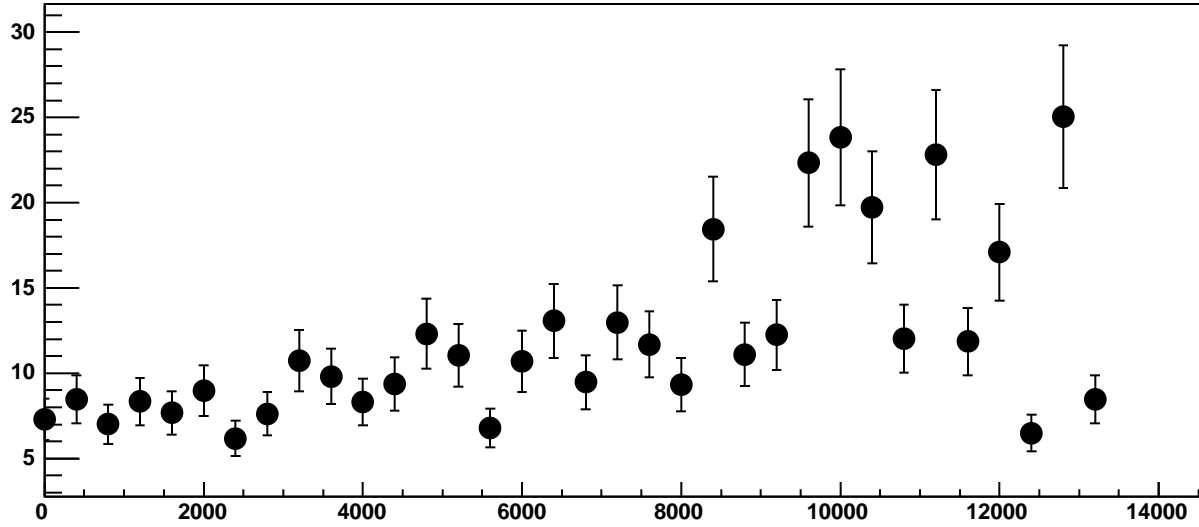
Chip 2, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



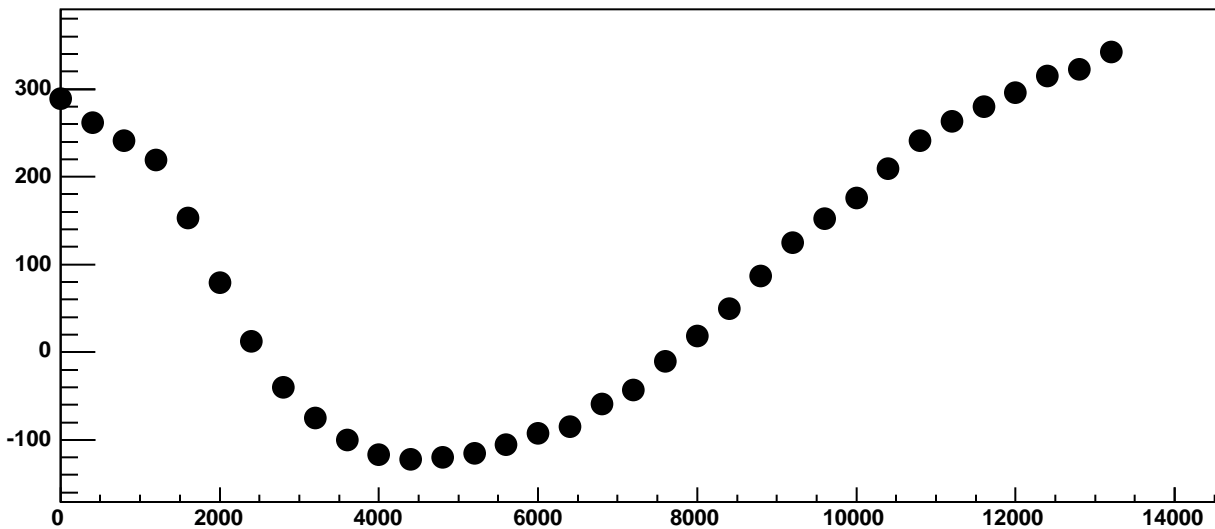
Chip 2, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



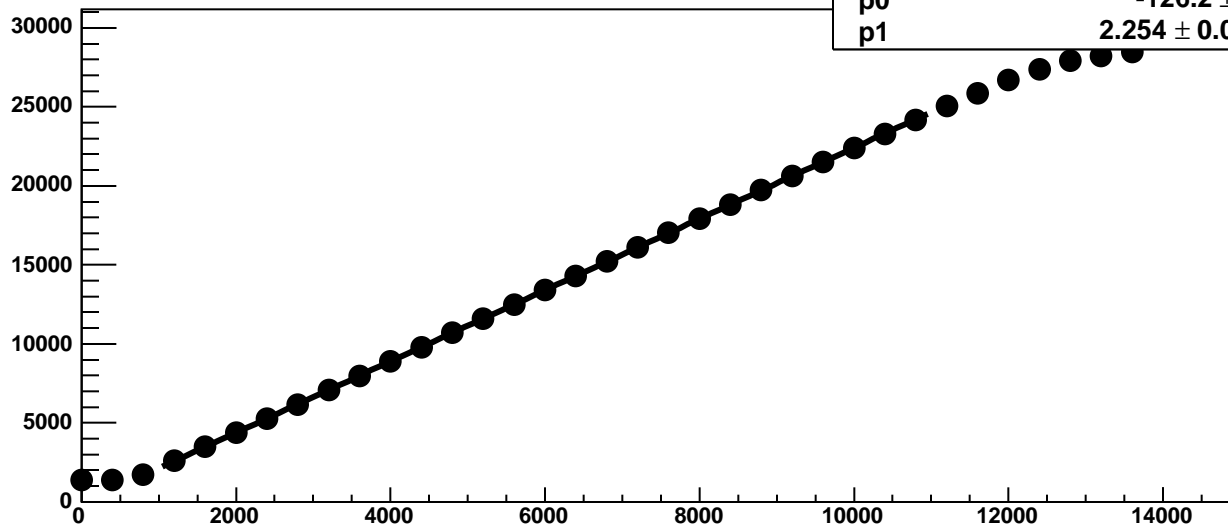
Chip 2, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



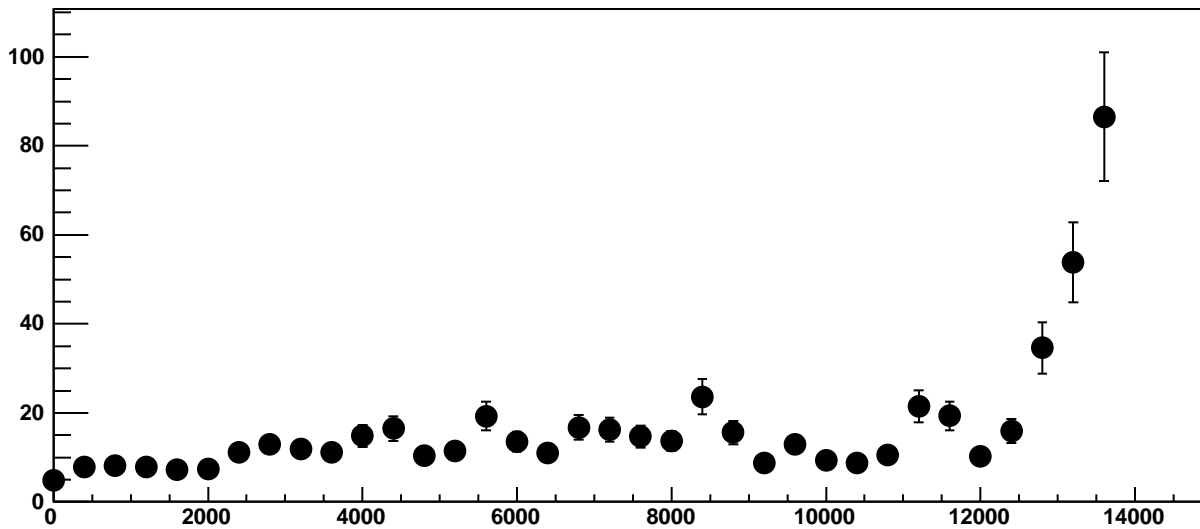
Chip 2, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



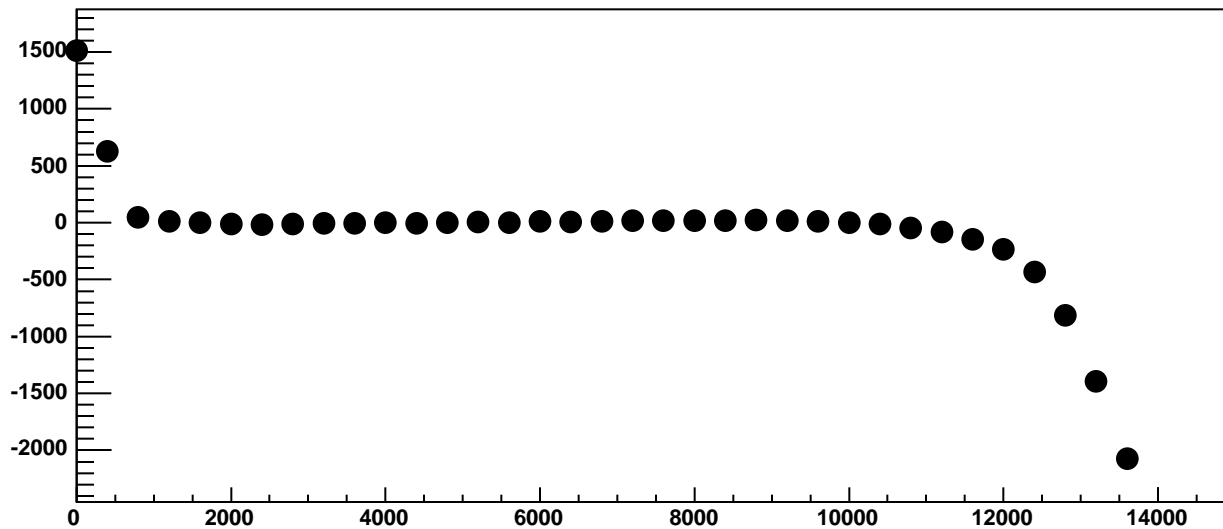
Chip 2, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC



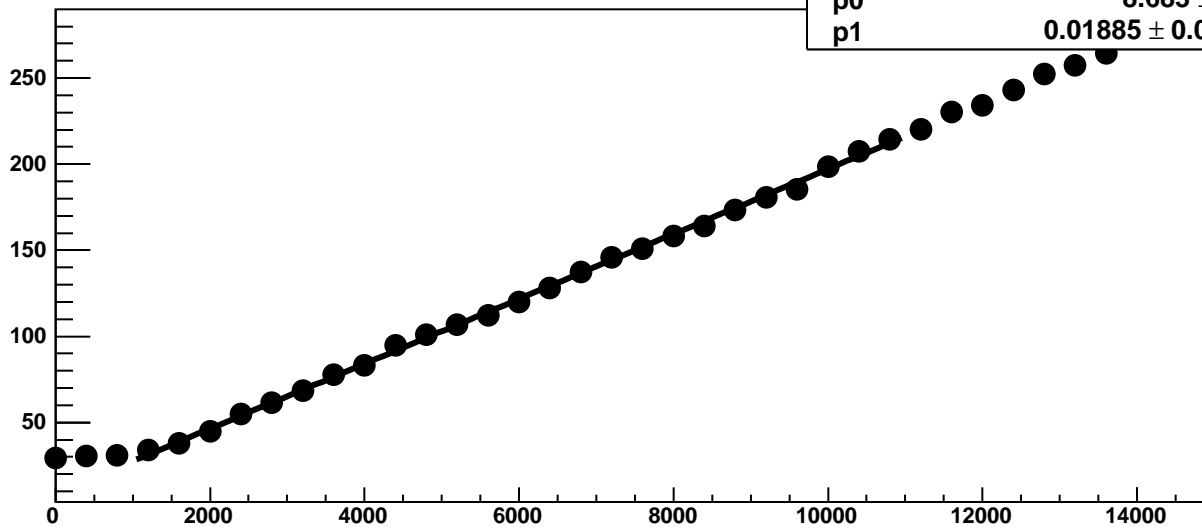
Chip 2, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC

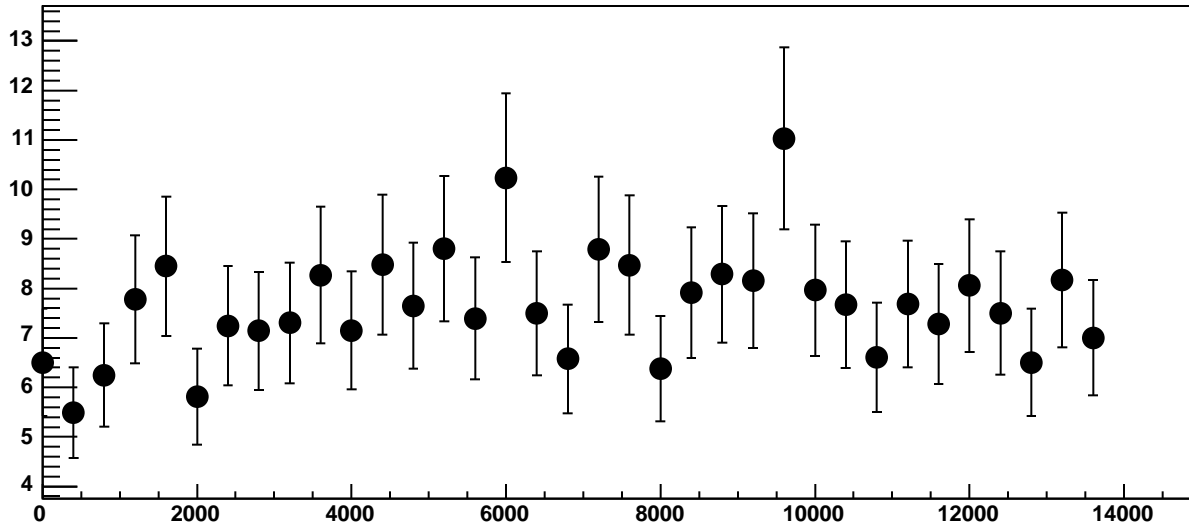


Chip 2, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

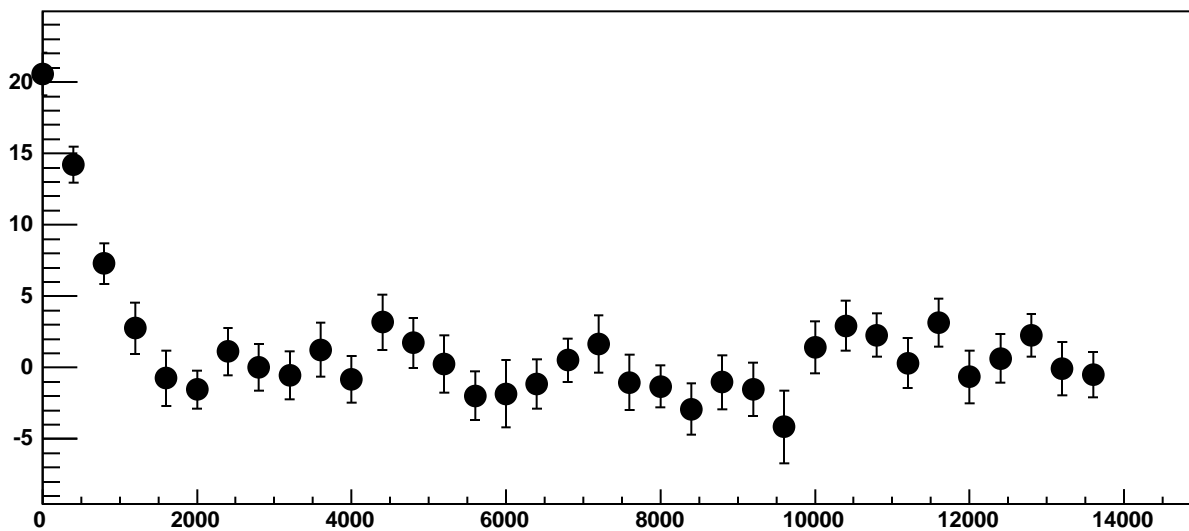


χ^2 / ndf 24.85 / 23
p0 8.683 ± 0.7831
p1 0.01885 ± 0.0001194

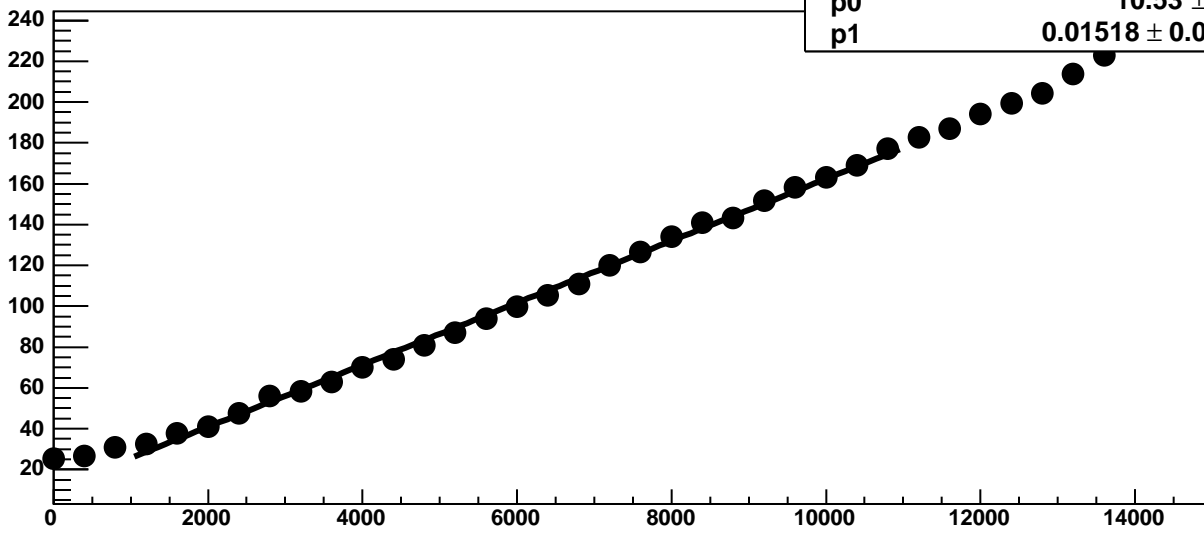
Chip 2, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

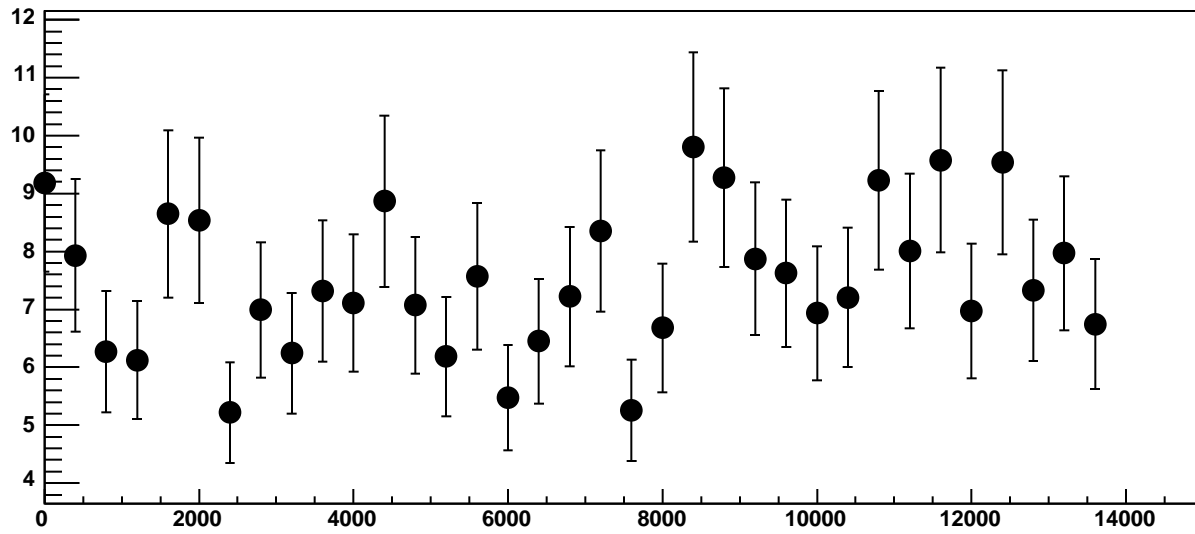


Chip 2, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

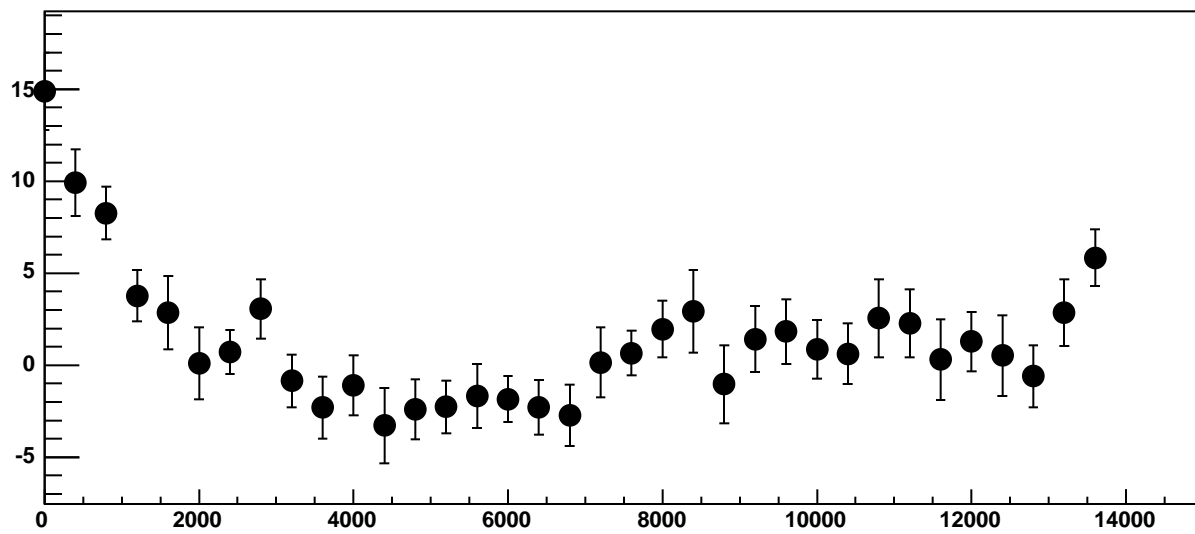


χ^2 / ndf 38.93 / 23
p0 10.53 ± 0.7376
p1 0.01518 ± 0.0001154

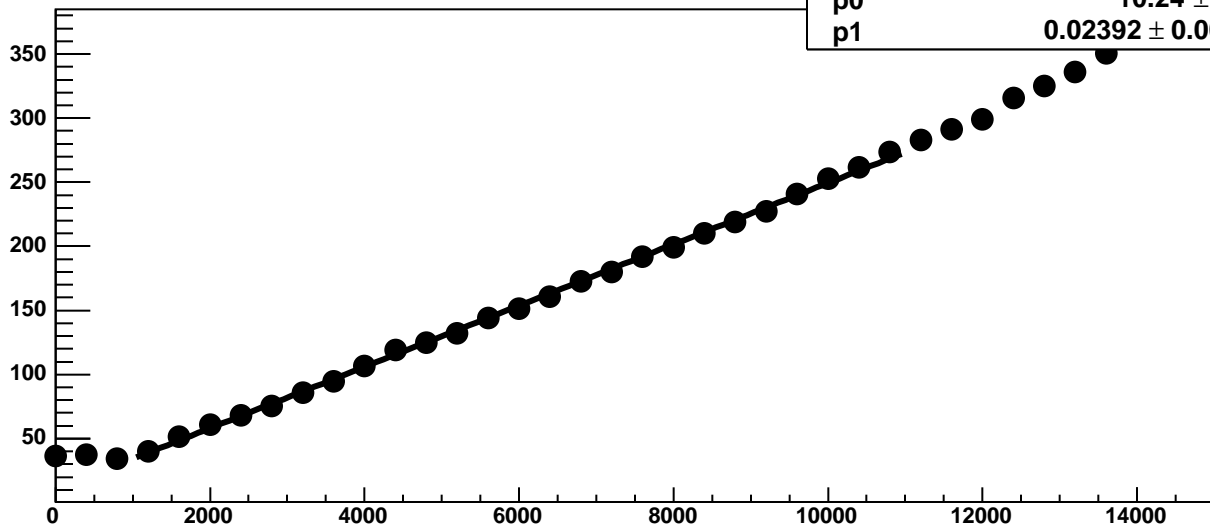
Chip 2, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

28.15 / 23

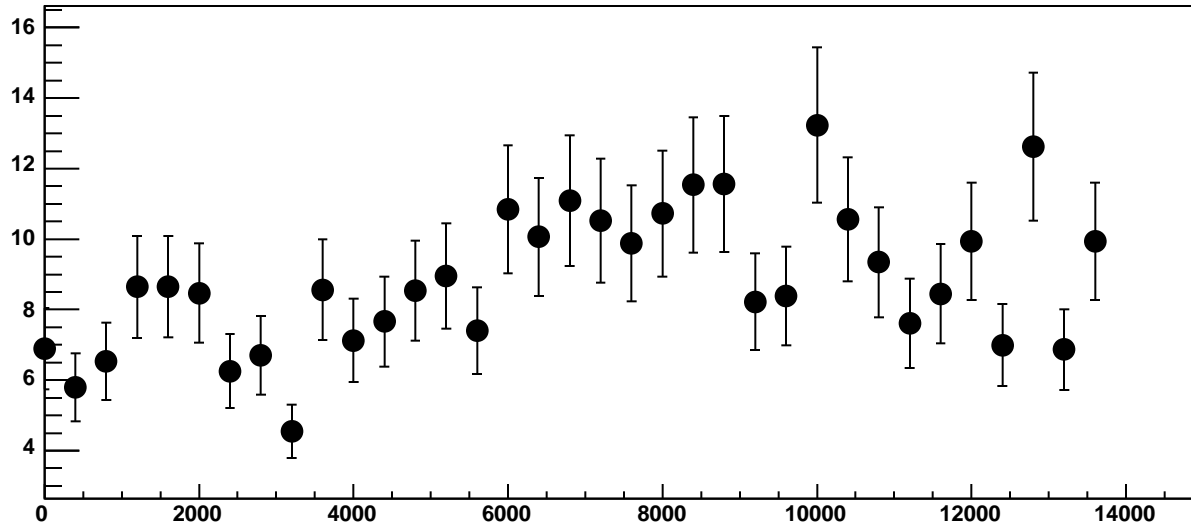
p0

10.24 ± 0.8102

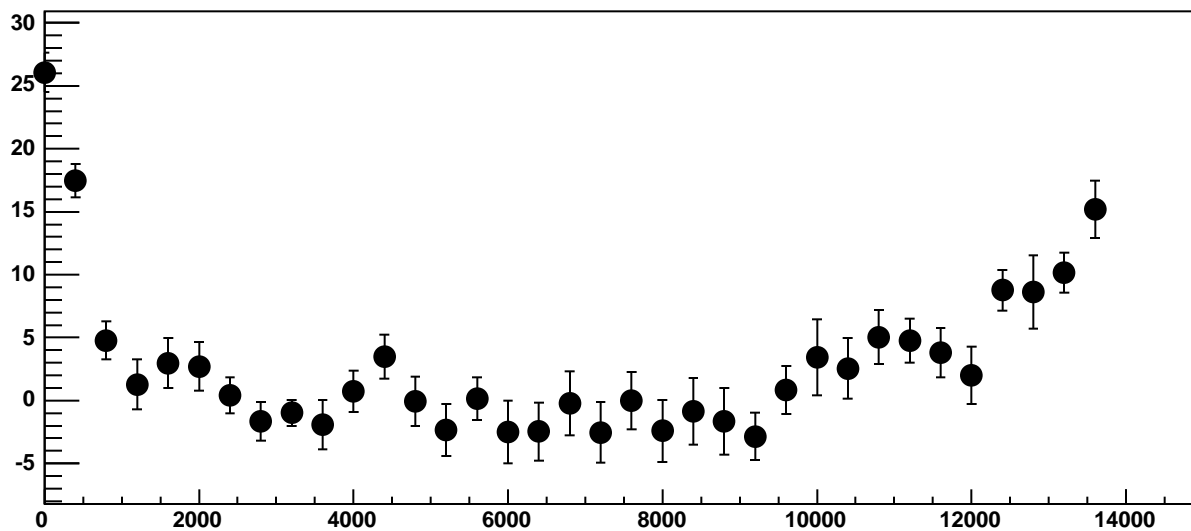
p1

0.02392 ± 0.0001383

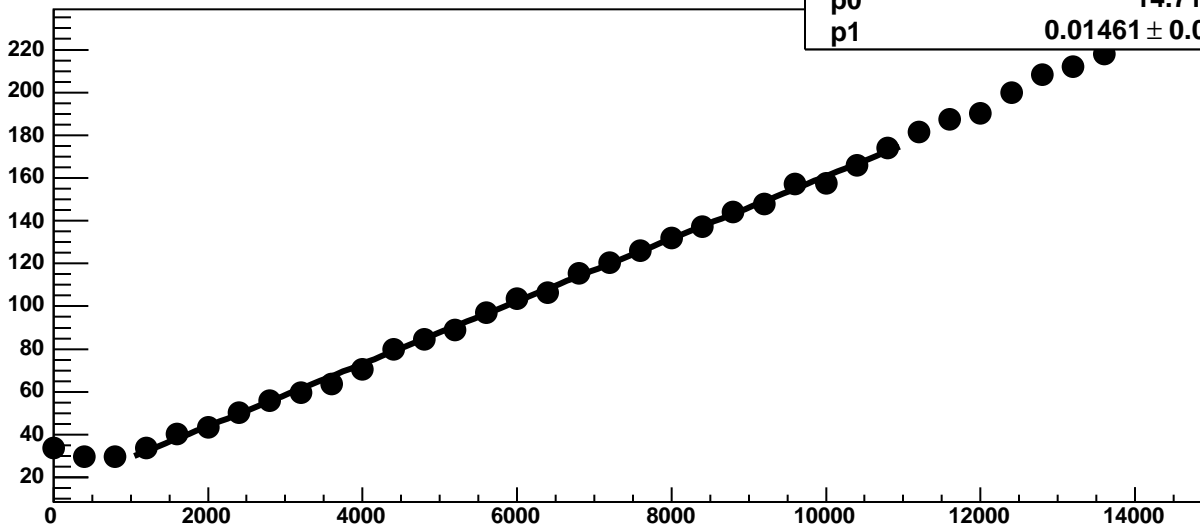
Chip 2, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

18.19 / 23

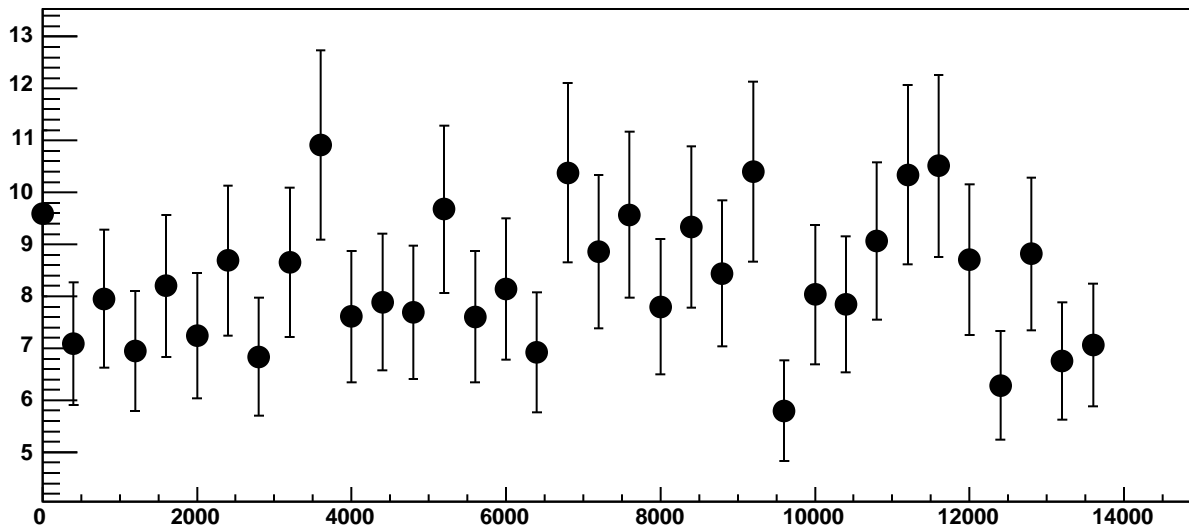
p0

14.71 ± 0.826

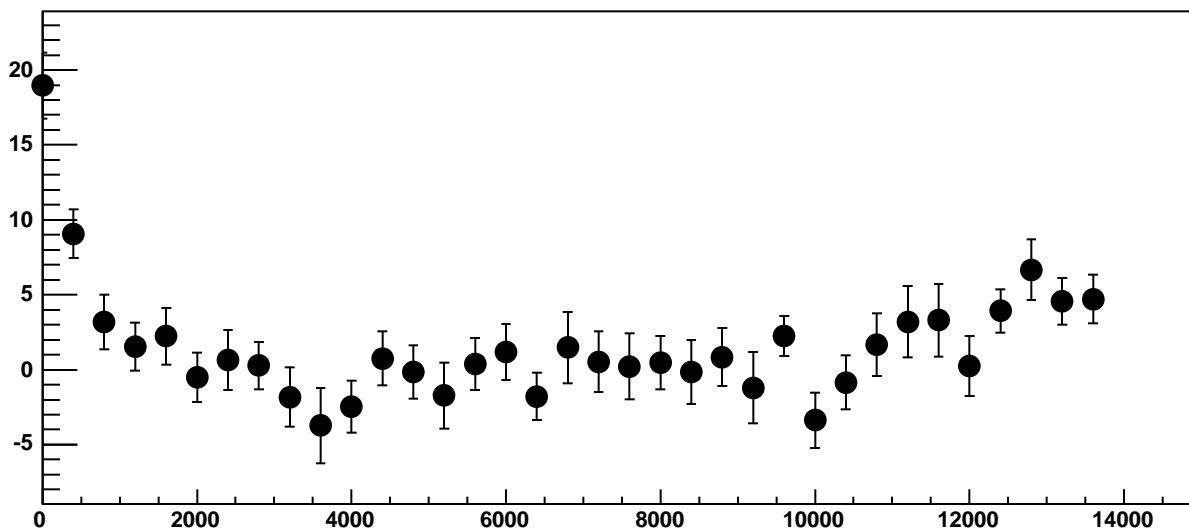
p1

0.01461 ± 0.0001249

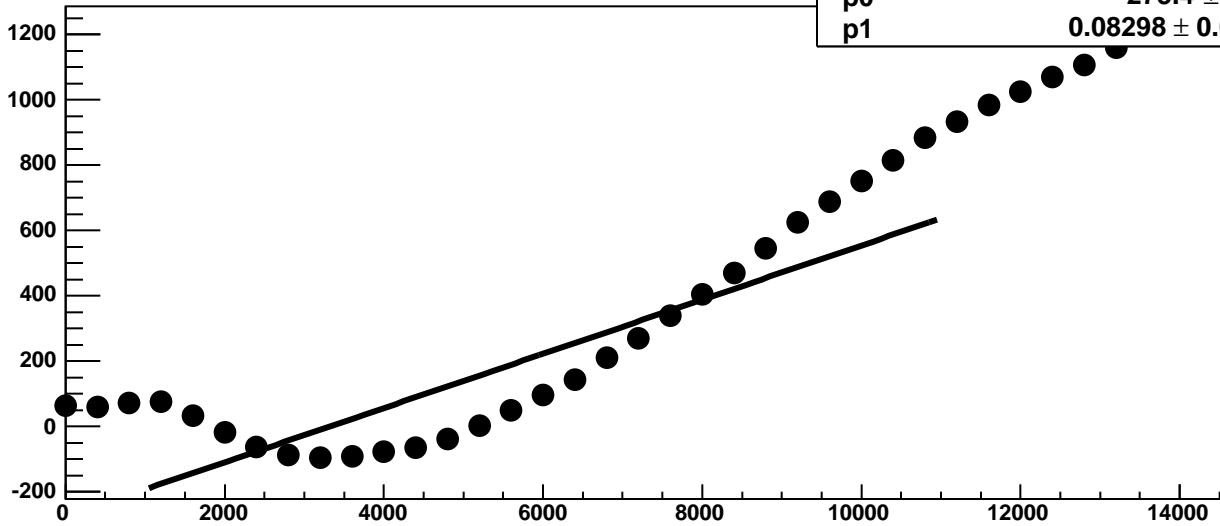
Chip 2, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

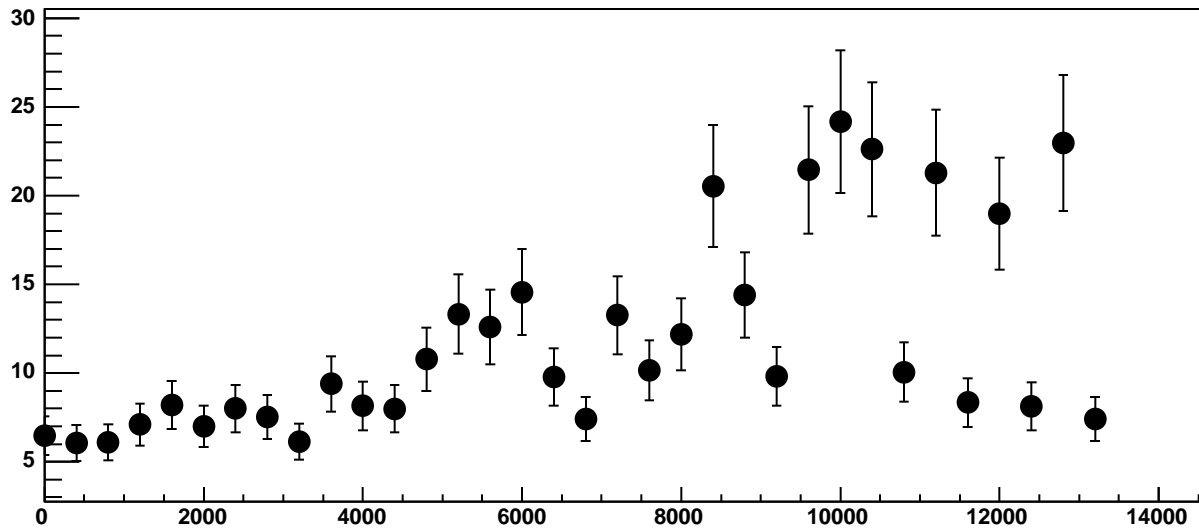


Chip 2, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

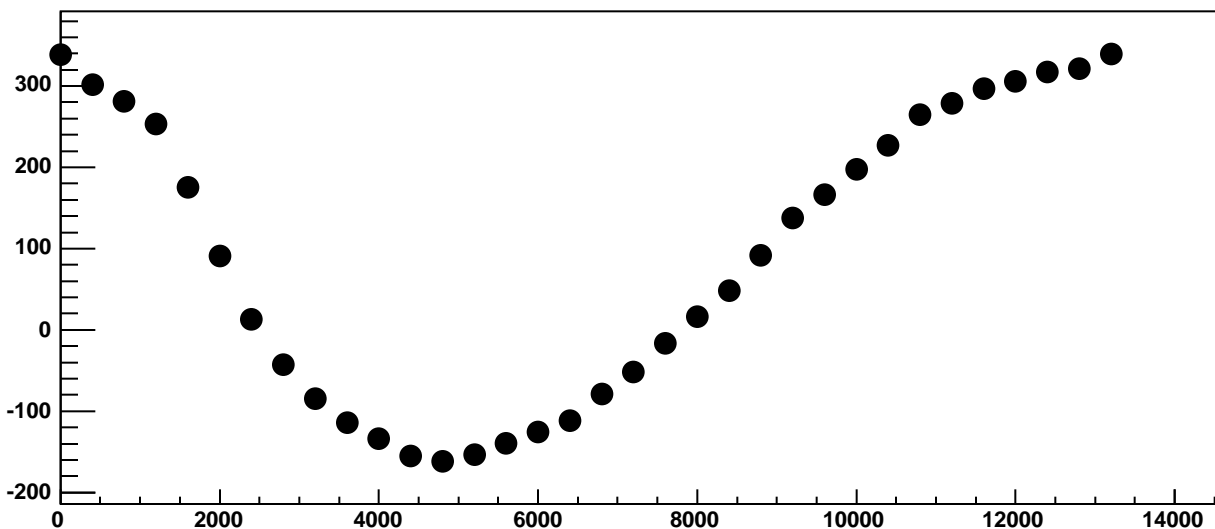


χ^2 / ndf 9.304e+04 / 23
p0 -275.4 ± 0.8909
p1 0.08298 ± 0.000165

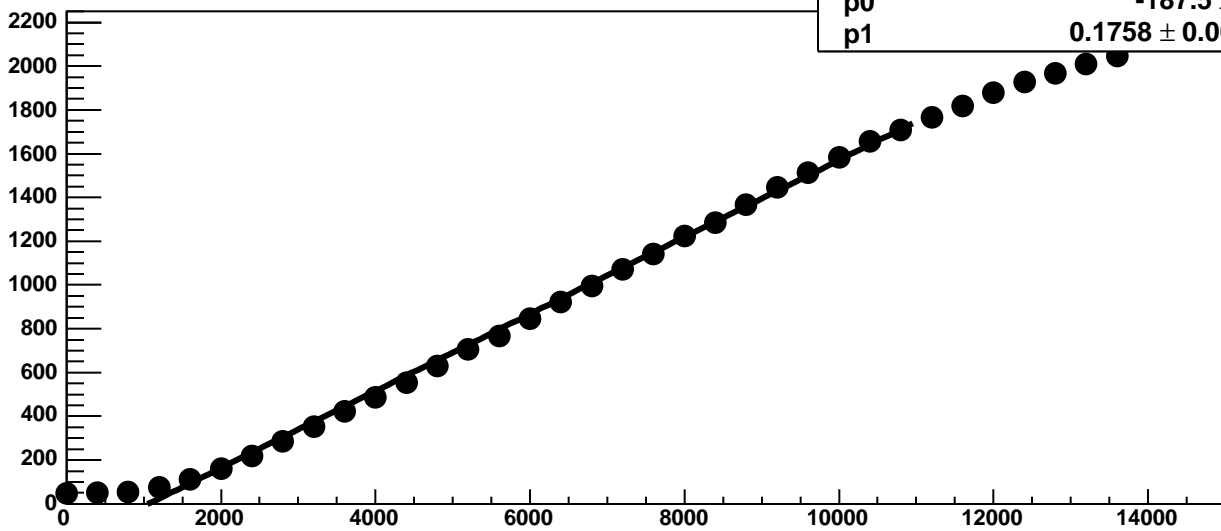
Chip 2, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

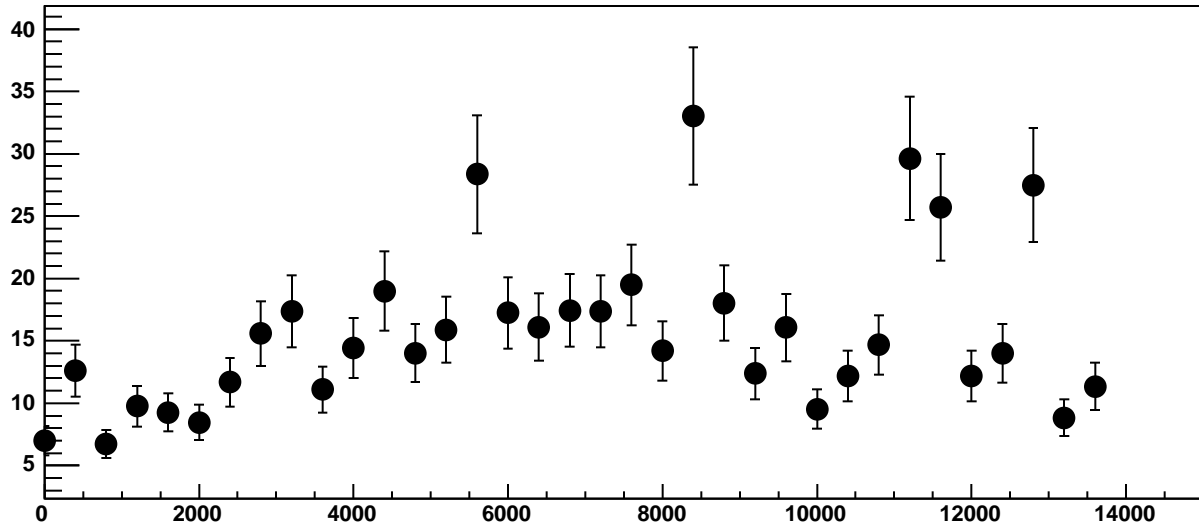


Chip 2, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

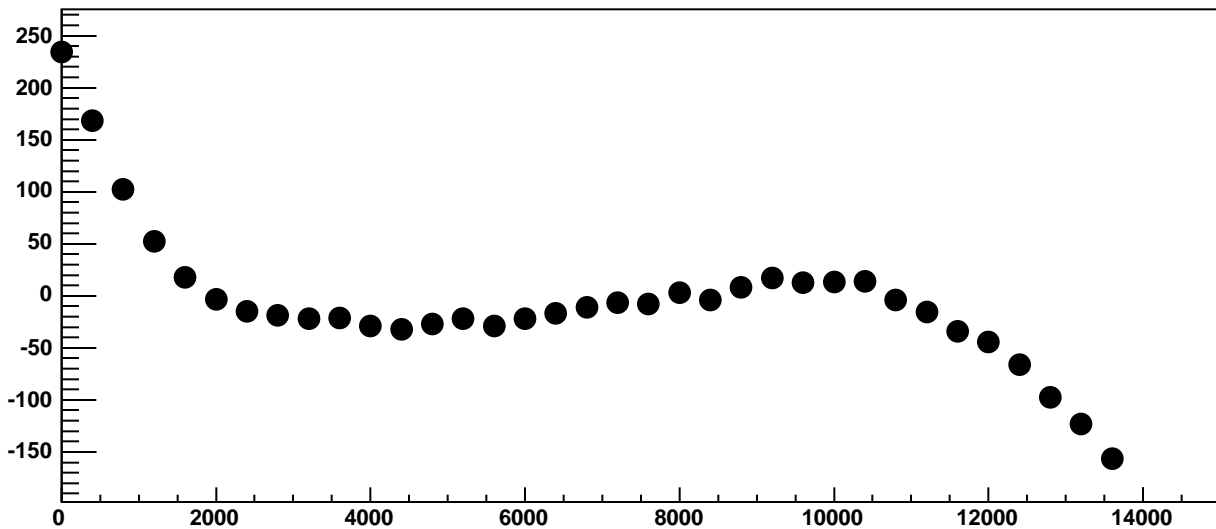


χ^2 / ndf 1225 / 23
p0 -187.5 ± 1.189
p1 0.1758 ± 0.0001884

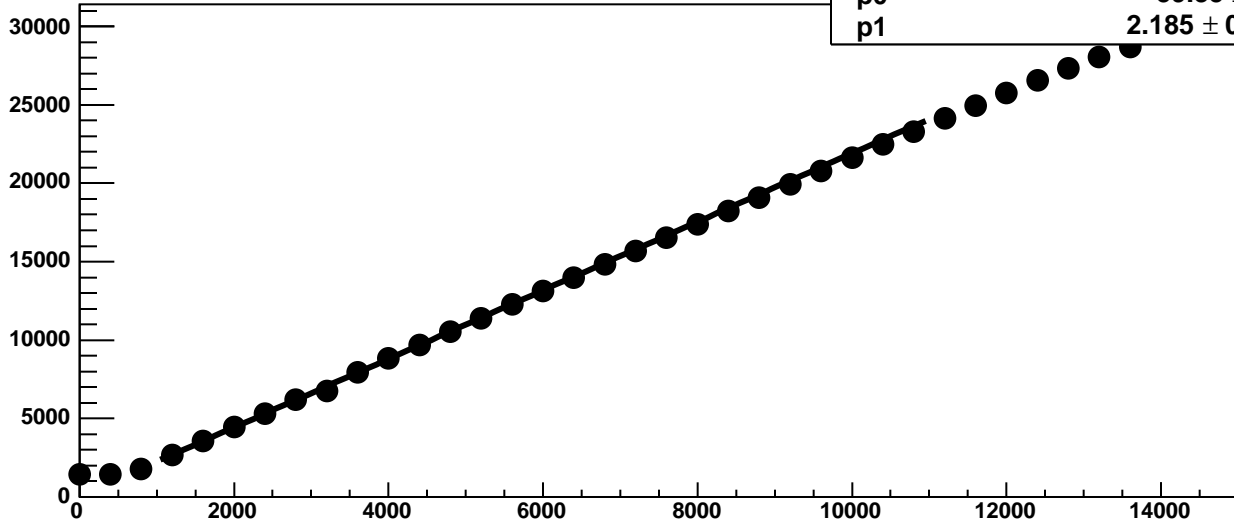
Chip 2, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

452.2 / 23

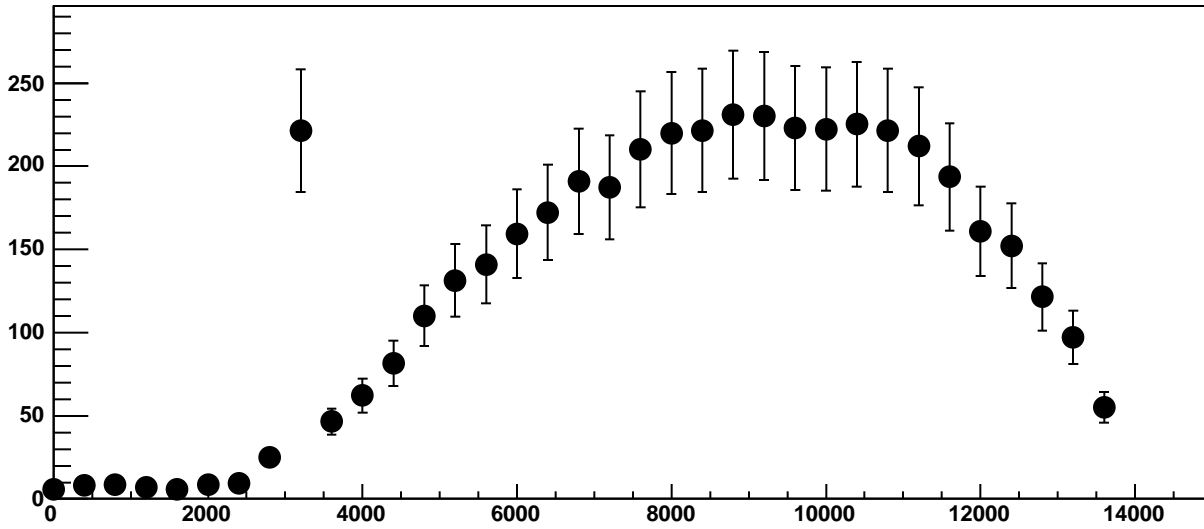
p0

59.35 ± 2.387

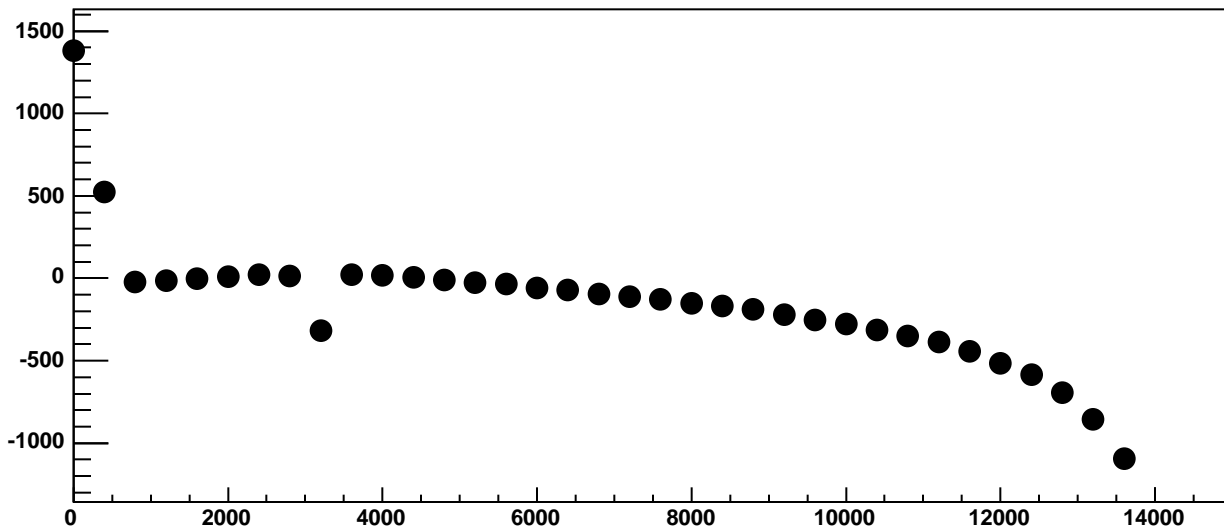
p1

2.185 ± 0.00126

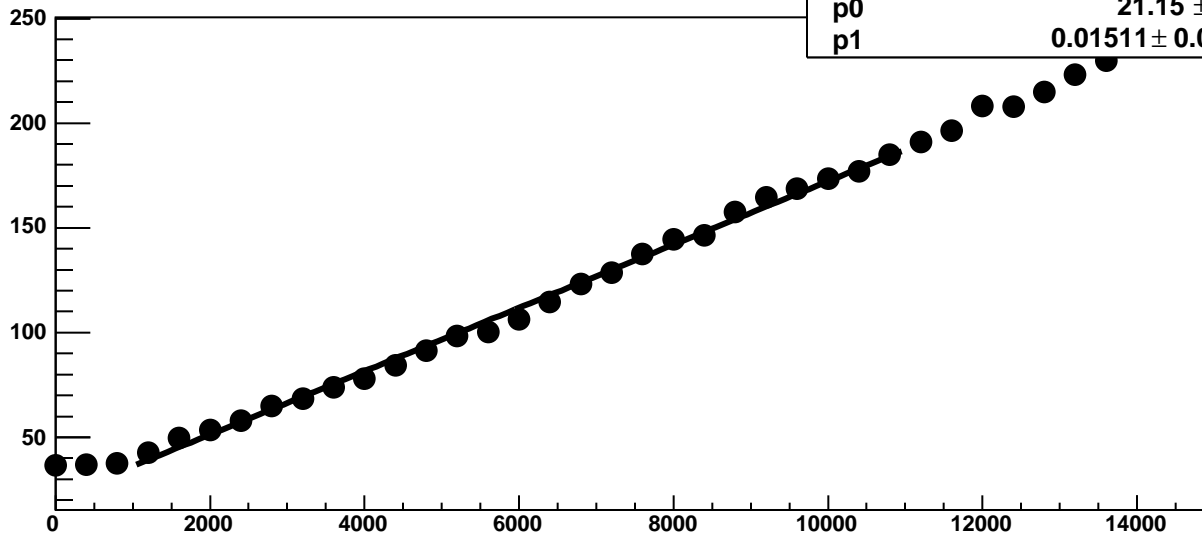
Chip 2, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC

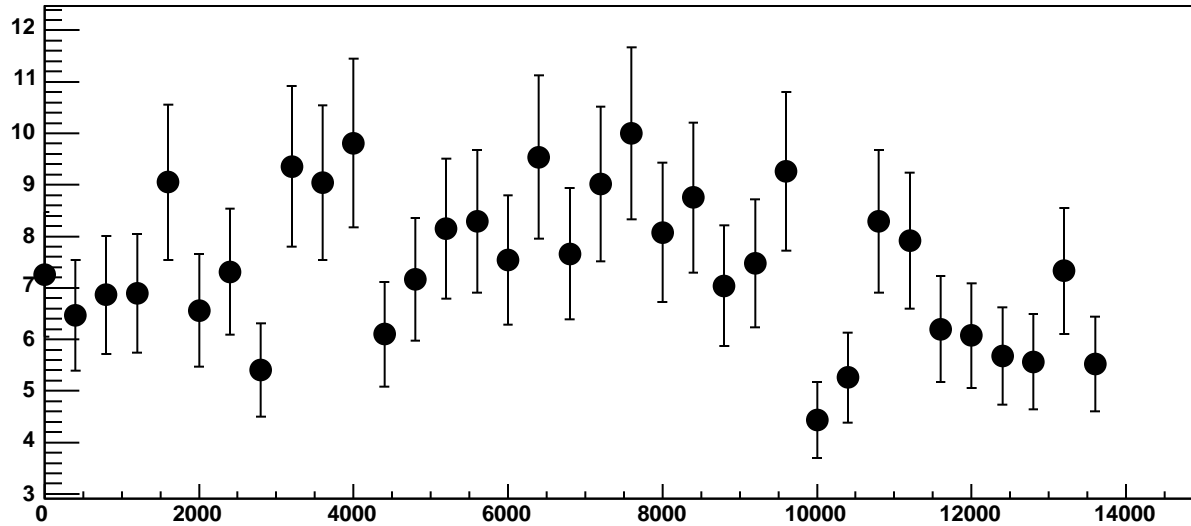


Chip 2, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

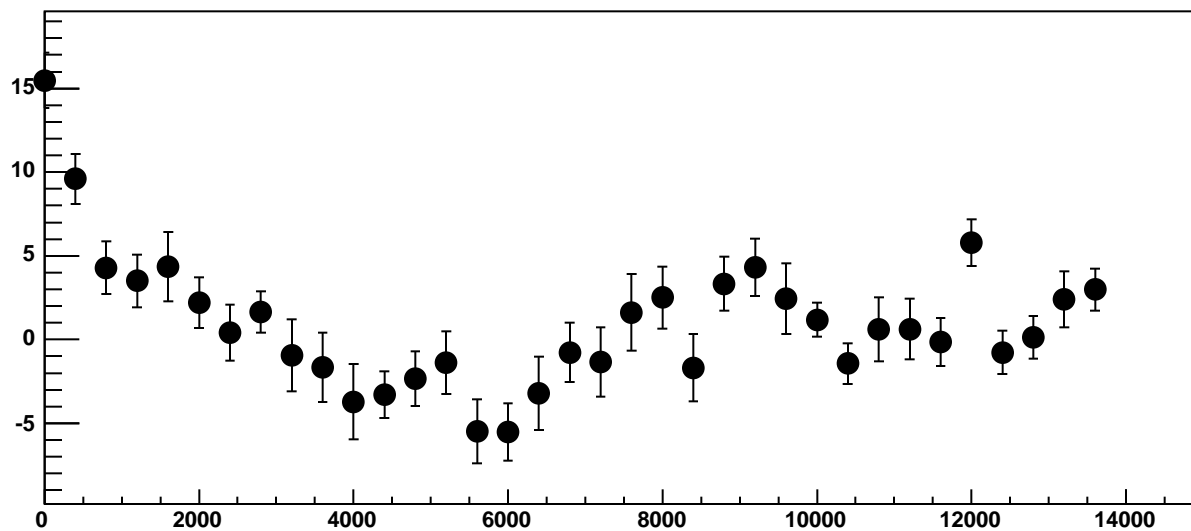


χ^2 / ndf 63.88 / 23
p0 21.15 ± 0.7526
p1 0.01511 ± 0.0001081

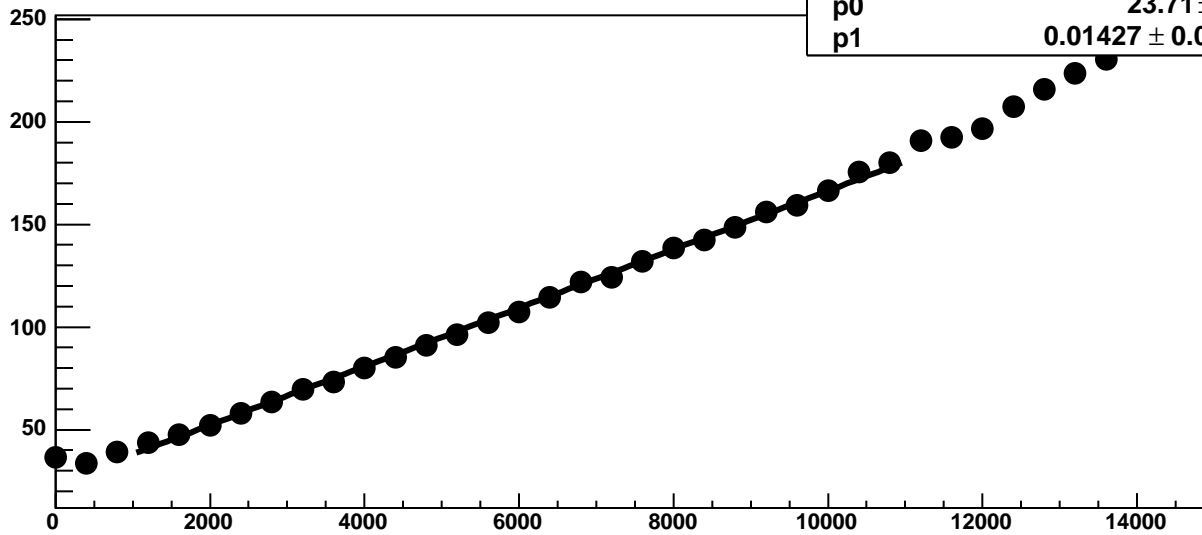
Chip 2, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

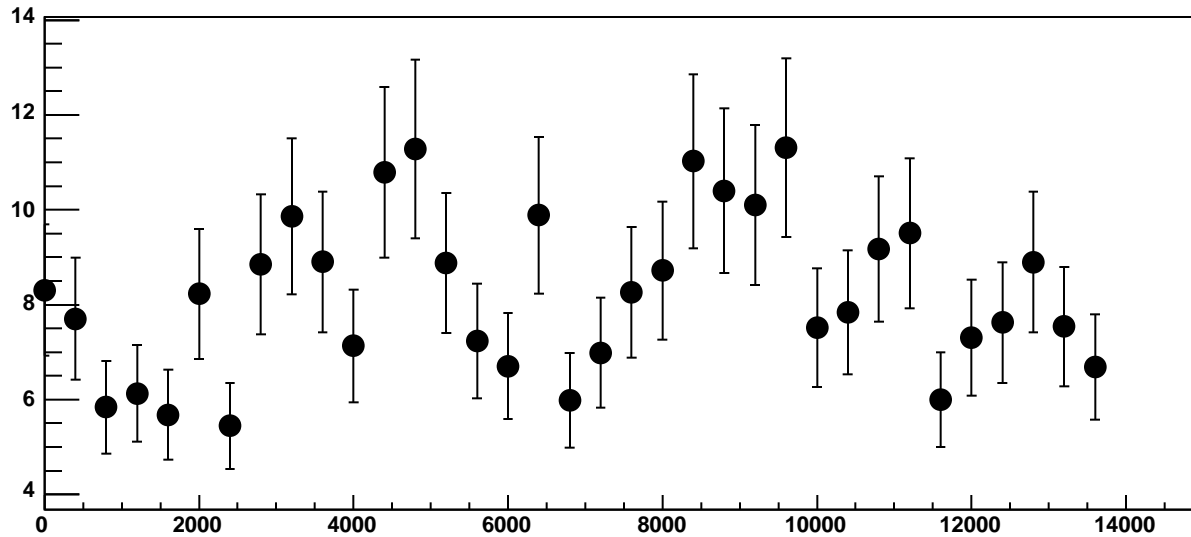


Chip 2, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

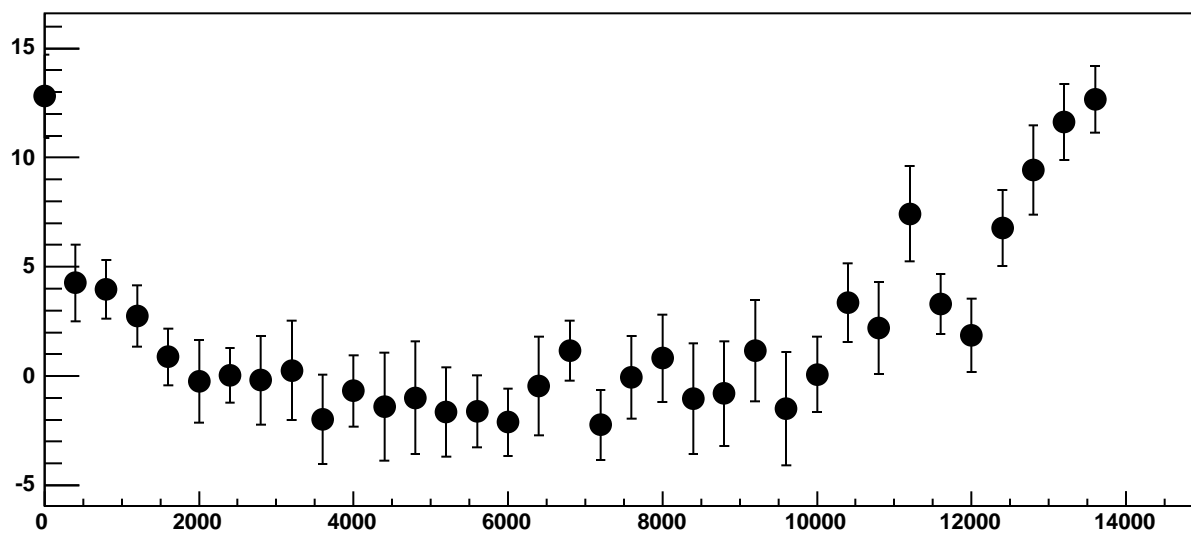


χ^2 / ndf 17.68 / 23
p0 23.71 ± 0.7601
p1 0.01427 ± 0.0001225

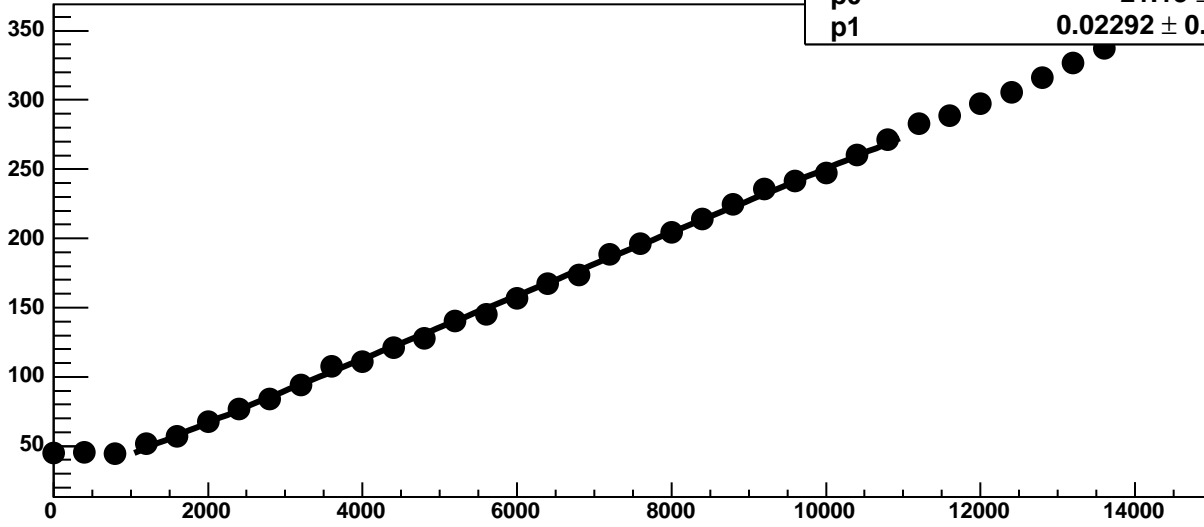
Chip 2, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

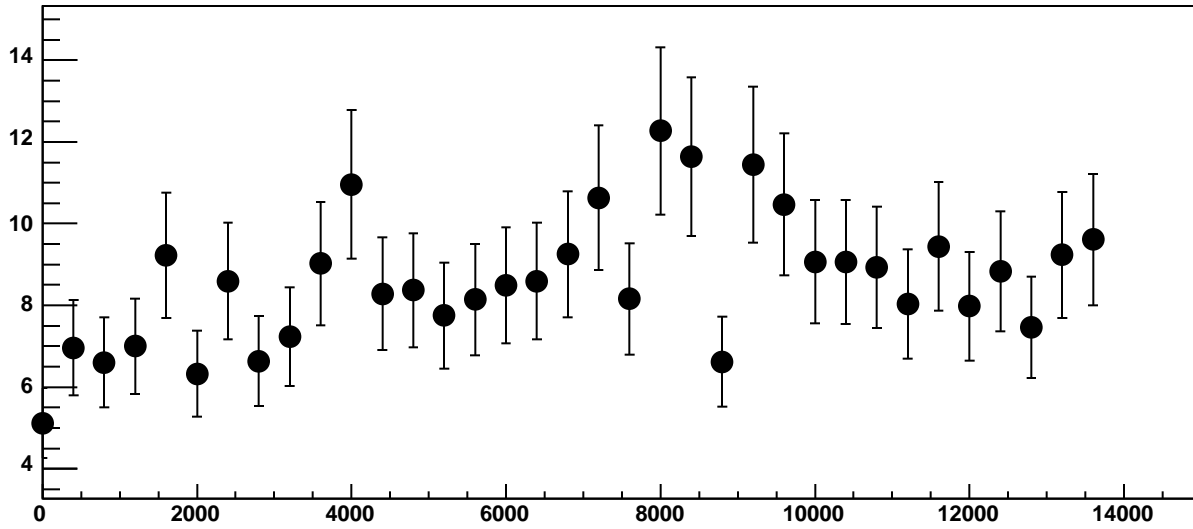


Chip 2, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

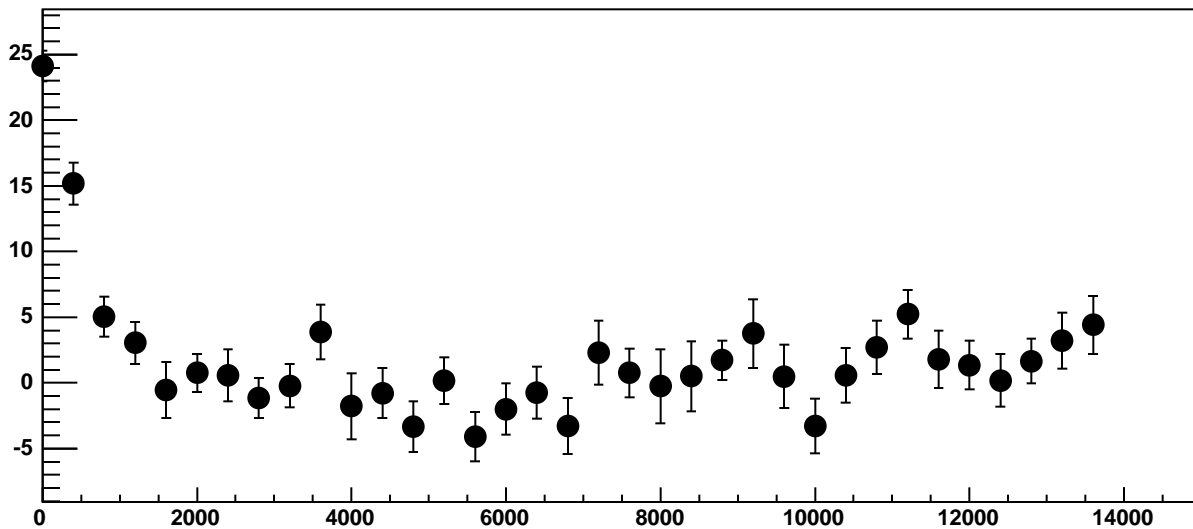


χ^2 / ndf 28.89 / 23
p0 21.13 ± 0.8311
p1 0.02292 ± 0.000133

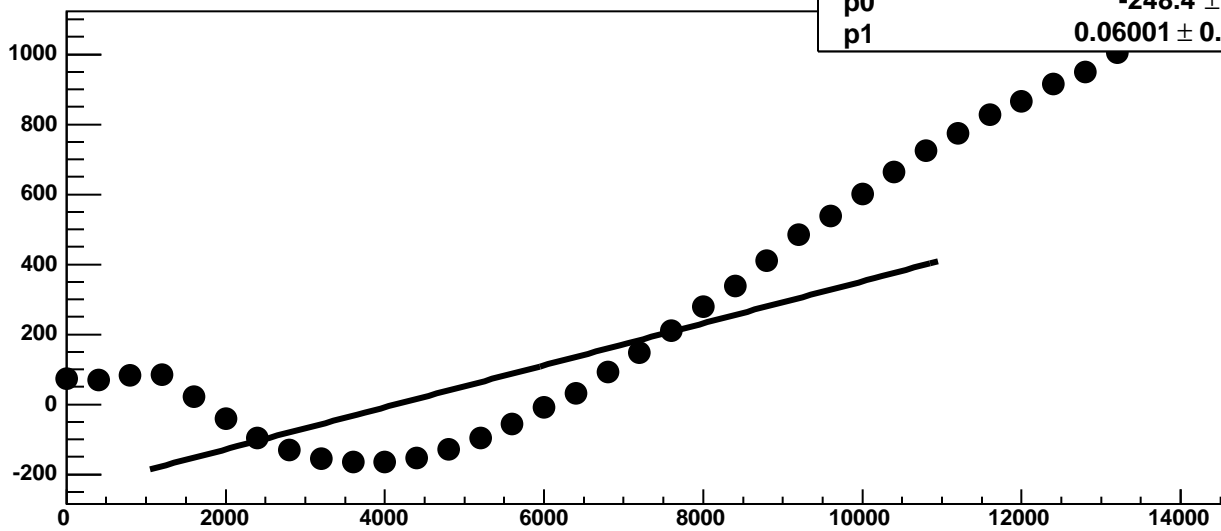
Chip 2, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



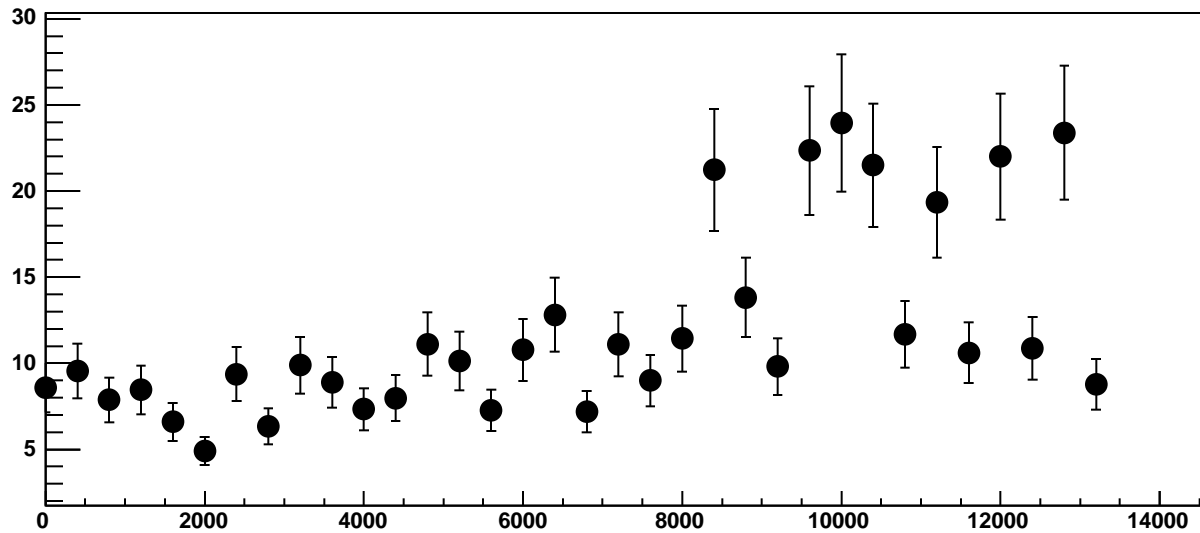
Chip 2, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC



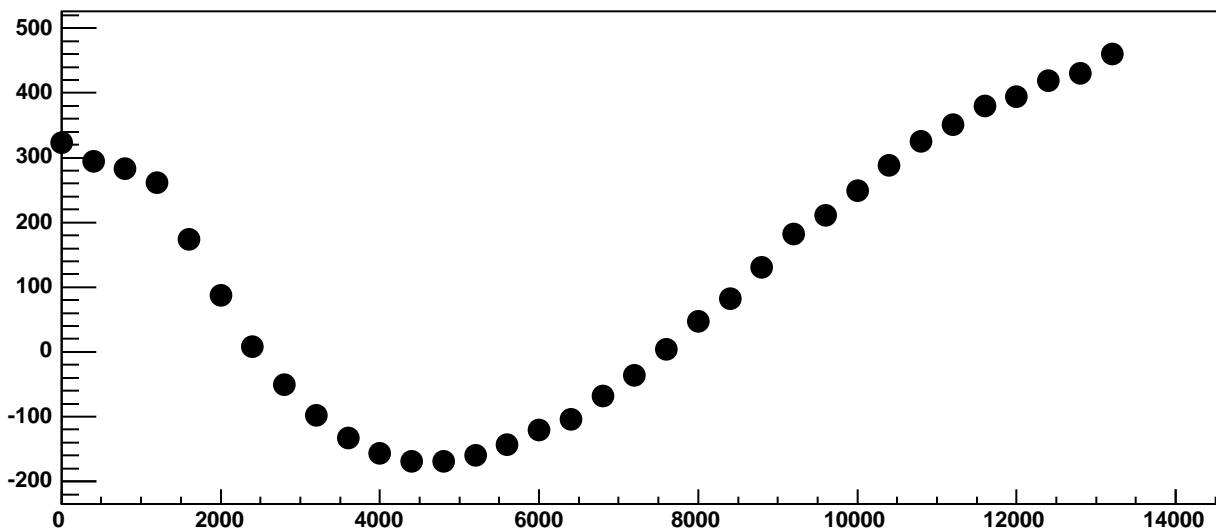
Chip 2, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



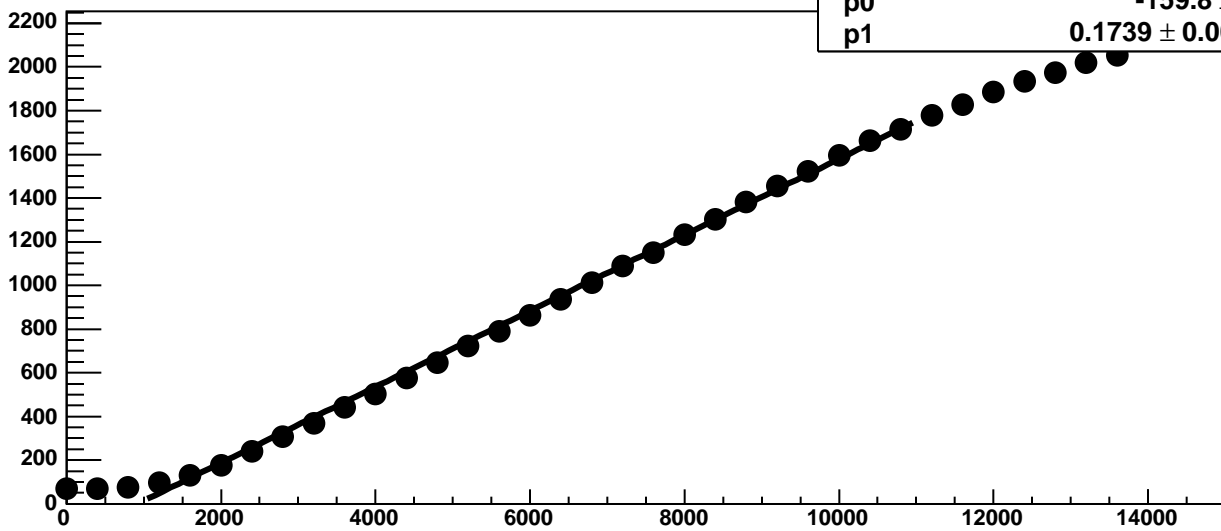
Chip 2, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

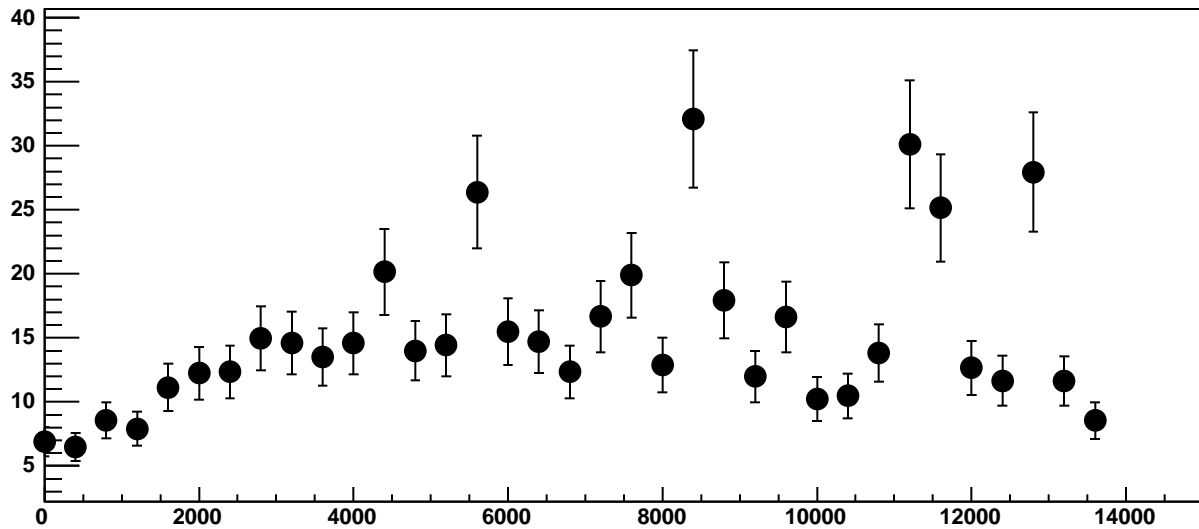


Chip 2, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

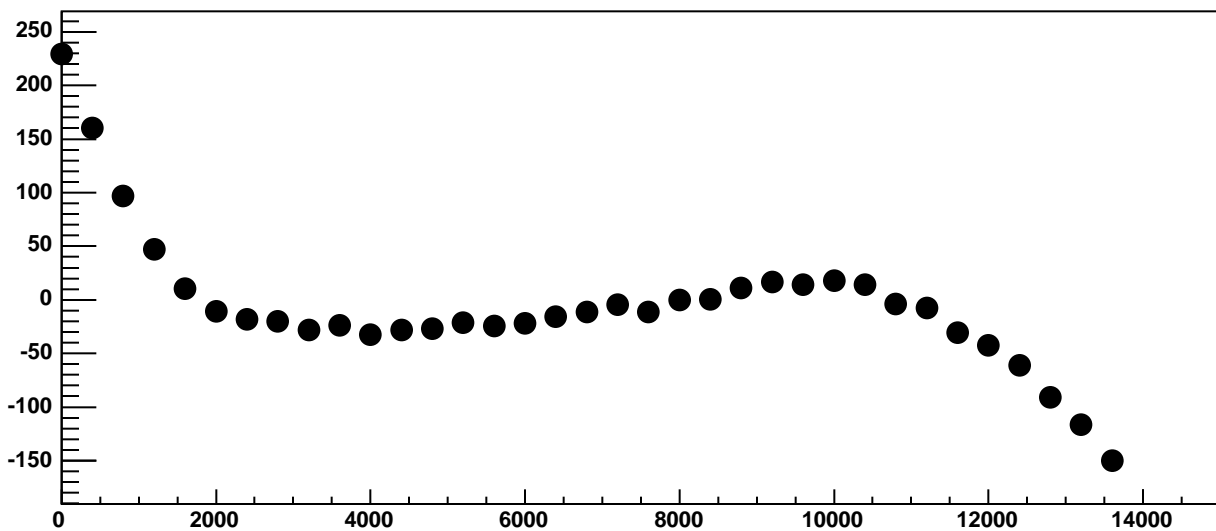


χ^2 / ndf 1410 / 23
p0 -159.8 ± 1.227
p1 0.1739 ± 0.0001876

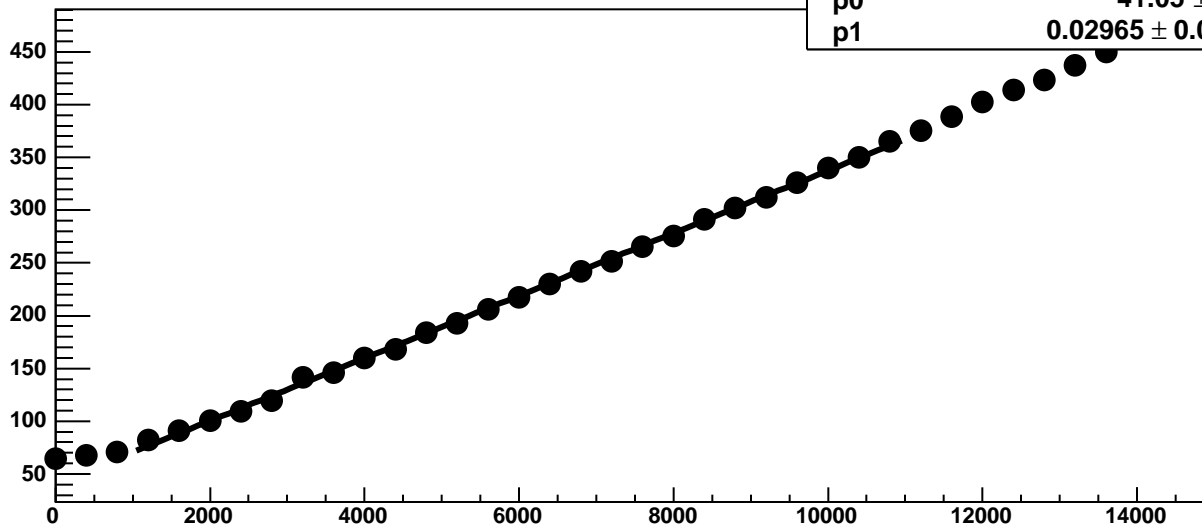
Chip 2, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

62.76 / 23

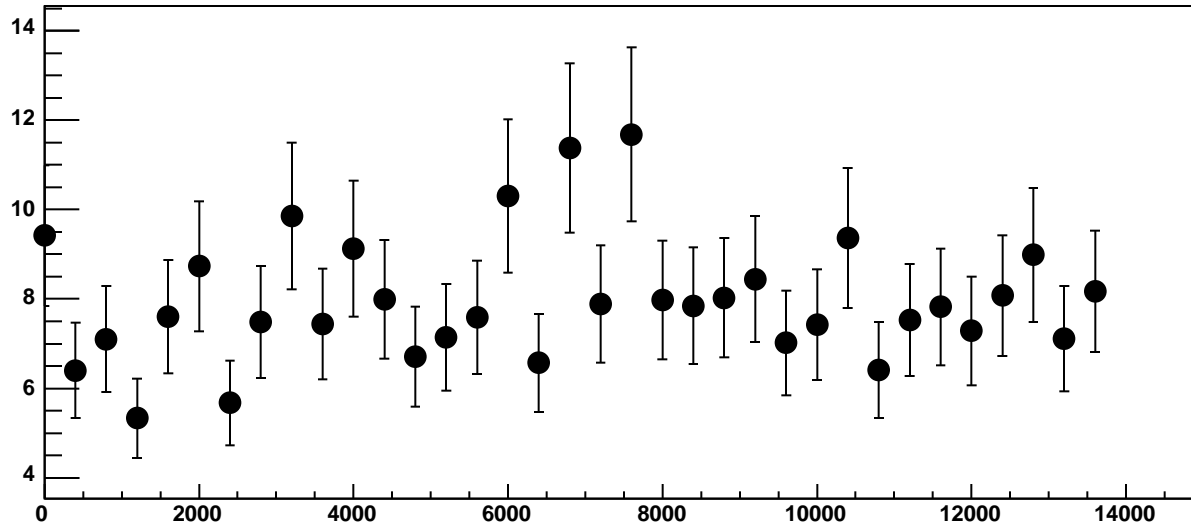
p0

41.05 ± 0.7417

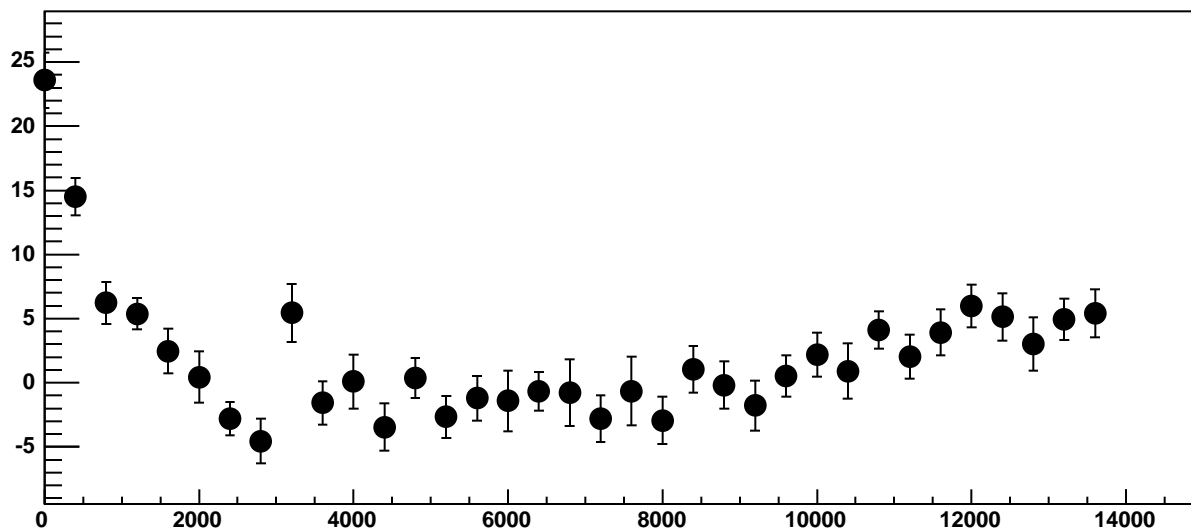
p1

0.02965 ± 0.0001142

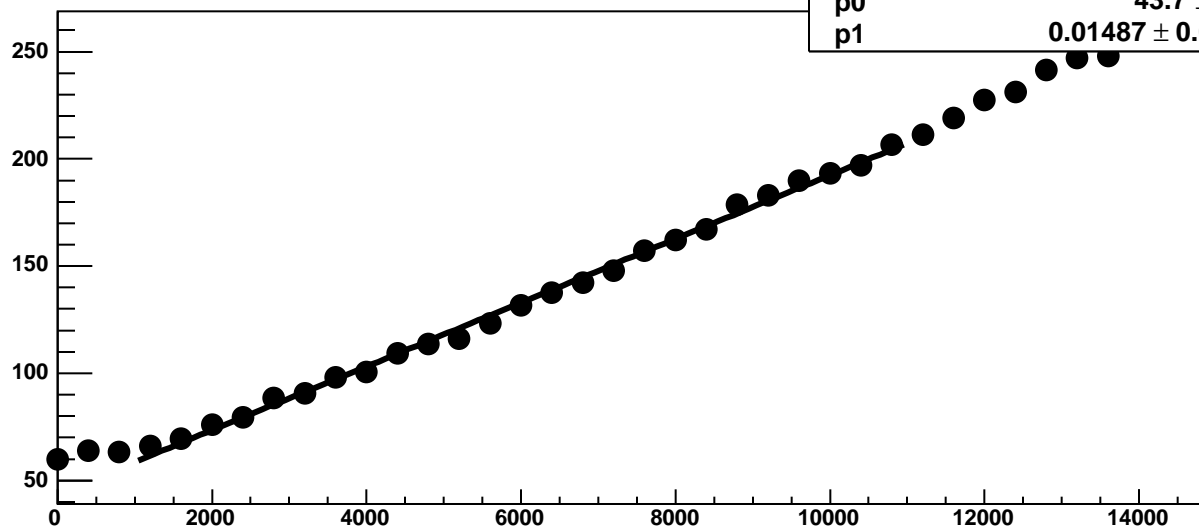
Chip 2, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC

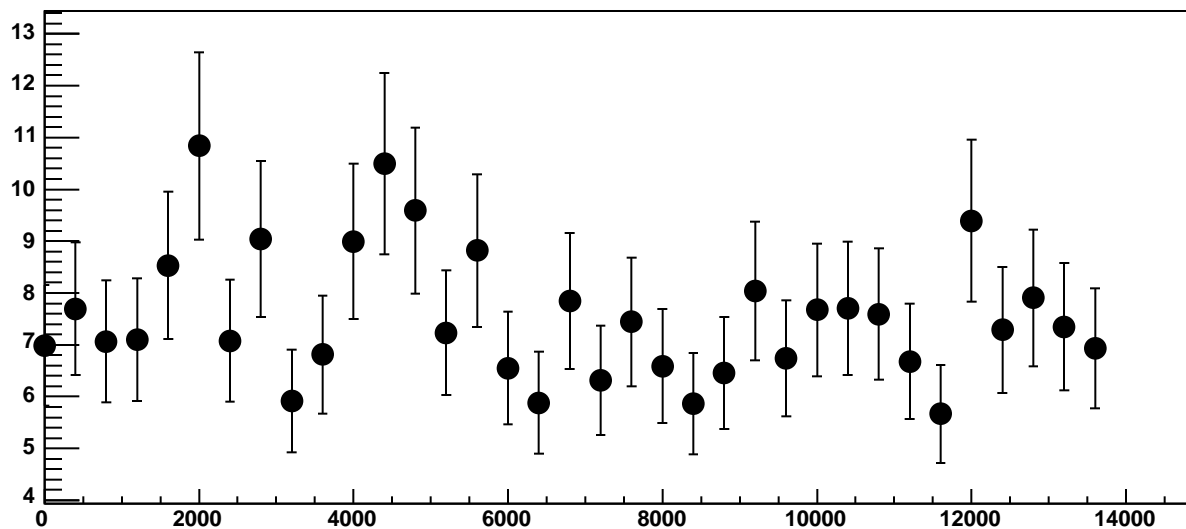


Chip 2, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

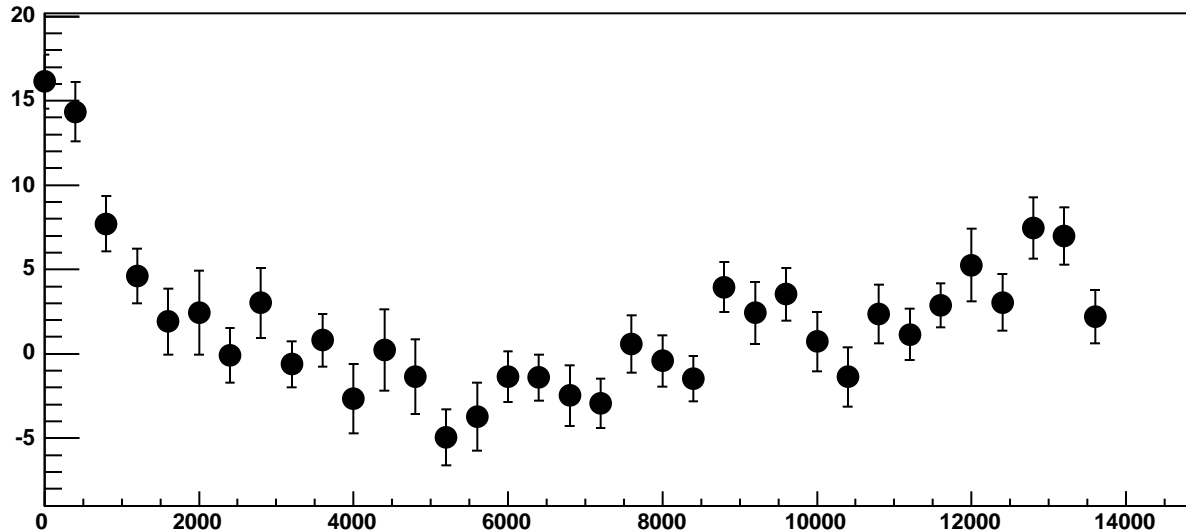


χ^2 / ndf 52.73 / 23
p0 43.7 ± 0.8222
p1 0.01487 ± 0.0001201

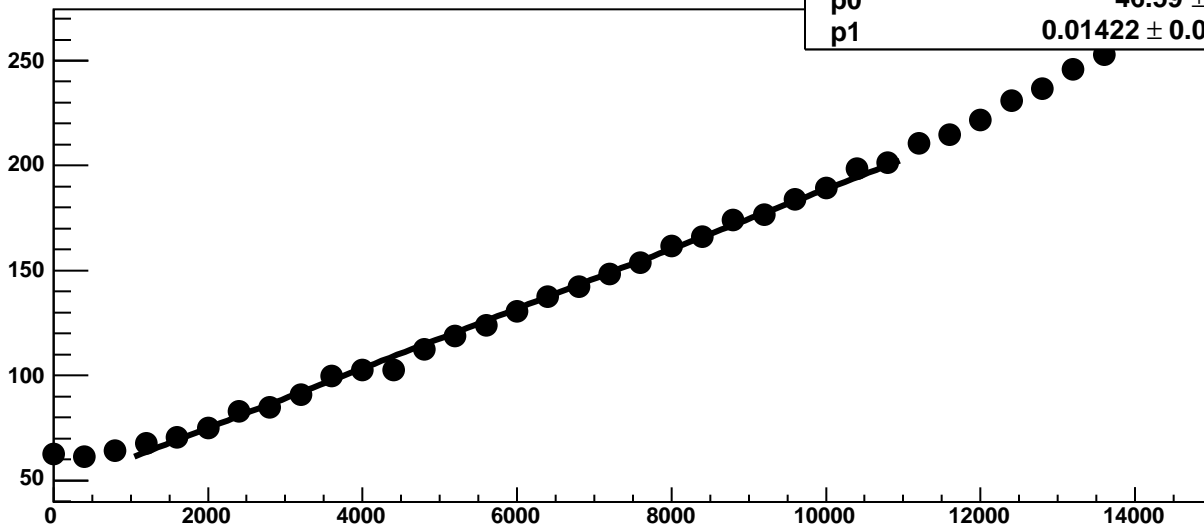
Chip 2, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

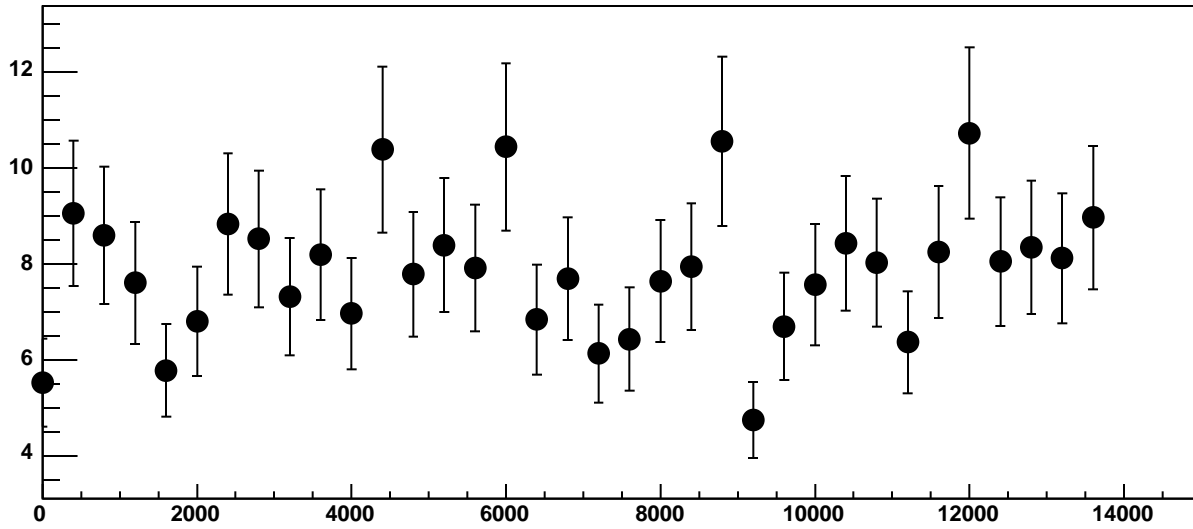


Chip 2, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

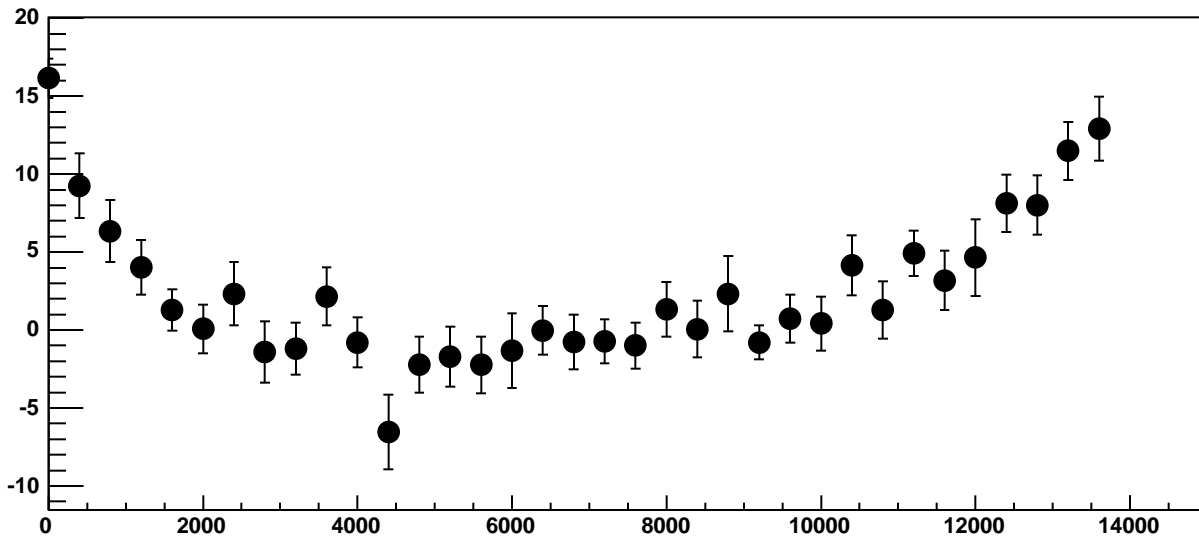


χ^2 / ndf 30.11 / 23
p0 46.59 ± 0.7778
p1 0.01422 ± 0.0001148

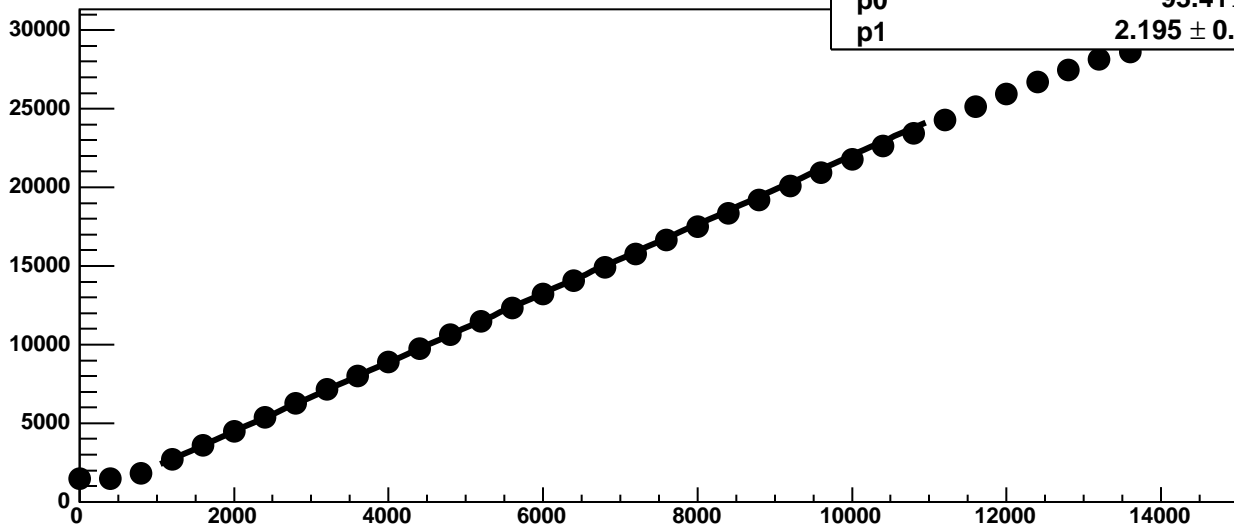
Chip 2, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

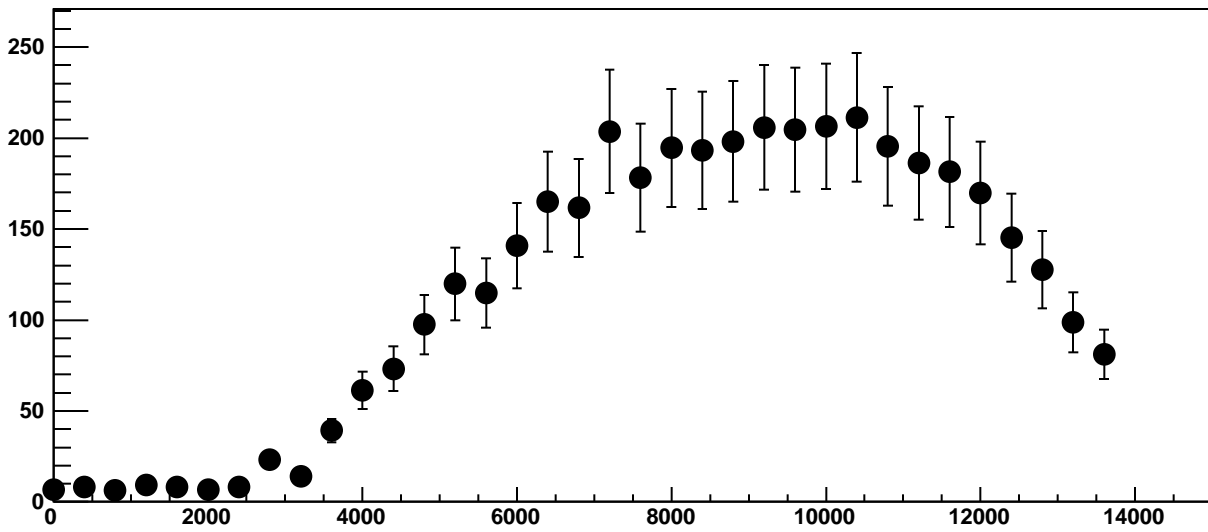


Chip 2, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC

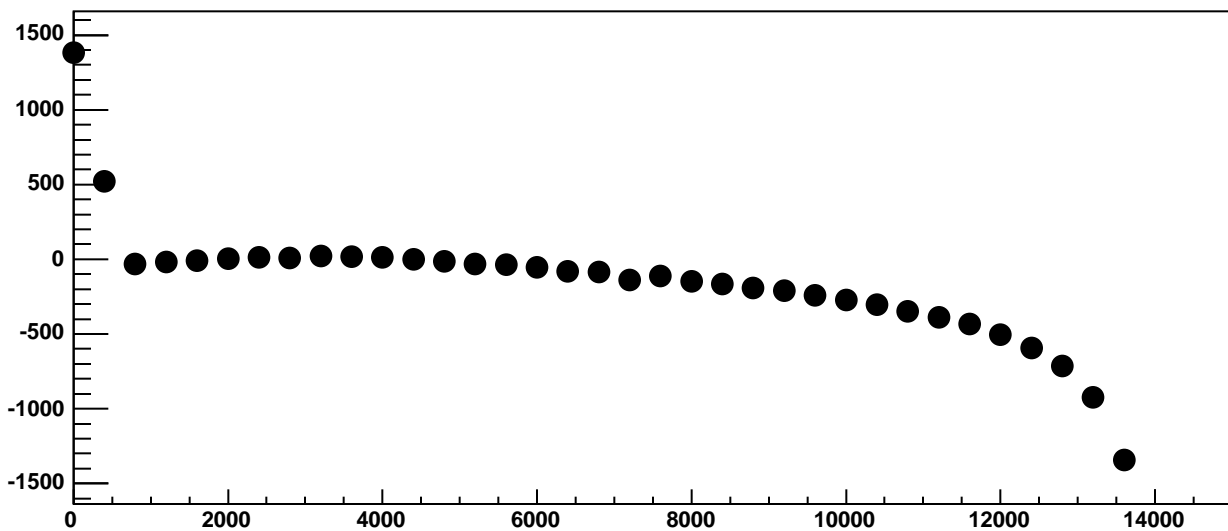


χ^2 / ndf 438.9 / 23
p0 93.41 ± 2.464
p1 2.195 ± 0.001126

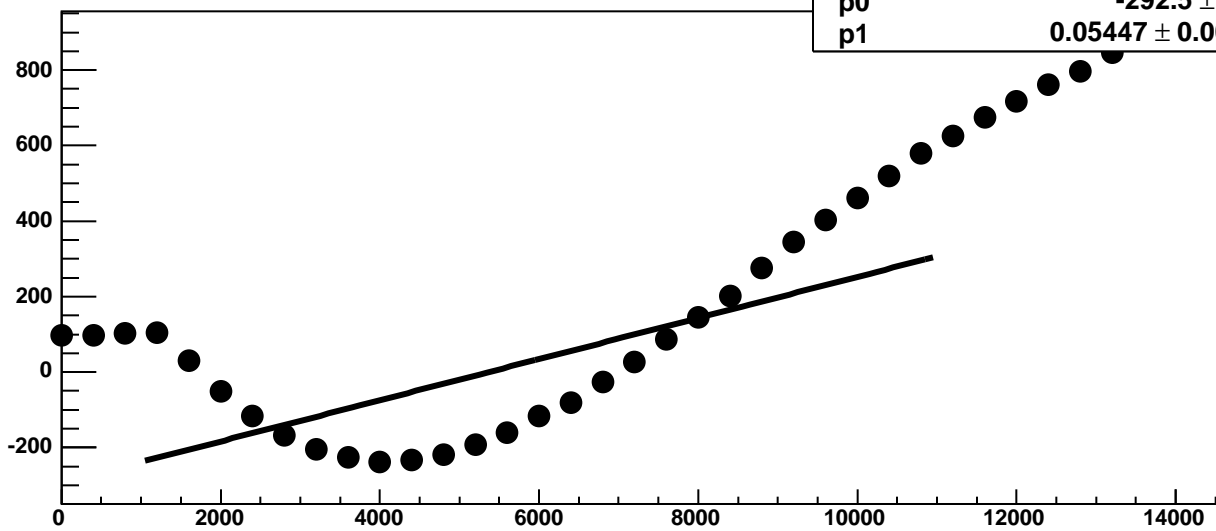
Chip 2, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



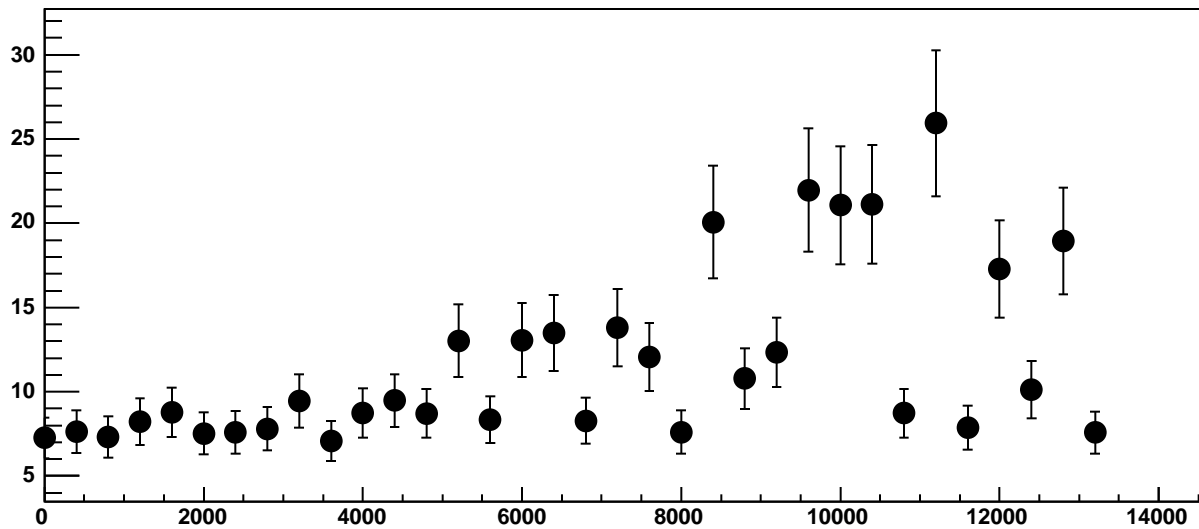
Chip 2, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC



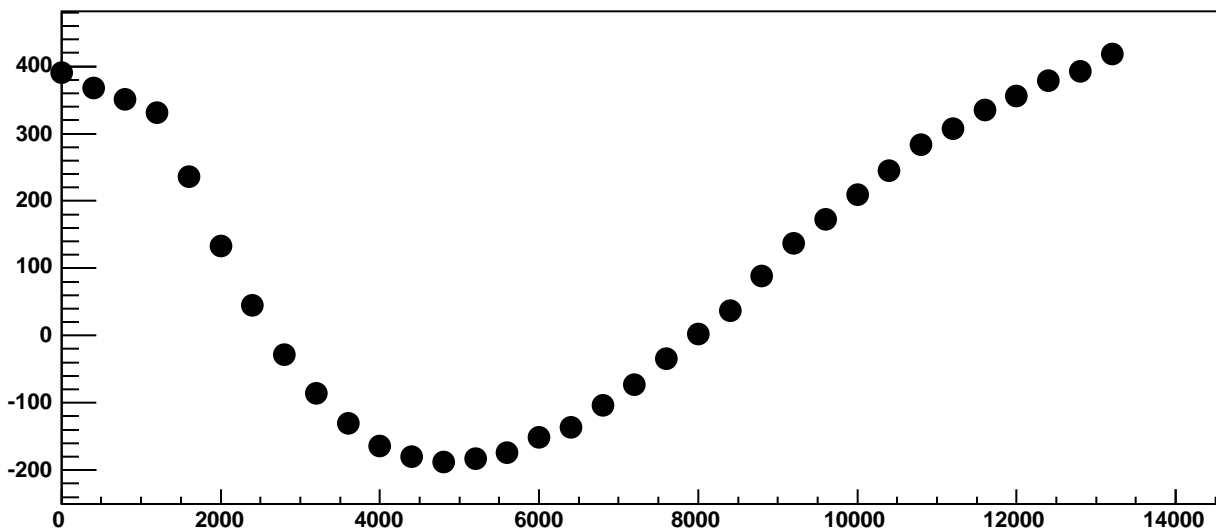
Chip 2, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



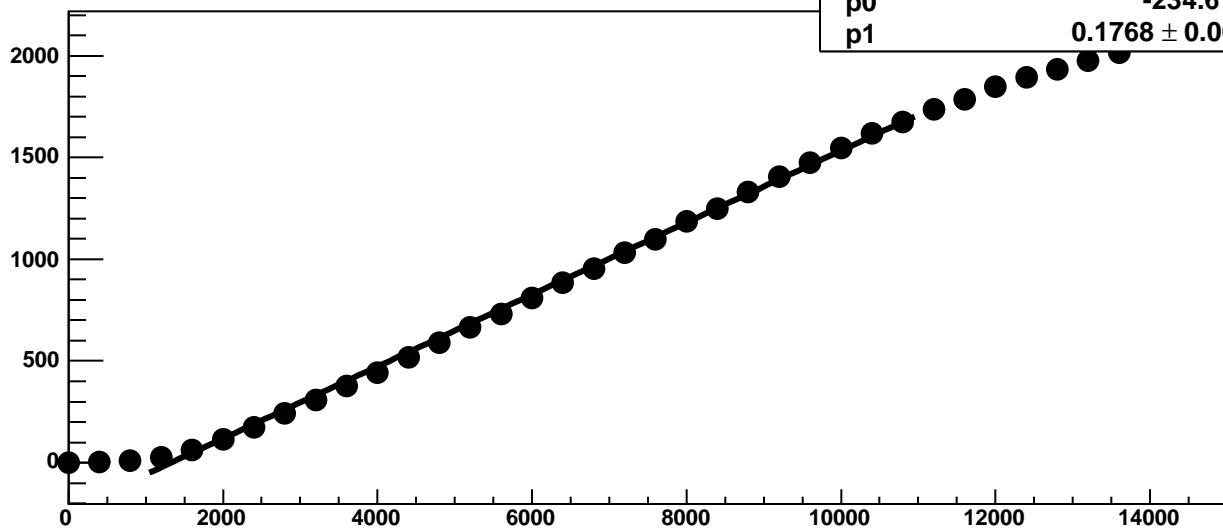
Chip 2, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

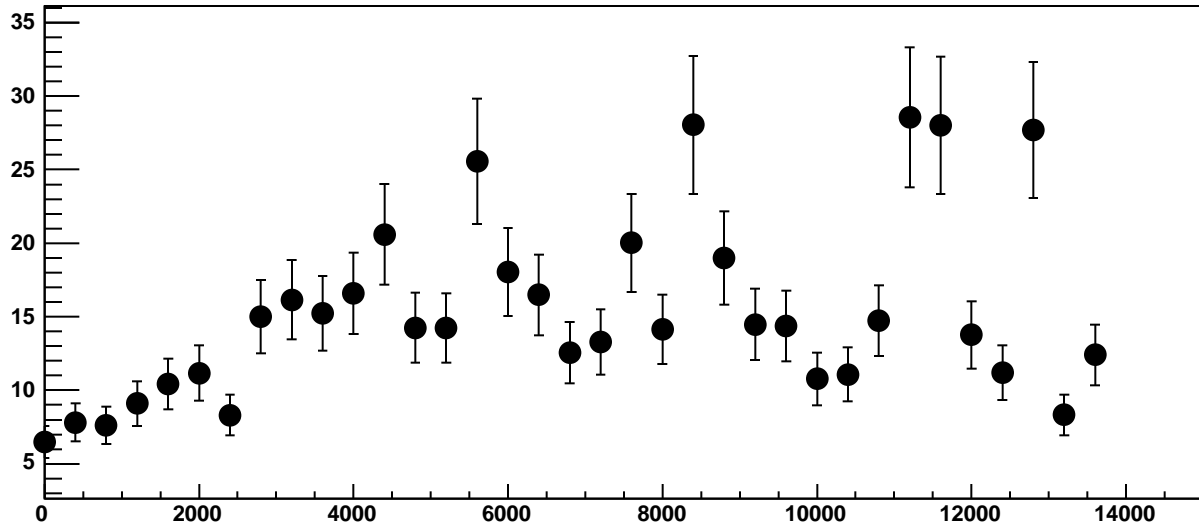


Chip 2, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

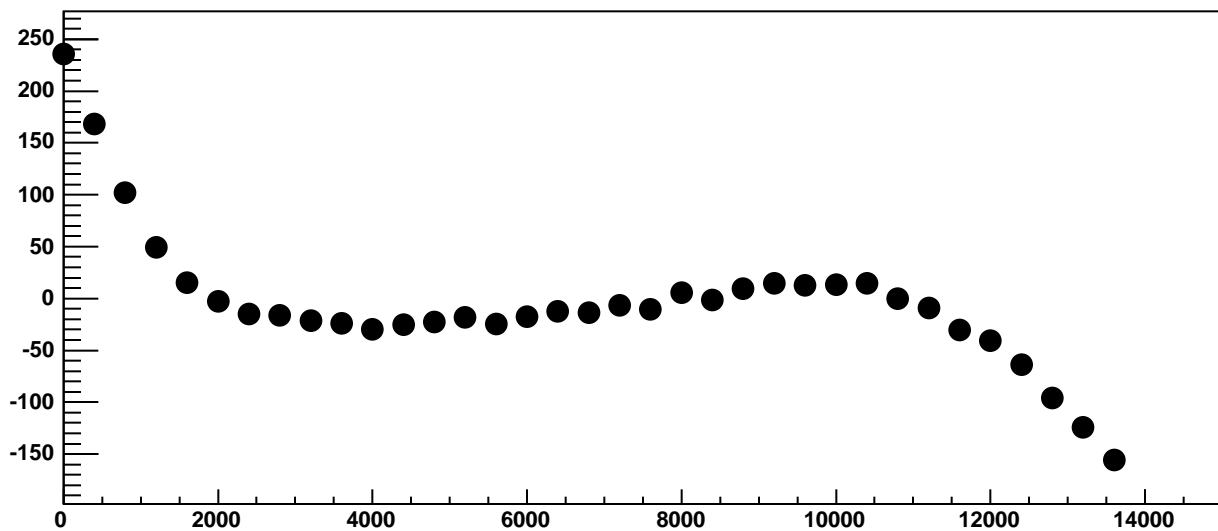


χ^2 / ndf 1118 / 23
p0 -234.6 ± 1.211
p1 0.1768 ± 0.0001906

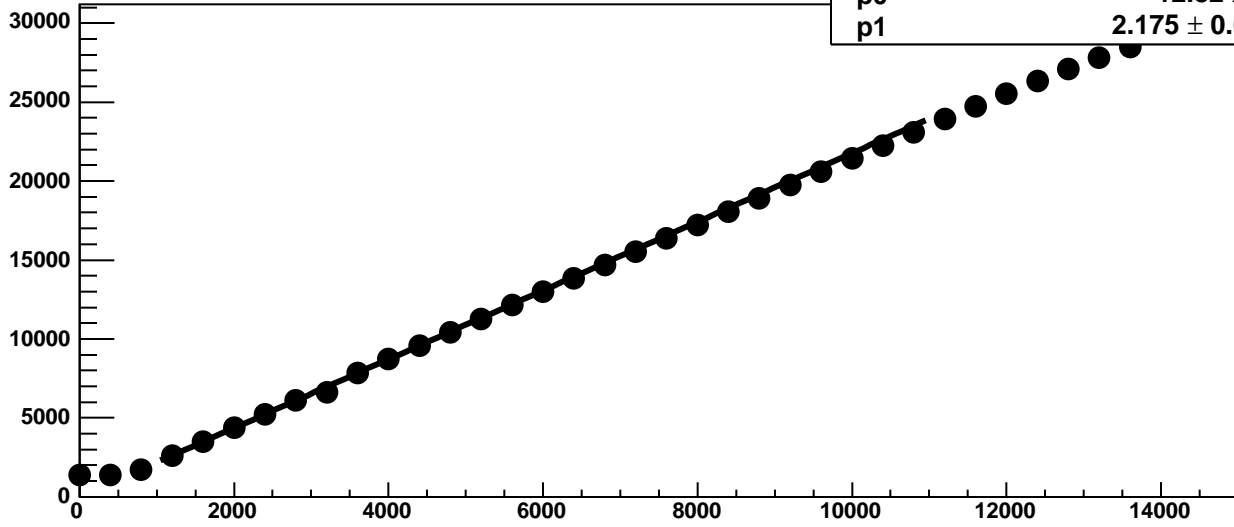
Chip 2, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

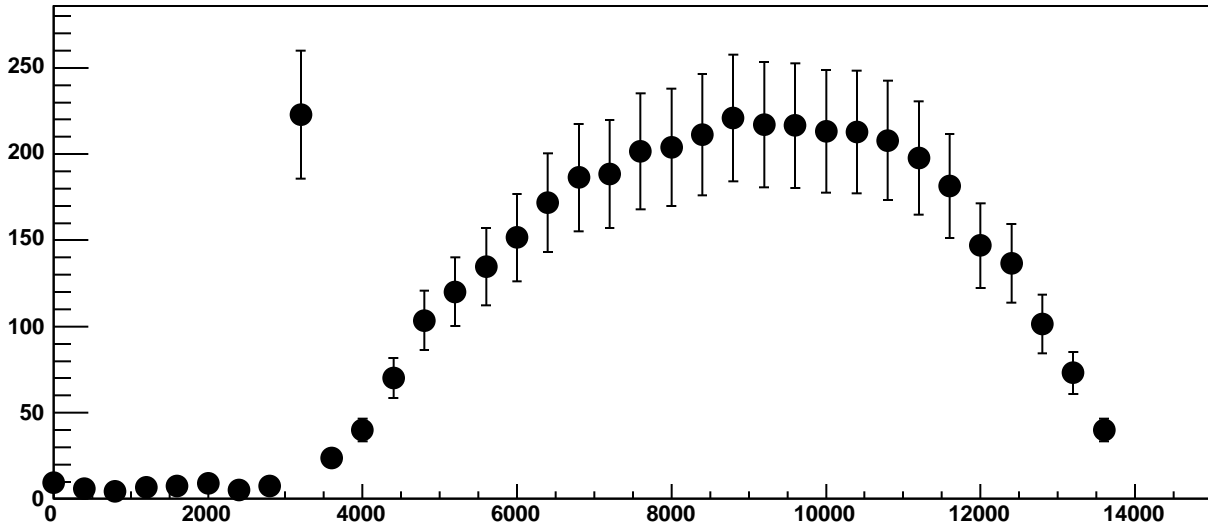


Chip 2, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC

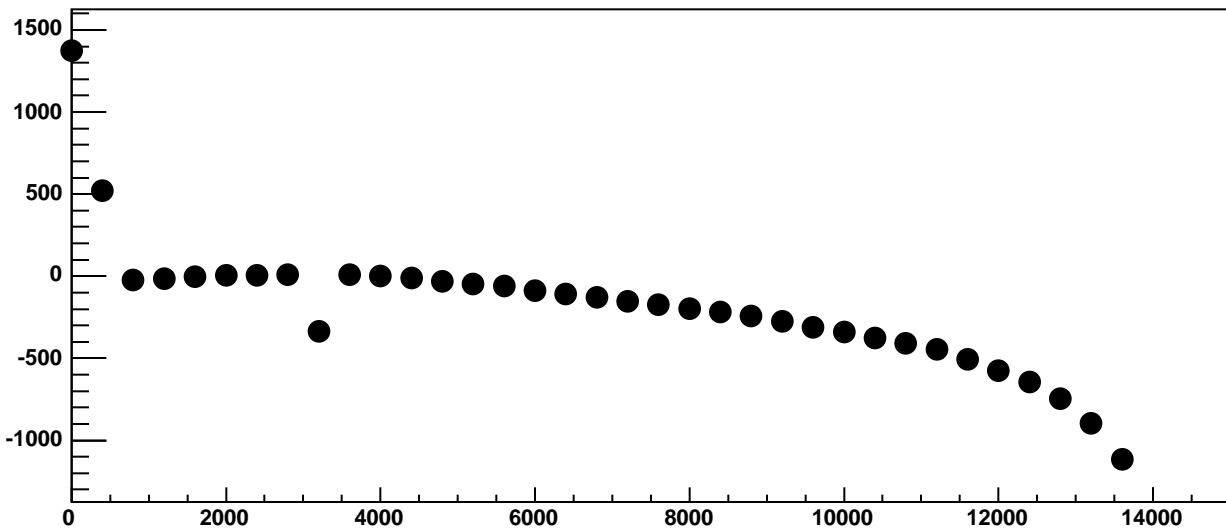


χ^2 / ndf 571.3 / 23
p0 12.82 ± 2.133
p1 2.175 ± 0.000962

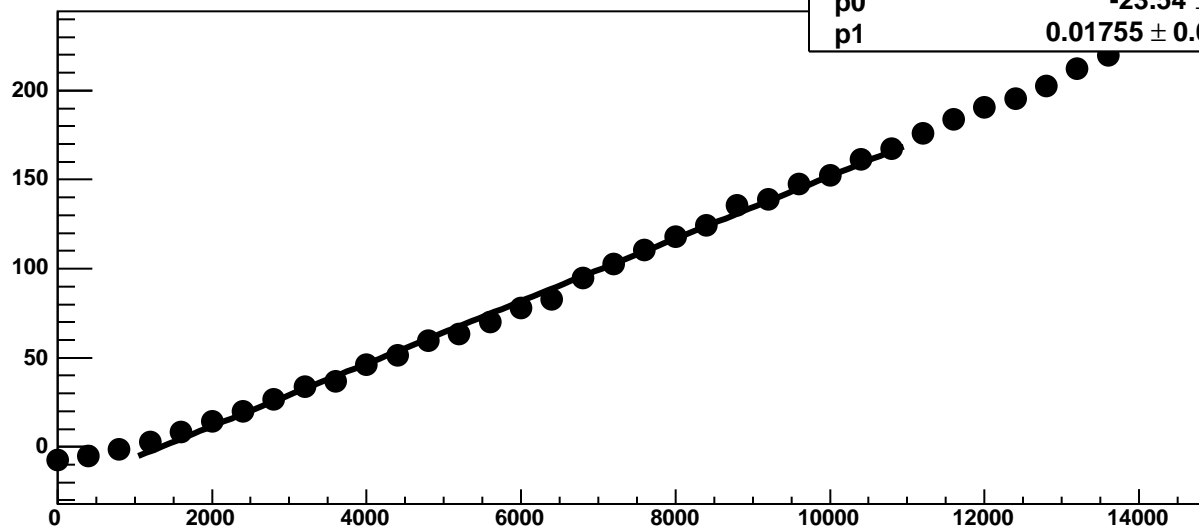
Chip 2, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



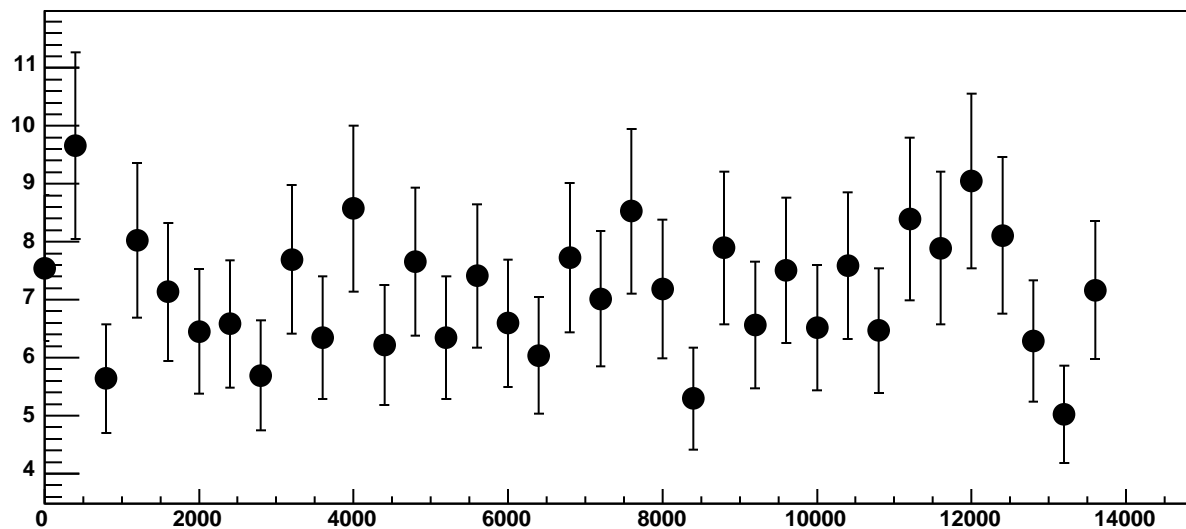
Chip 2, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC



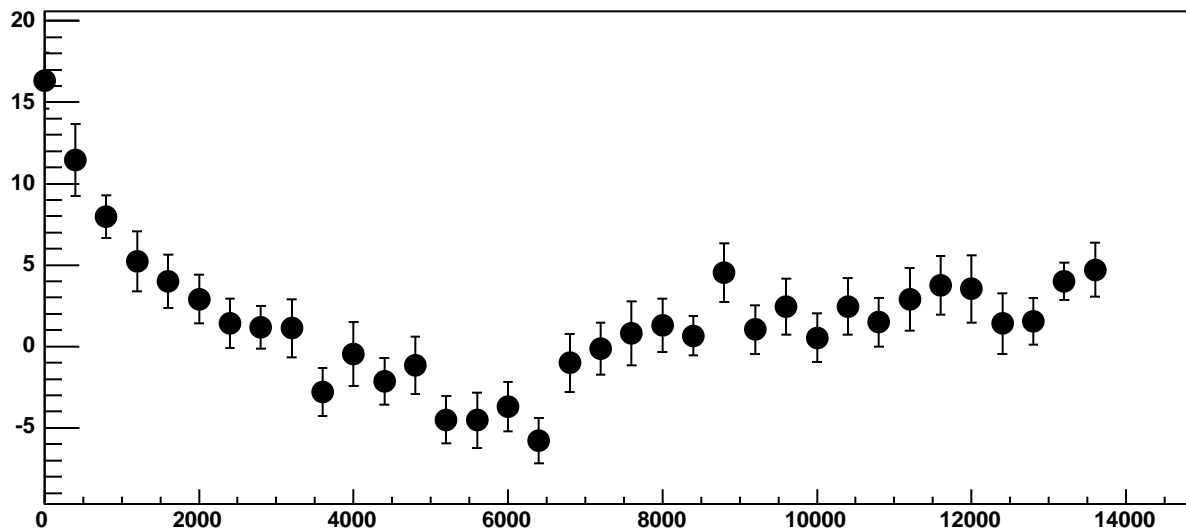
Chip 2, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



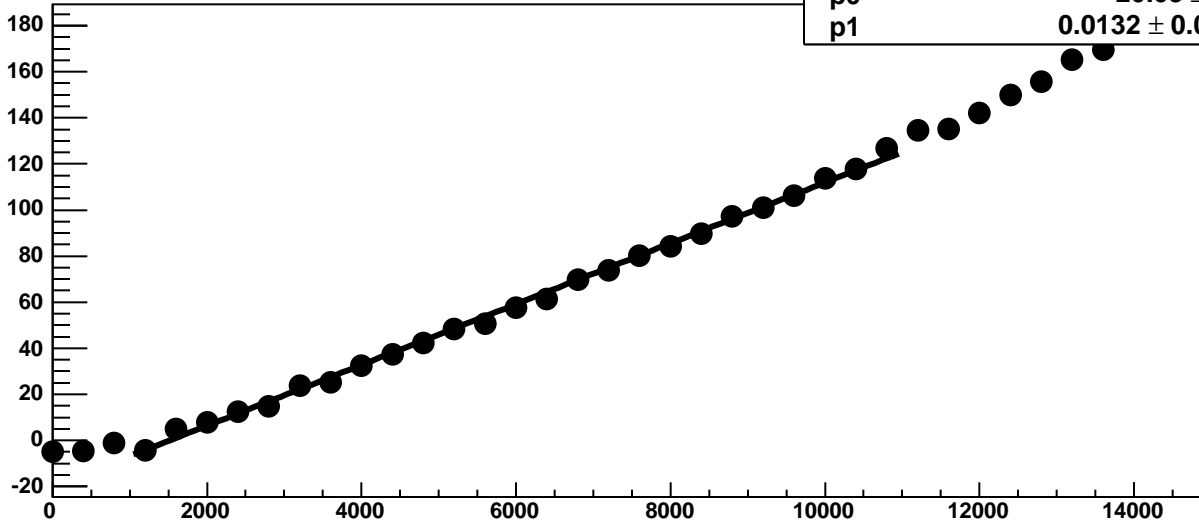
Chip 2, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

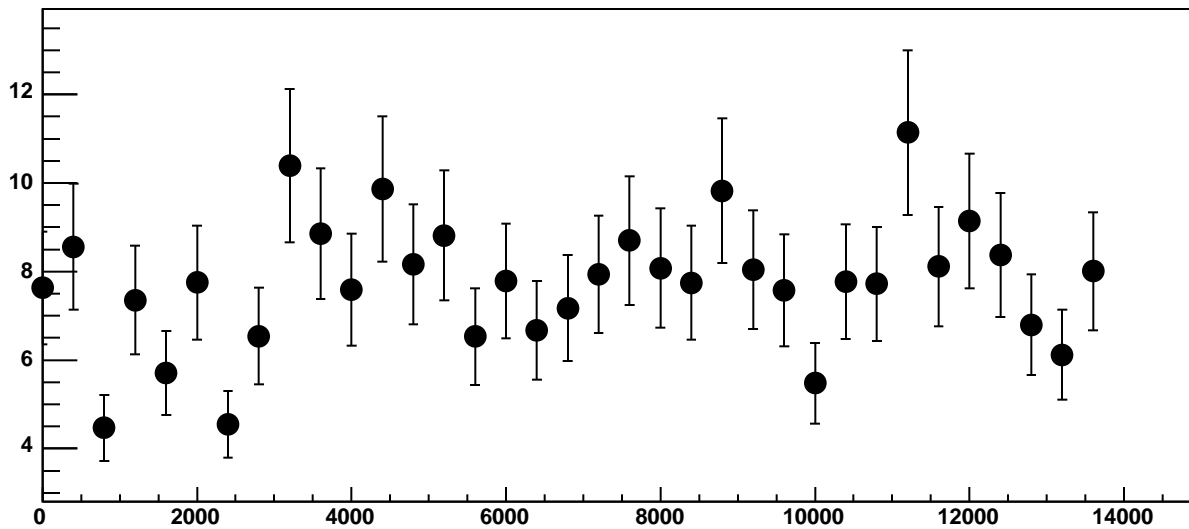


Chip 2, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

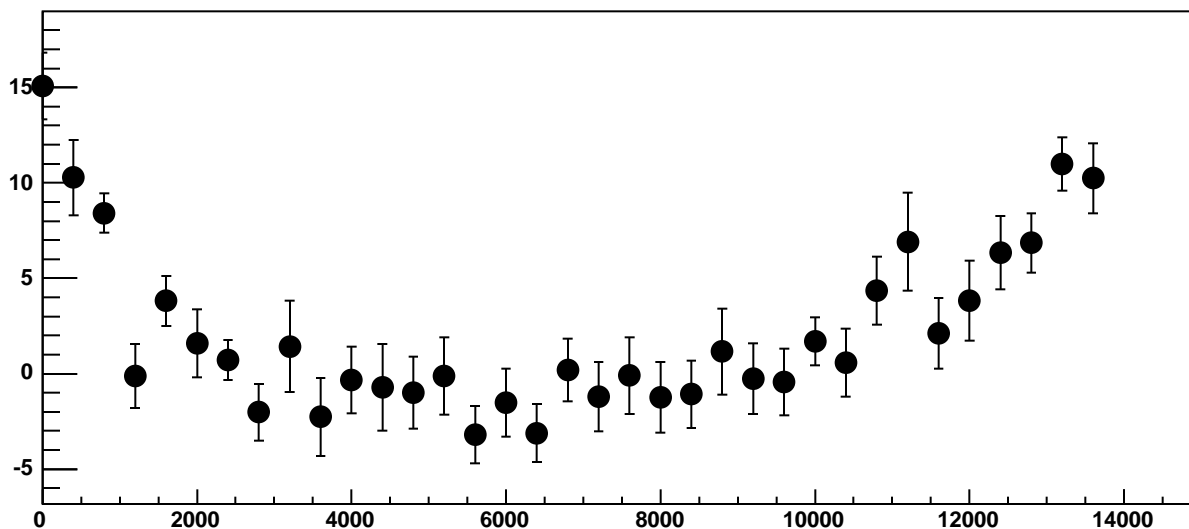


χ^2 / ndf 32.54 / 23
p0 -20.03 ± 0.7162
p1 0.0132 ± 0.0001102

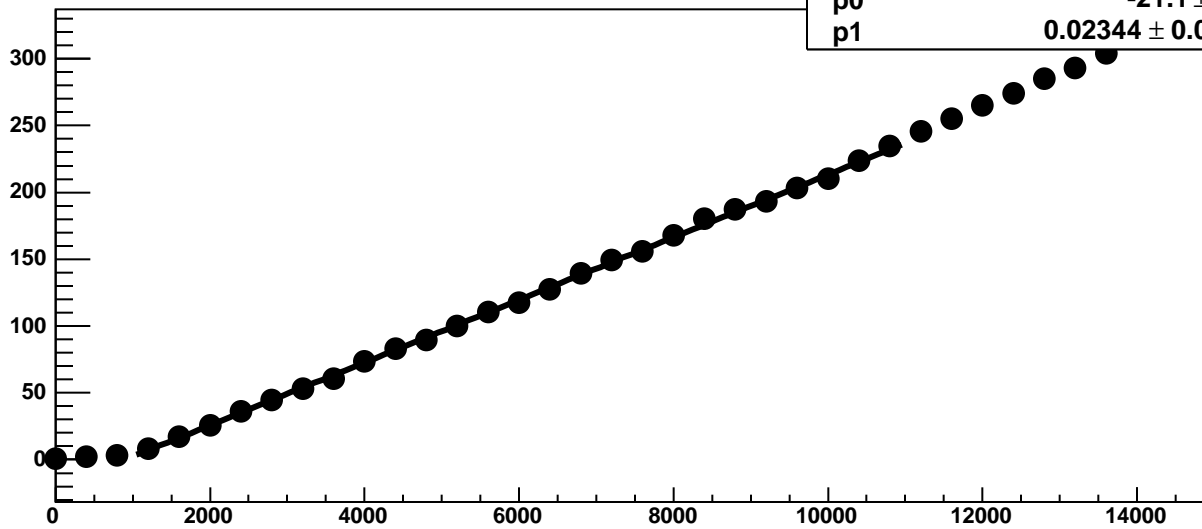
Chip 2, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

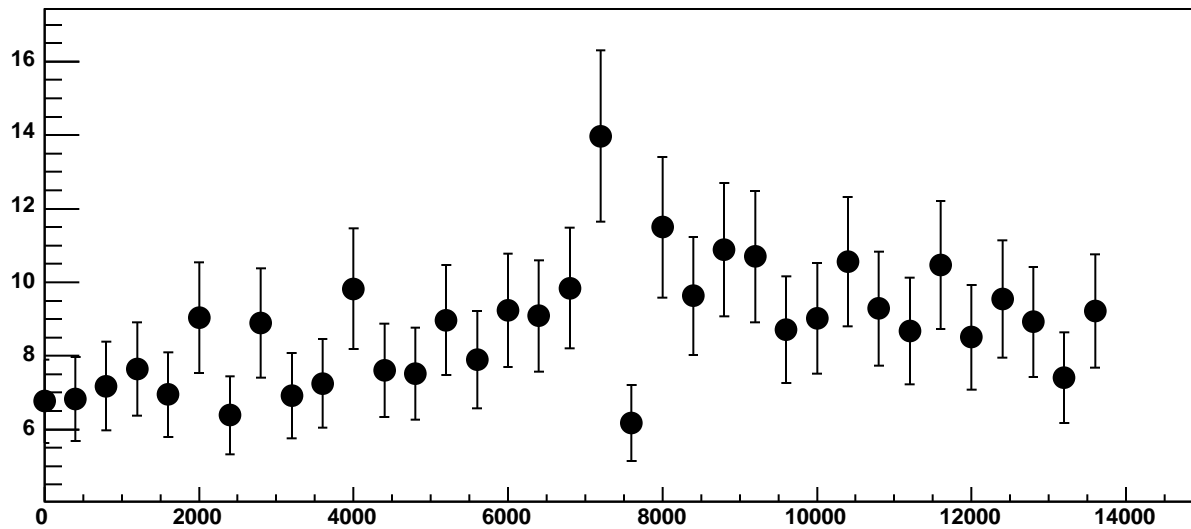


Chip 2, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

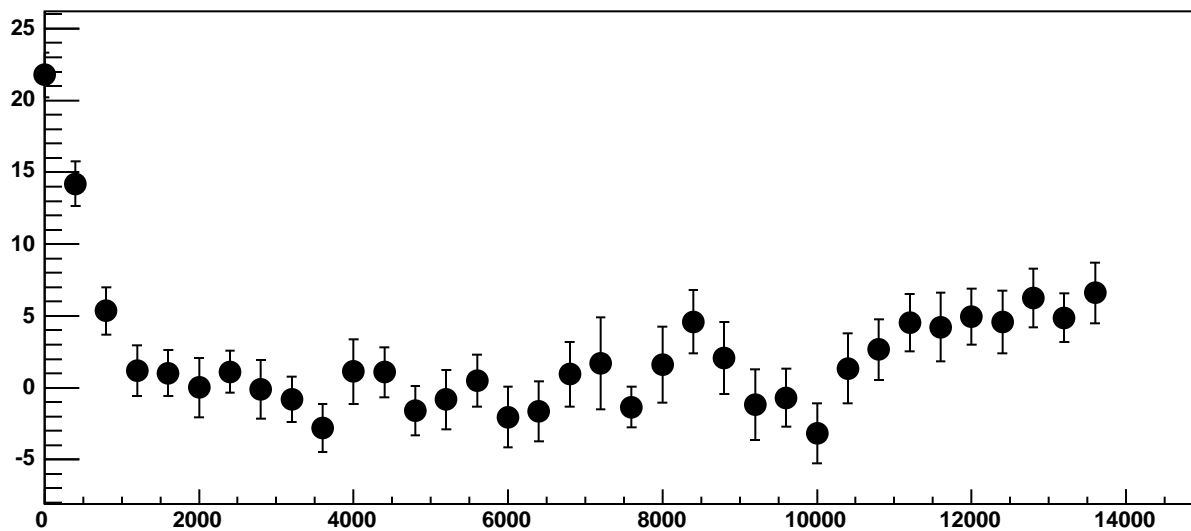


χ^2 / ndf 19.12 / 23
p0 -21.1 ± 0.8335
p1 0.02344 ± 0.0001356

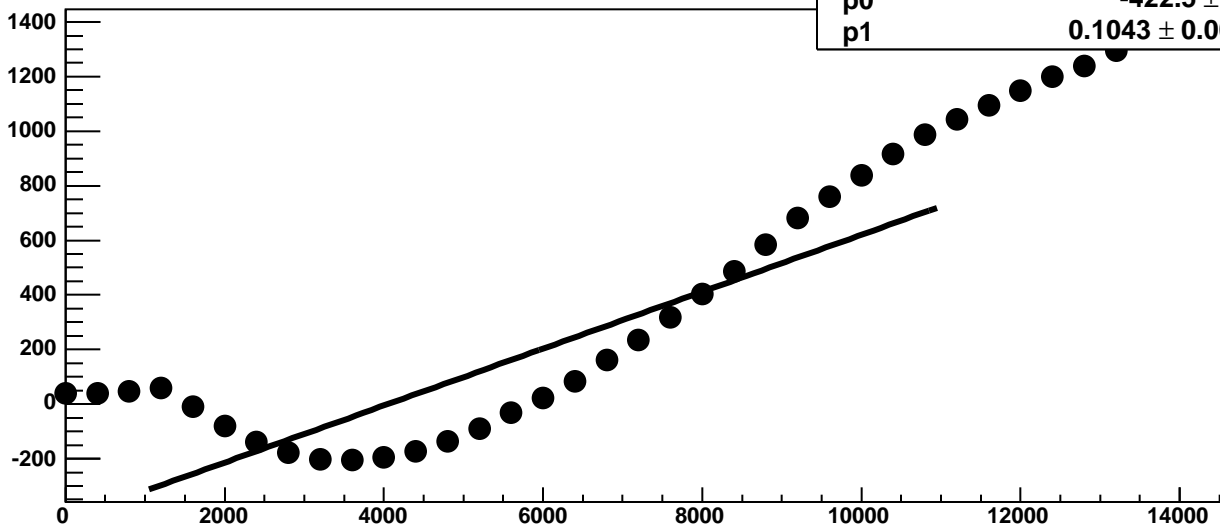
Chip 2, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC

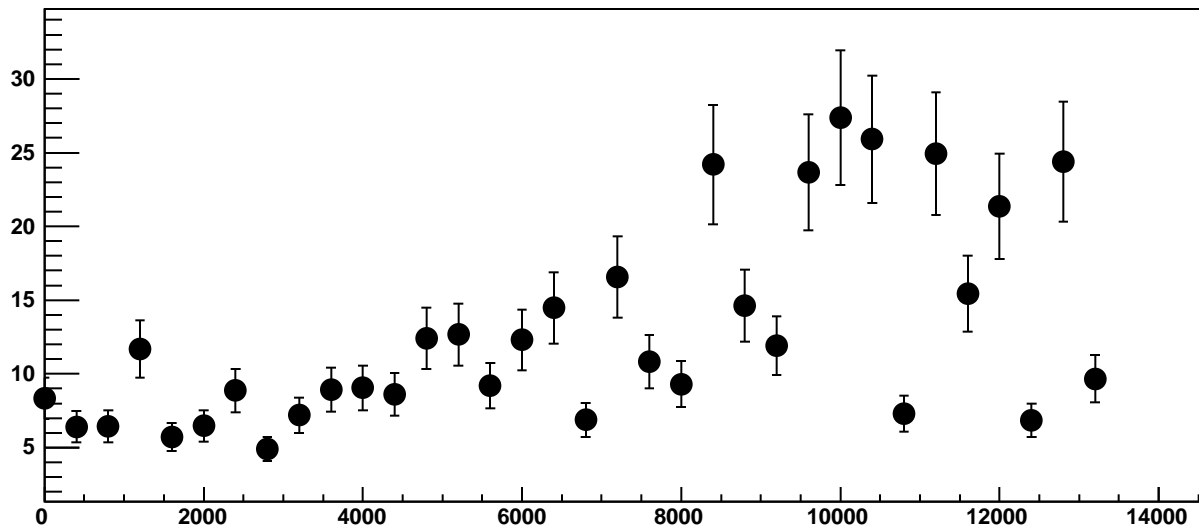


Chip 2, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

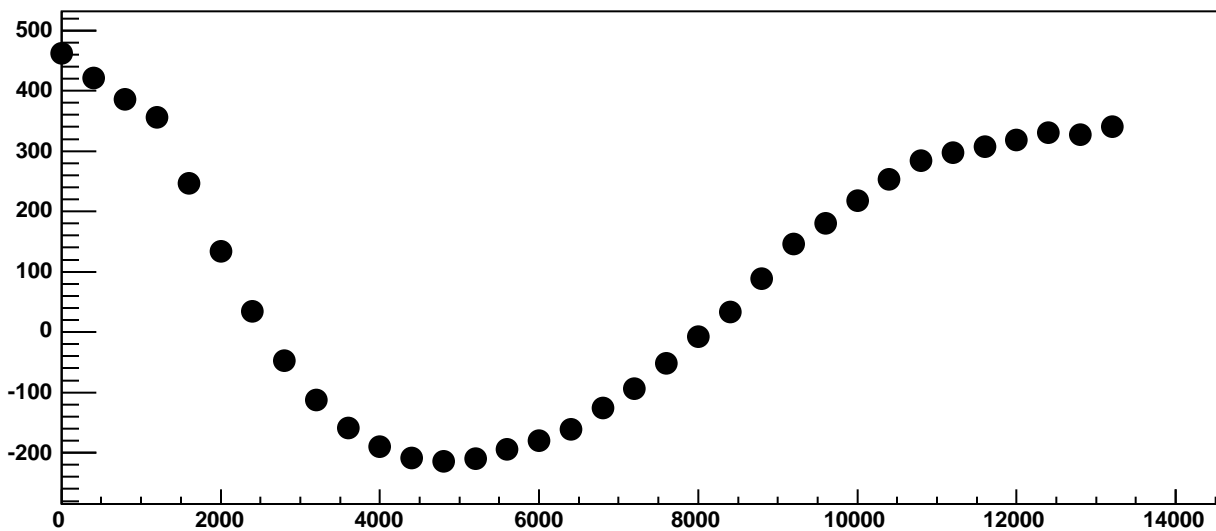


χ^2 / ndf 1.633e+05 / 23
p0 -422.5 ± 0.8305
p1 0.1043 ± 0.0001524

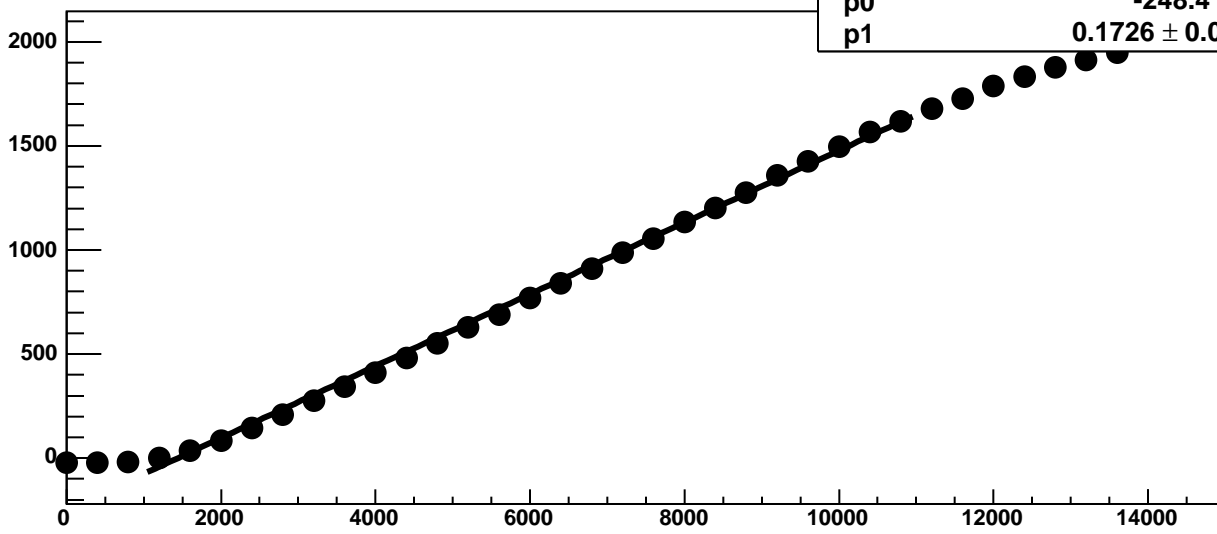
Chip 2, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

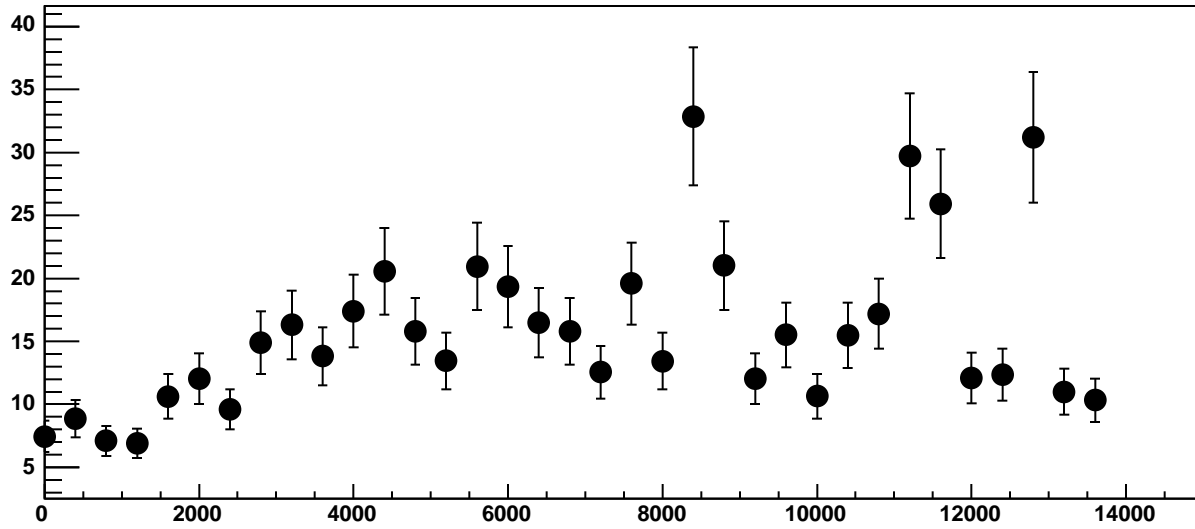


Chip 2, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

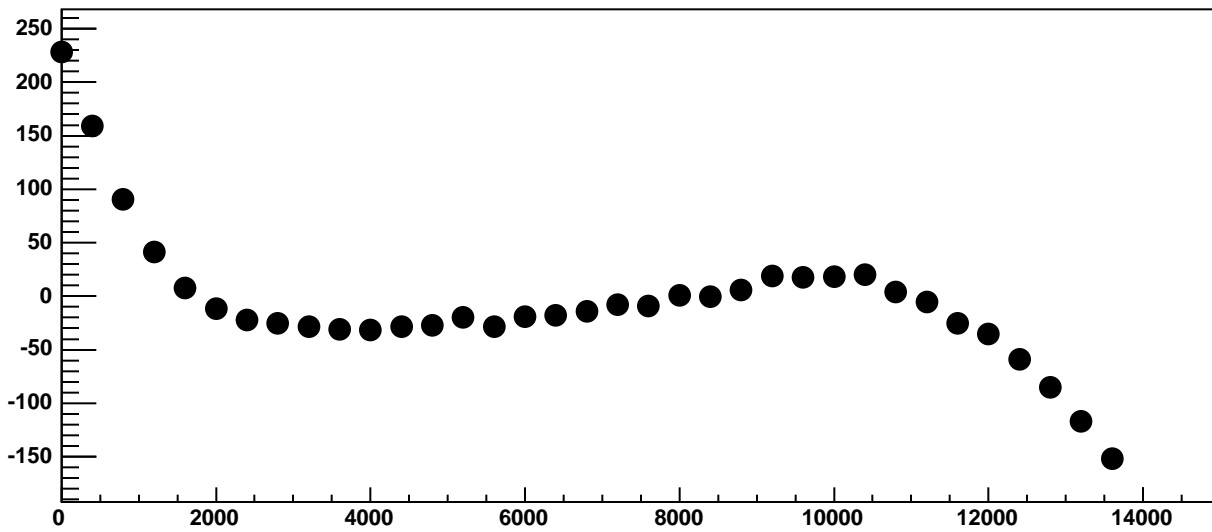


χ^2 / ndf 1476 / 23
p0 -248.4 ± 1.163
p1 0.1726 ± 0.0001921

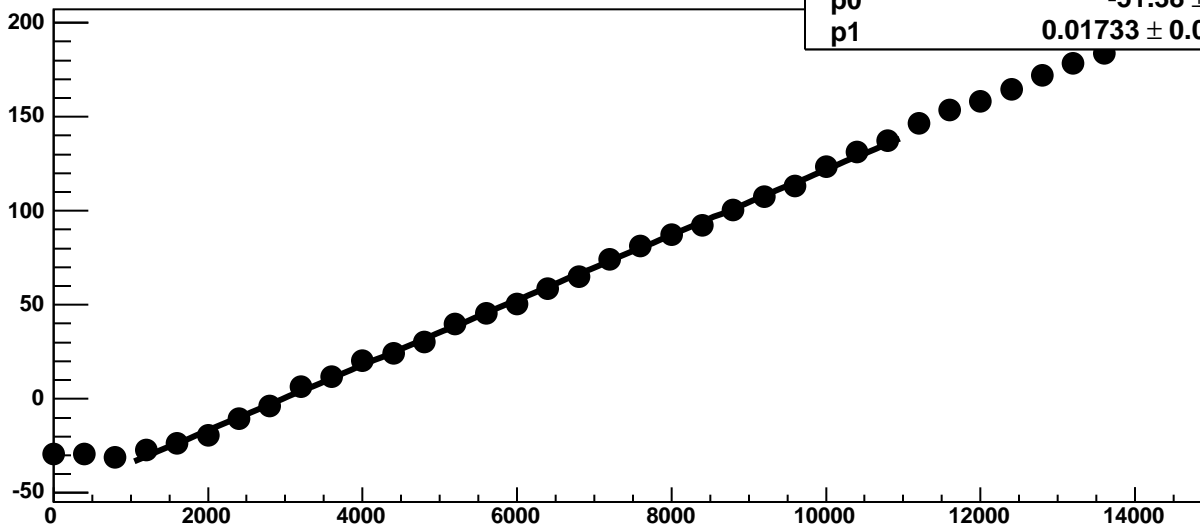
Chip 2, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

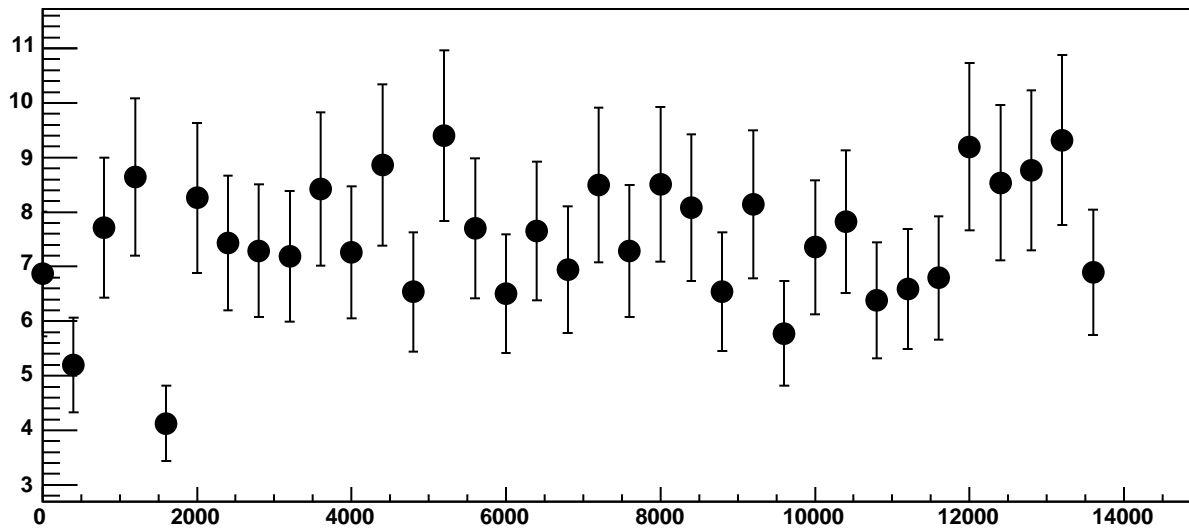


Chip 2, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

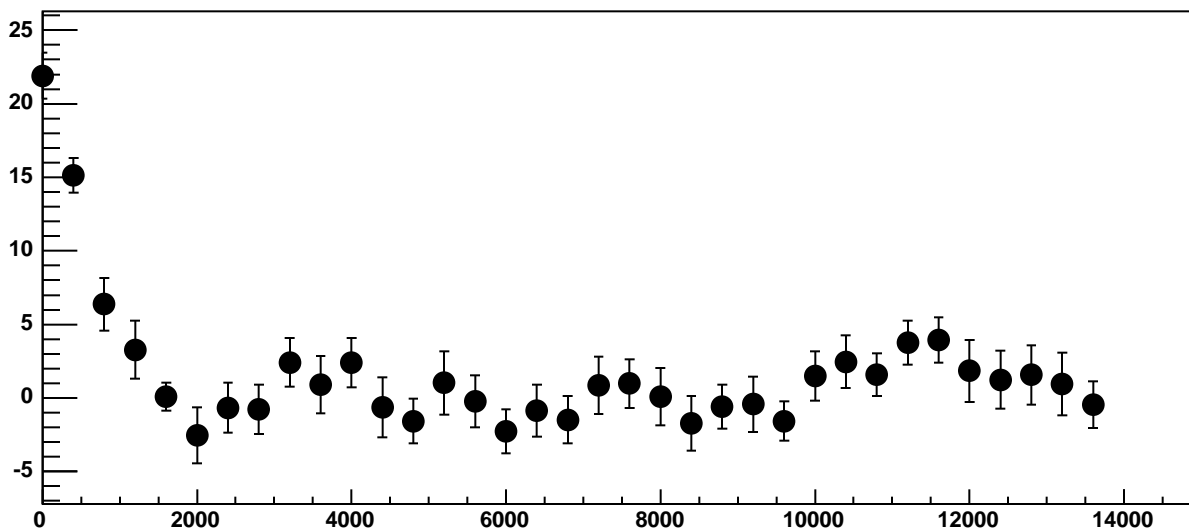


χ^2 / ndf 21.01 / 23
p0 -51.38 ± 0.7049
p1 0.01733 ± 0.0001068

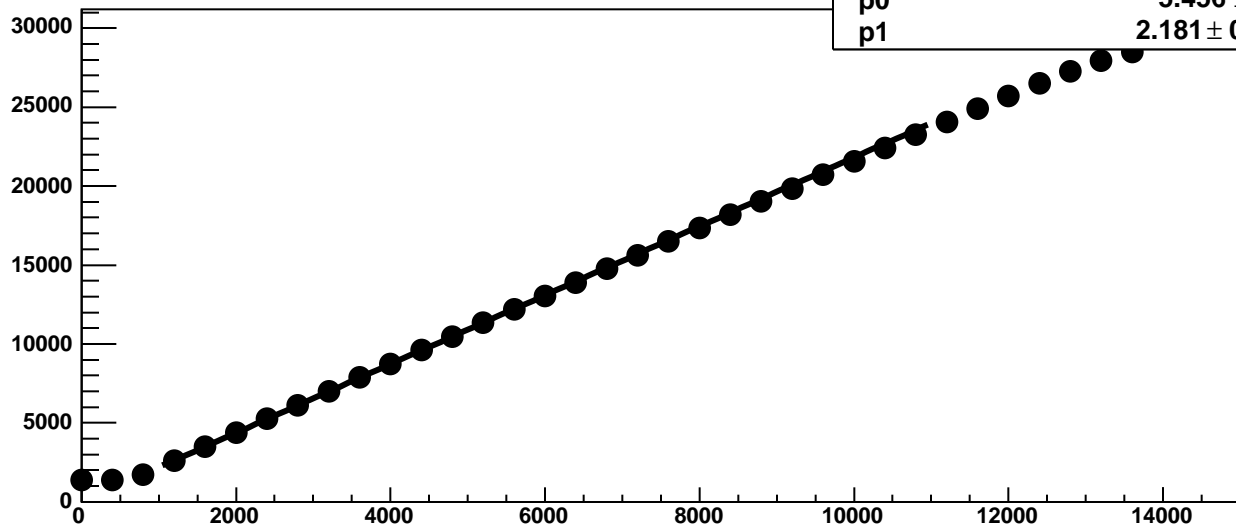
Chip 2, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

374.8 / 23

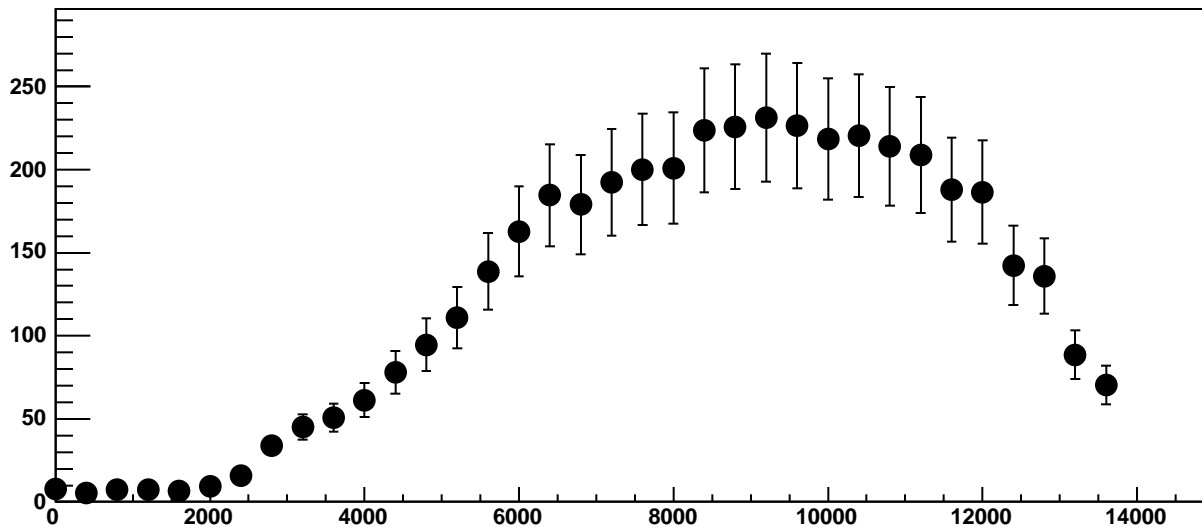
p0

5.456 ± 2.438

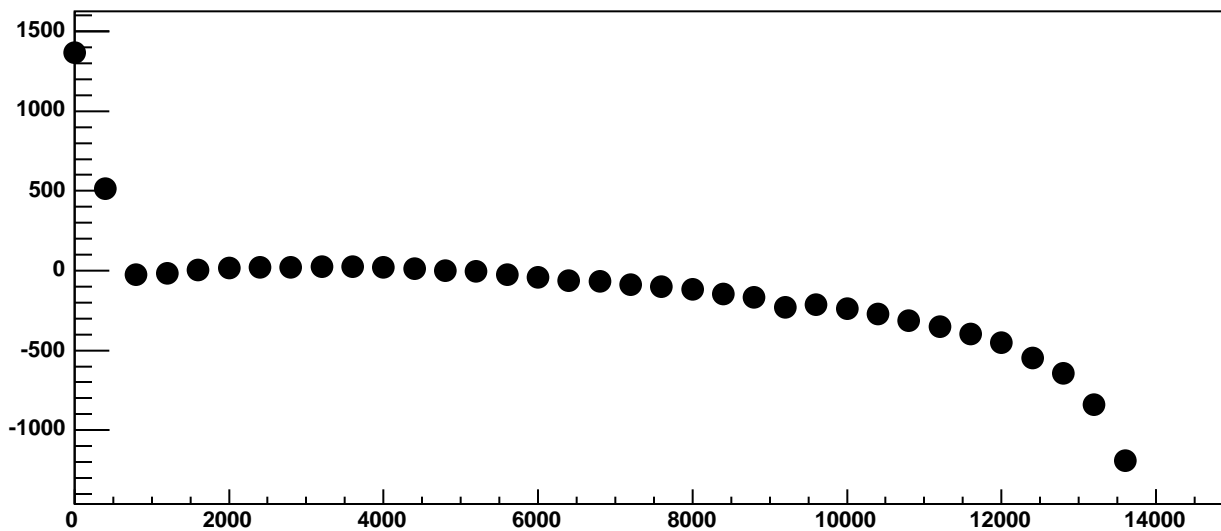
p1

2.181 ± 0.00131

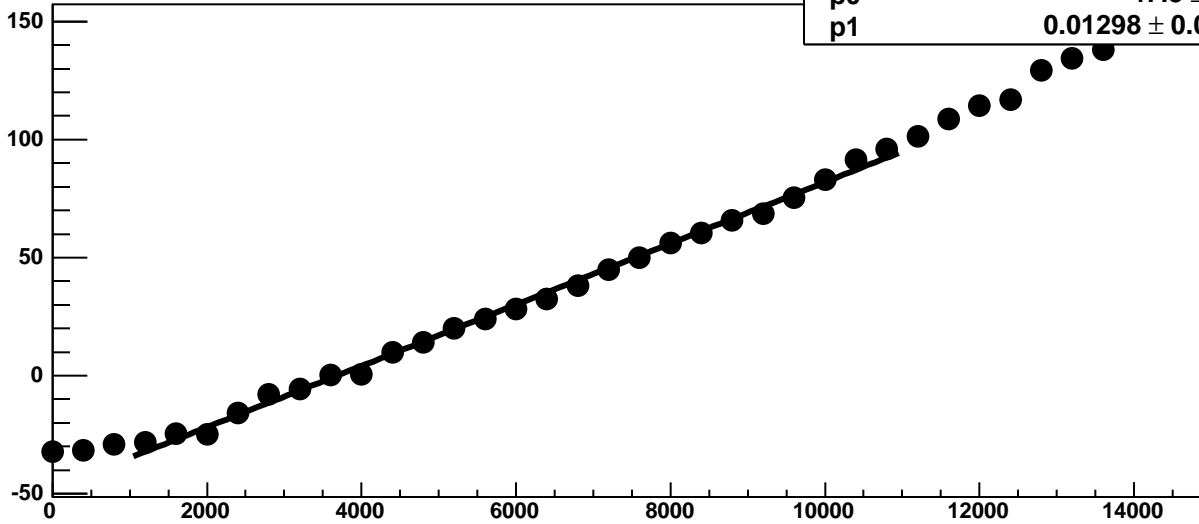
Chip 2, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



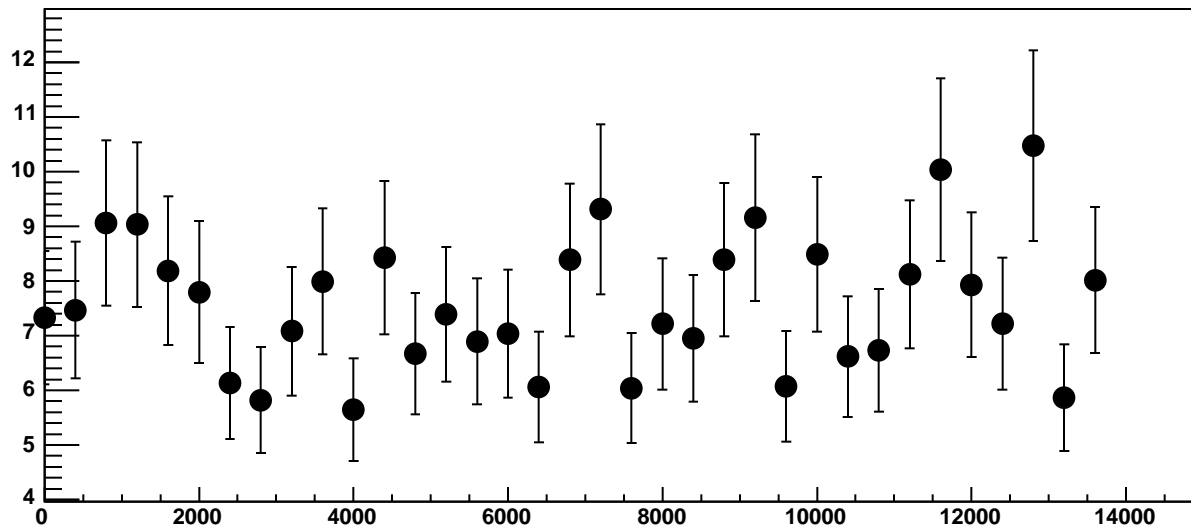
Chip 2, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC



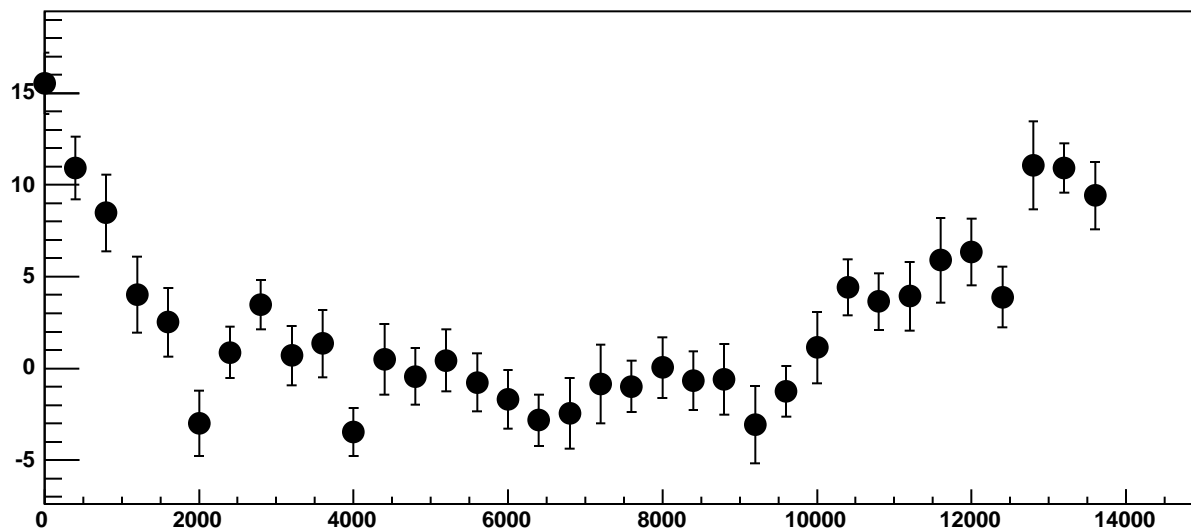
Chip 2, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



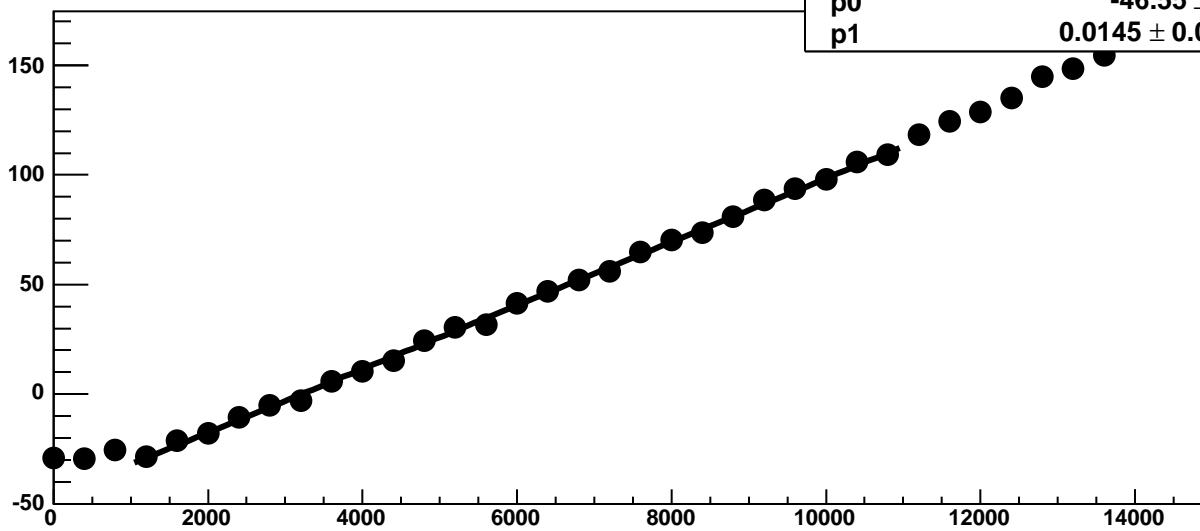
Chip 2, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



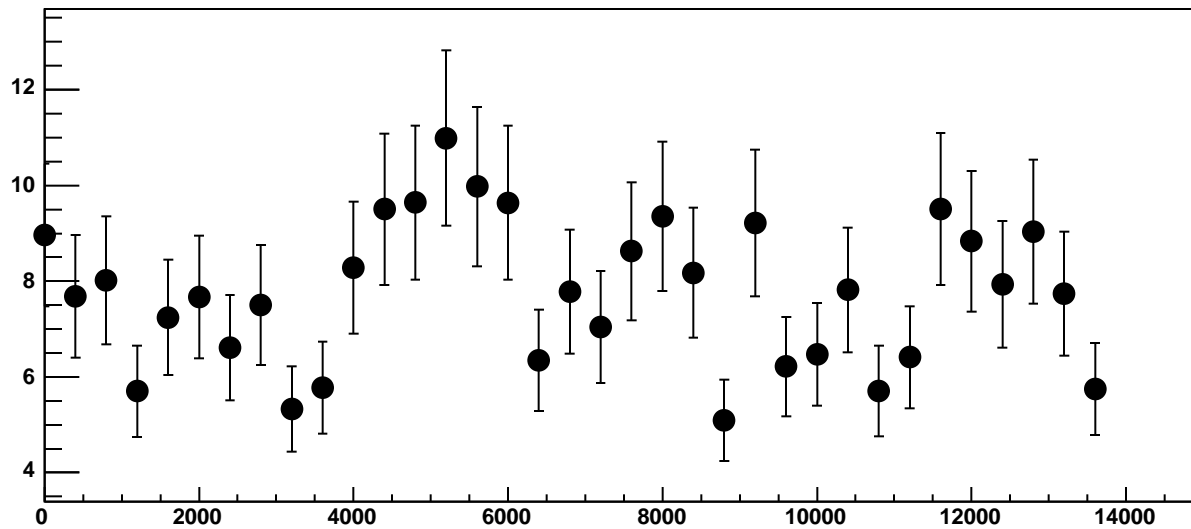
Chip 2, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



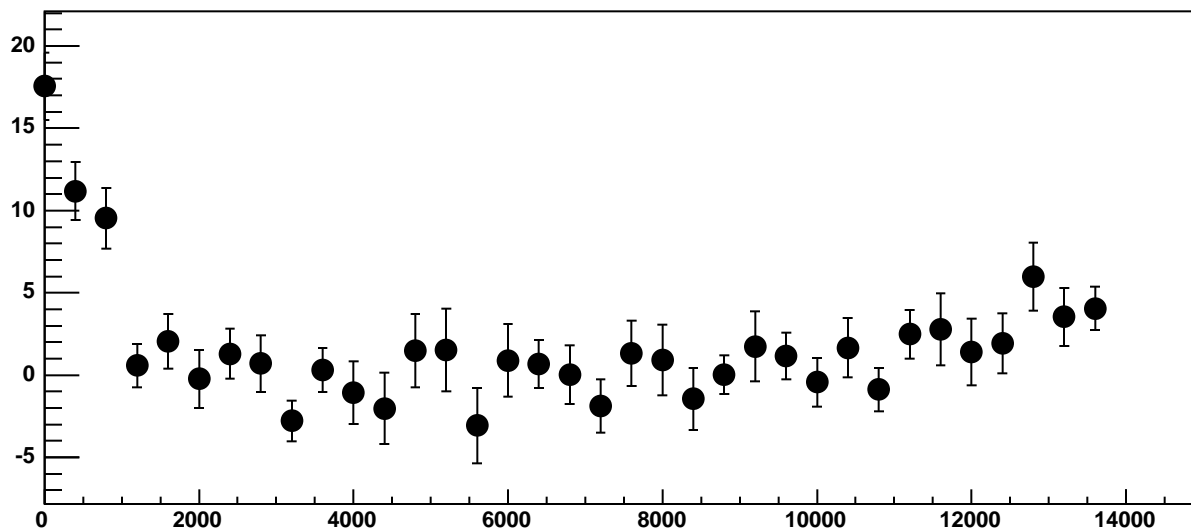
Chip 2, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



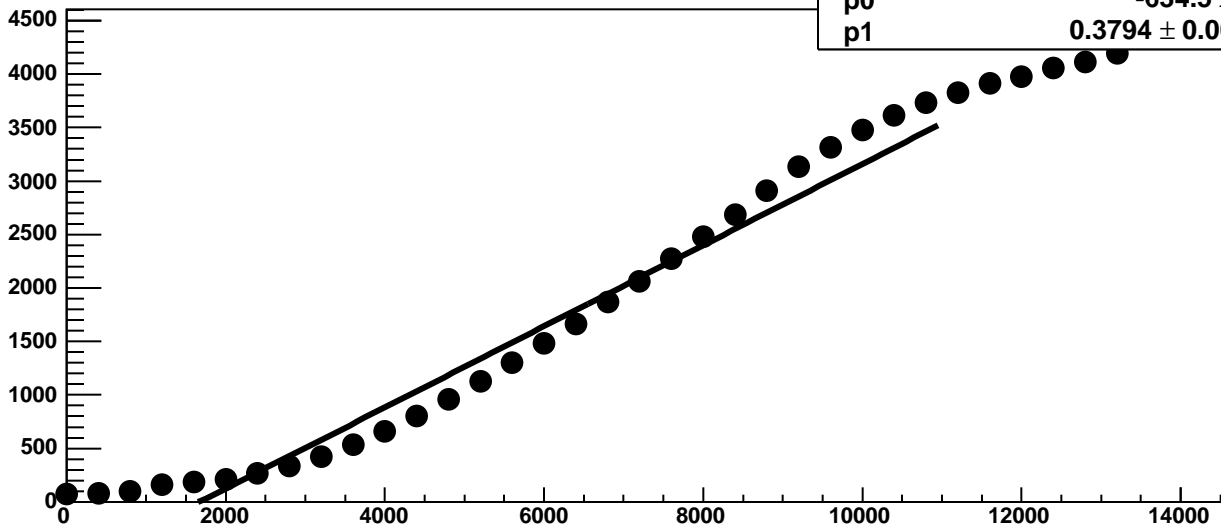
Chip 2, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1.048e+05 / 23

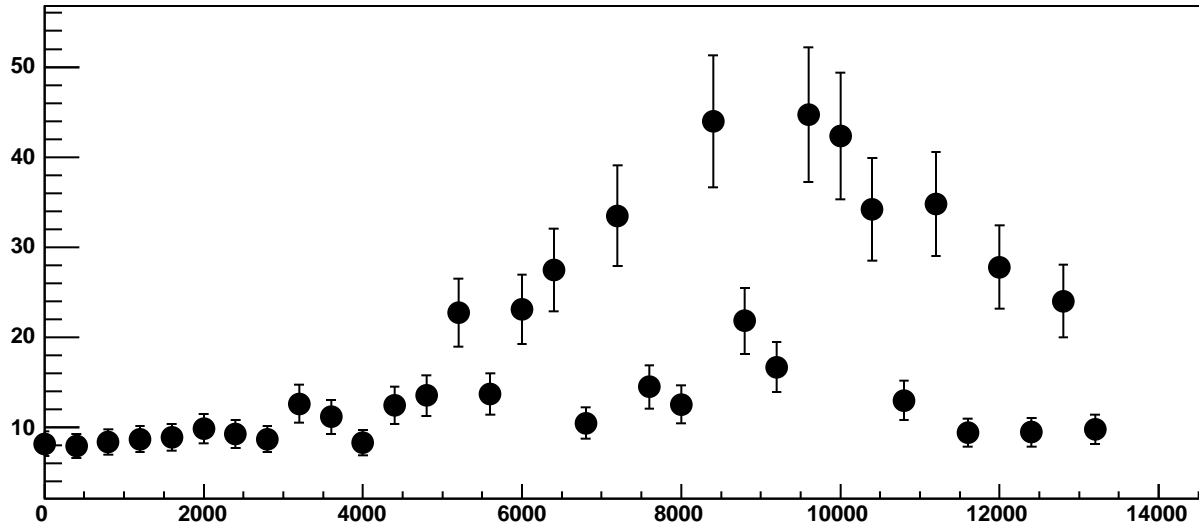
p0

-634.5 ± 1.144

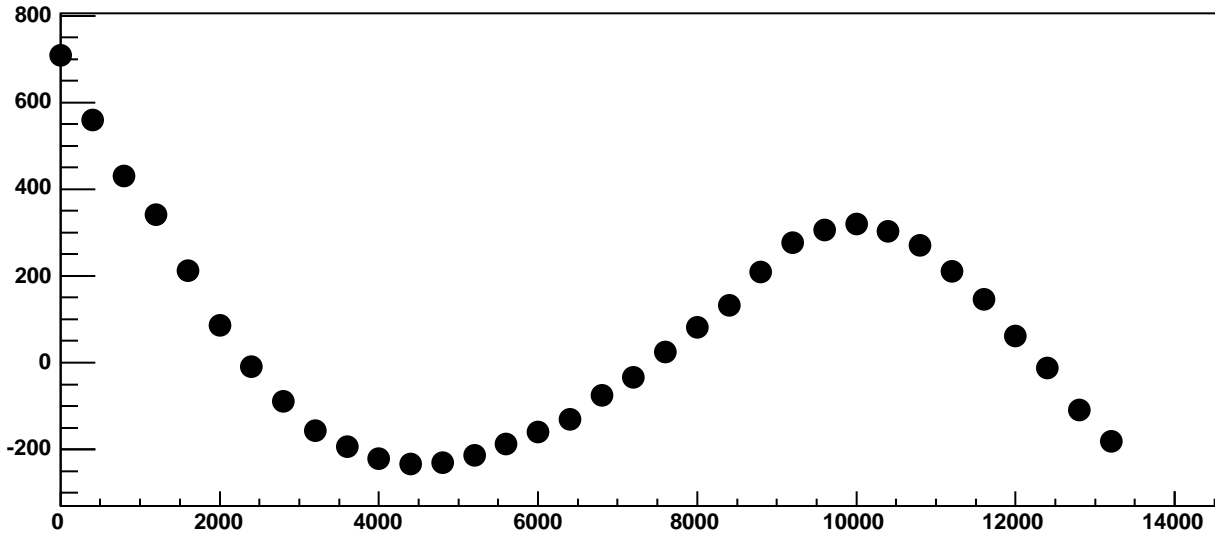
p1

0.3794 ± 0.0002255

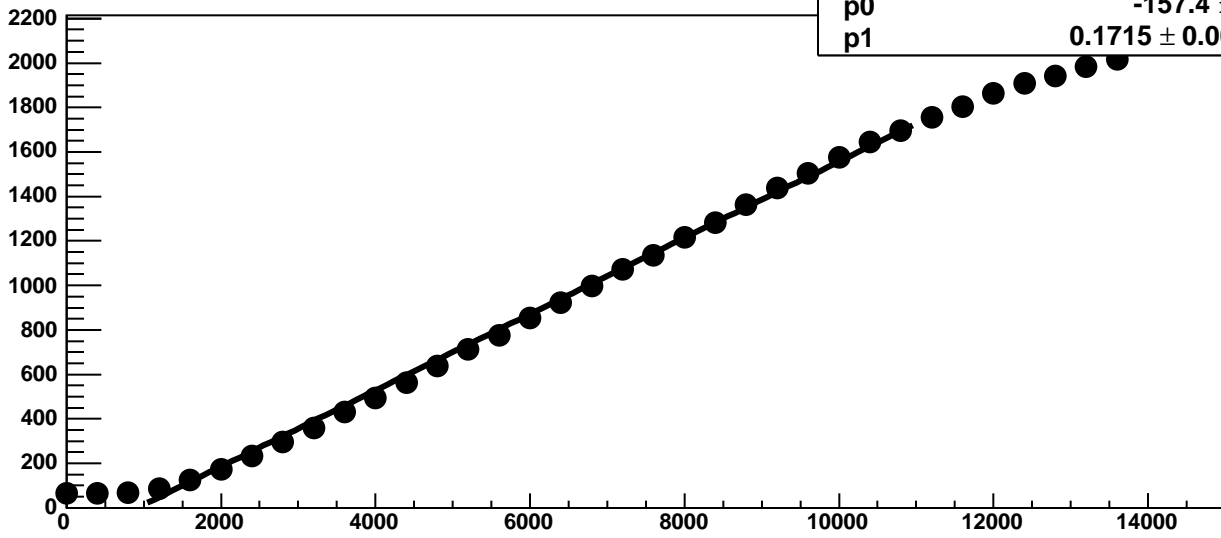
Chip 2, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1697 / 23

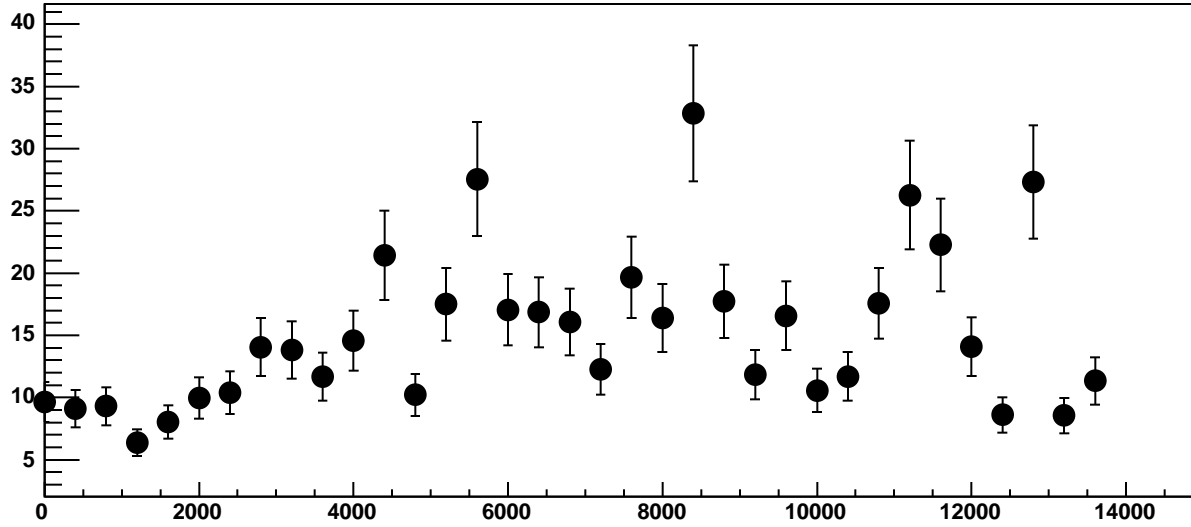
p0

-157.4 ± 1.038

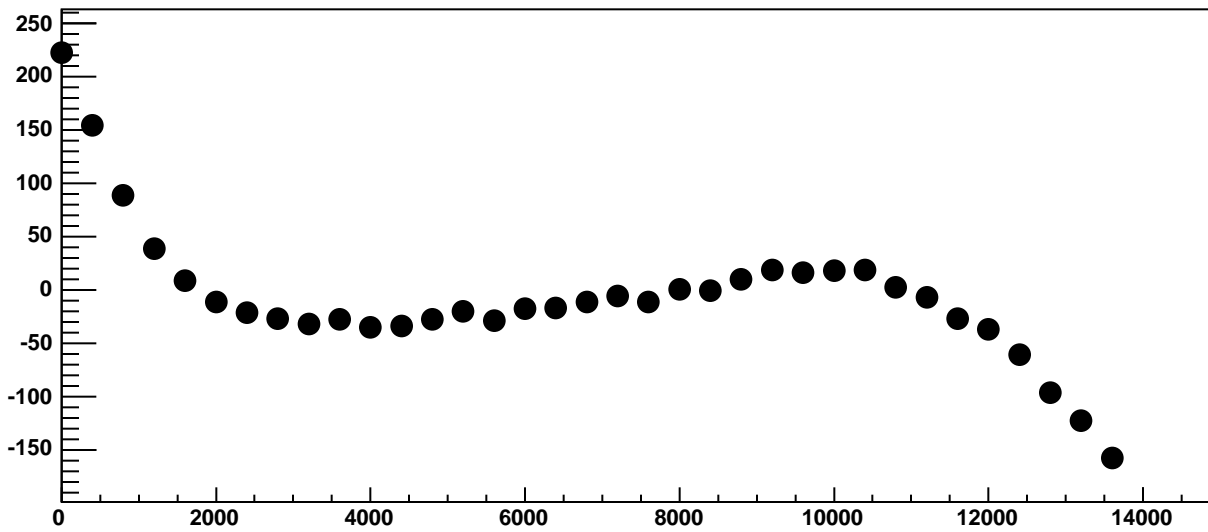
p1

0.1715 ± 0.0001778

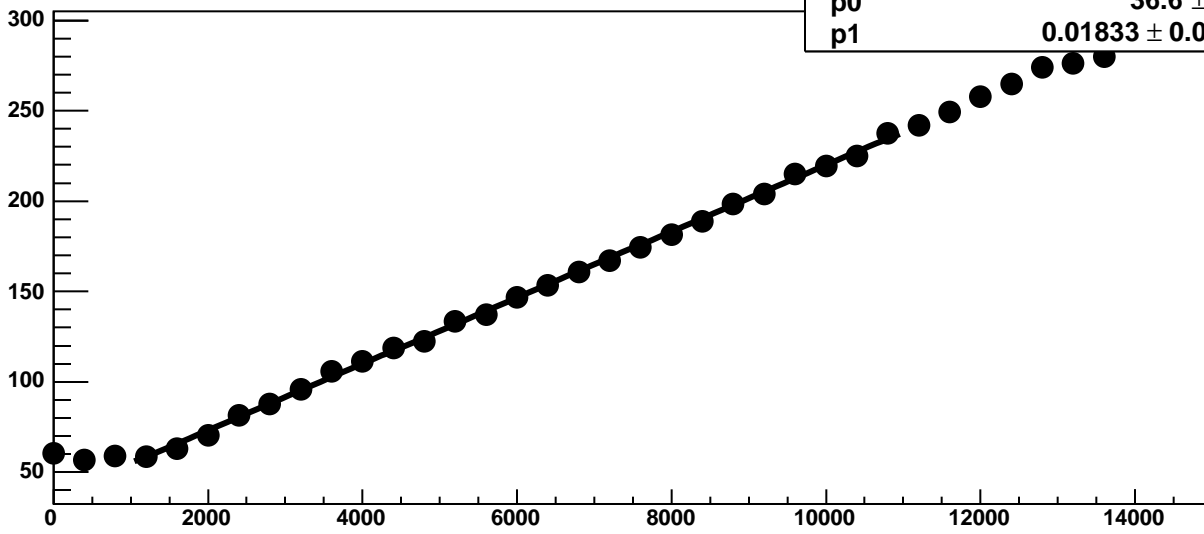
Chip 2, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

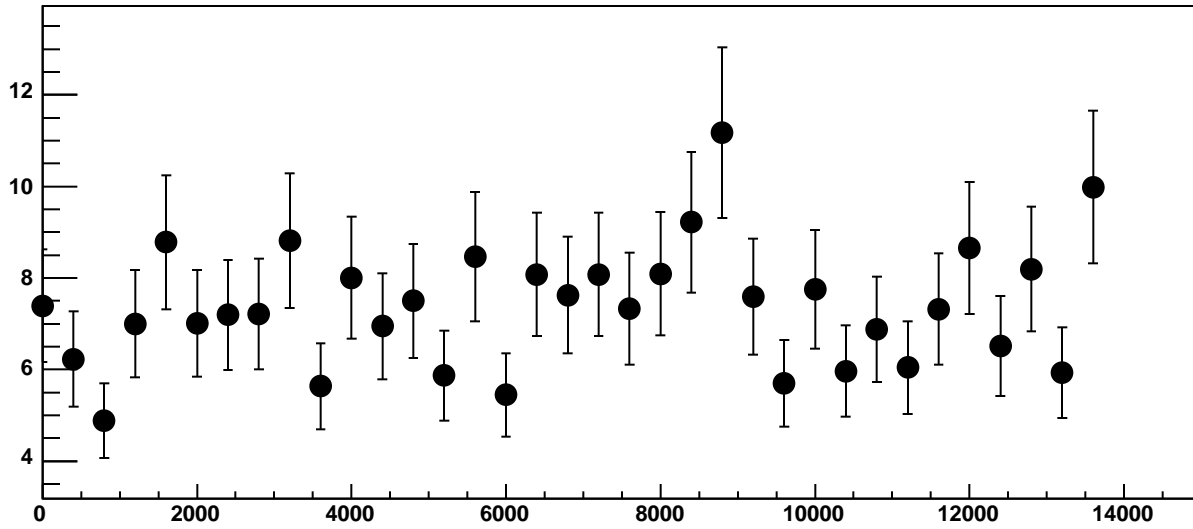


Chip 2, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

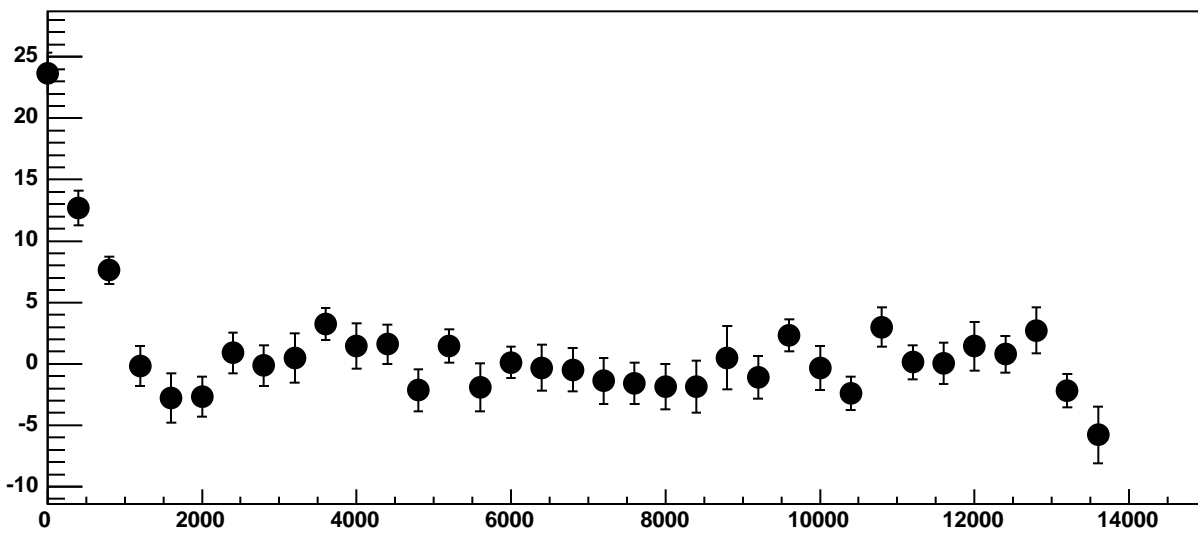


χ^2 / ndf 30.34 / 23
p0 36.6 ± 0.7599
p1 0.01833 ± 0.0001135

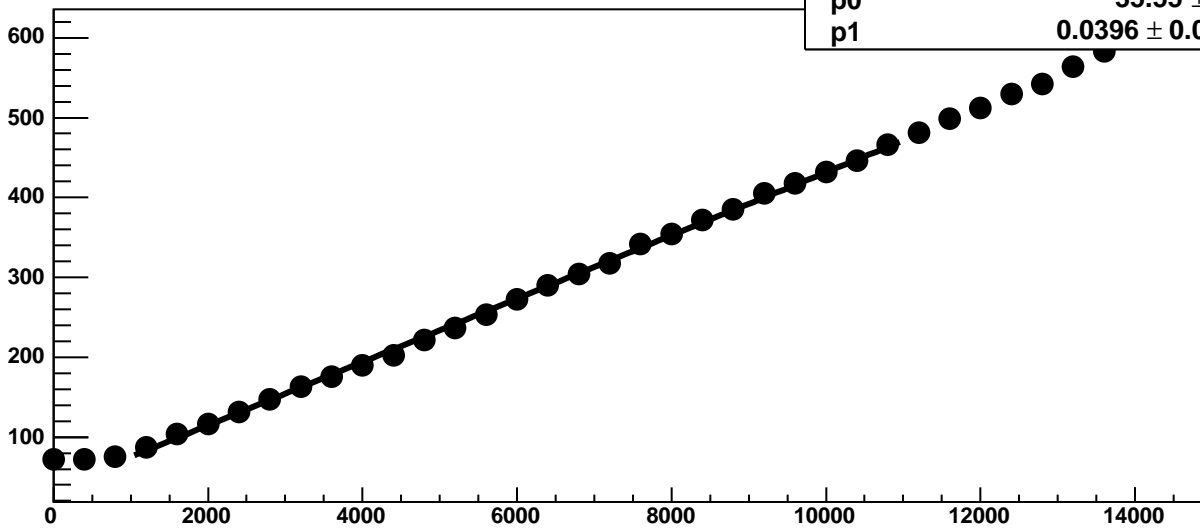
Chip 2, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

99.32 / 23

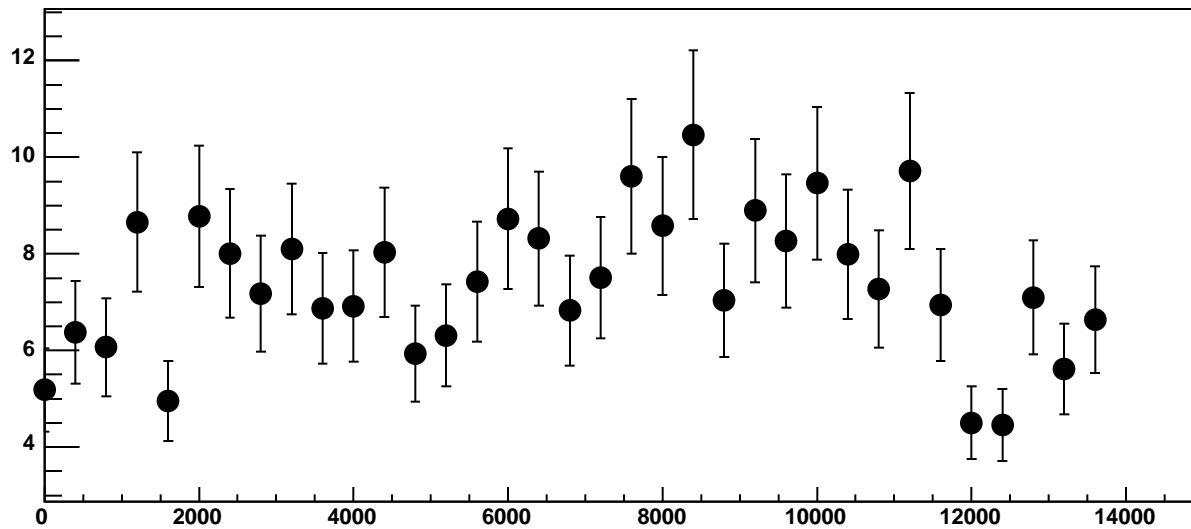
p0

35.55 ± 0.7587

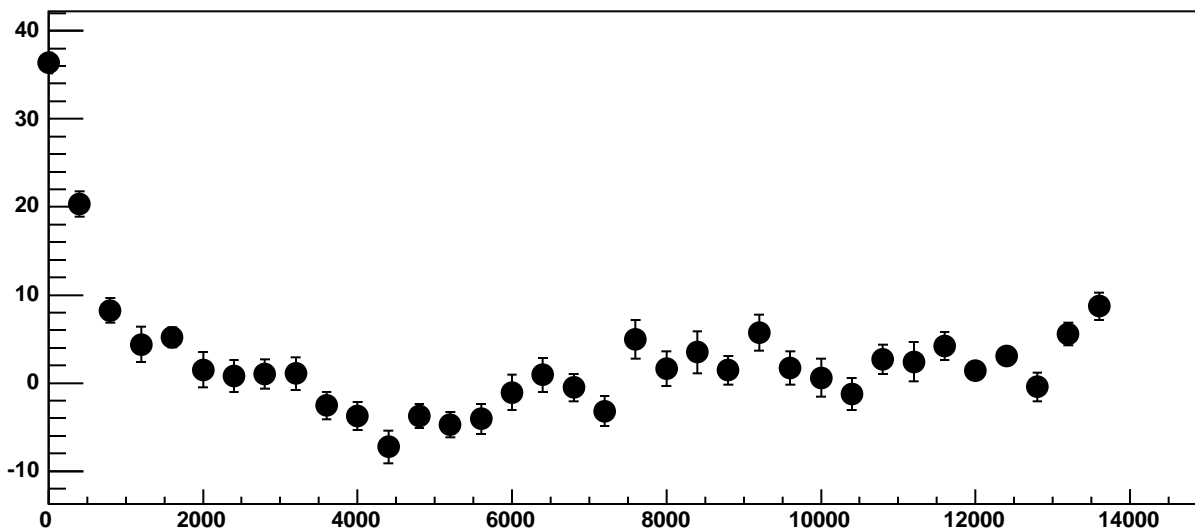
p1

0.0396 ± 0.0001203

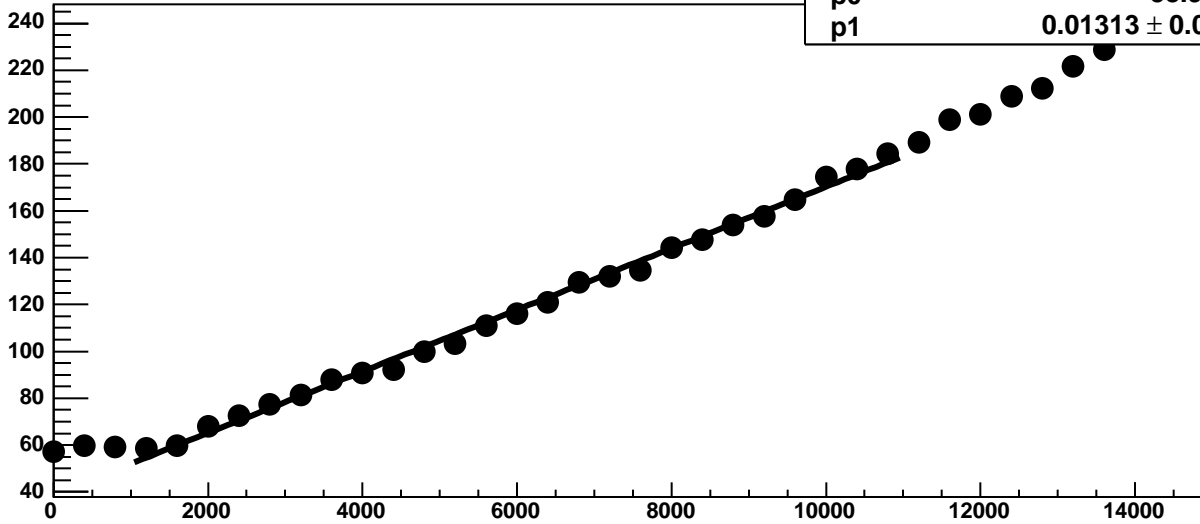
Chip 2, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

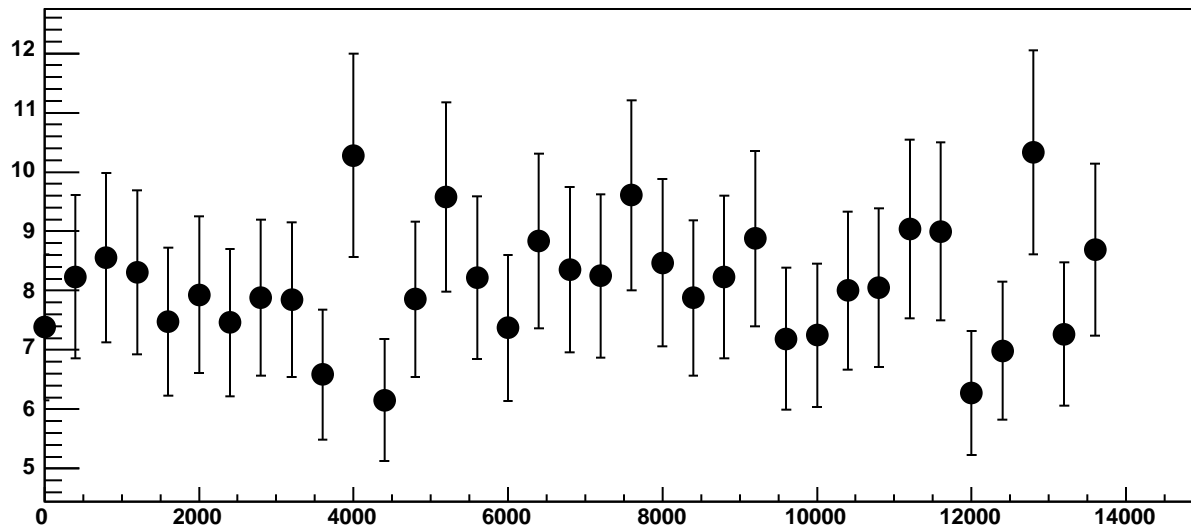


Chip 2, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

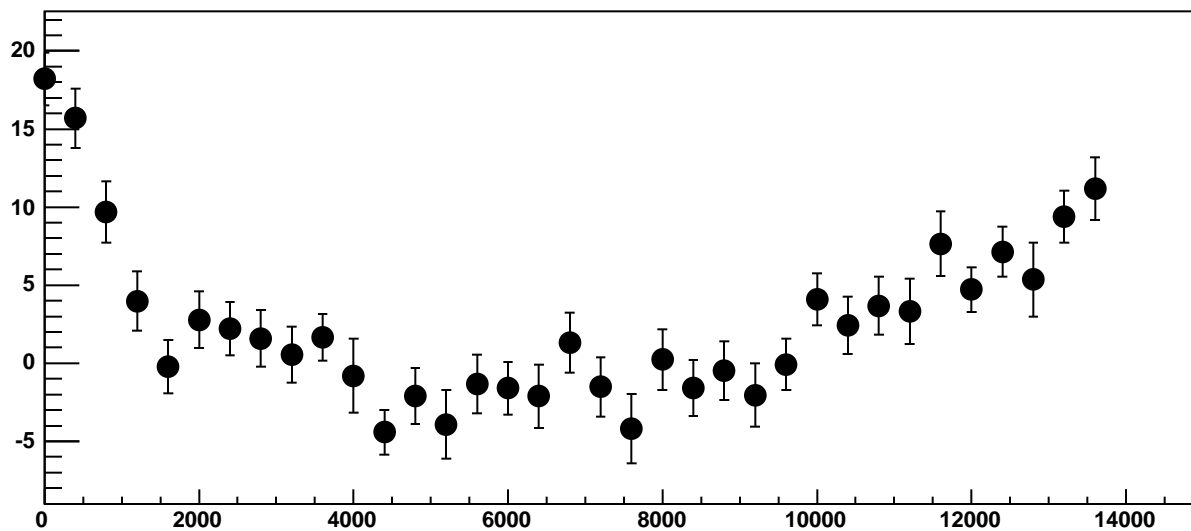


χ^2 / ndf 45.71 / 23
p0 38.9 ± 0.821
p1 0.01313 ± 0.0001245

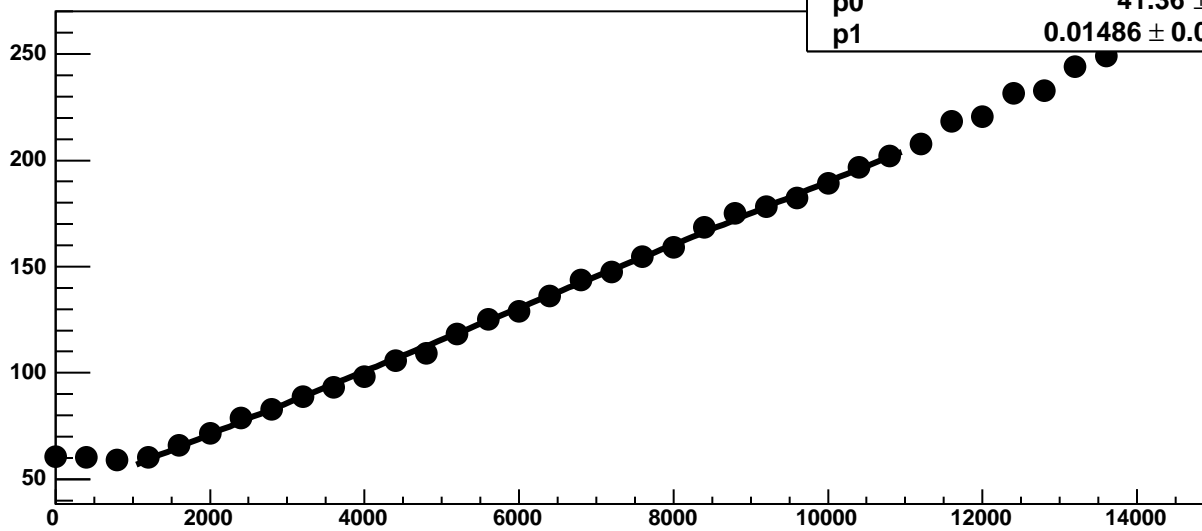
Chip 2, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

14.9 / 23

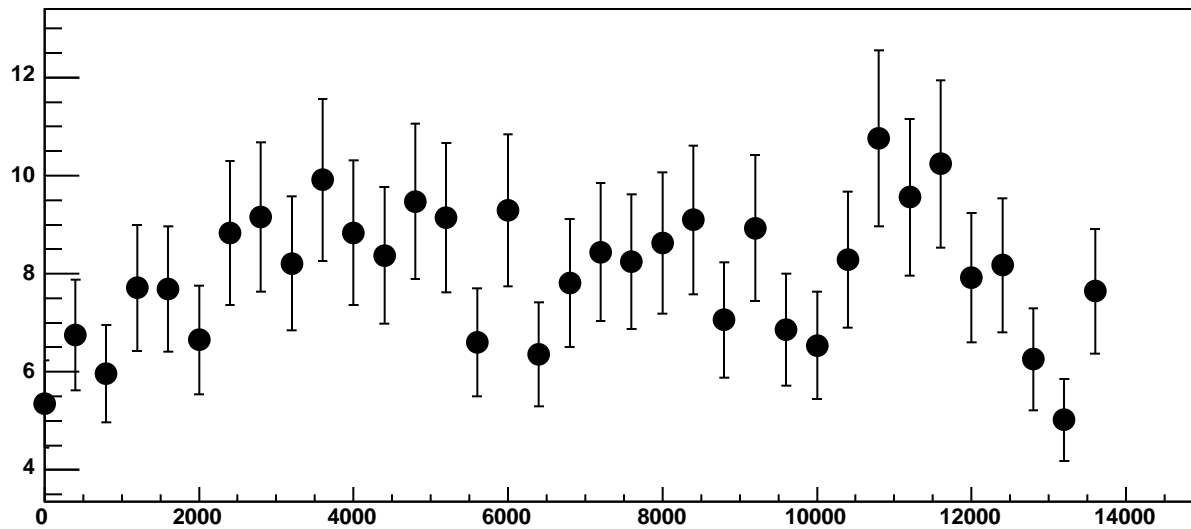
p0

41.36 ± 0.8507

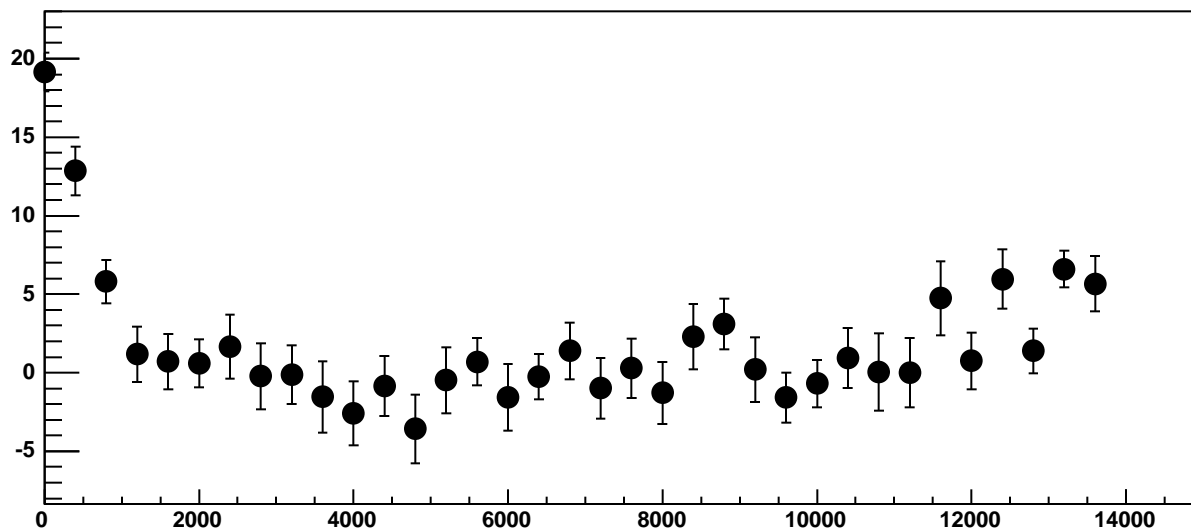
p1

0.01486 ± 0.0001269

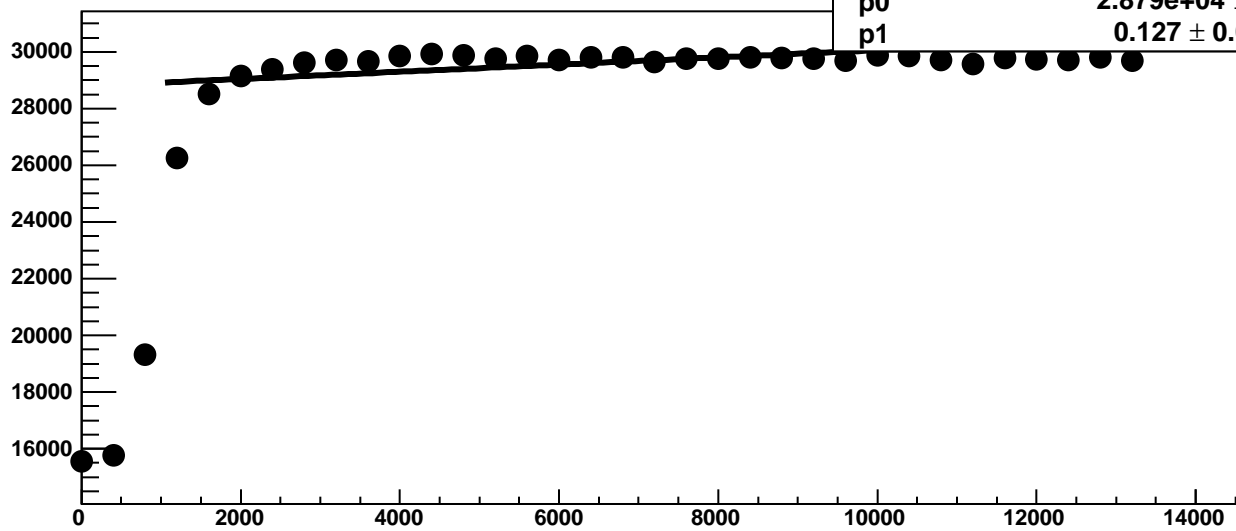
Chip 2, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



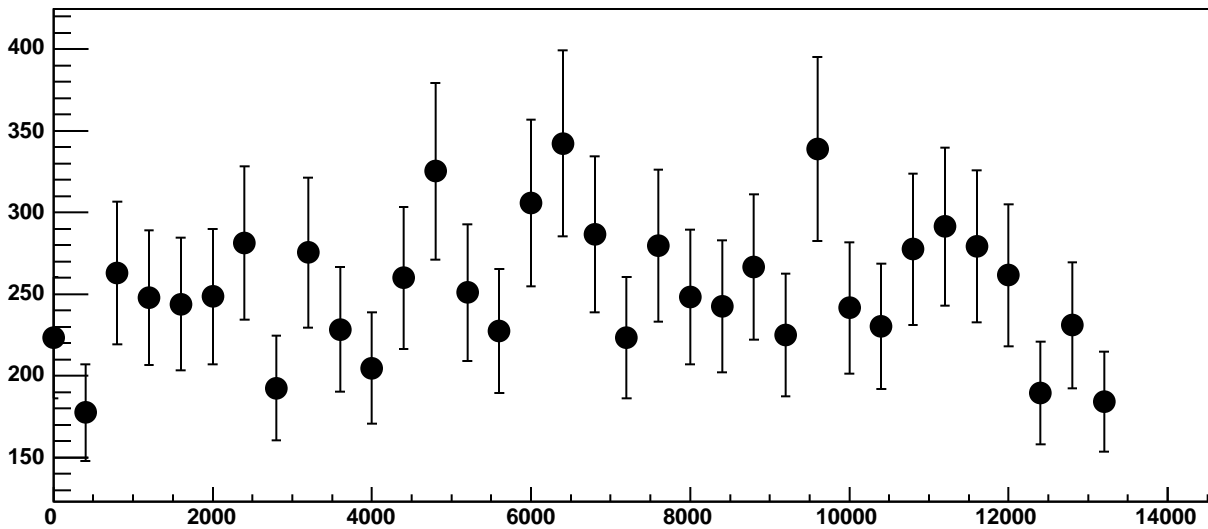
Chip 2, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



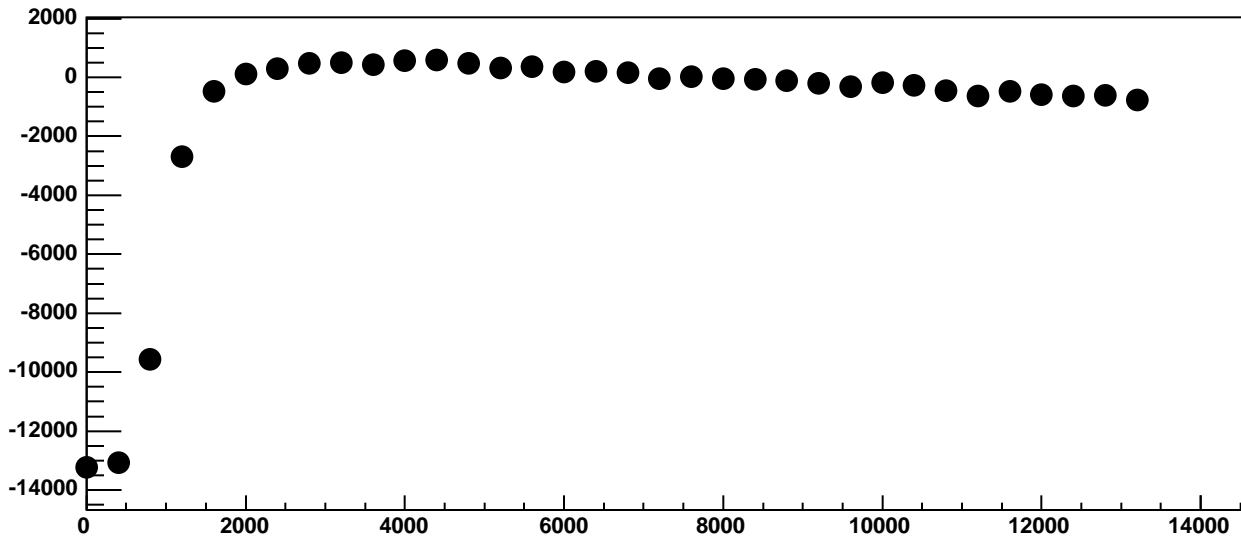
Chip 2, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC



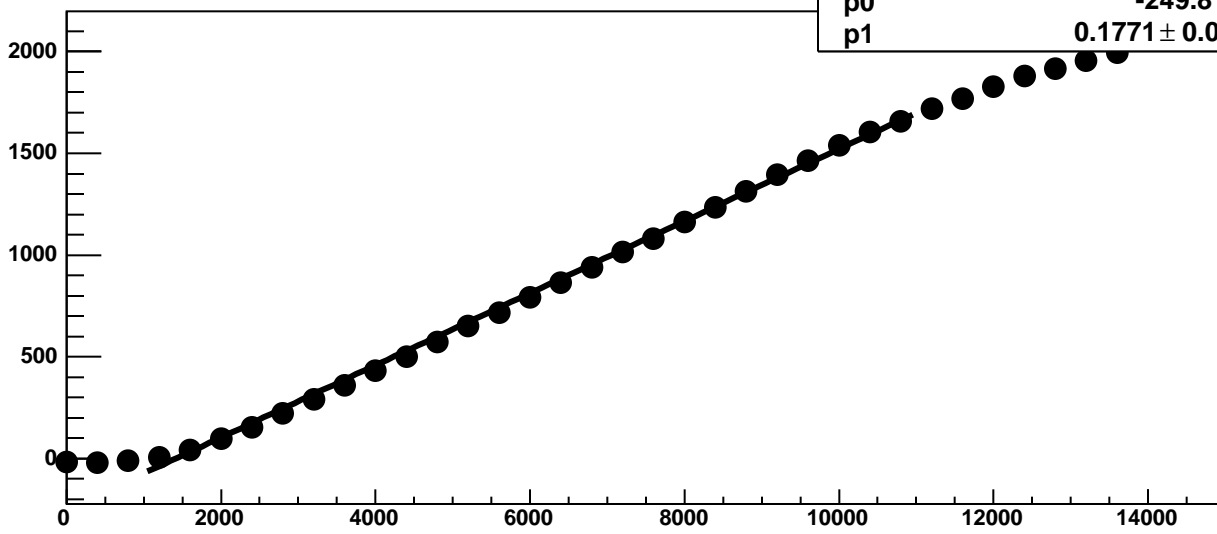
Chip 2, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC

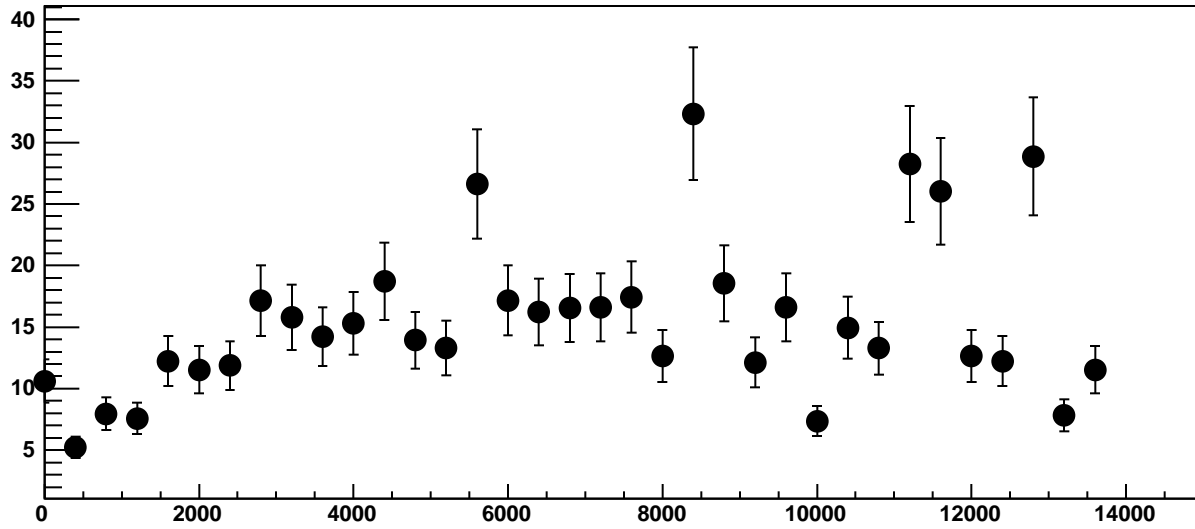


Chip 2, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

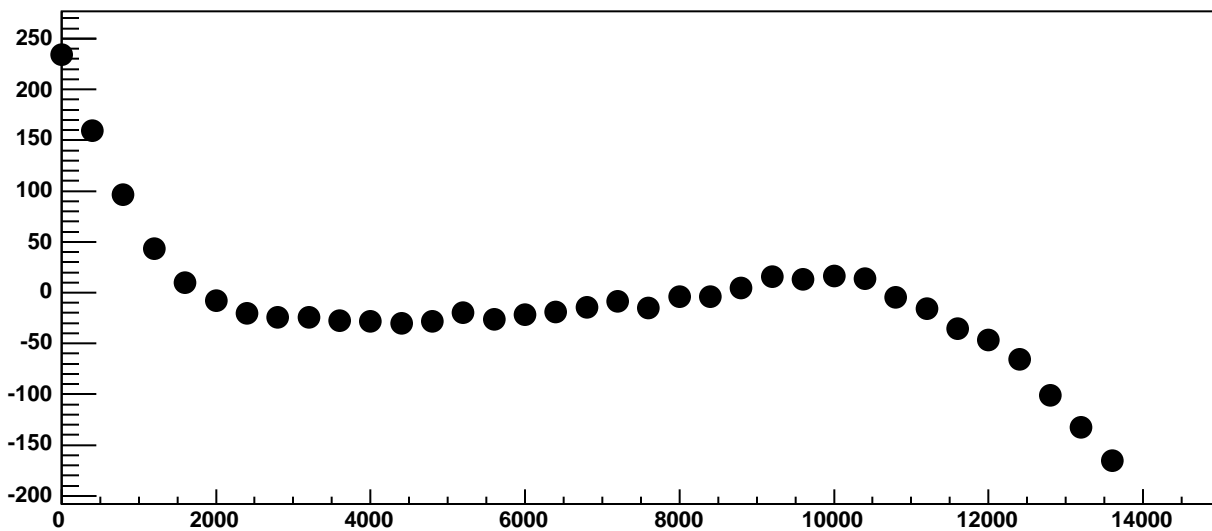


χ^2 / ndf 1363 / 23
p0 -249.8 ± 1.222
p1 0.1771 ± 0.0001829

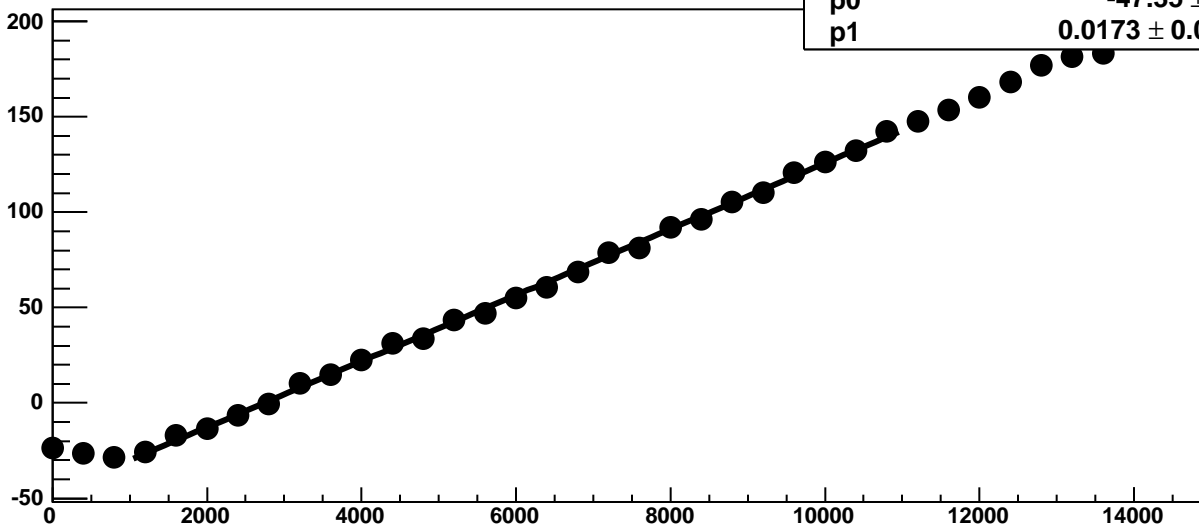
Chip 2, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

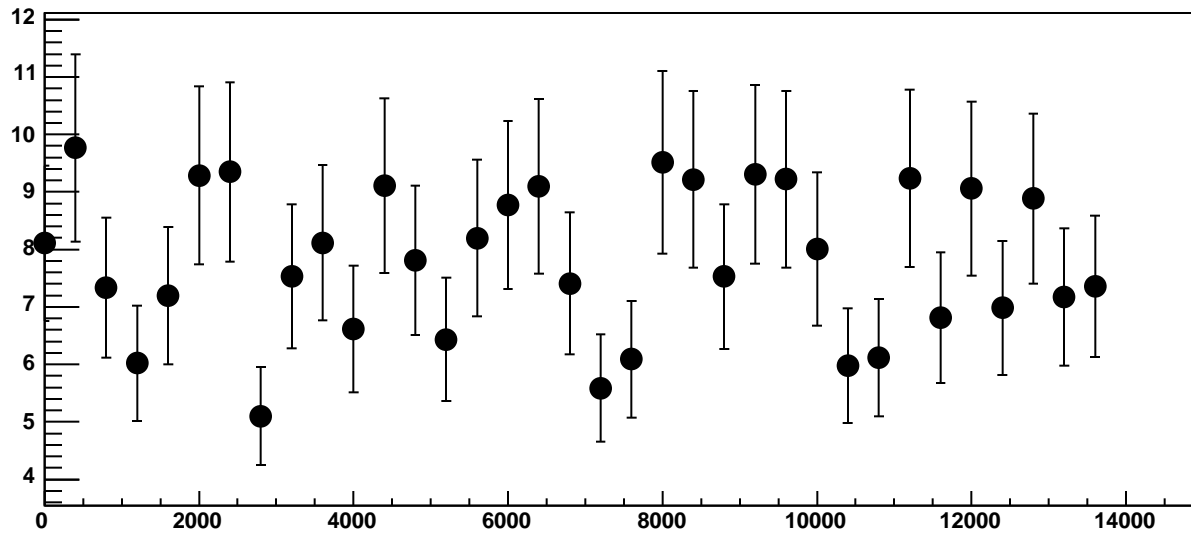


Chip 2, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

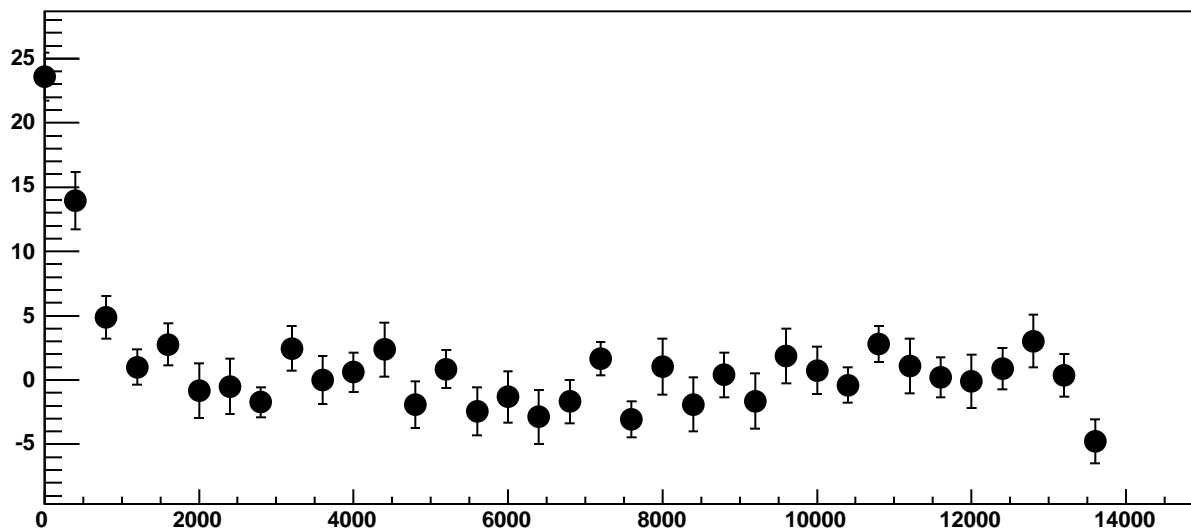


χ^2 / ndf 28.73 / 23
p0 -47.35 ± 0.7457
p1 0.0173 ± 0.0001122

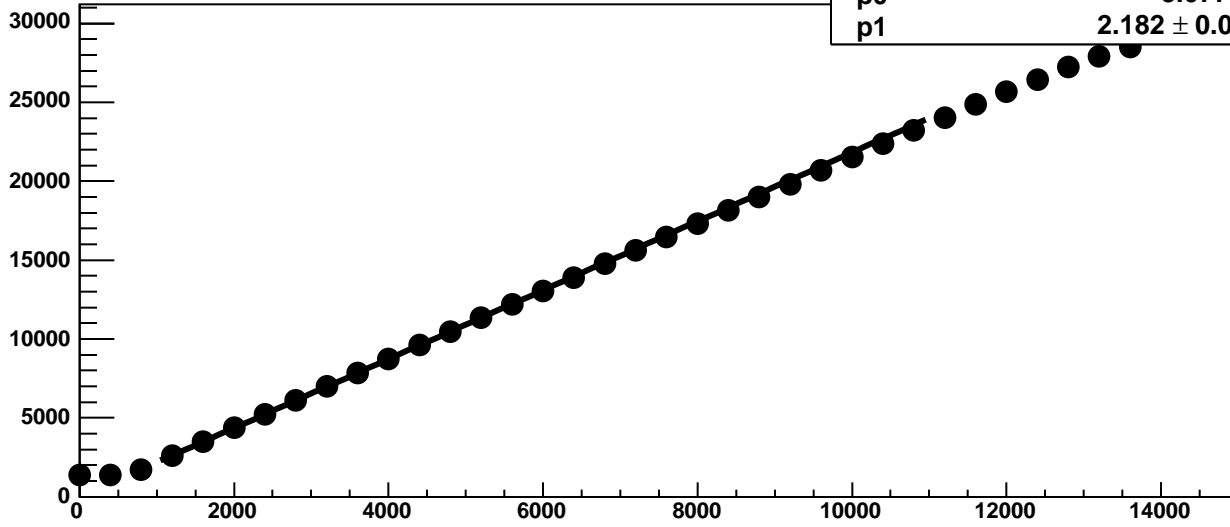
Chip 2, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

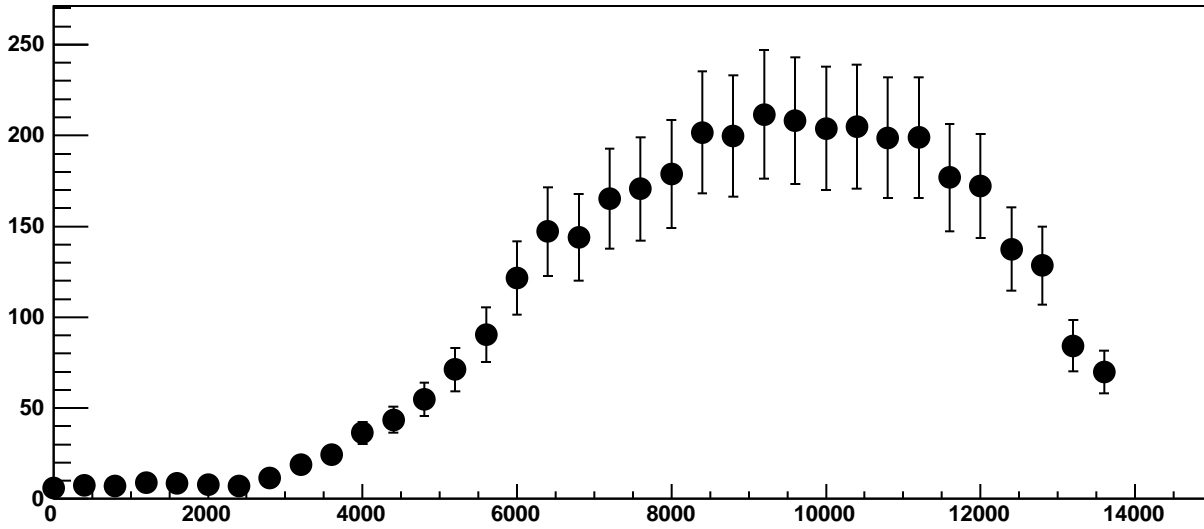


Chip 2, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC

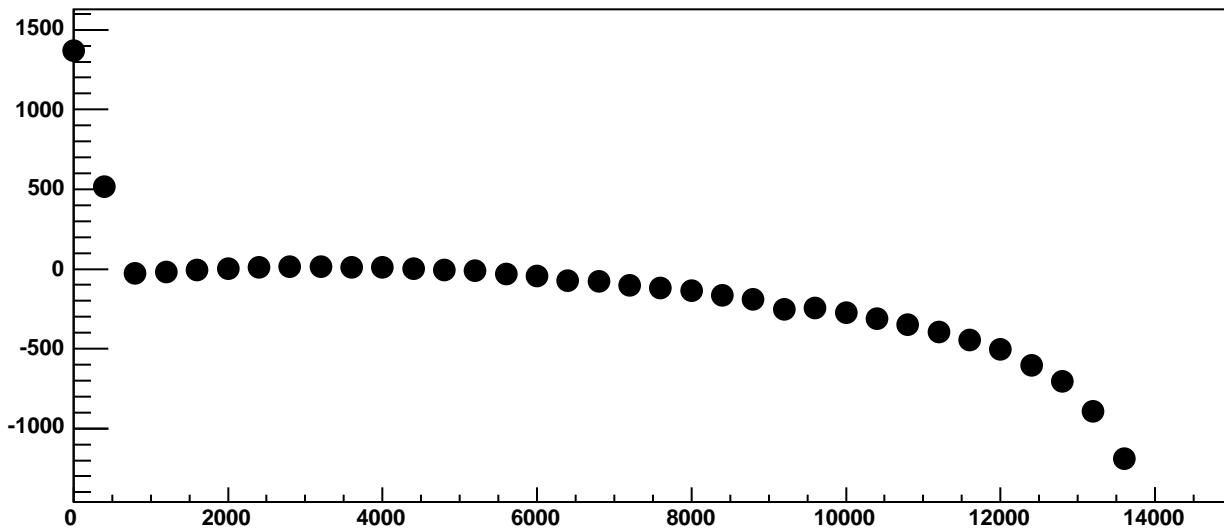


χ^2 / ndf 441 / 23
p0 3.077 ± 2.301
p1 2.182 ± 0.000967

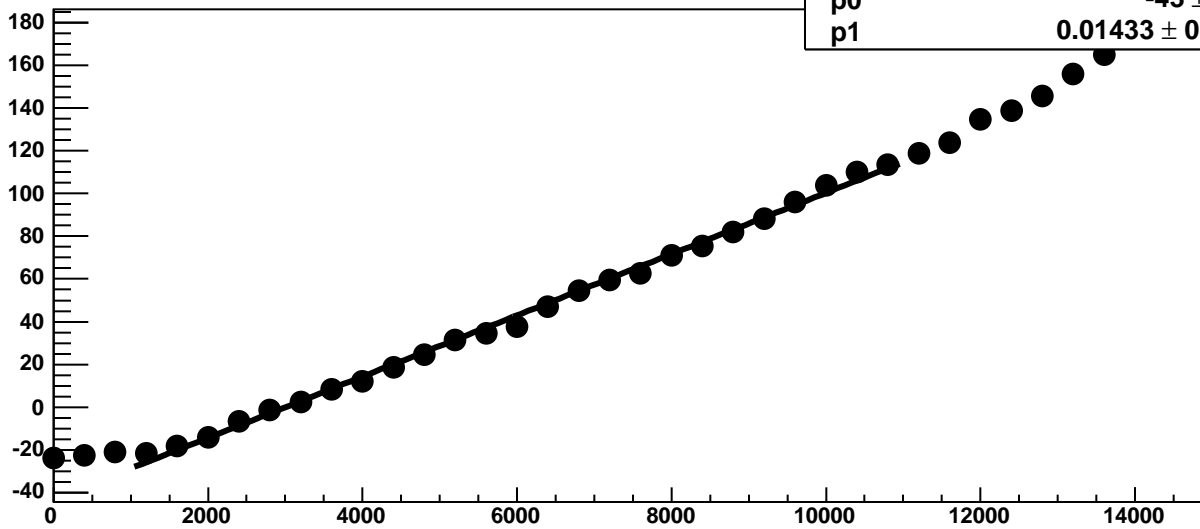
Chip 2, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

31.53 / 23

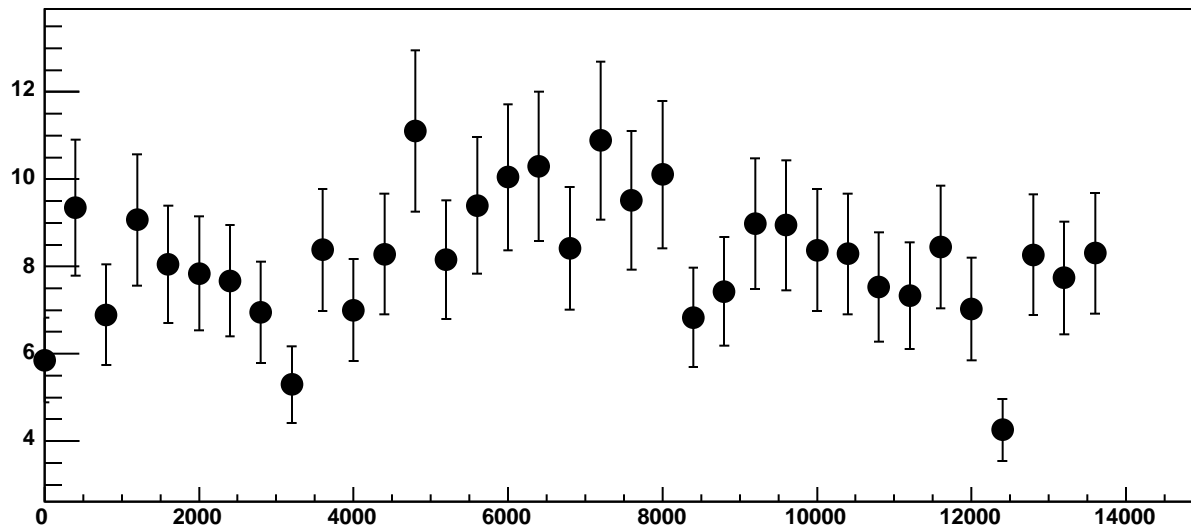
p0

-43 ± 0.8175

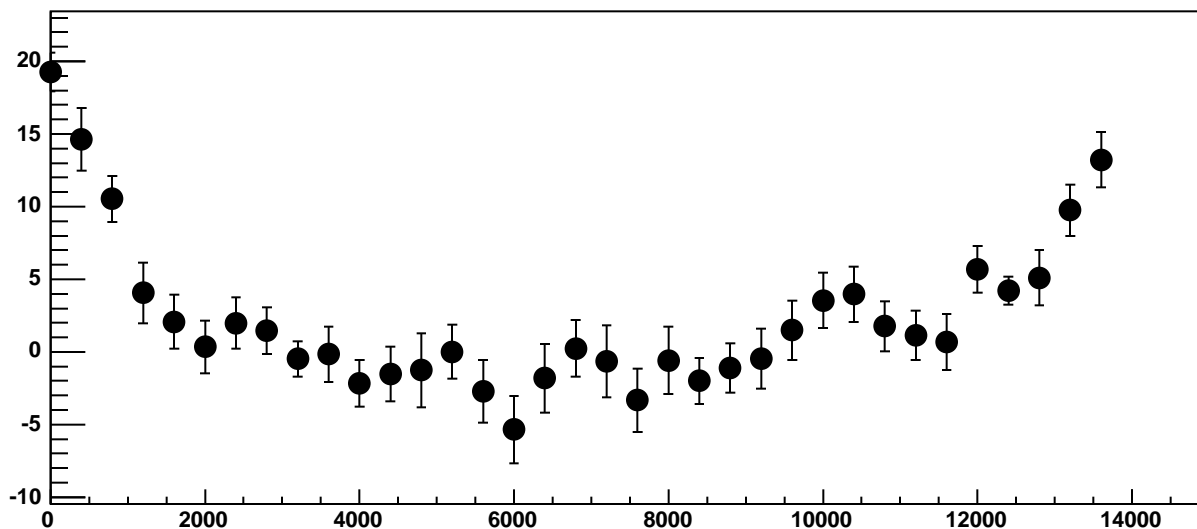
p1

0.01433 ± 0.000126

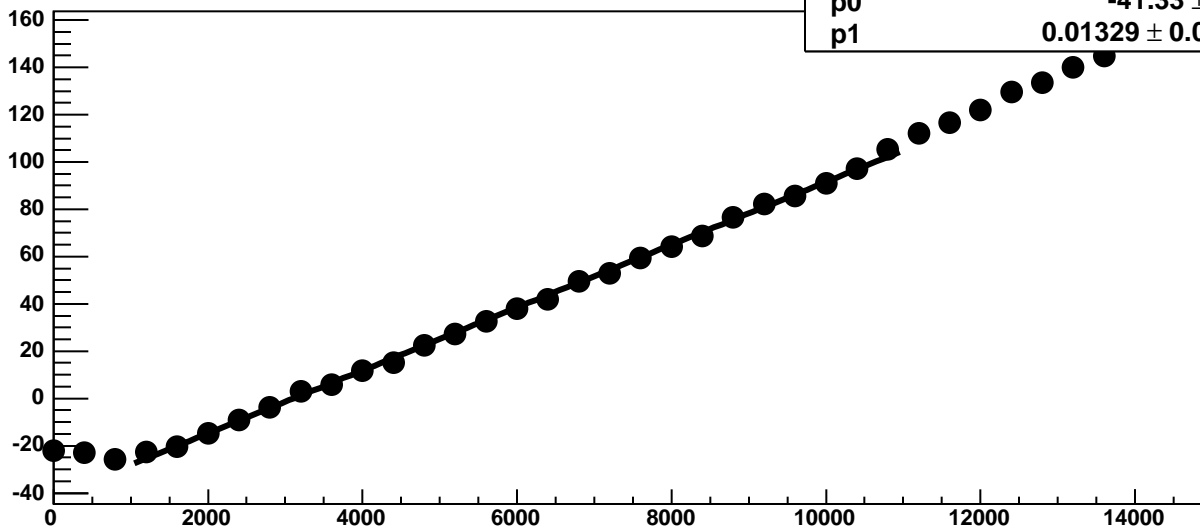
Chip 2, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

10.09 / 23

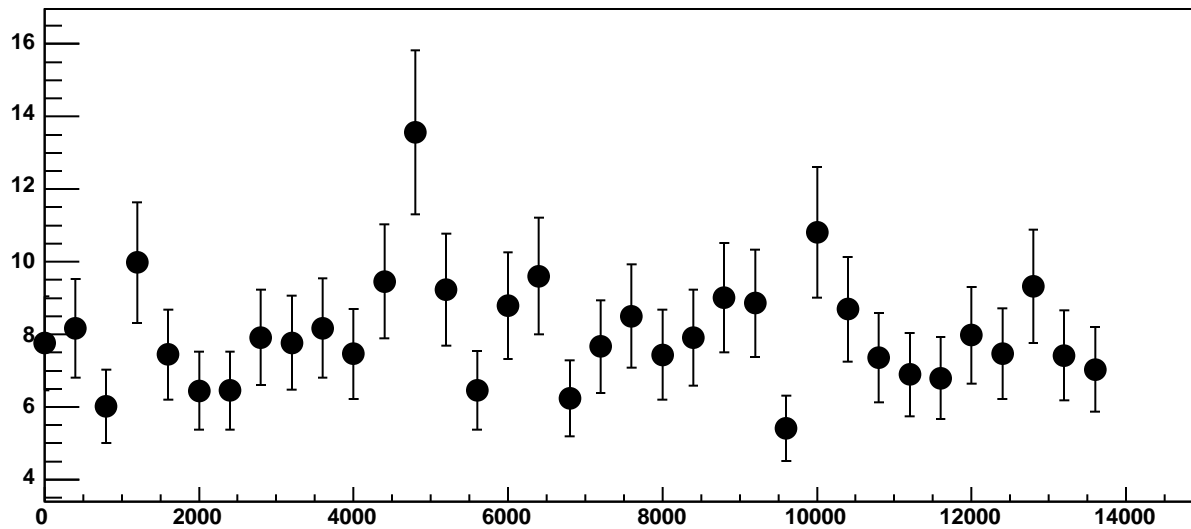
p0

-41.33 ± 0.8178

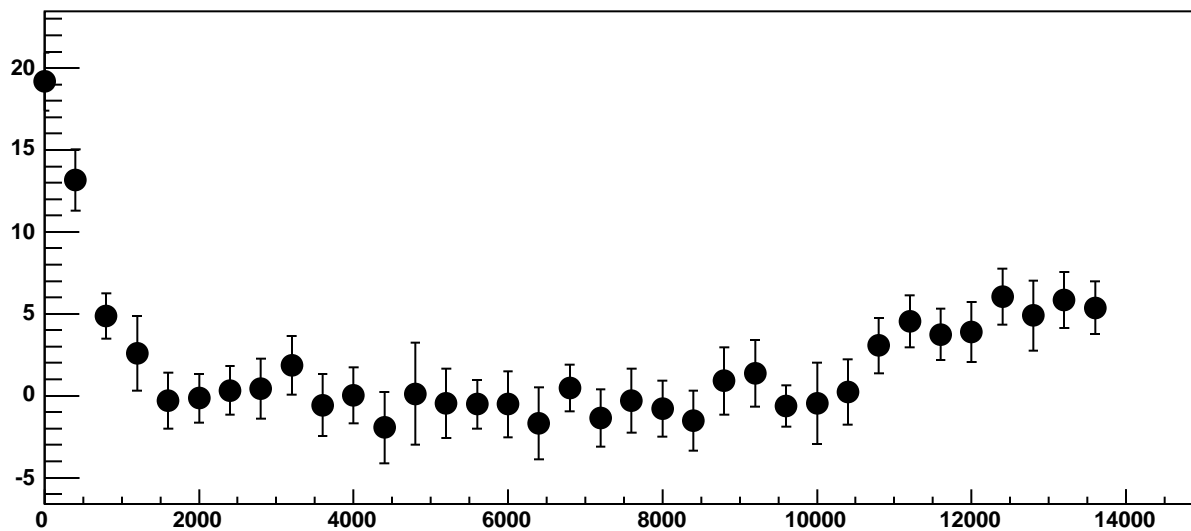
p1

0.01329 ± 0.0001225

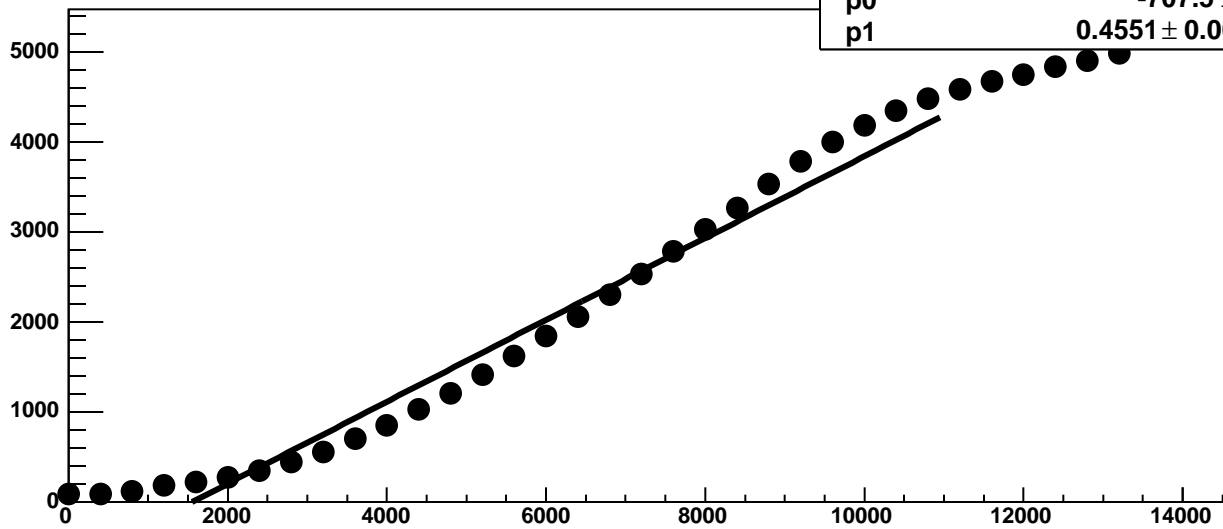
Chip 2, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



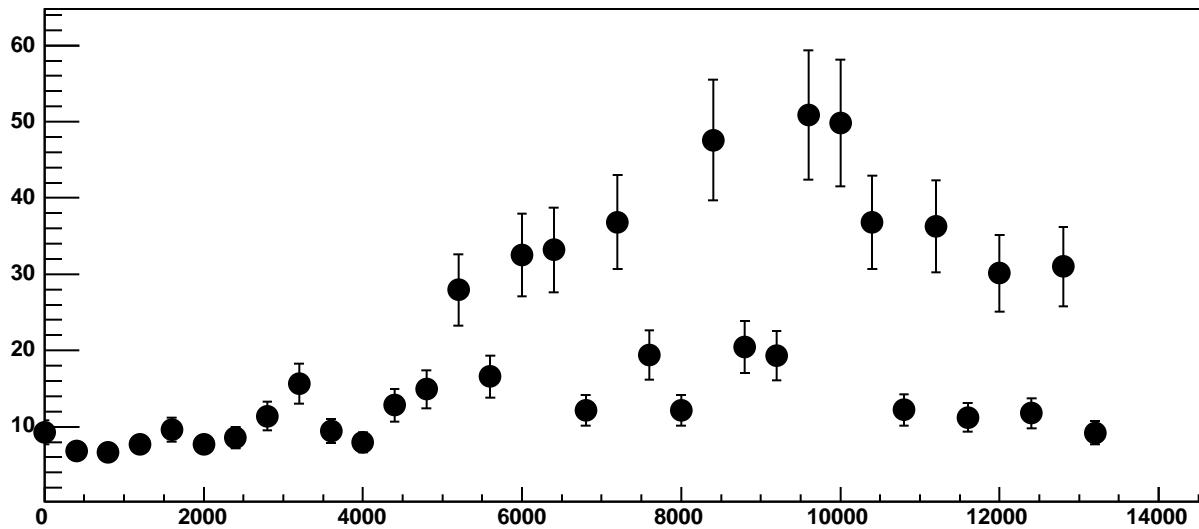
Chip 2, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



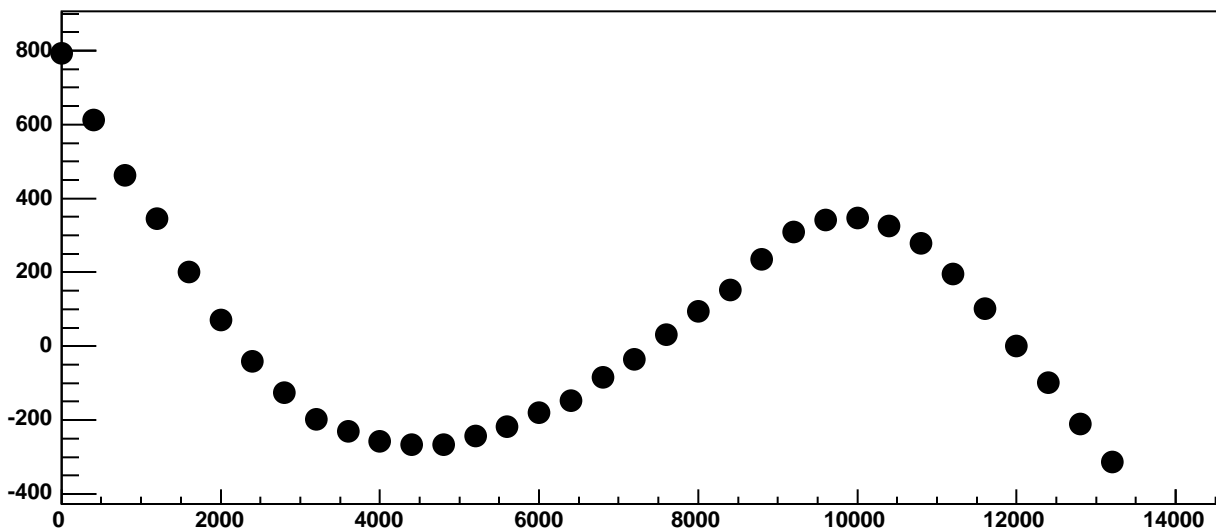
Chip 2, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC



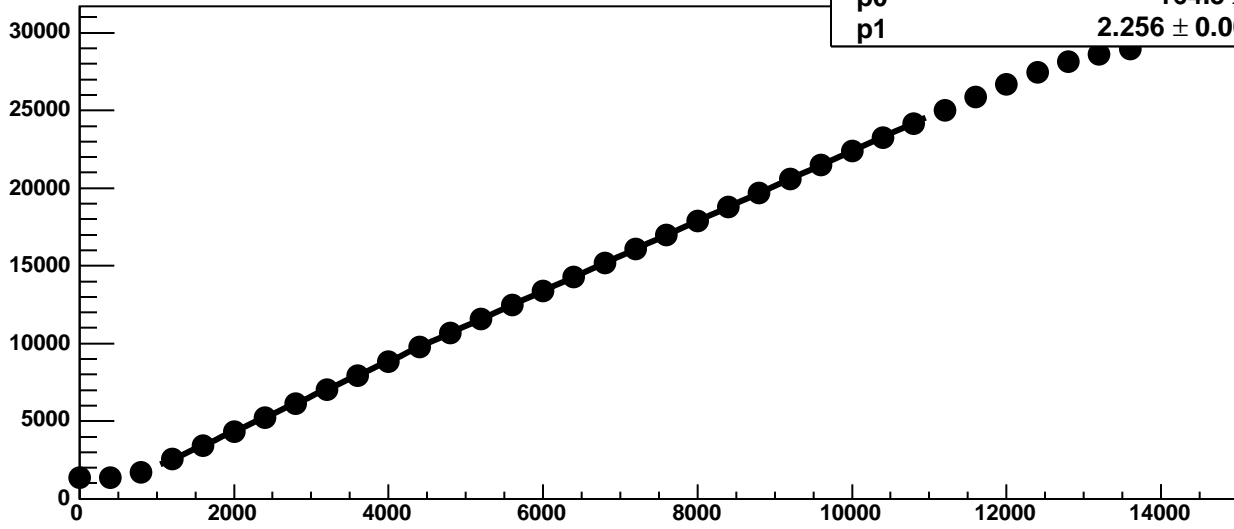
Chip 2, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC

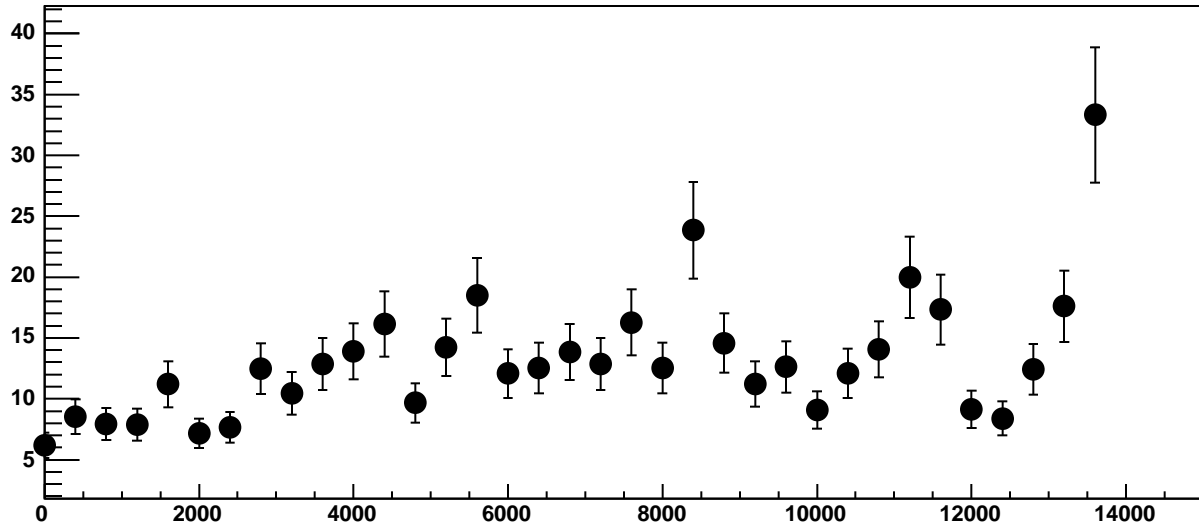


Chip 2, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC

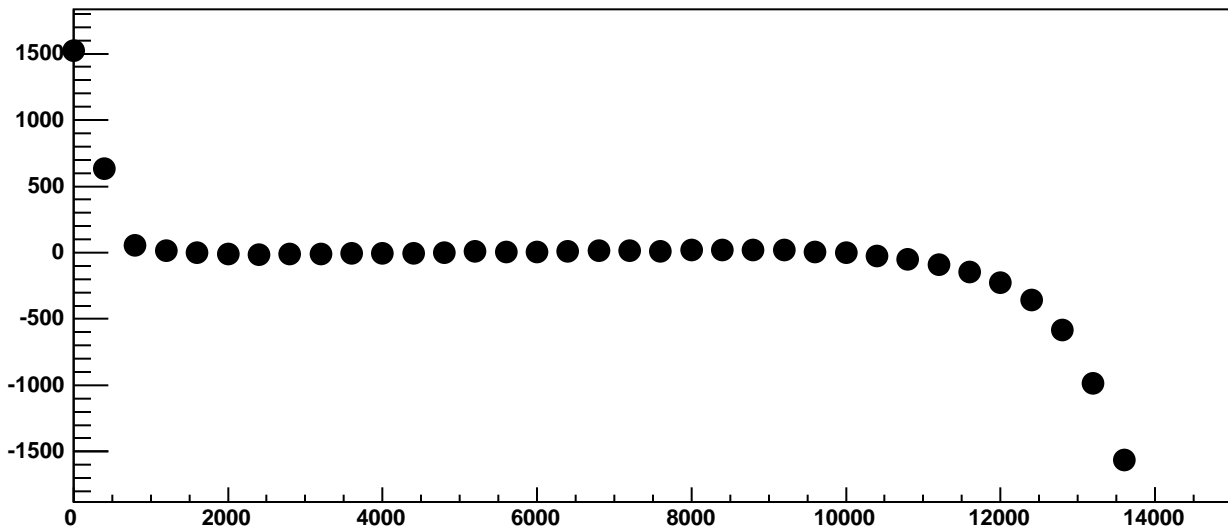


χ^2 / ndf 787.1 / 23
p0 -164.5 ± 1.023
p1 2.256 ± 0.0001675

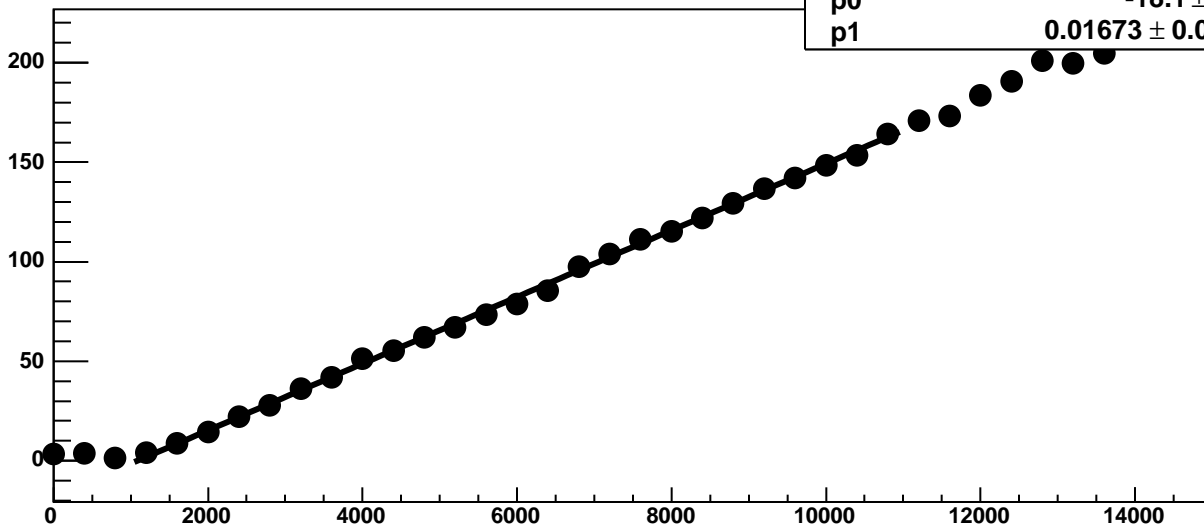
Chip 2, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC

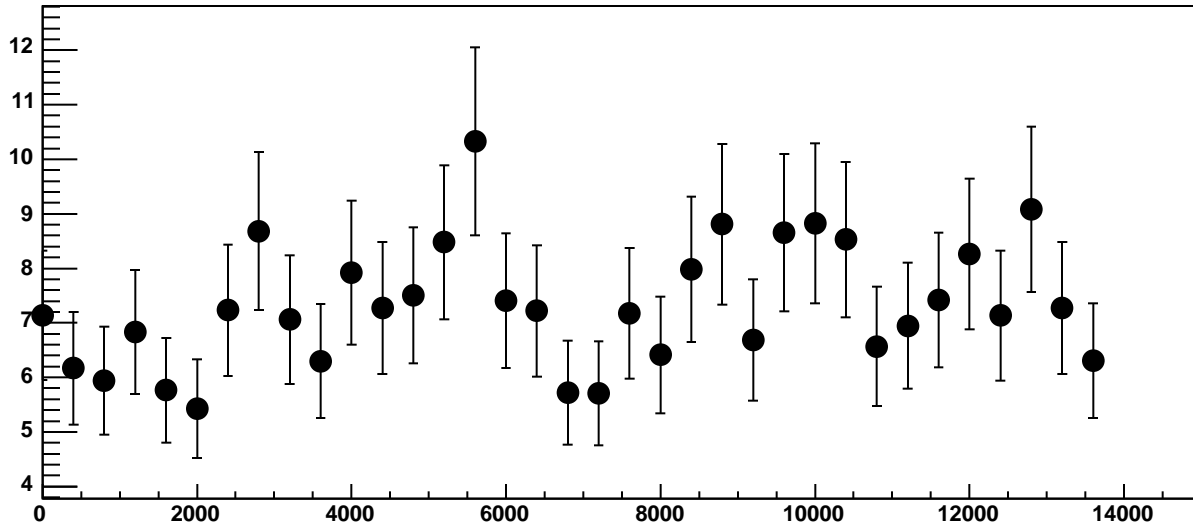


Chip 2, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

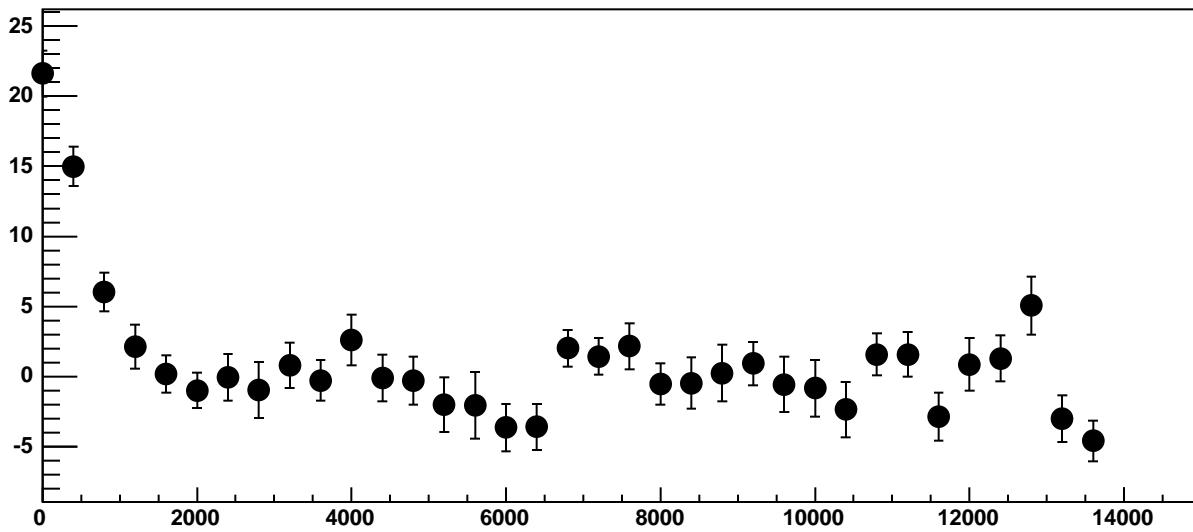


χ^2 / ndf 24.92 / 23
p0 -18.1 ± 0.7178
p1 0.01673 ± 0.0001115

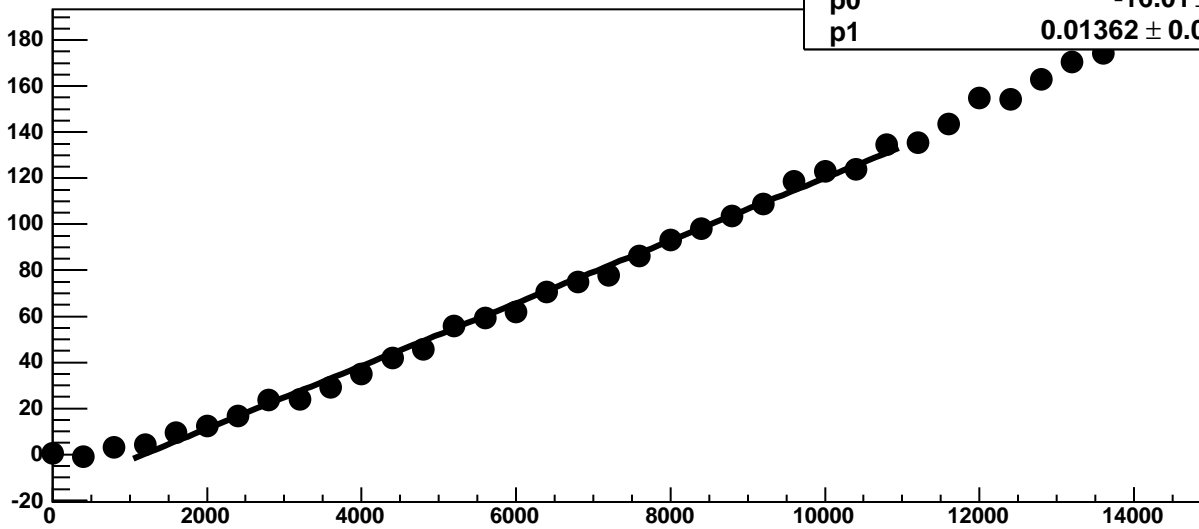
Chip 2, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



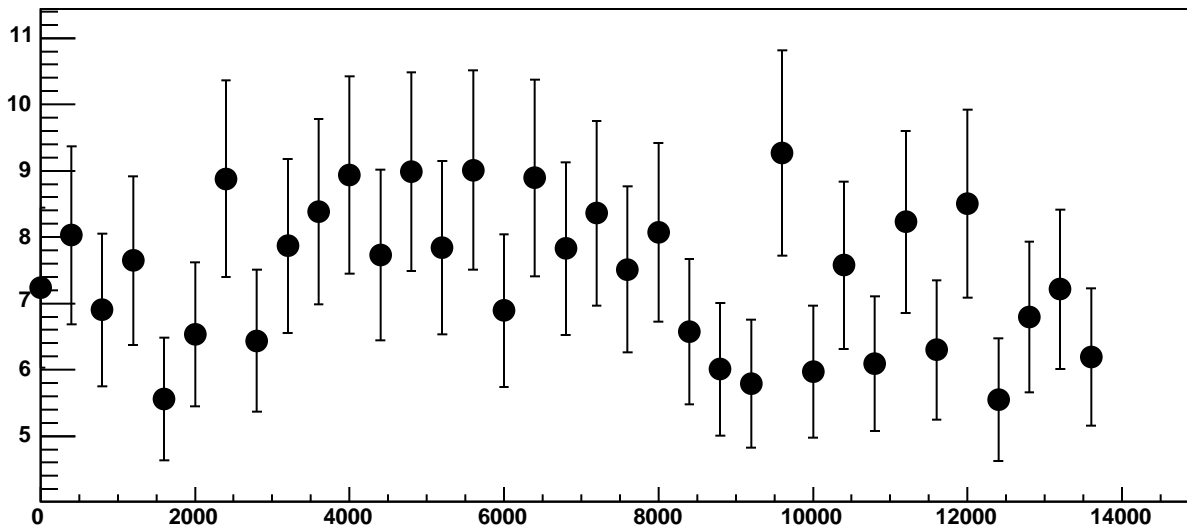
Chip 2, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



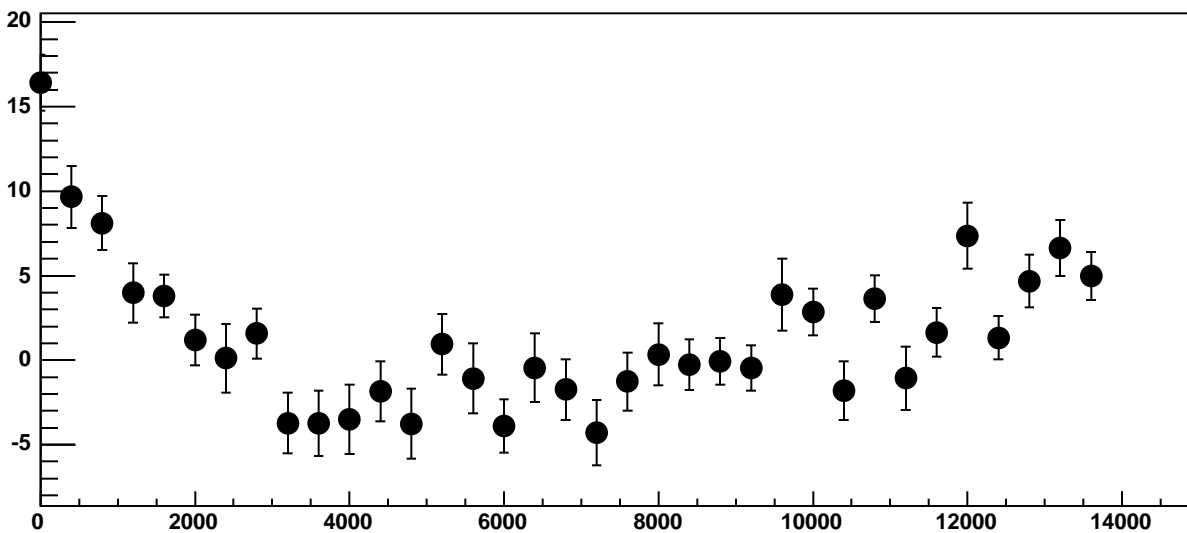
Chip 2, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



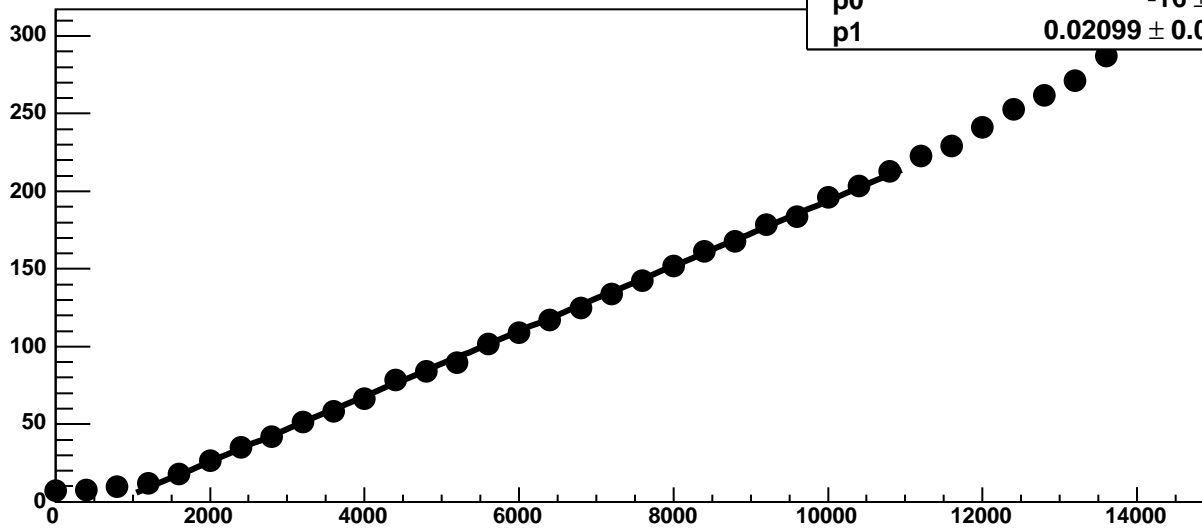
Chip 2, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

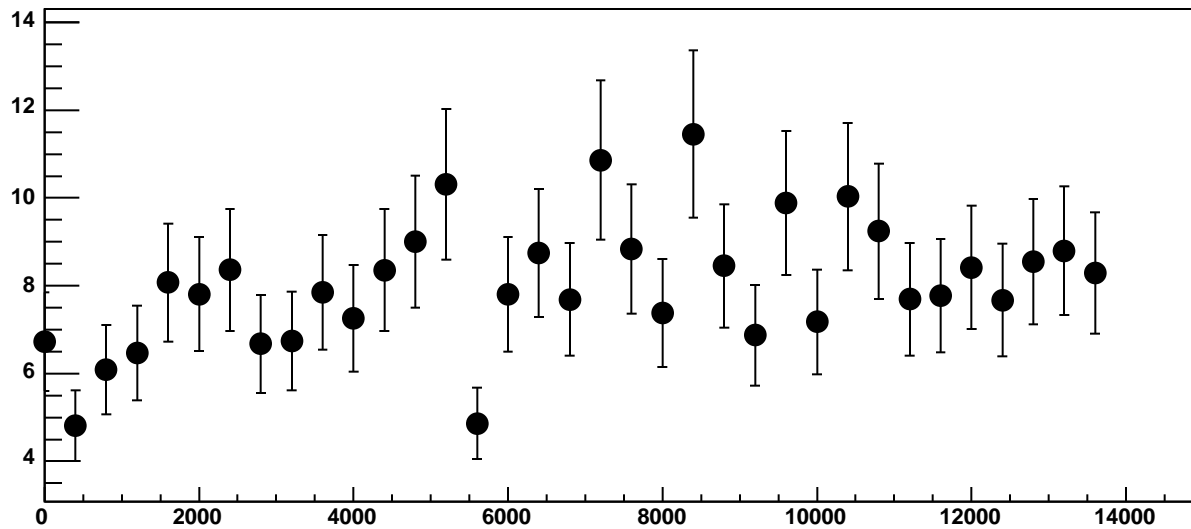


Chip 2, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

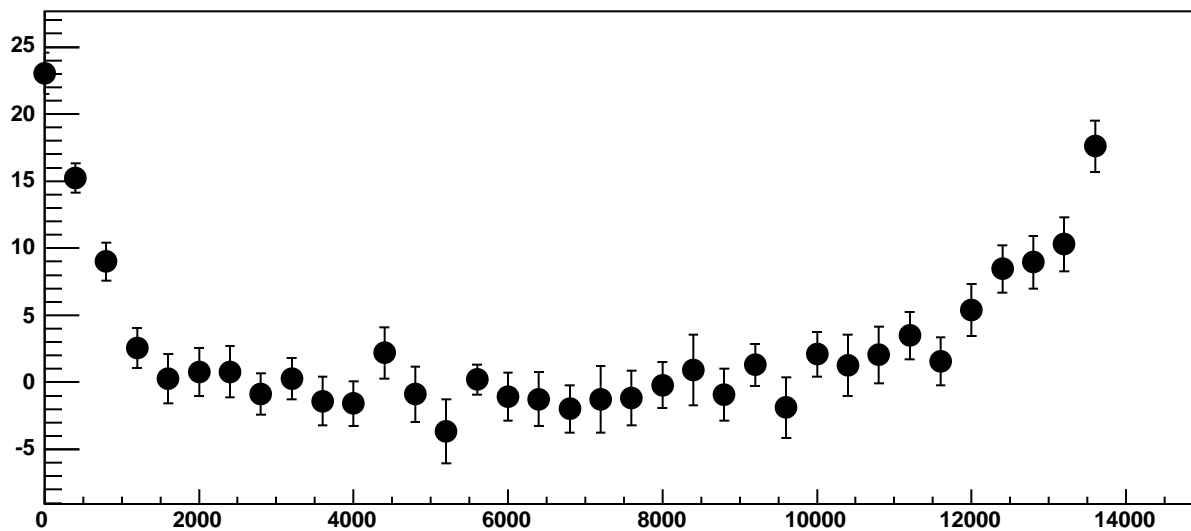


χ^2 / ndf 16.26 / 23
p0 -16 ± 0.7987
p1 0.02099 ± 0.0001265

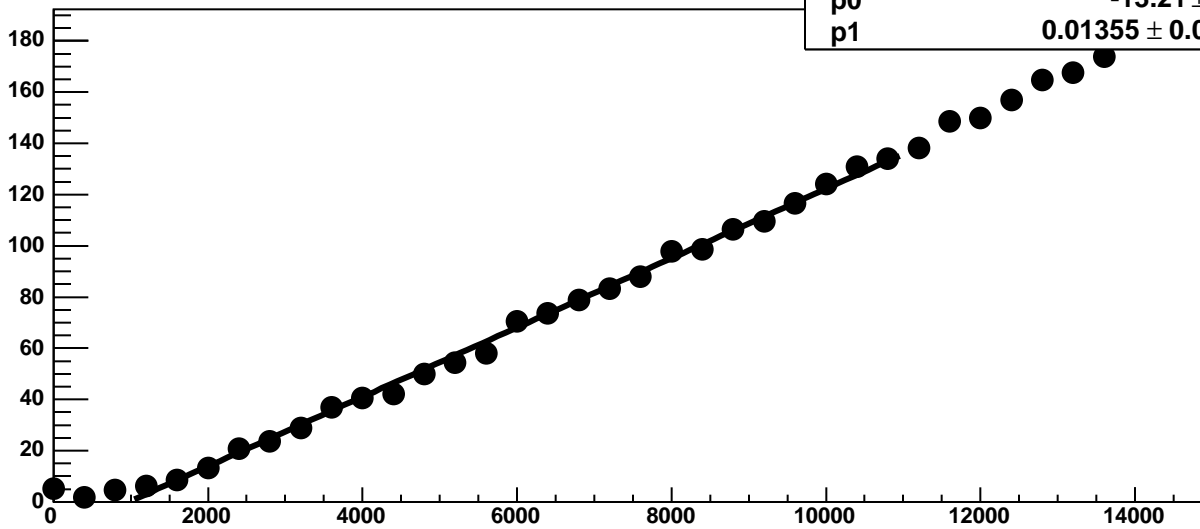
Chip 2, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

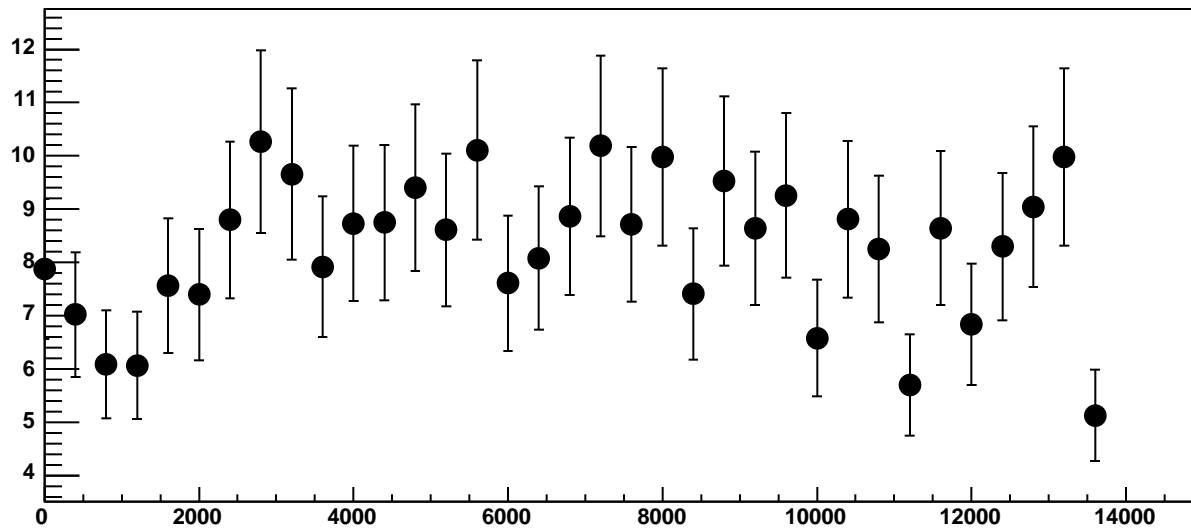


Chip 2, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

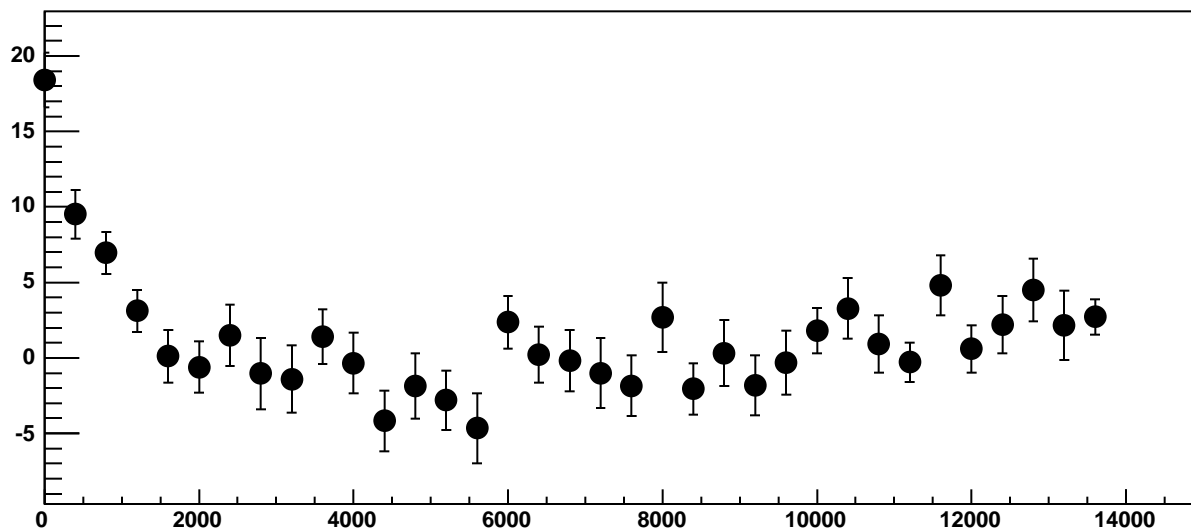


χ^2 / ndf 28.91 / 23
p0 -13.21 ± 0.8295
p1 0.01355 ± 0.0001256

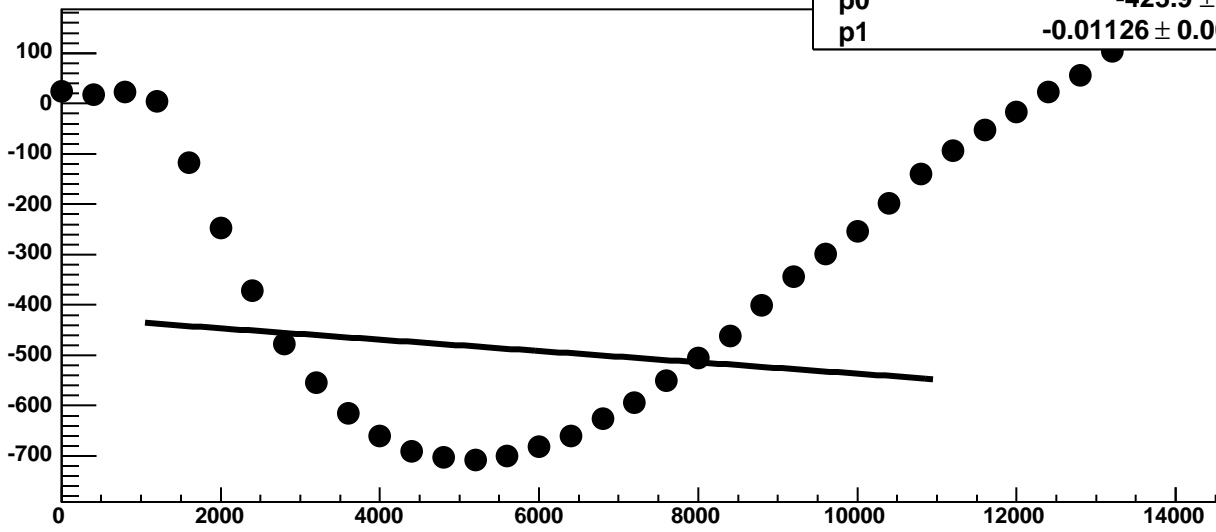
Chip 2, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



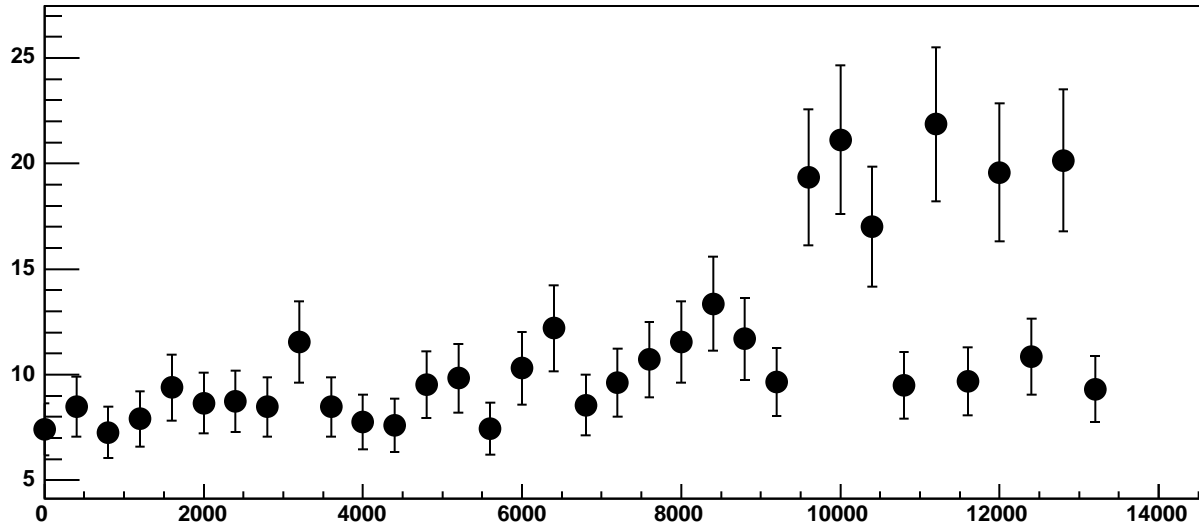
Chip 2, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



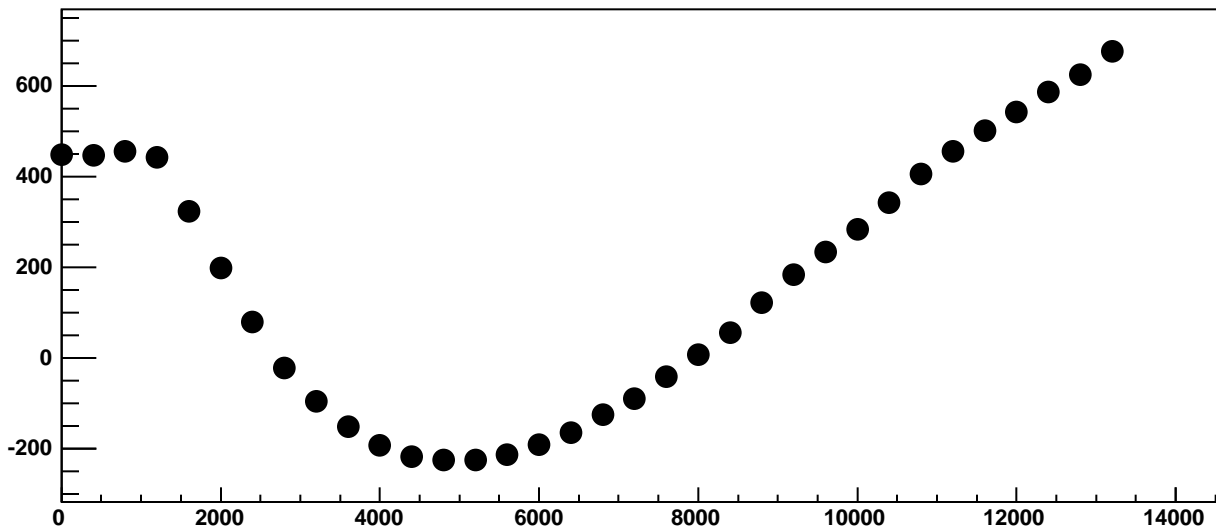
Chip 2, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



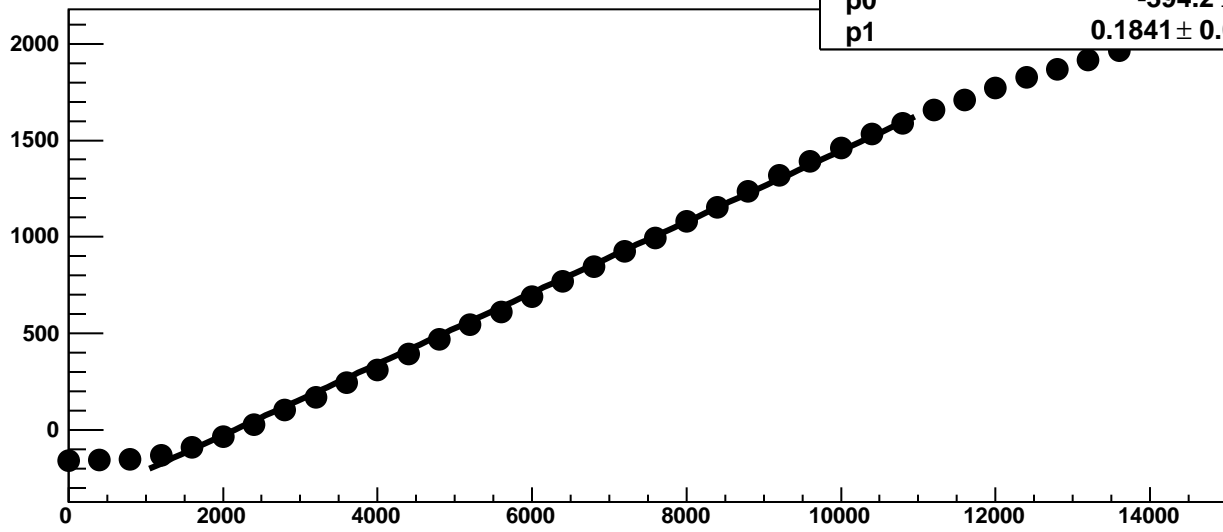
Chip 2, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



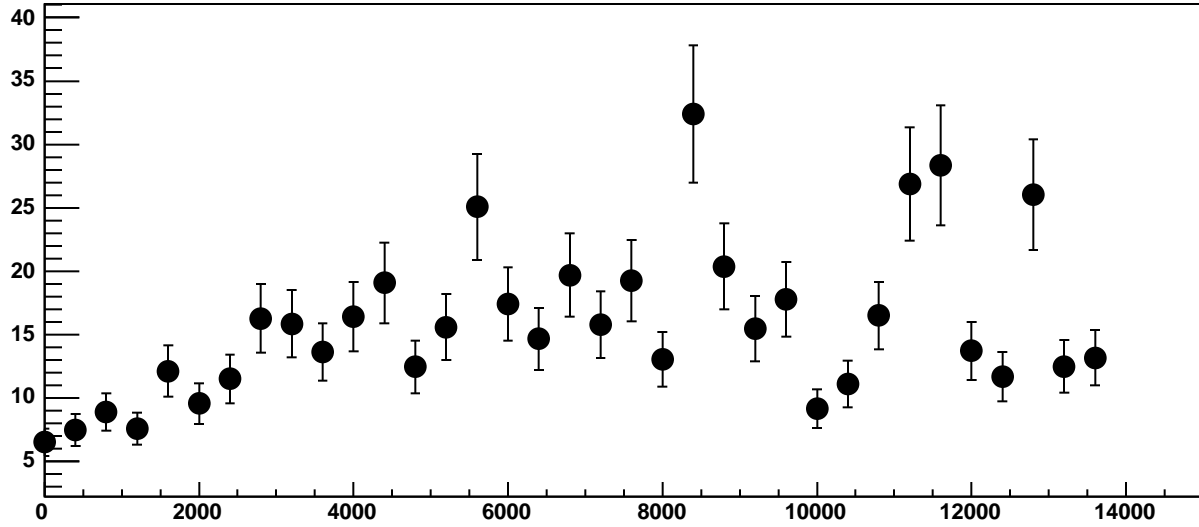
Chip 2, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



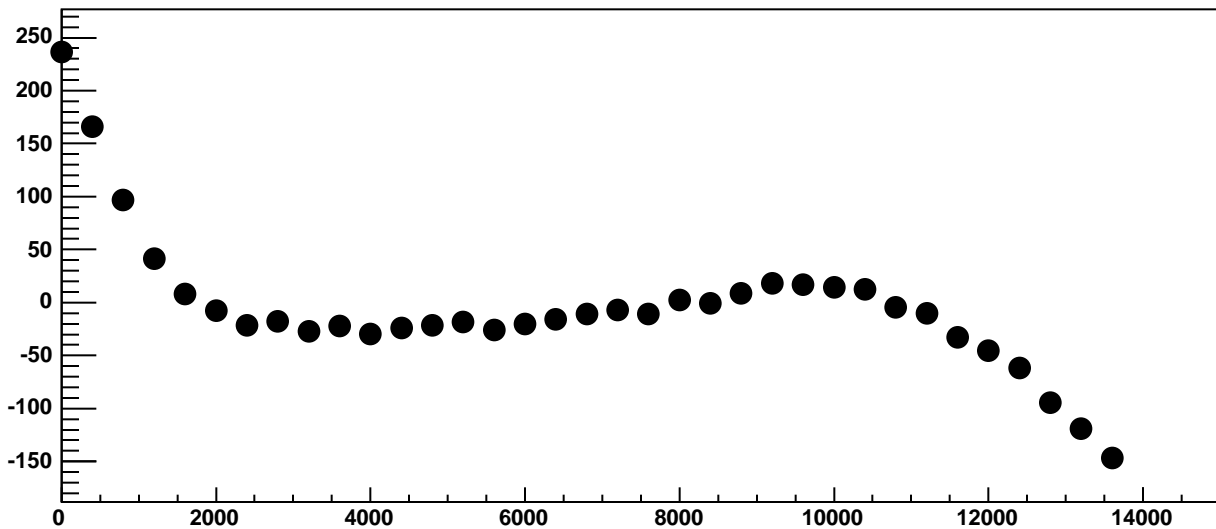
Chip 2, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC



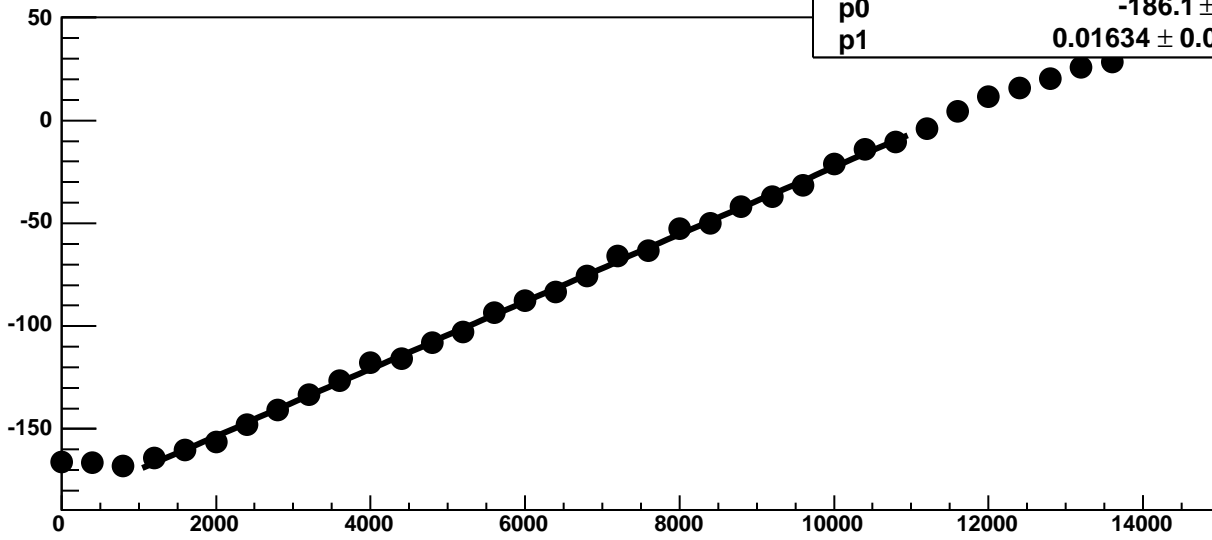
Chip 2, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC

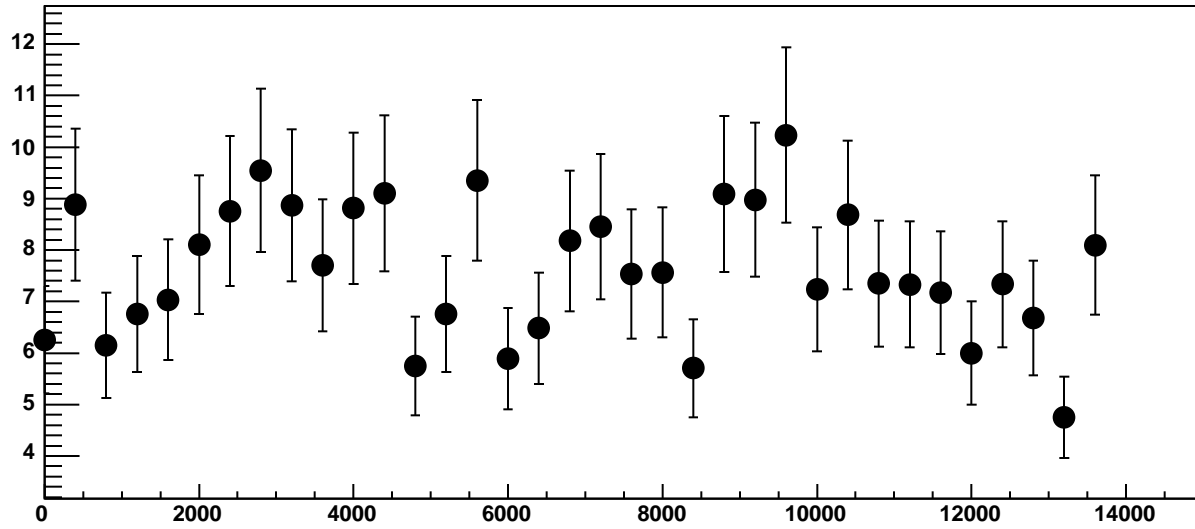


Chip 2, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

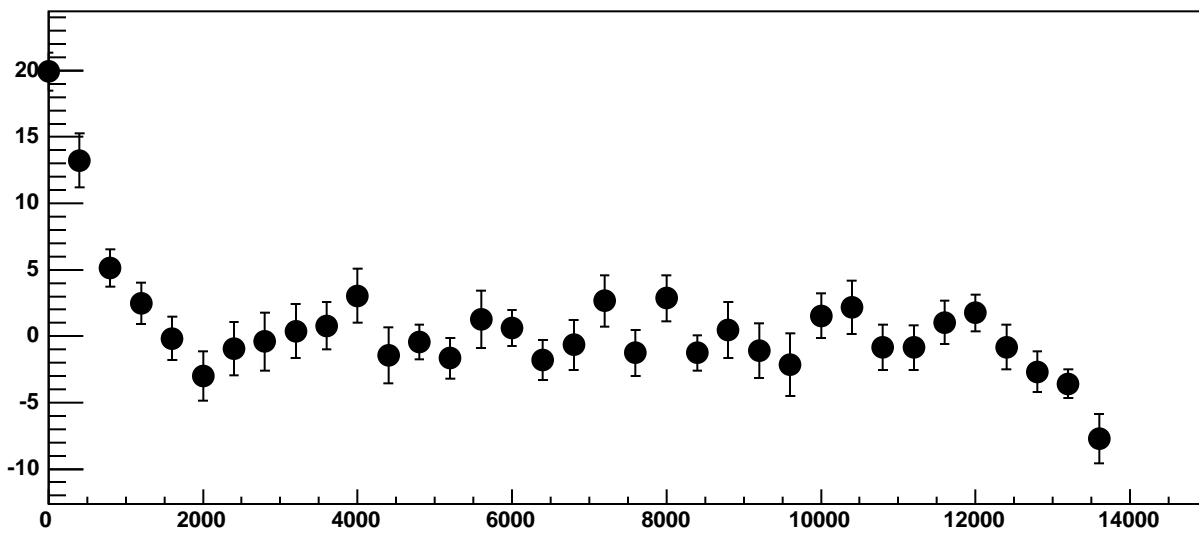


χ^2 / ndf 21.29 / 23
p0 -186.1 ± 0.8198
p1 0.01634 ± 0.0001241

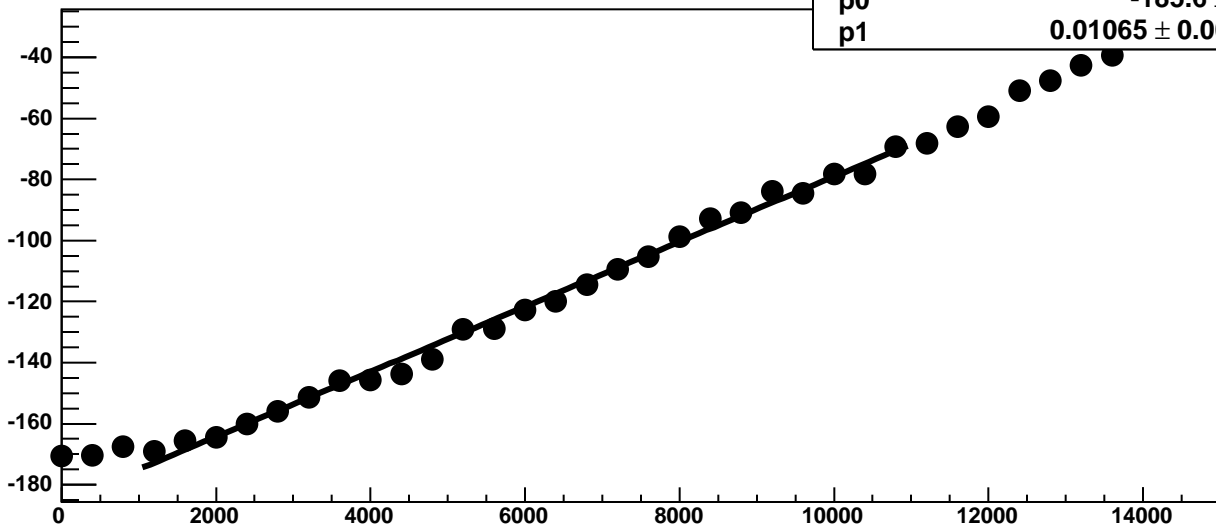
Chip 2, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

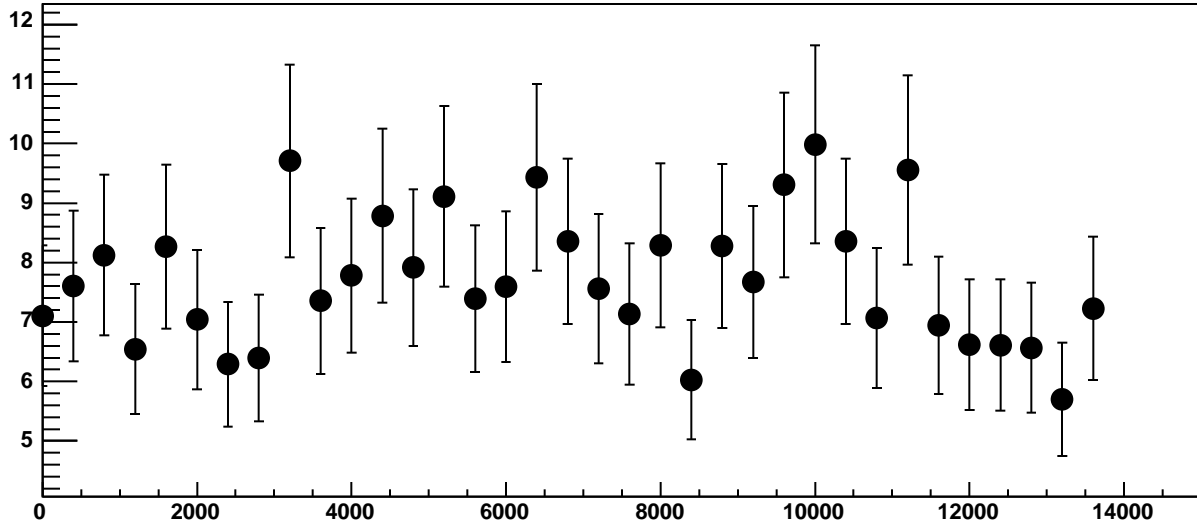


Chip 2, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

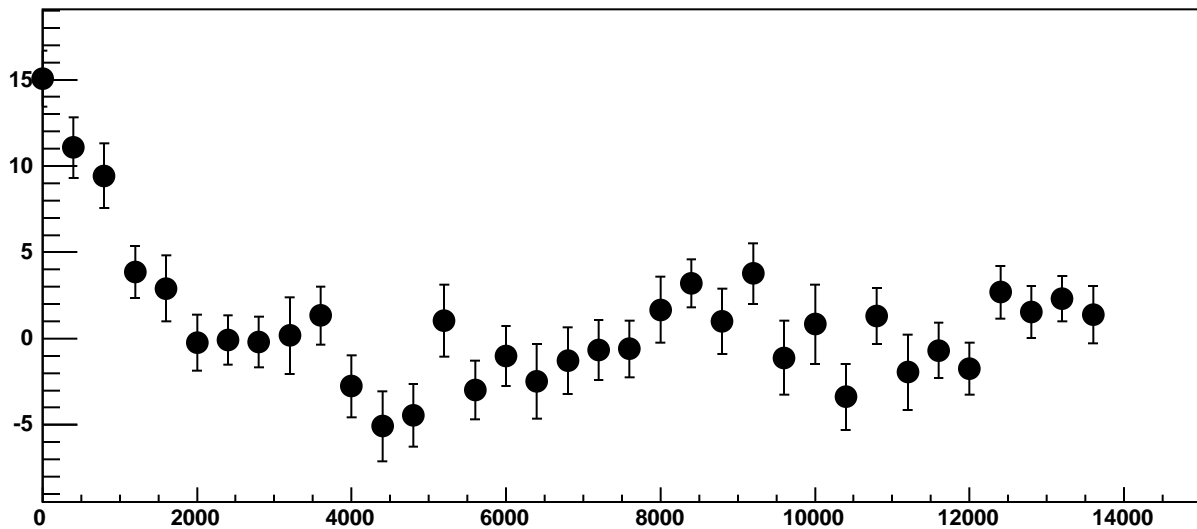


χ^2 / ndf 45.24 / 23
p0 -185.6 ± 0.778
p1 0.01065 ± 0.0001198

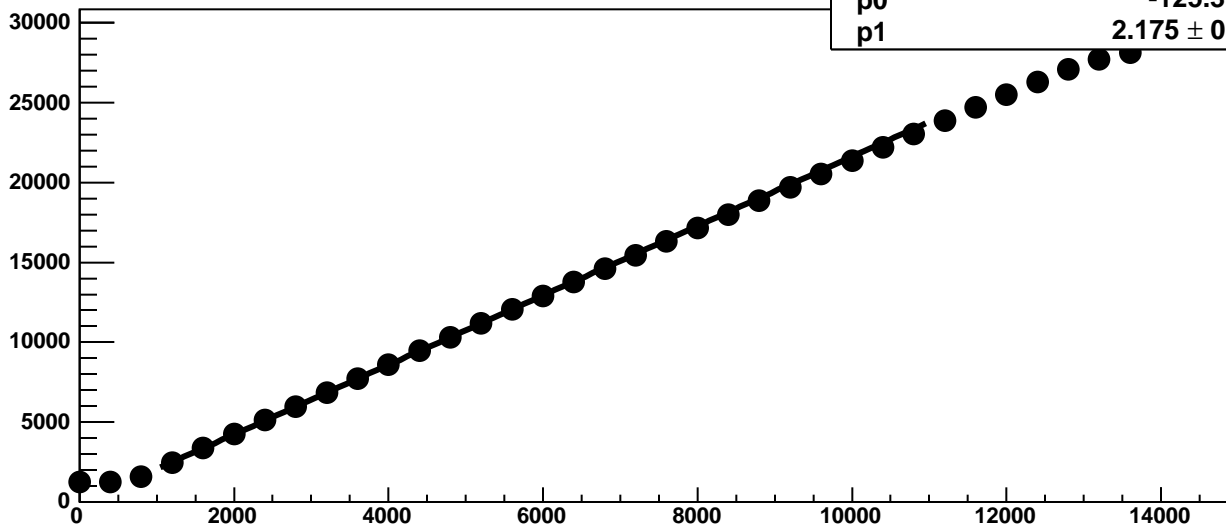
Chip 2, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



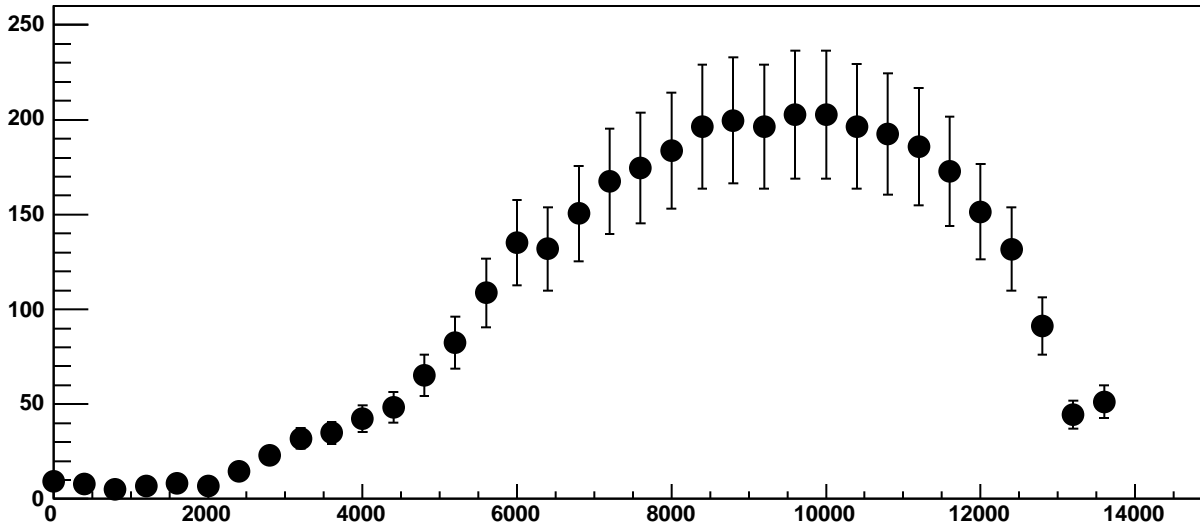
Chip 2, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



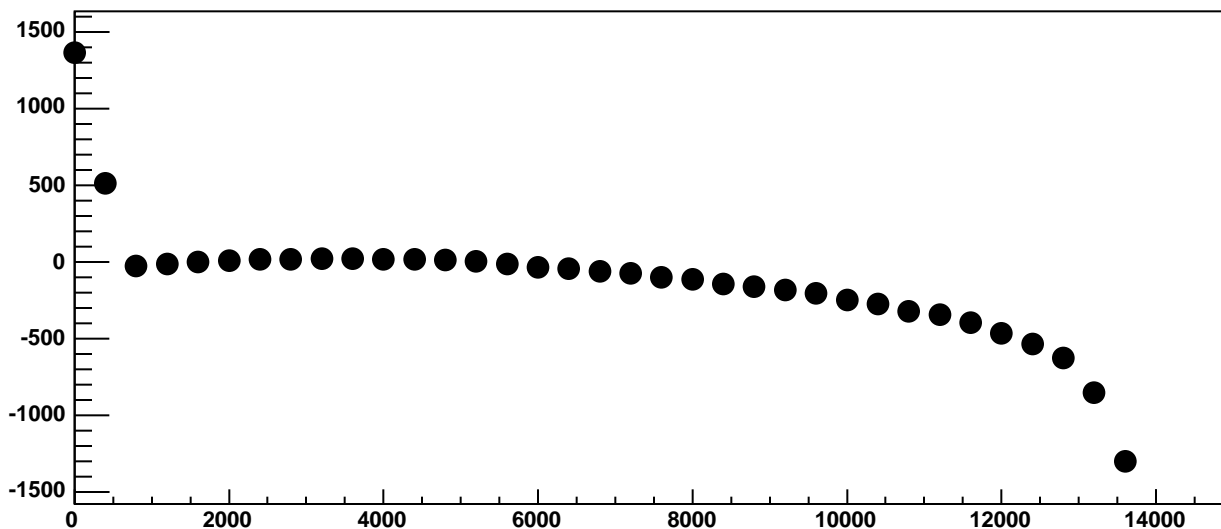
Chip 2, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC



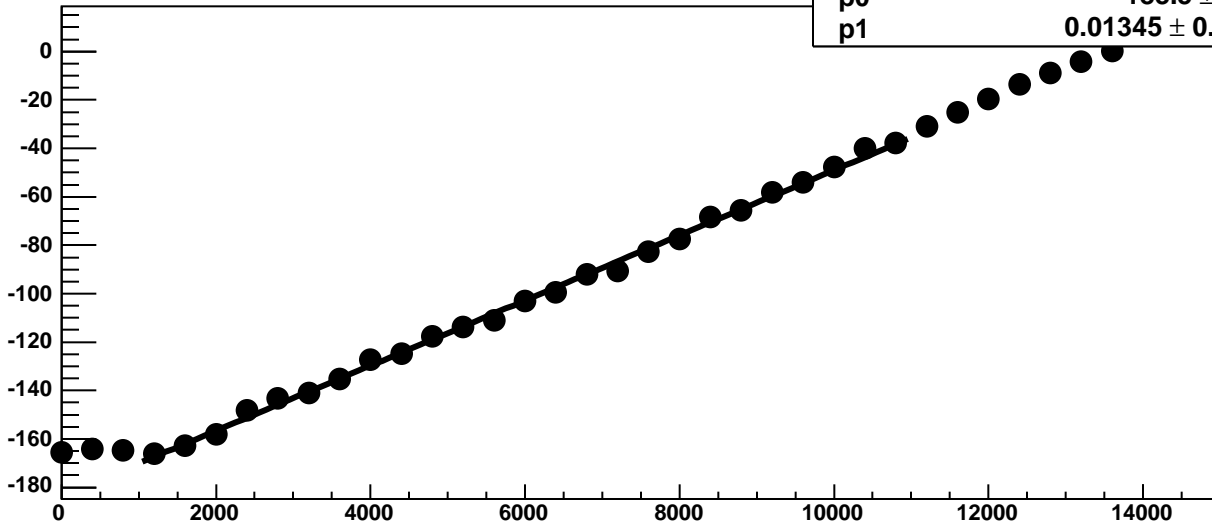
Chip 2, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



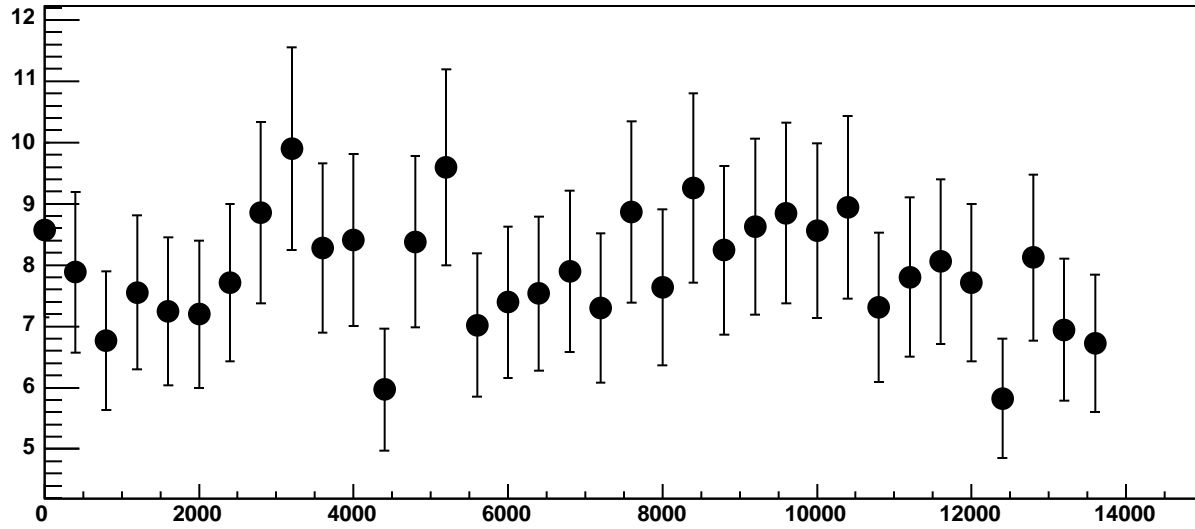
Chip 2, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC



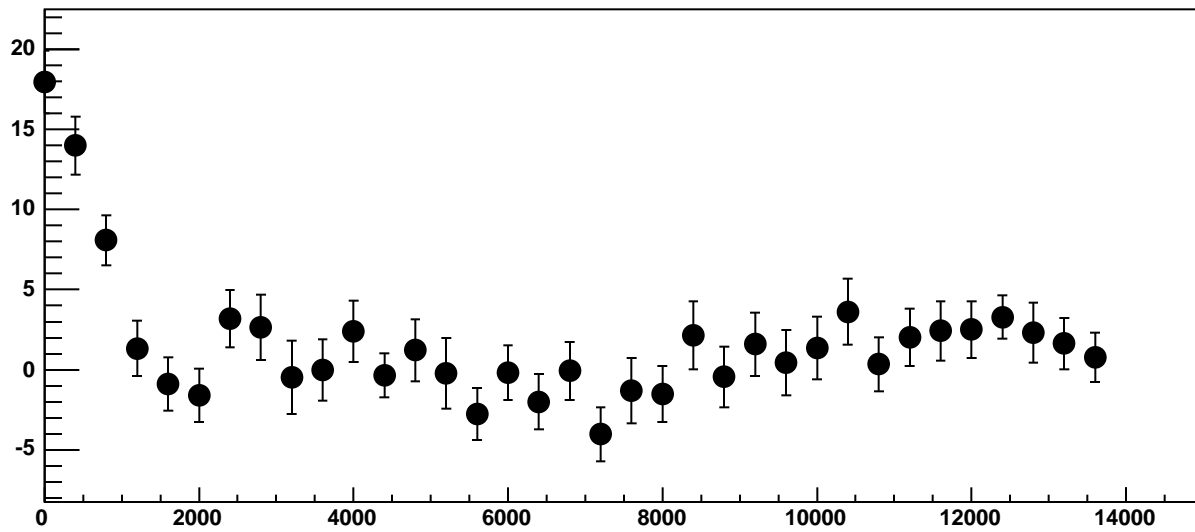
Chip 2, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



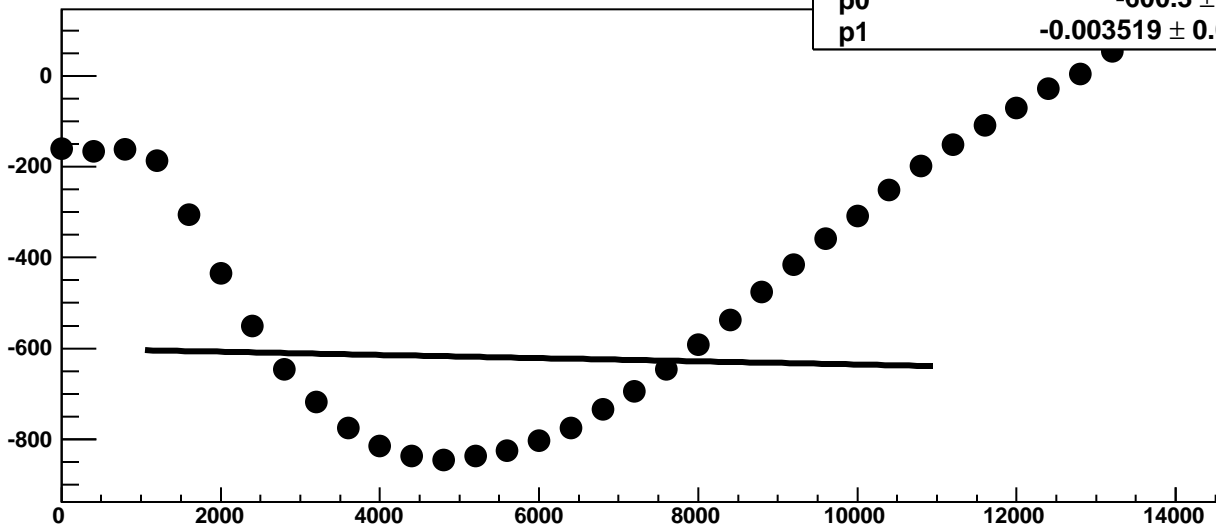
Chip 2, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

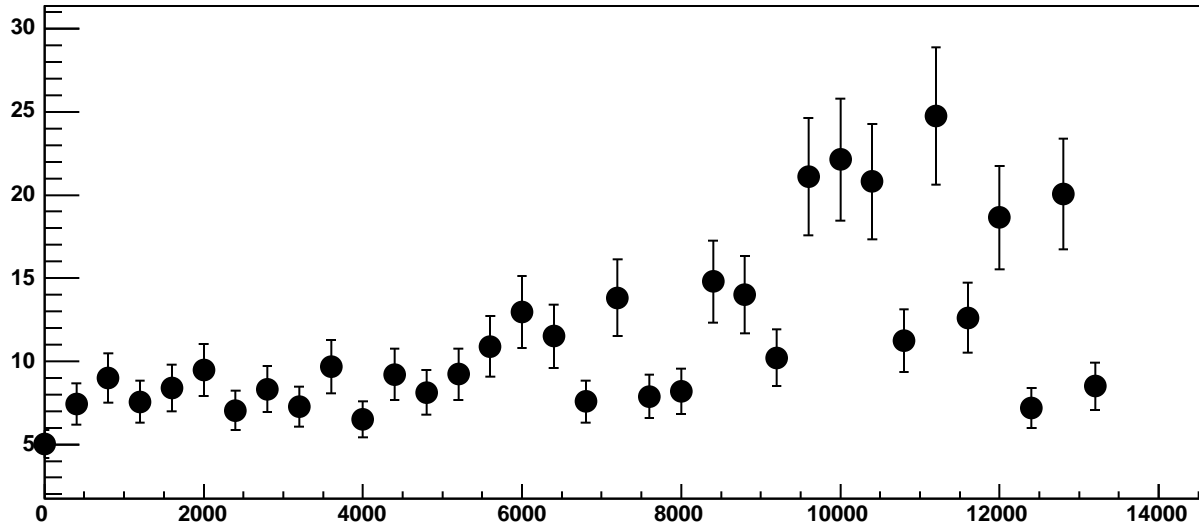


Chip 2, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

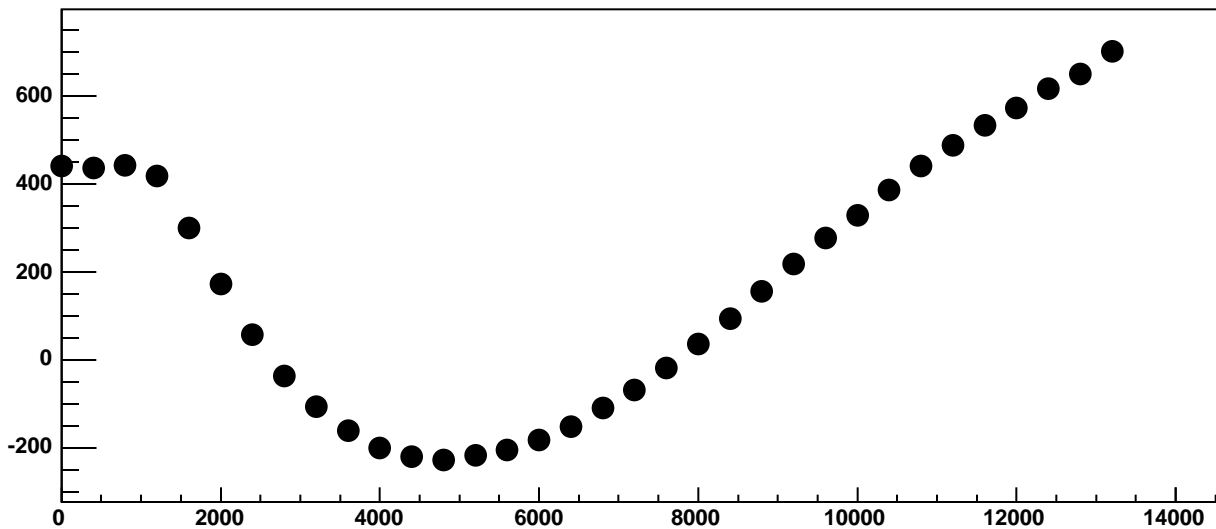


χ^2 / ndf 2.28e+05 / 23
p0 -600.3 ± 0.9329
p1 -0.003519 ± 0.000166

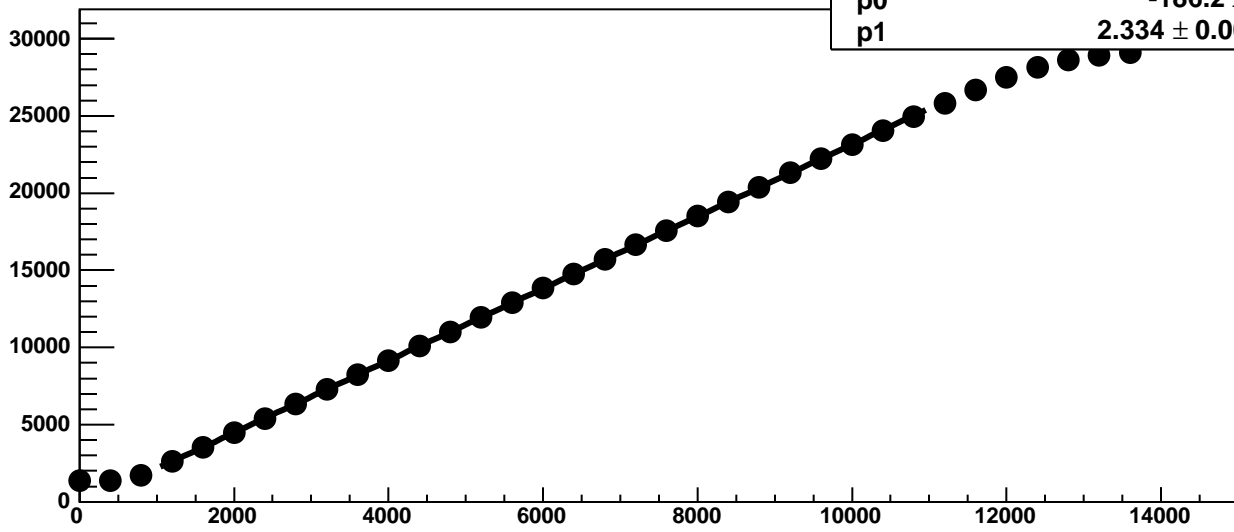
Chip 2, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

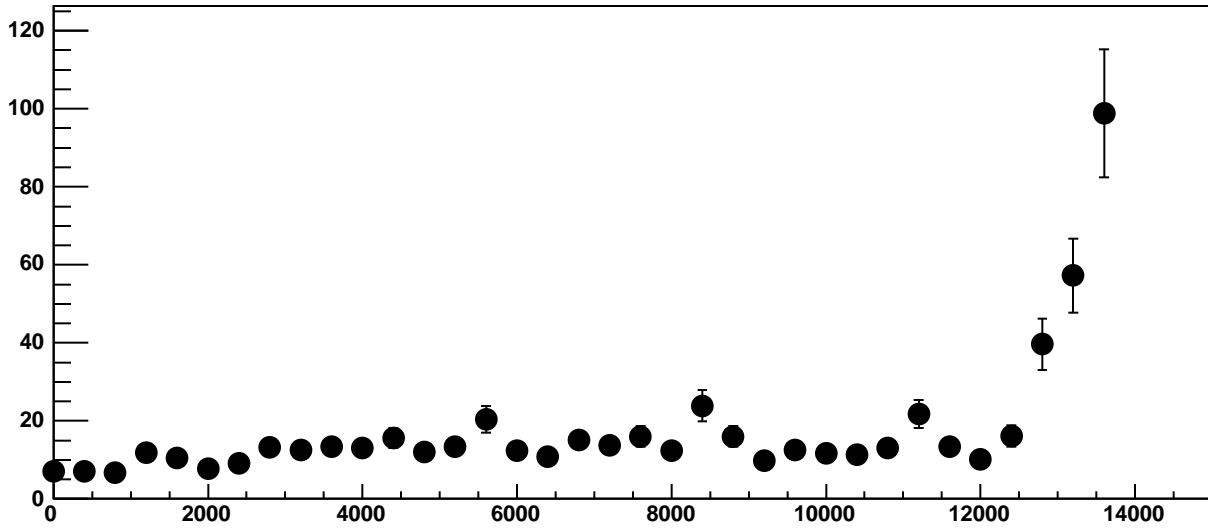


Chip 2, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC

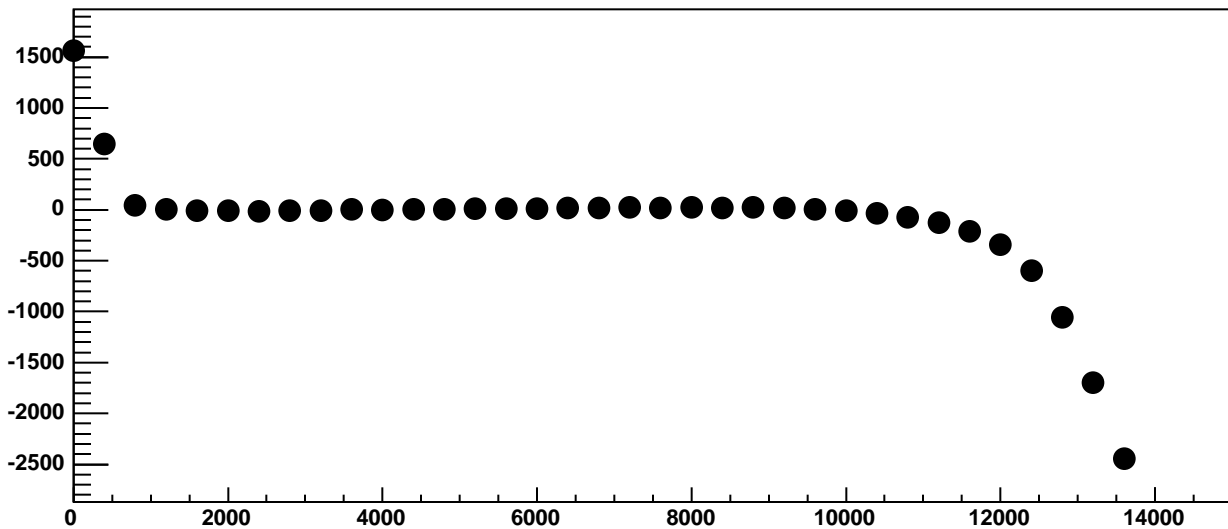


χ^2 / ndf 1348 / 23
p0 -186.2 ± 1.159
p1 2.334 ± 0.0001812

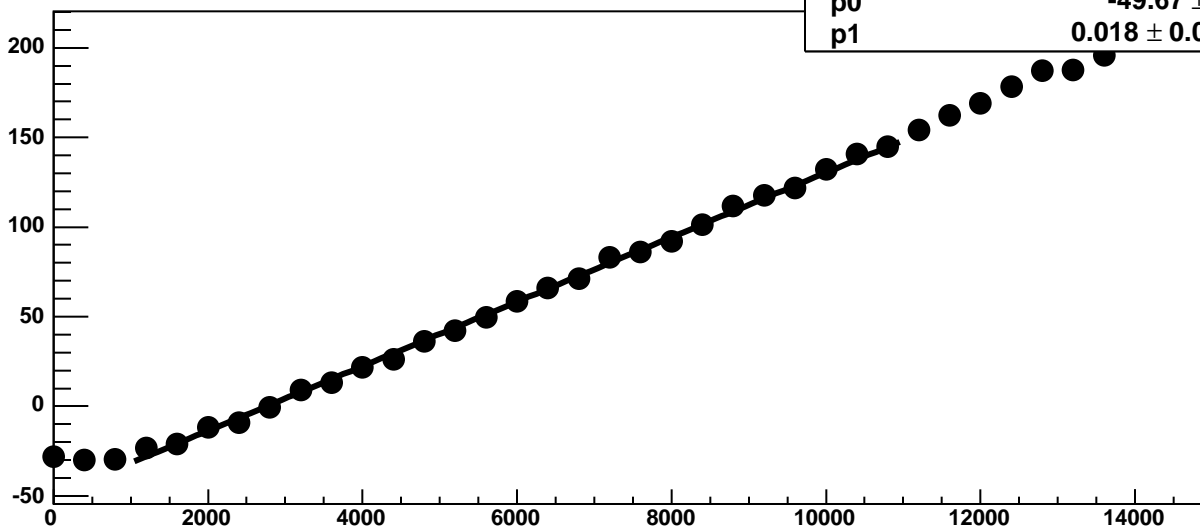
Chip 2, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC

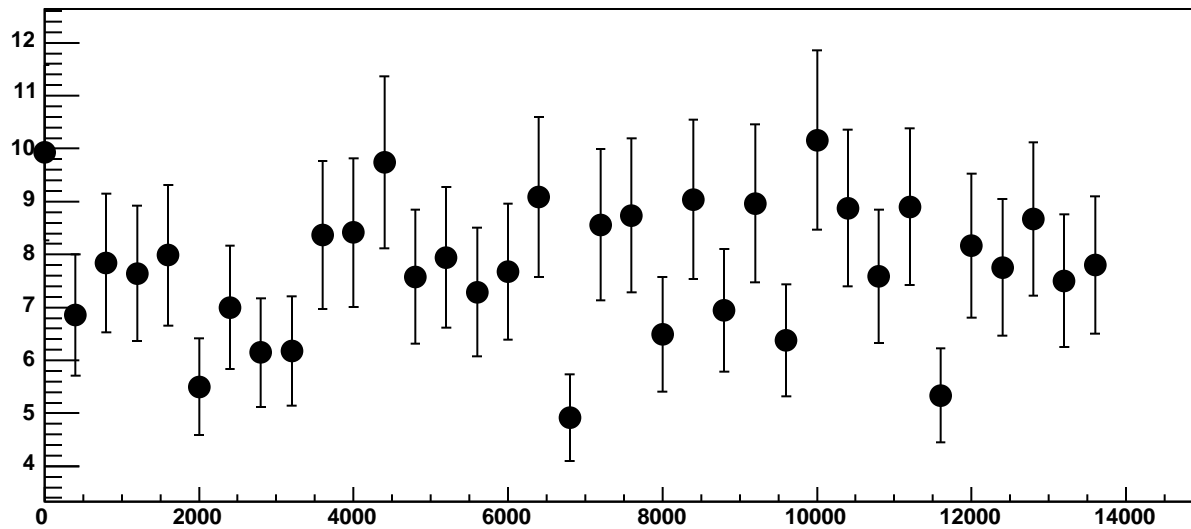


Chip 2, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

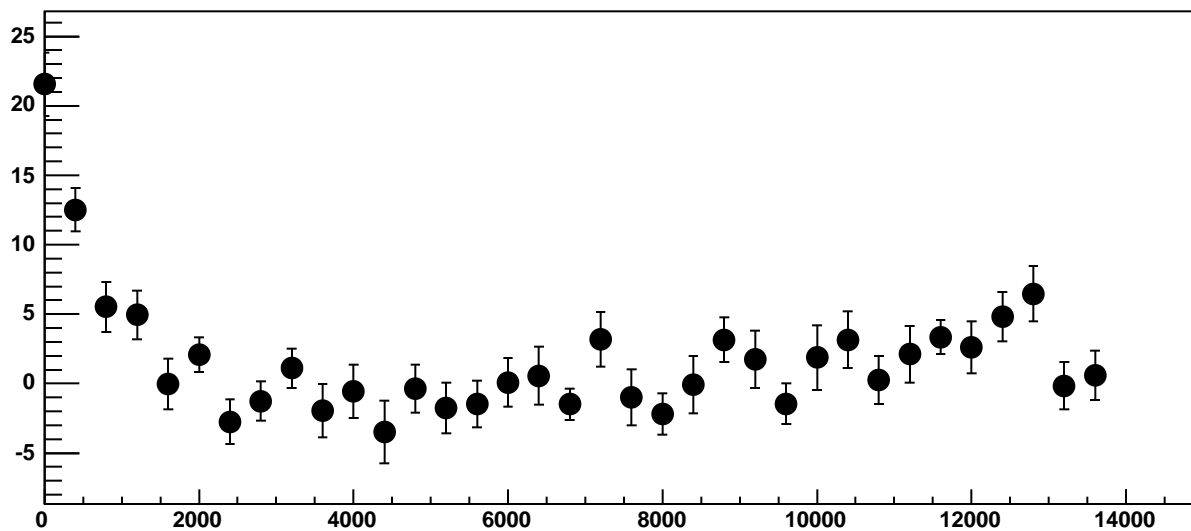


χ^2 / ndf 35.8 / 23
p0 -49.67 ± 0.7558
p1 0.018 ± 0.0001177

Chip 2, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

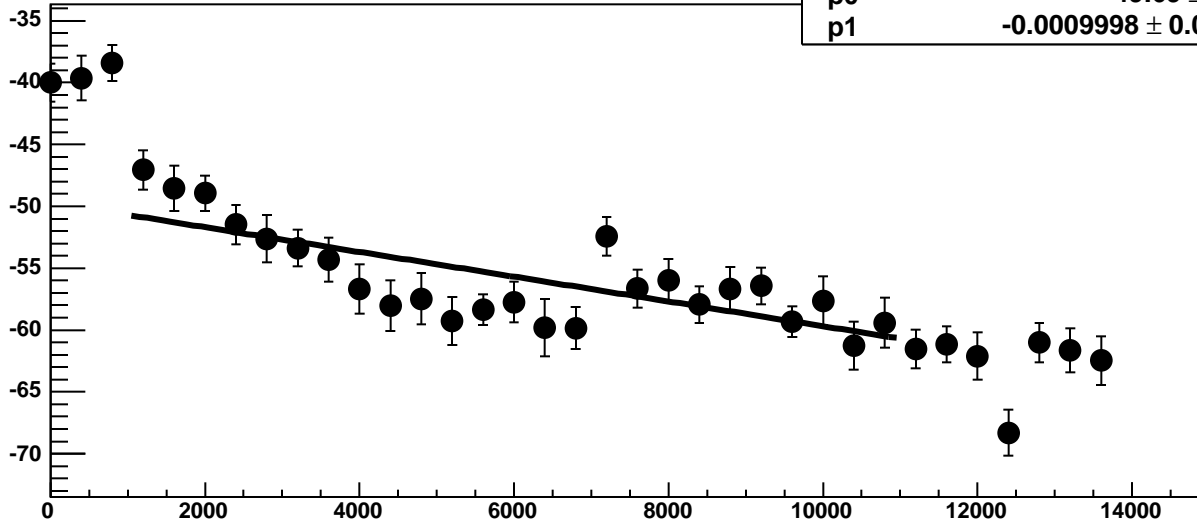


Chip 2, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

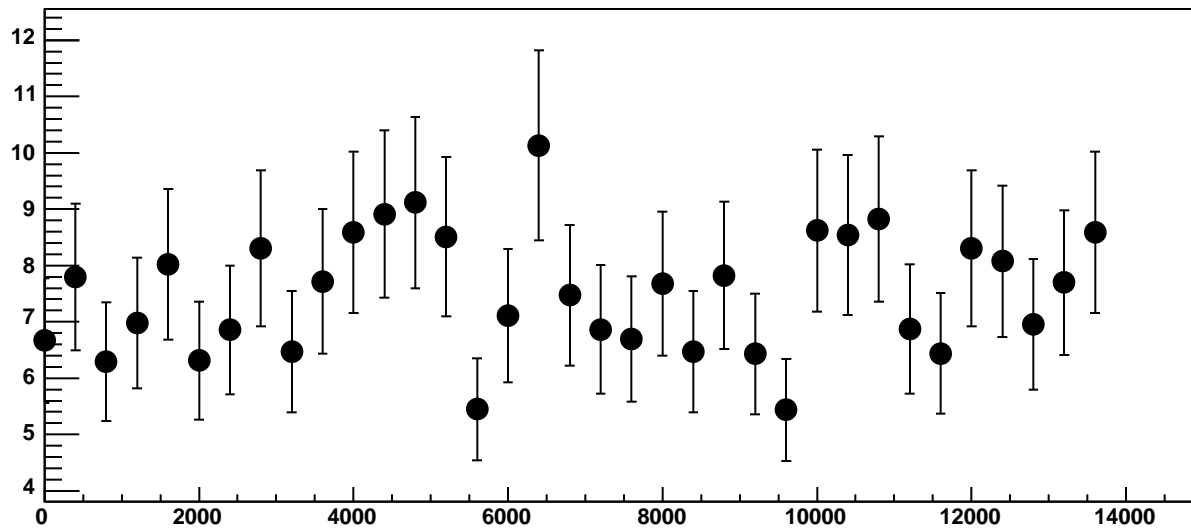


Chip 2, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

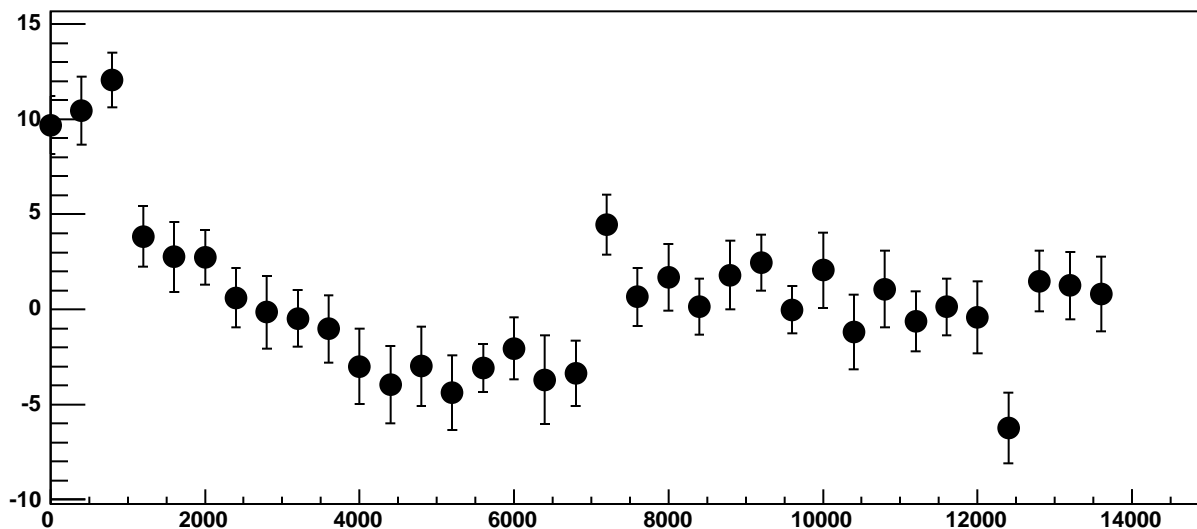
χ^2 / ndf 53.99 / 23
p0 -49.69 ± 0.7752
p1 -0.0009998 ± 0.0001162



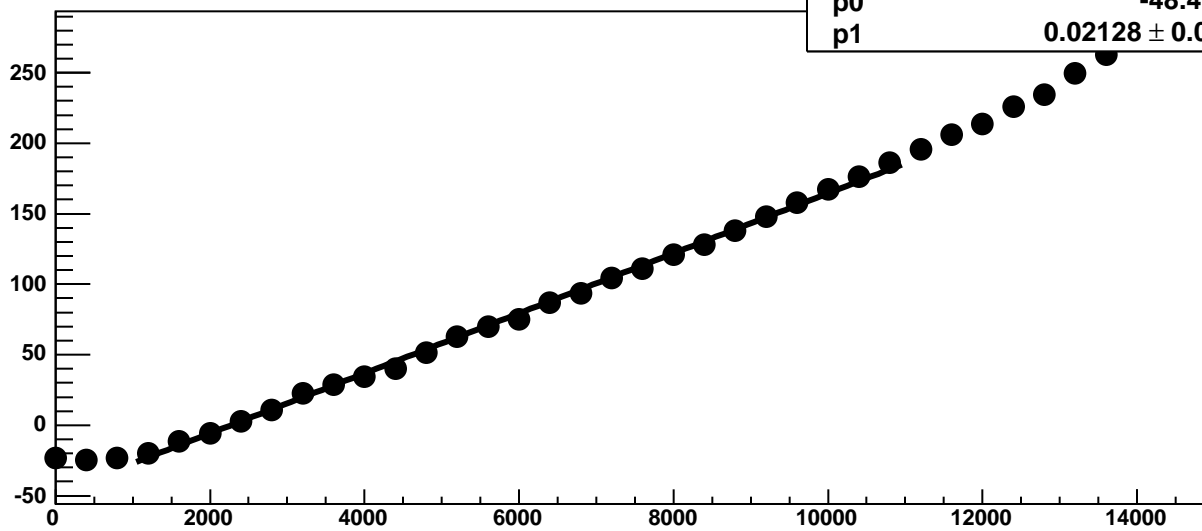
Chip 2, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

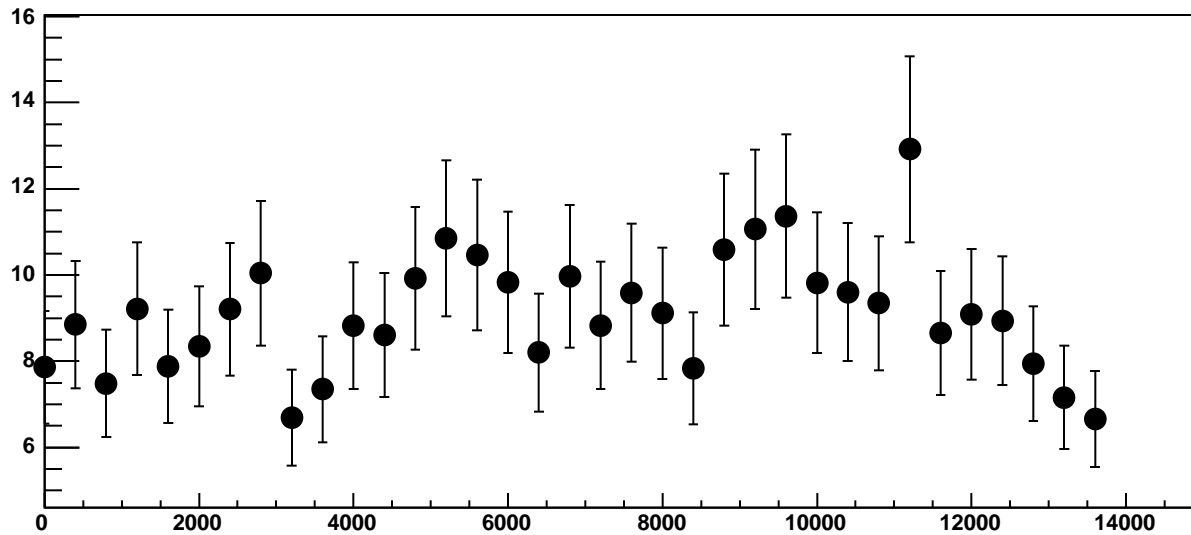


Chip 2, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

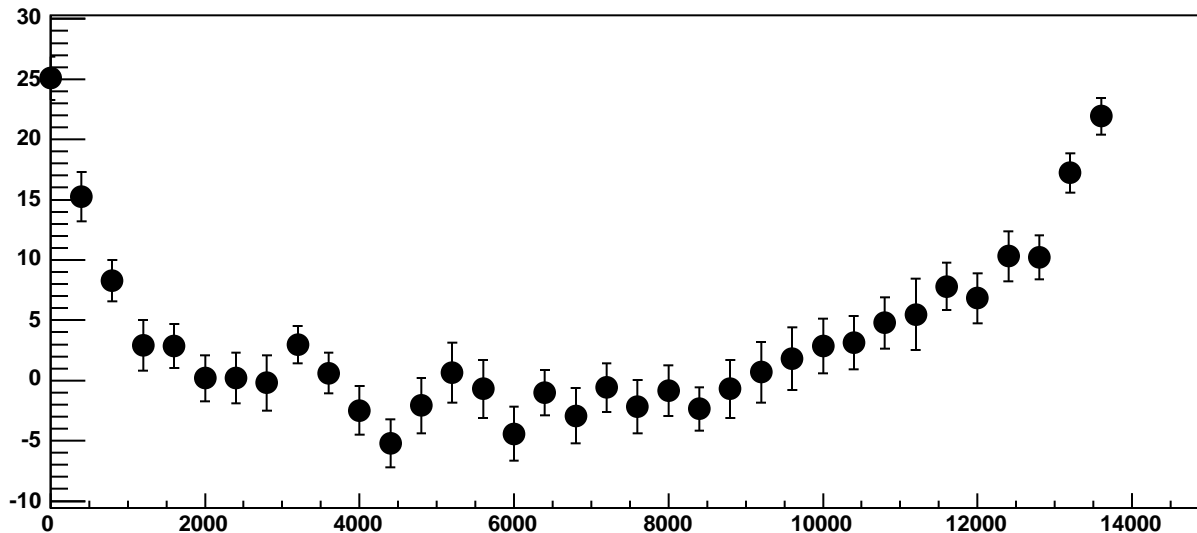


χ^2 / ndf 35.64 / 23
p0 -48.4 ± 0.919
p1 0.02128 ± 0.0001447

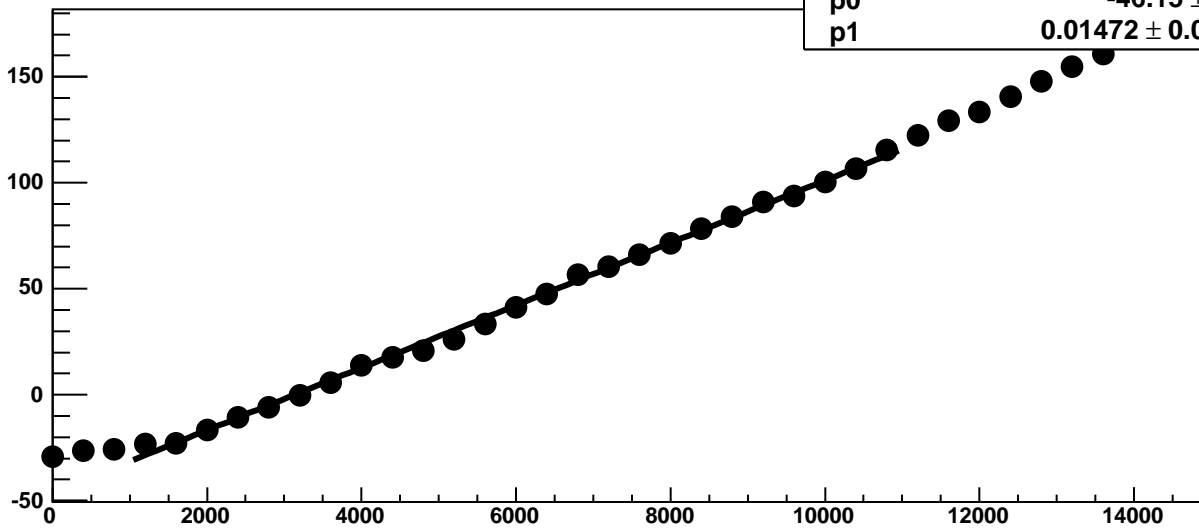
Chip 2, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

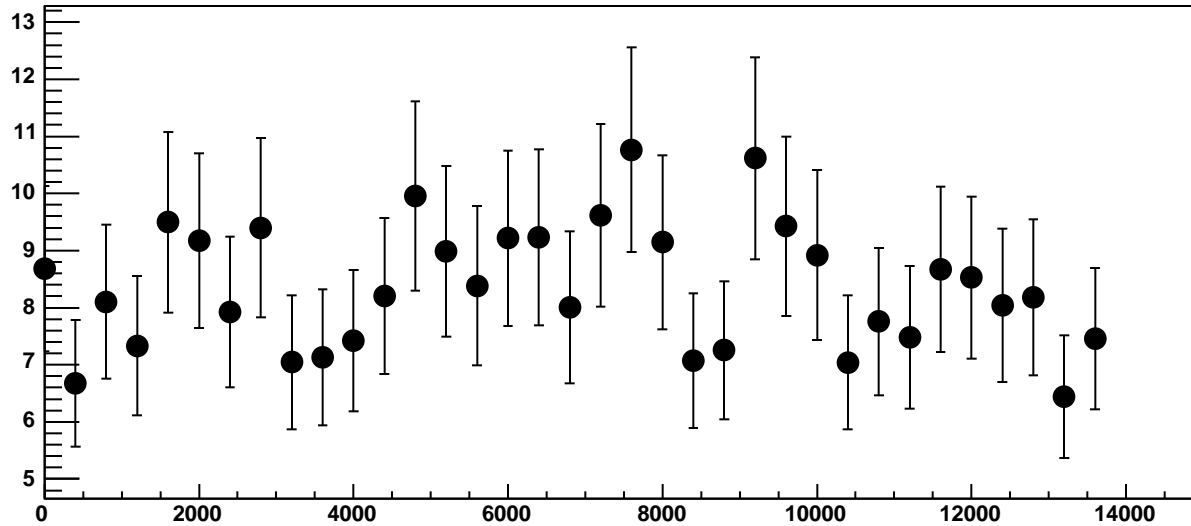


Chip 2, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

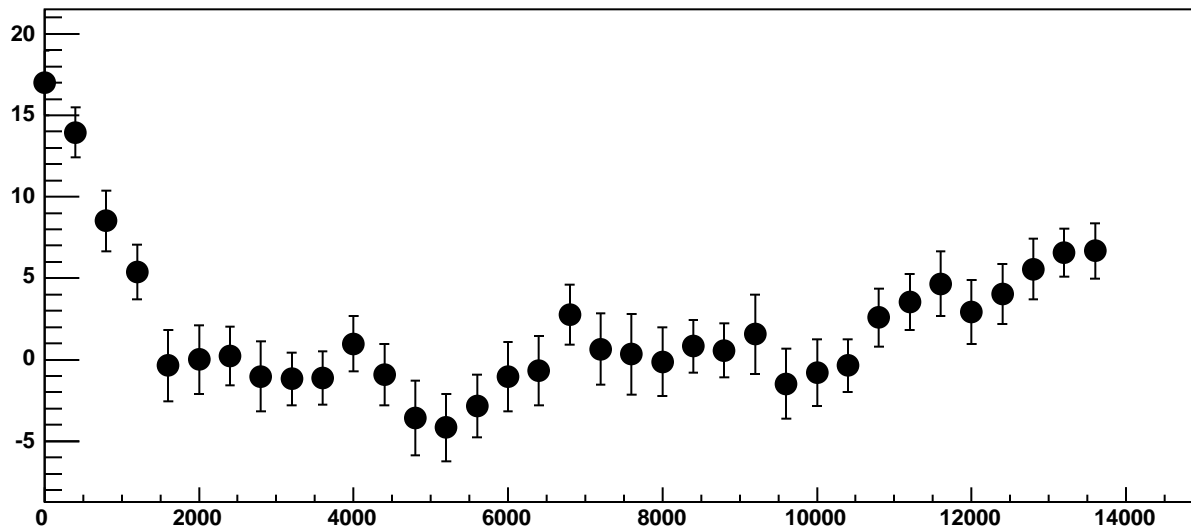


χ^2 / ndf 27.04 / 23
p0 -46.15 ± 0.8626
p1 0.01472 ± 0.0001296

Chip 2, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

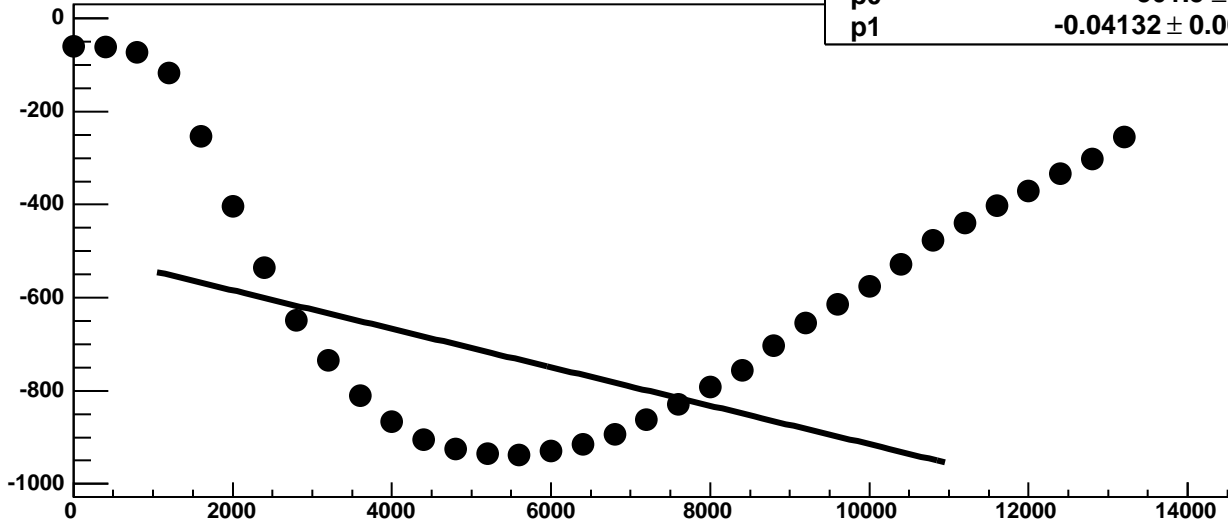


Chip 2, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

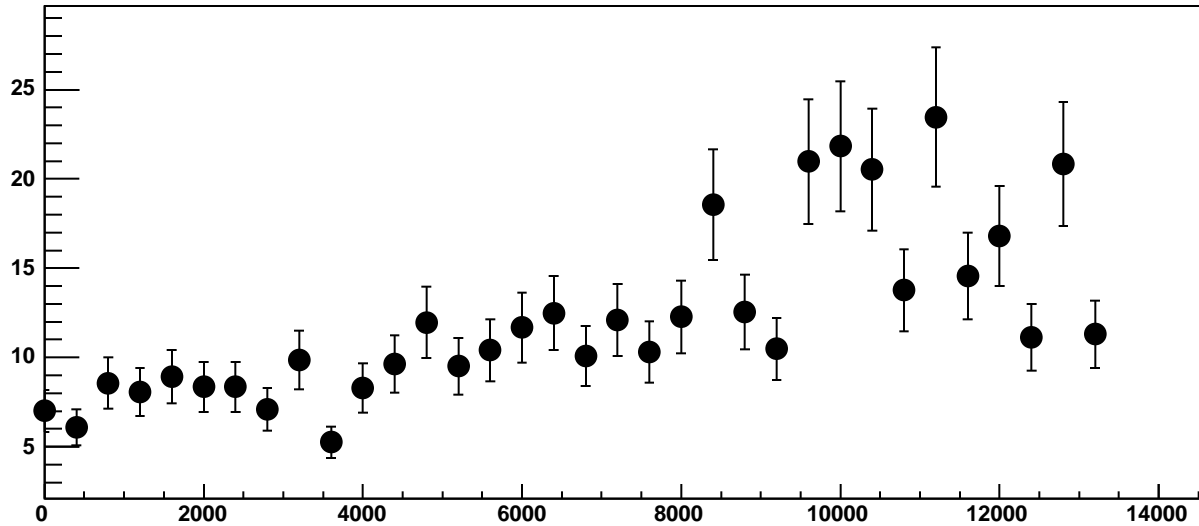


Chip 2, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

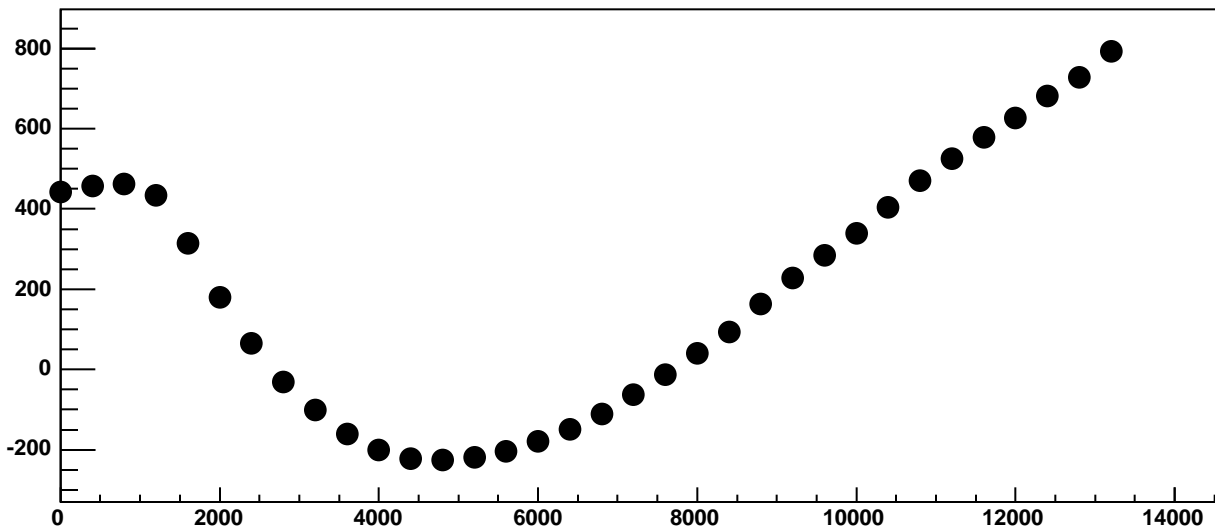
χ^2 / ndf 2.143e+05 / 23
p0 -501.5 ± 0.9578
p1 -0.04132 ± 0.0001796



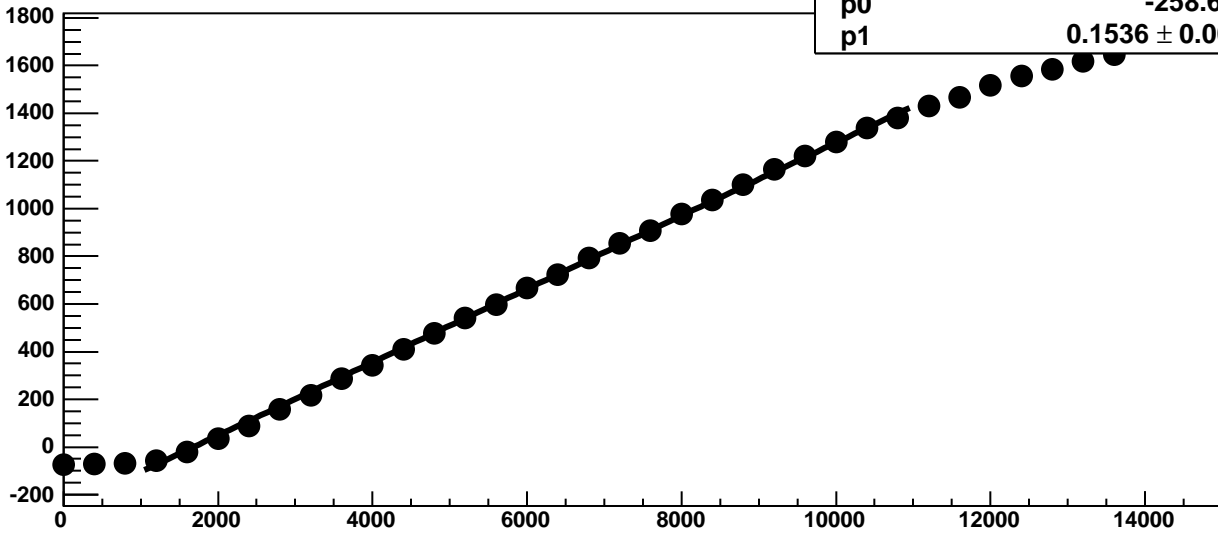
Chip 2, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

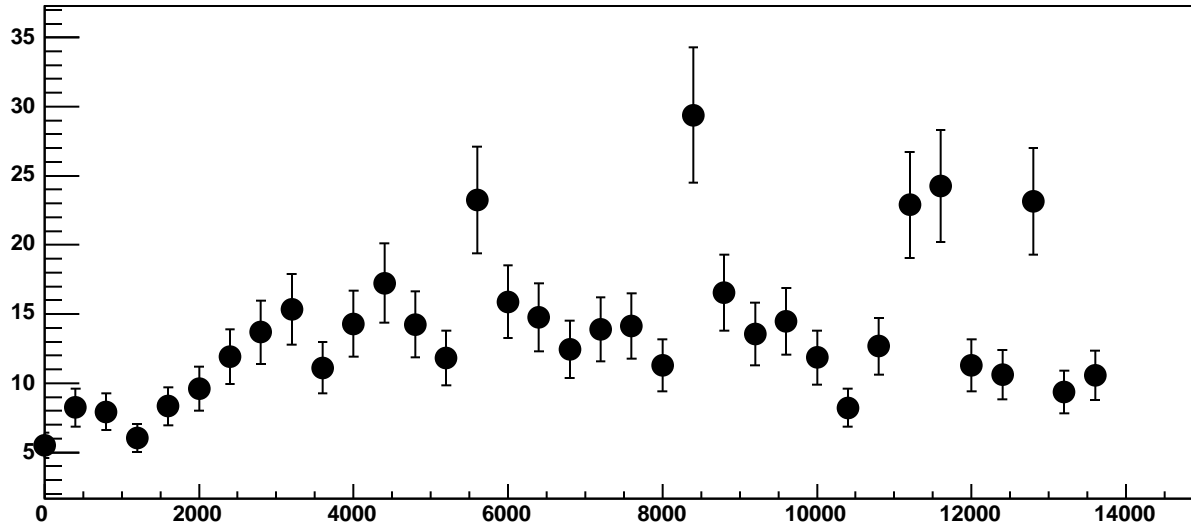


Chip 2, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

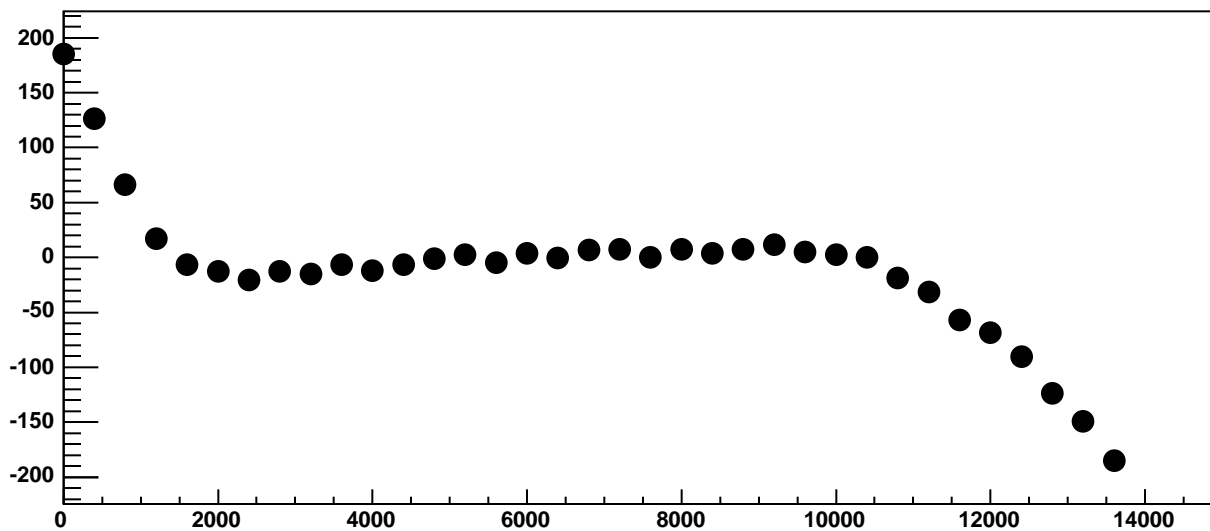


χ^2 / ndf 397 / 23
p0 -258.6 ± 1.01
p1 0.1536 ± 0.0001616

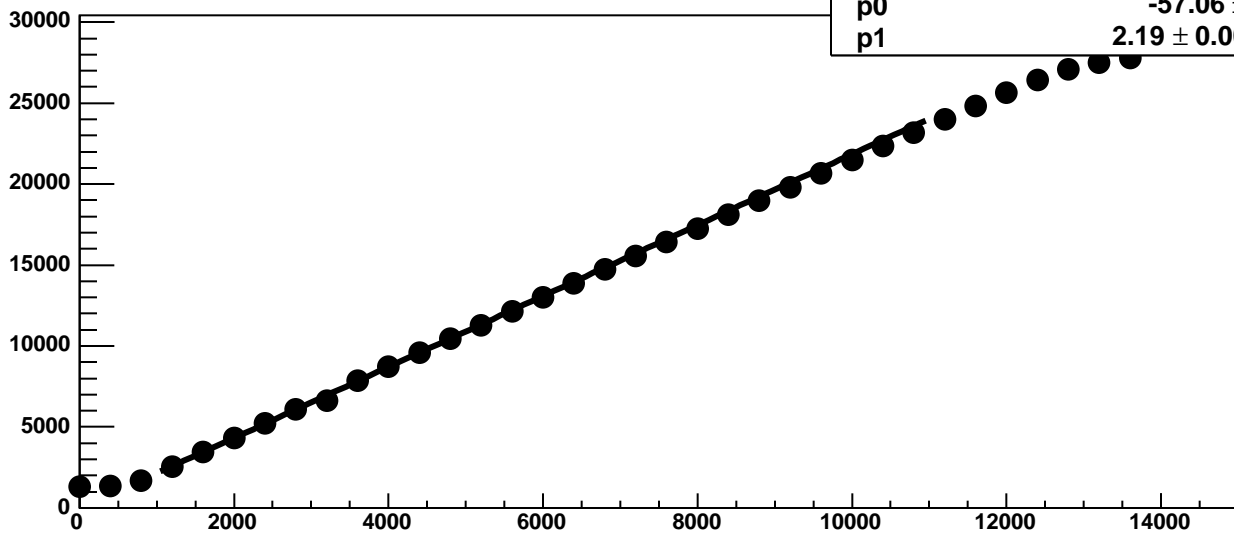
Chip 2, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



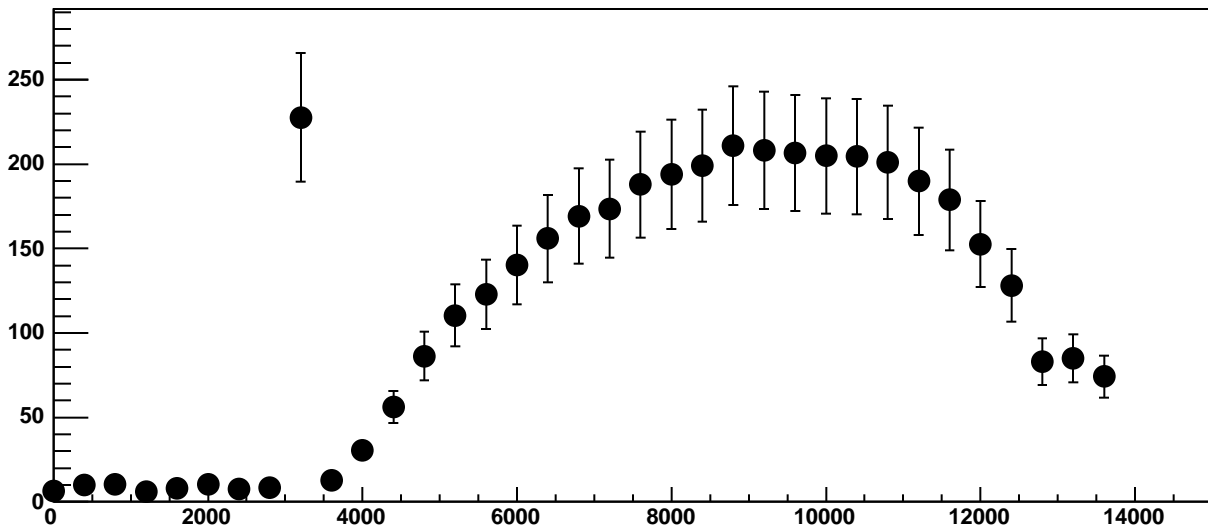
Chip 2, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



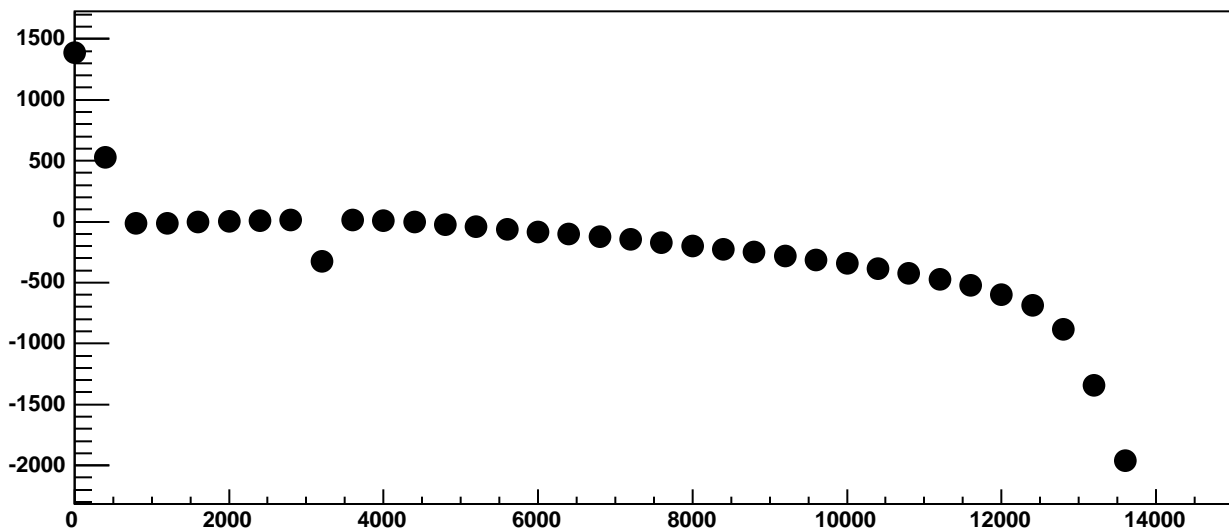
Chip 2, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC



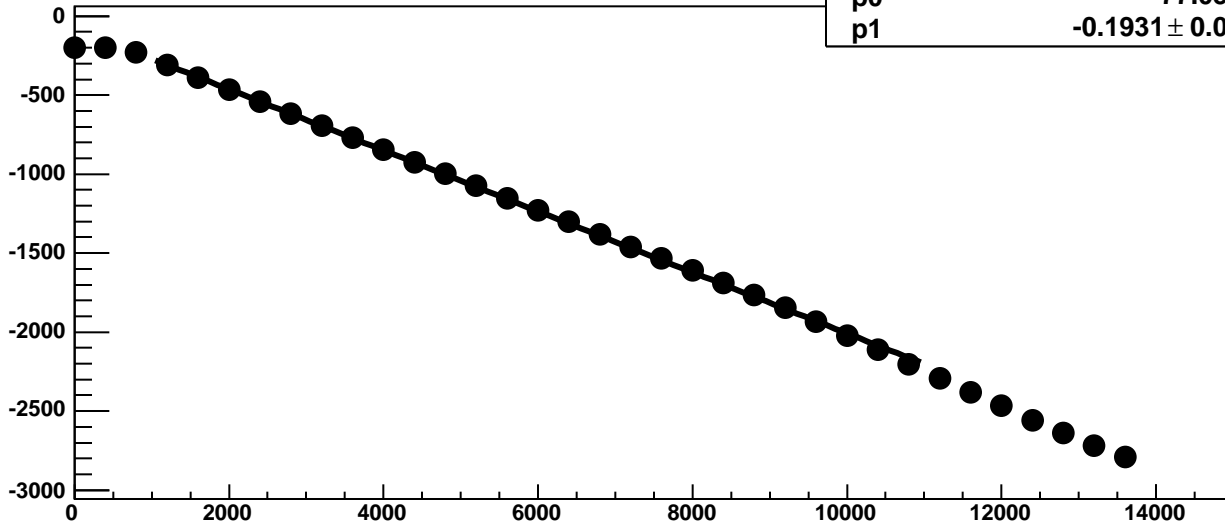
Chip 2, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC

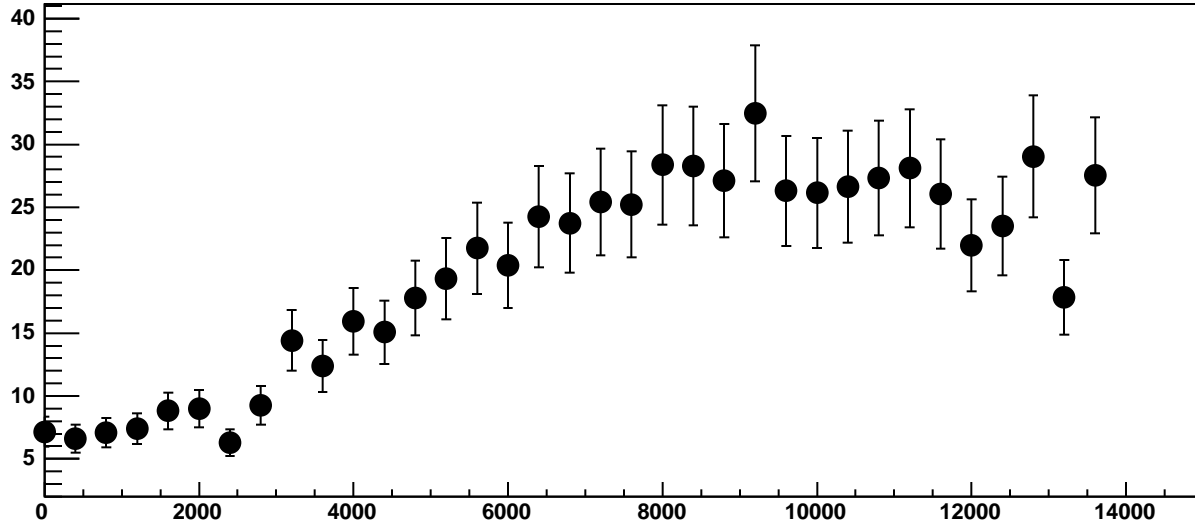


Chip 2, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

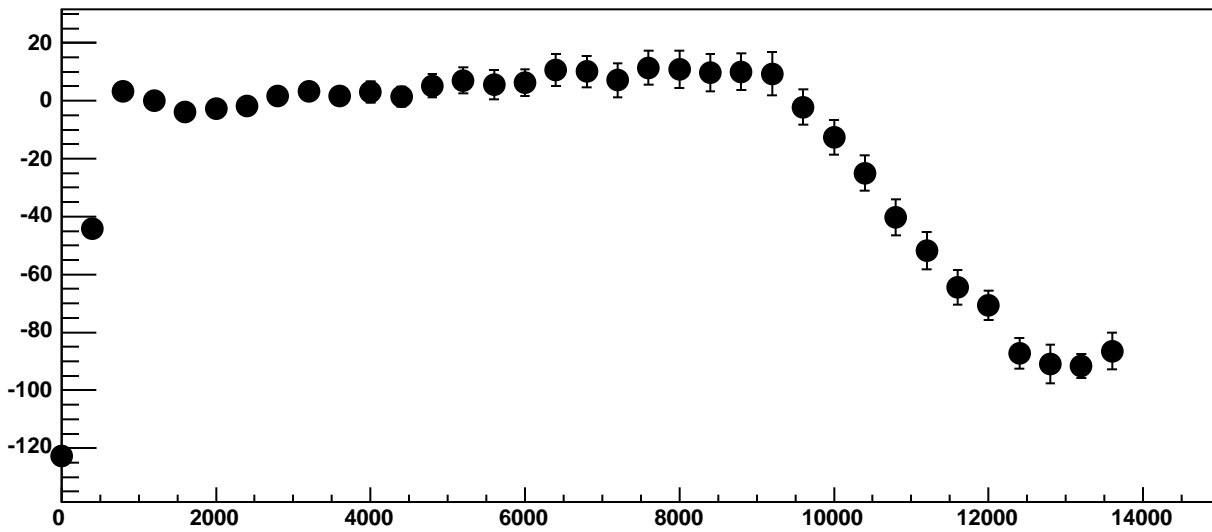


χ^2 / ndf	100.7 / 23
p0	-77.05 ± 1.13
p1	-0.1931 ± 0.0002727

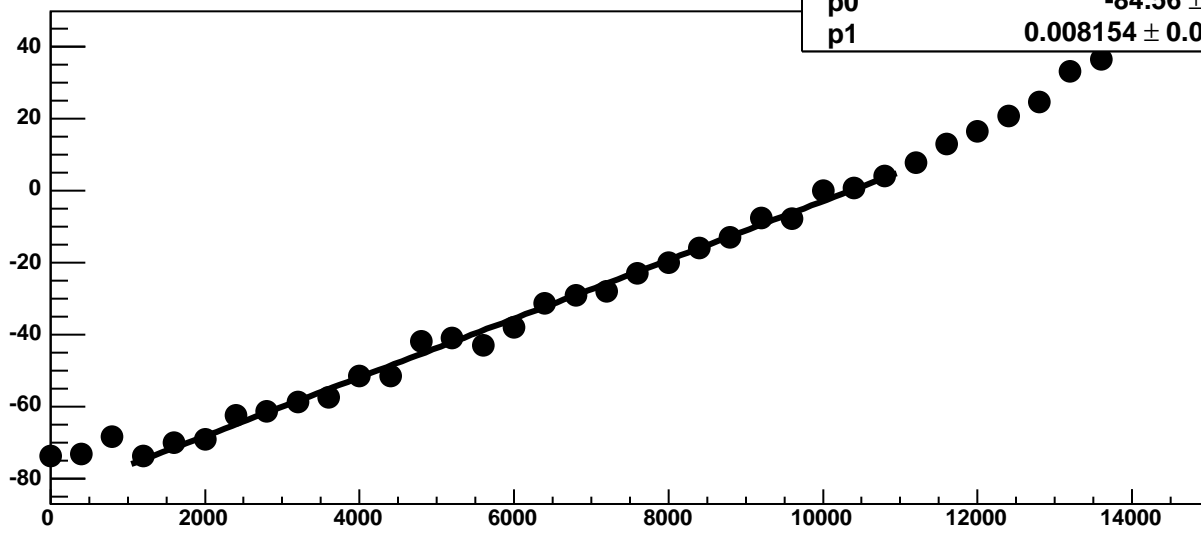
Chip 2, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

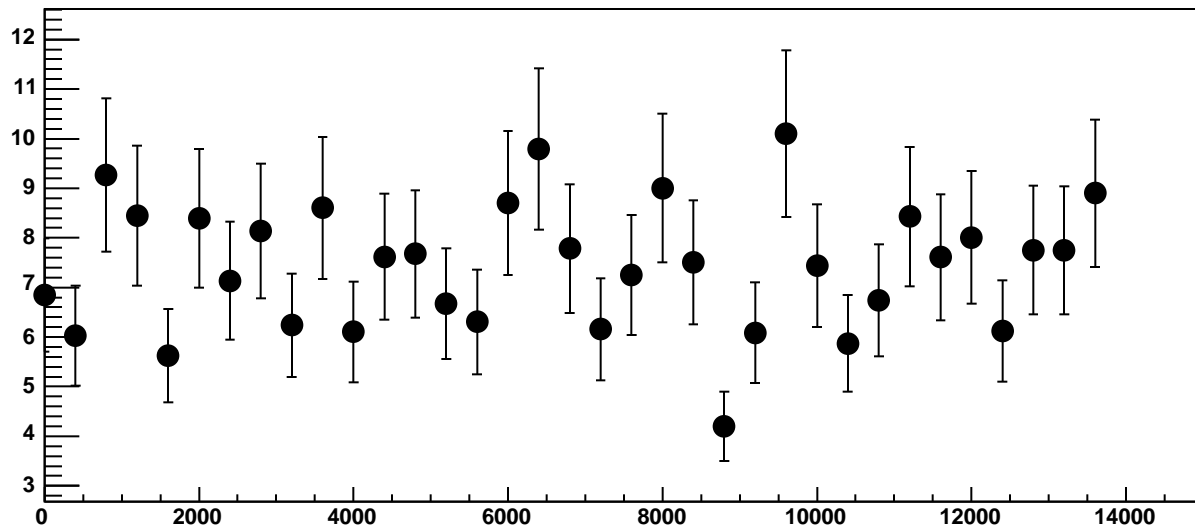


Chip 2, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

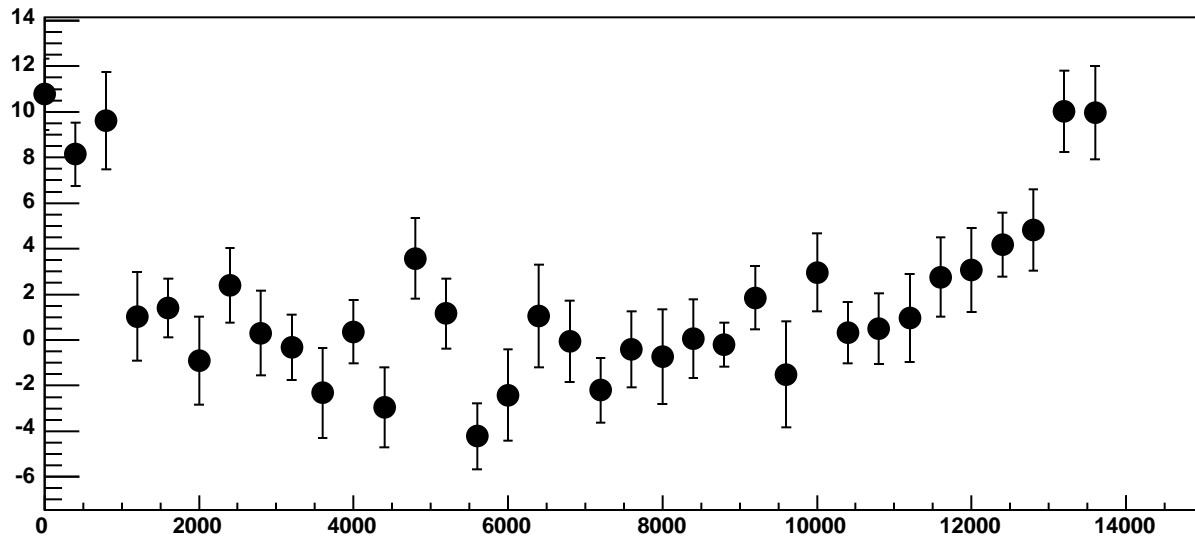


χ^2 / ndf 31.13 / 23
p0 -84.56 ± 0.7463
p1 0.008154 ± 0.0001085

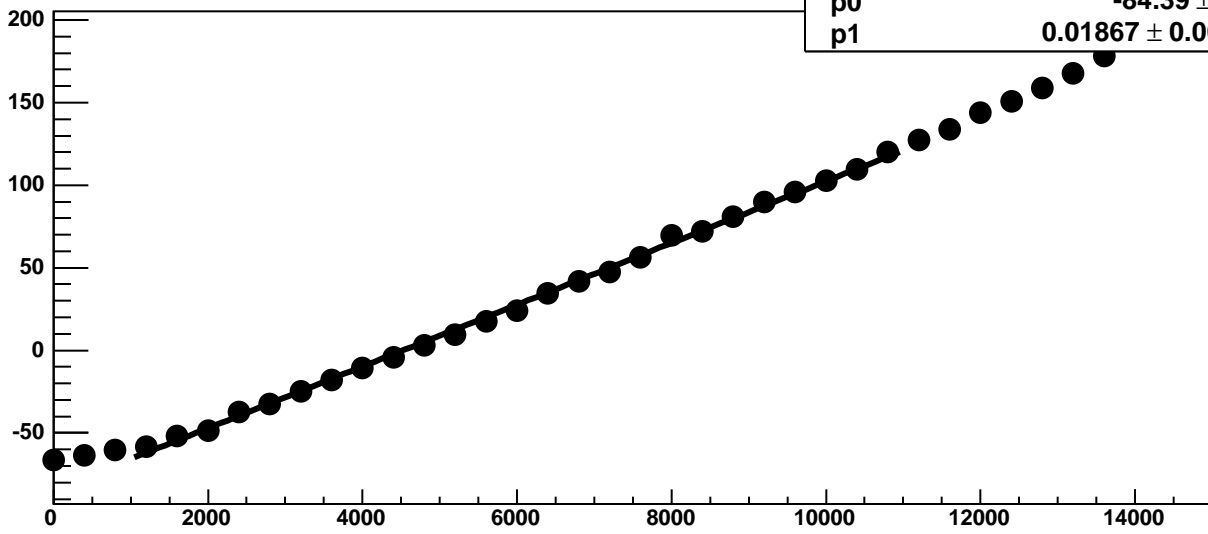
Chip 2, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

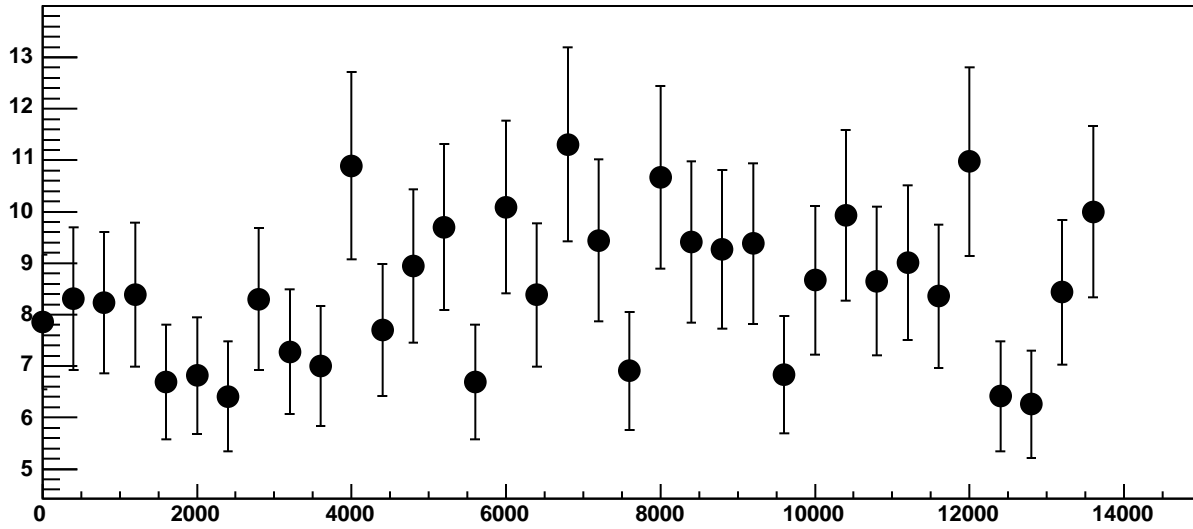


Chip 2, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC

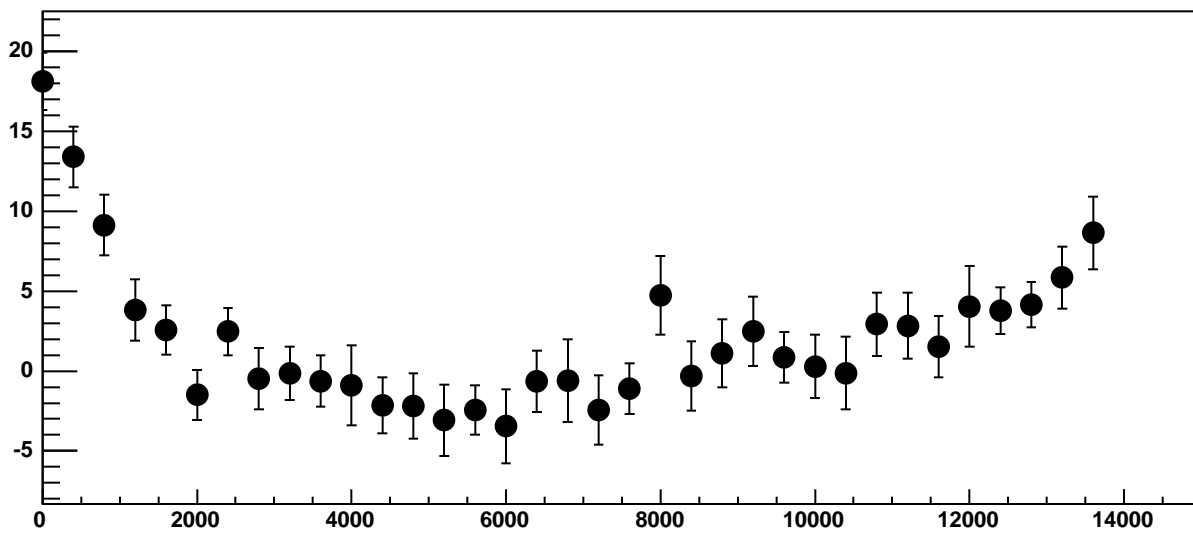


χ^2 / ndf 29.86 / 23
p0 -84.39 ± 0.8041
p1 0.01867 ± 0.0001272

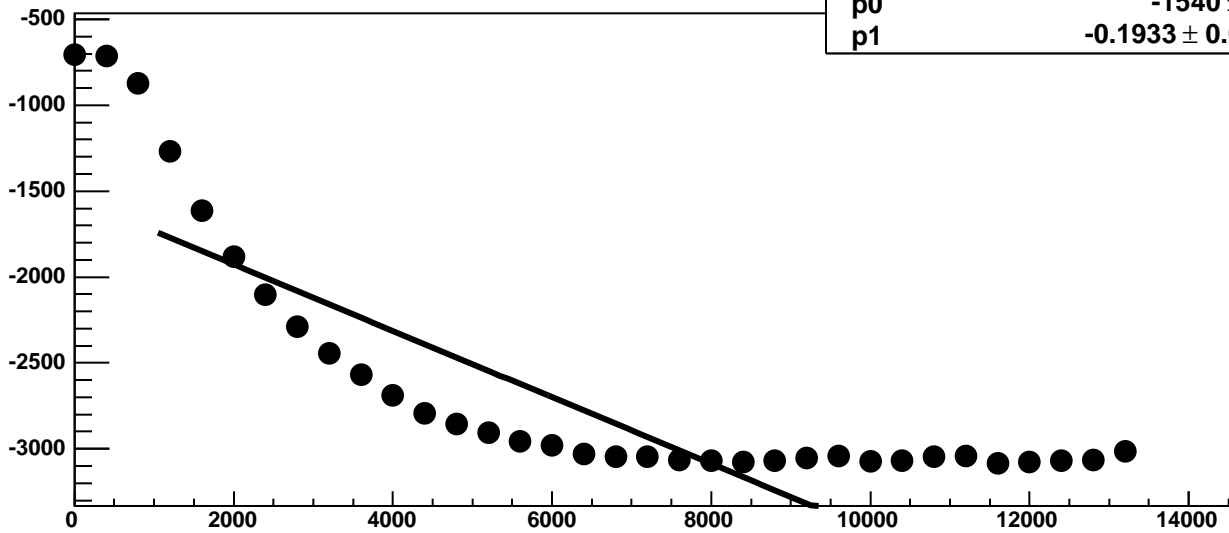
Chip 2, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



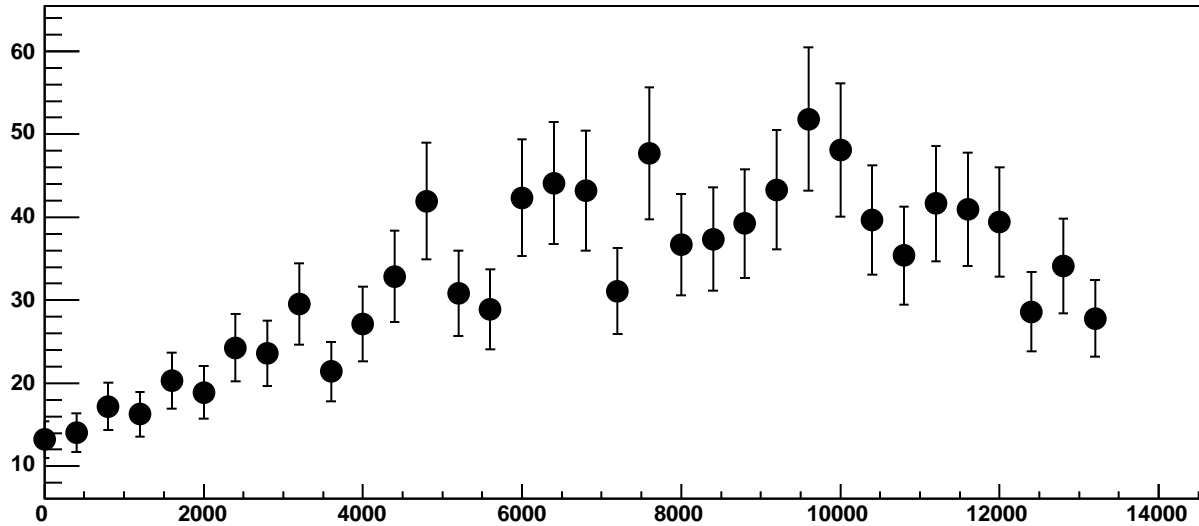
Chip 2, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC



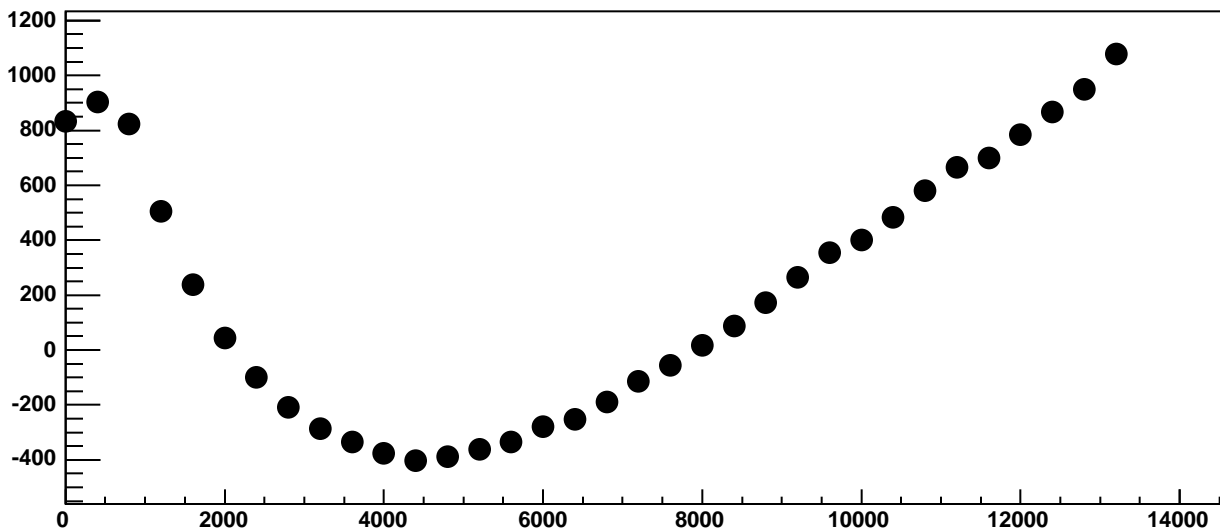
Chip 2, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC



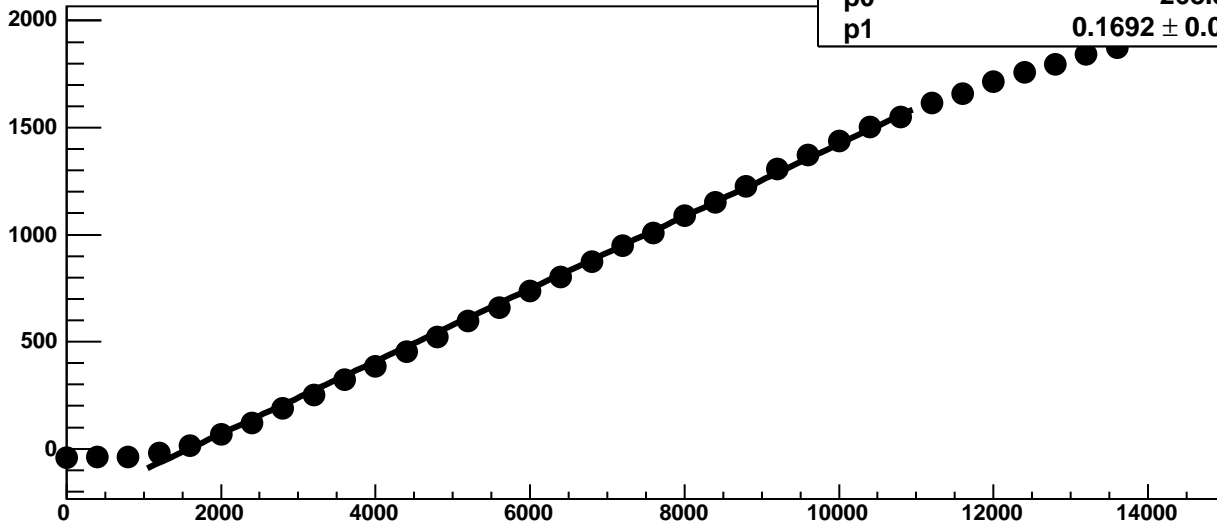
Chip 2, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



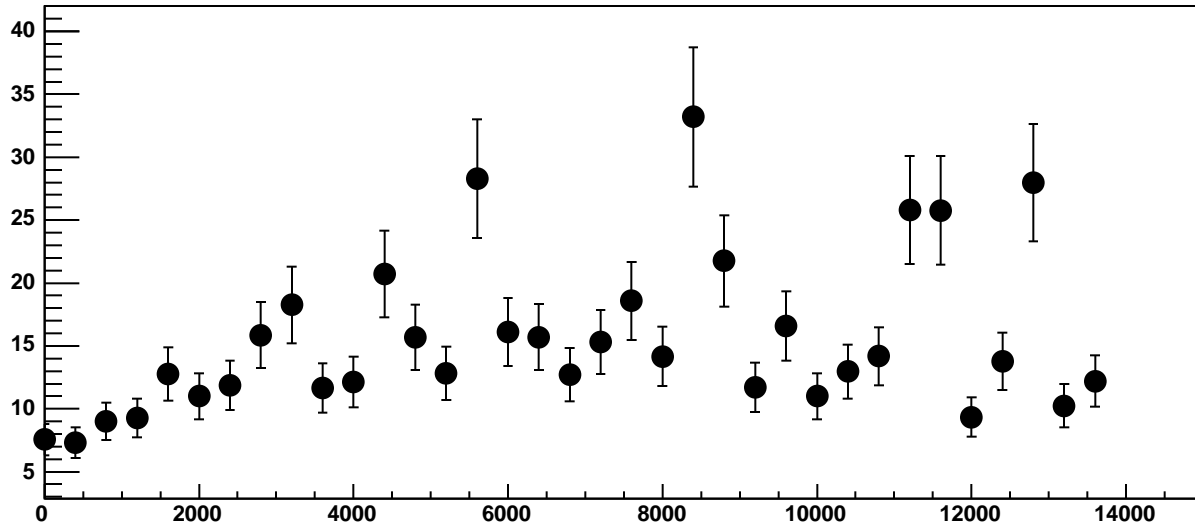
Chip 2, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC



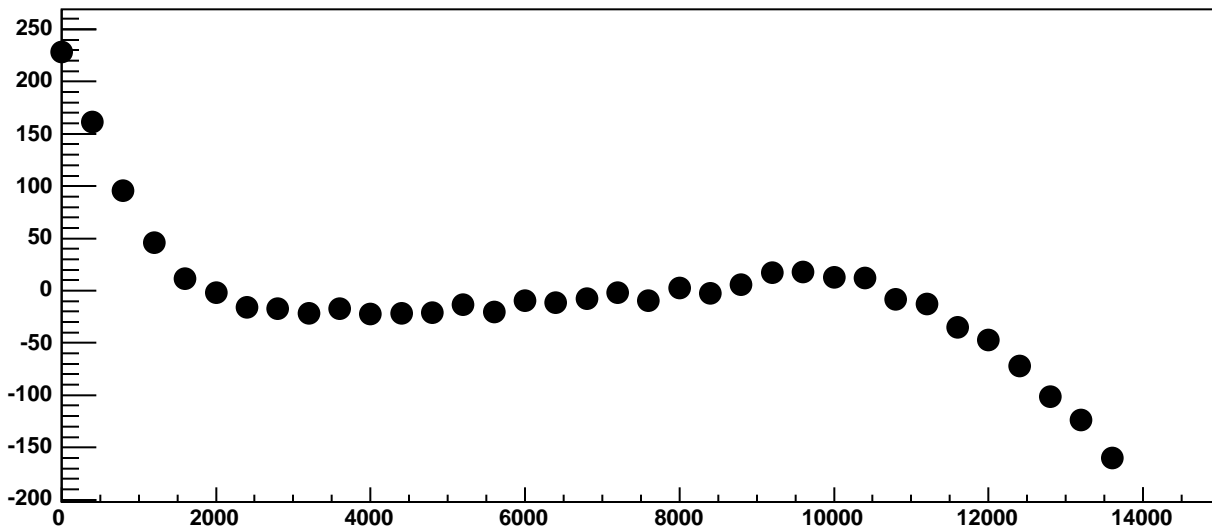
Chip 2, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



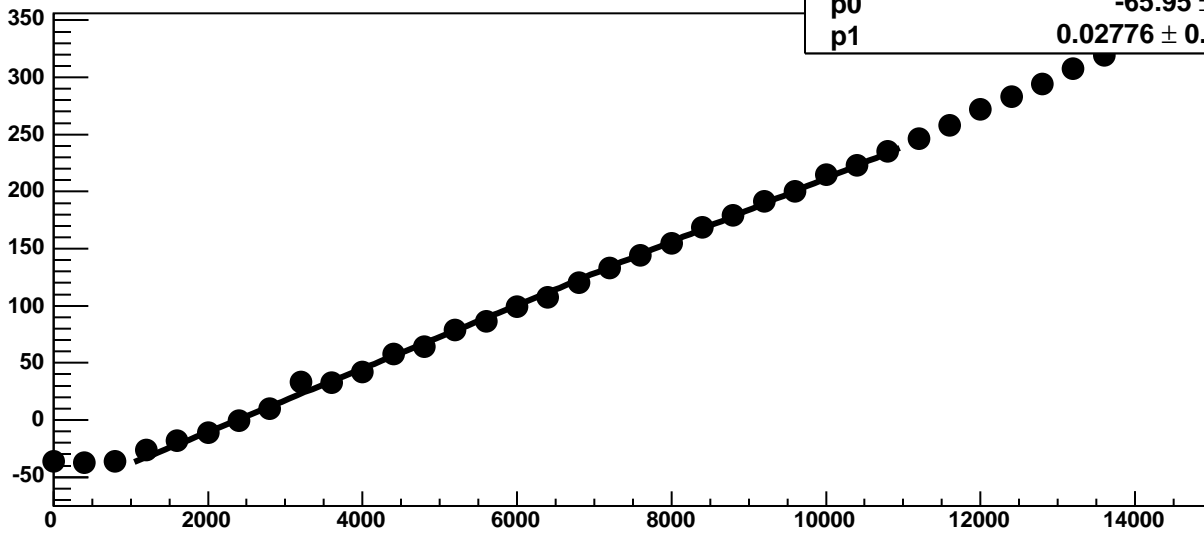
Chip 2, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

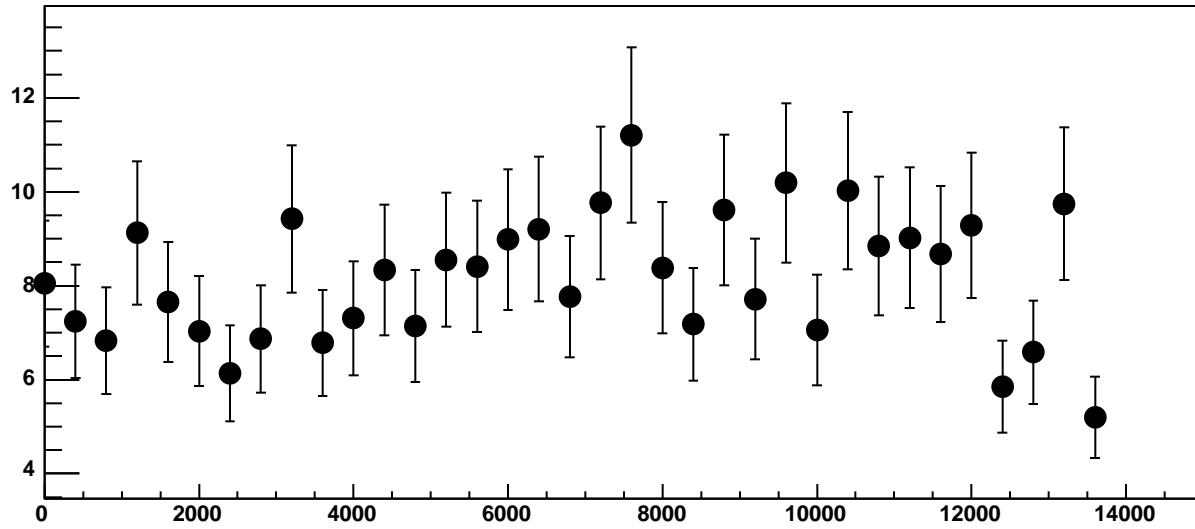


Chip 2, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC

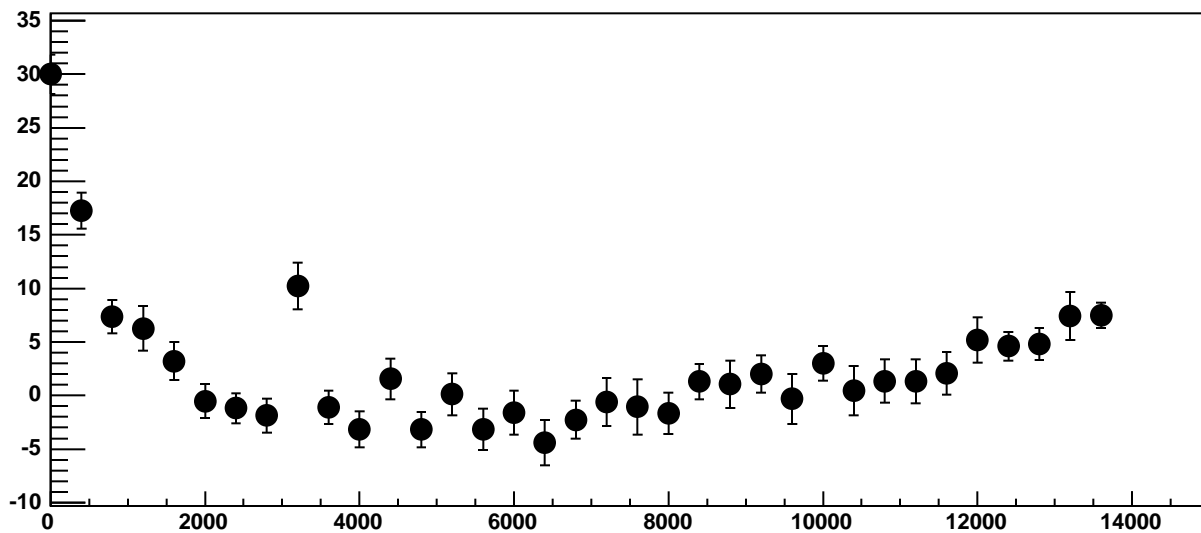


χ^2 / ndf 61.72 / 23
p0 -65.95 ± 0.8111
p1 0.02776 ± 0.000128

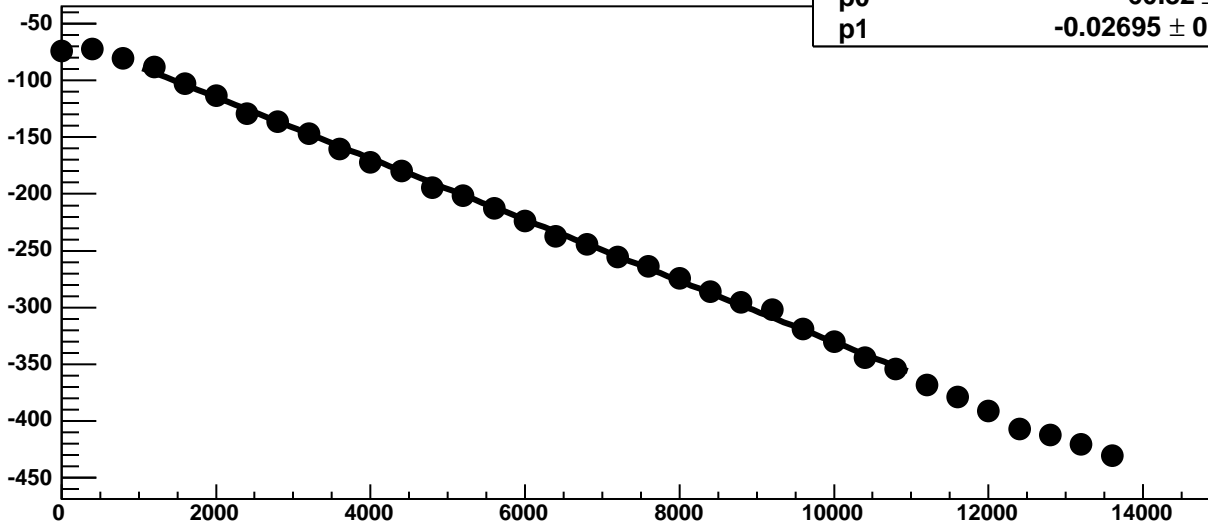
Chip 2, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC

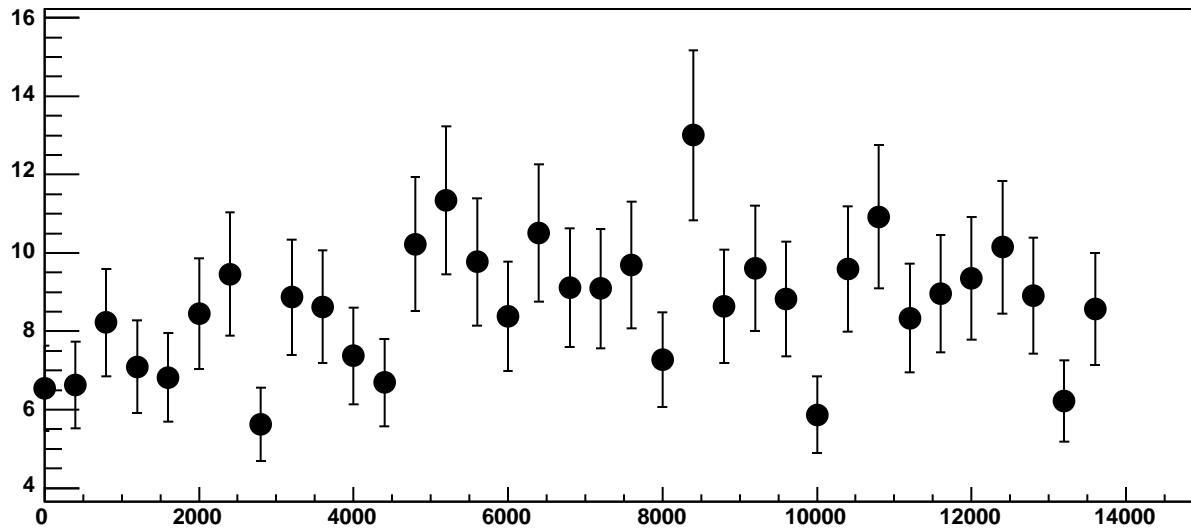


Chip 2, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

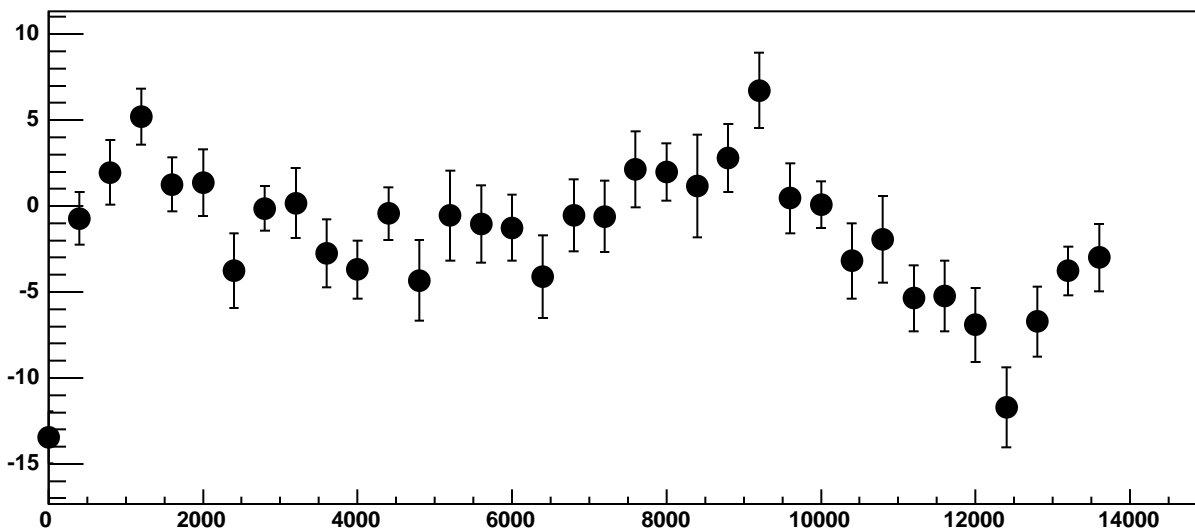


χ^2 / ndf	44.83 / 23
p0	-60.82 ± 0.8119
p1	-0.02695 ± 0.000127

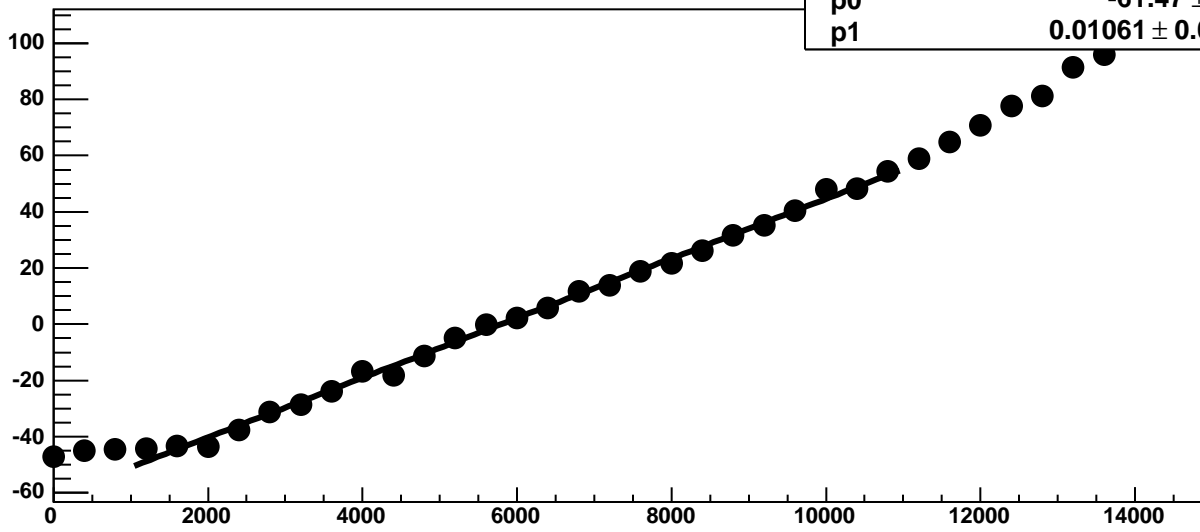
Chip 2, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



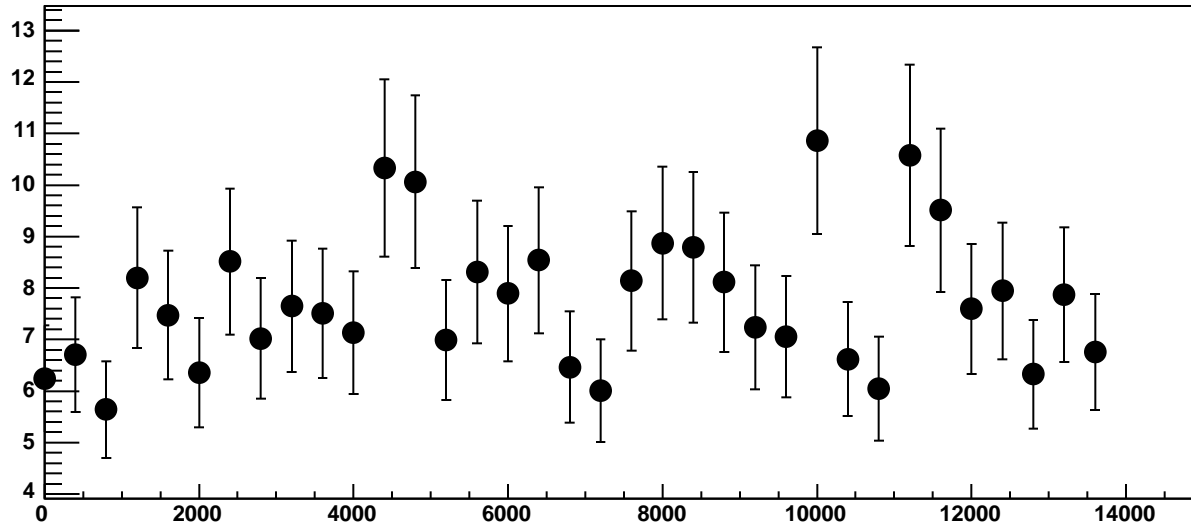
Chip 2, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



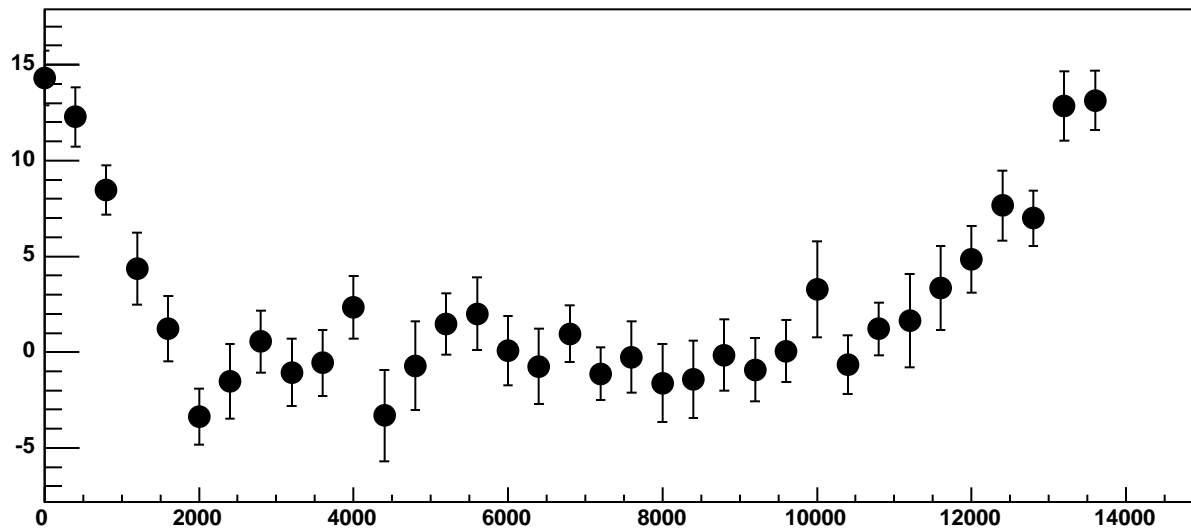
Chip 2, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC



Chip 2, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

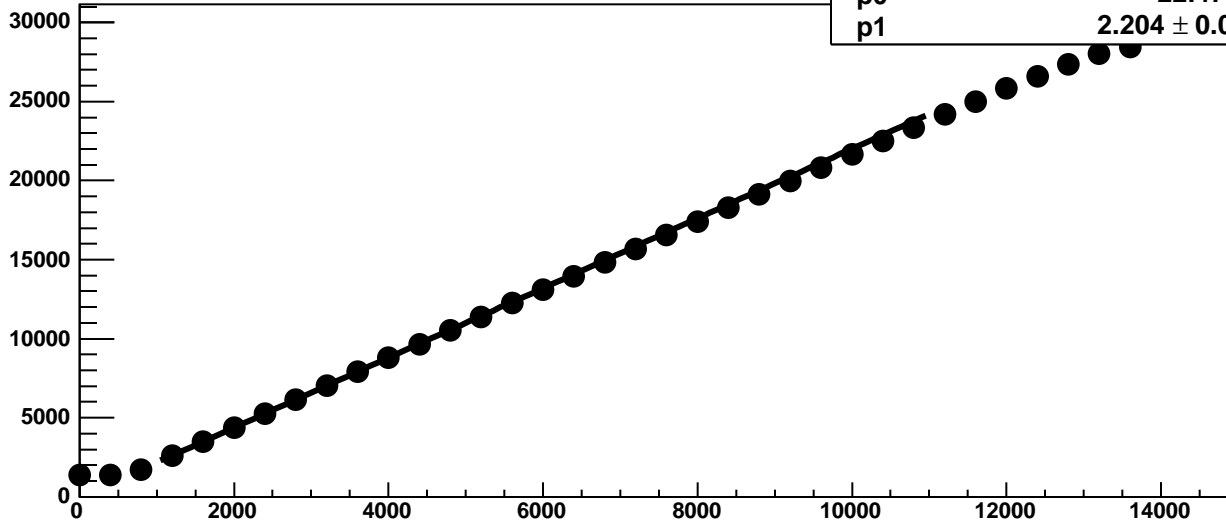
570 / 23

p0

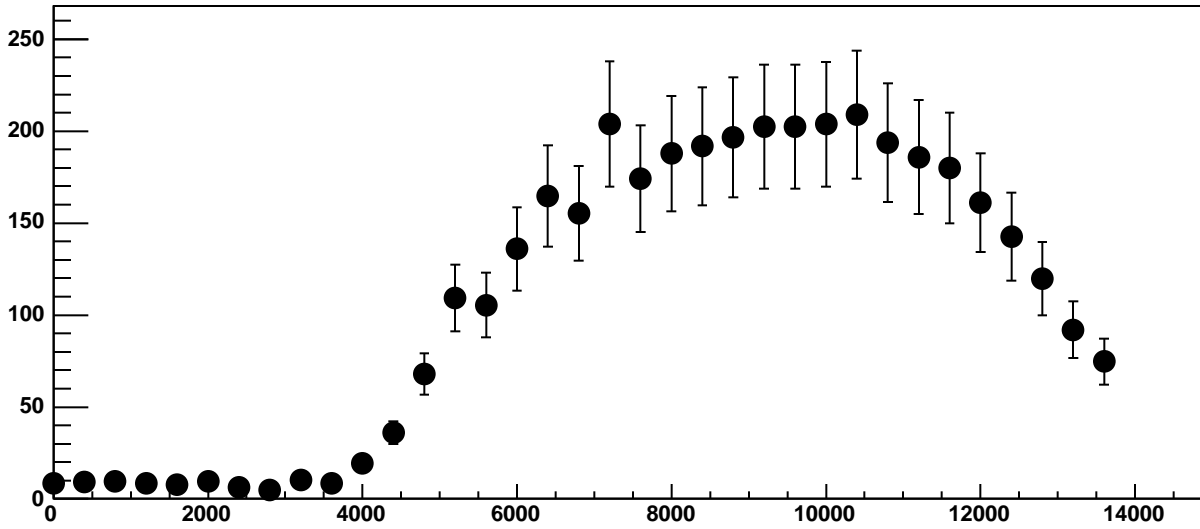
-22.47 ± 2.026

p1

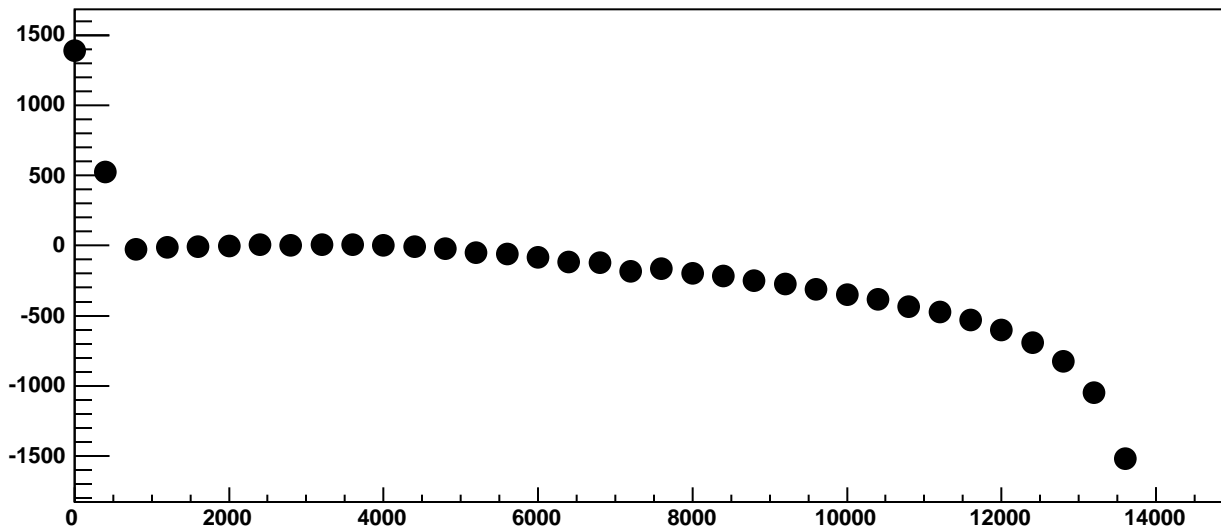
2.204 ± 0.0007658



Chip 2, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC

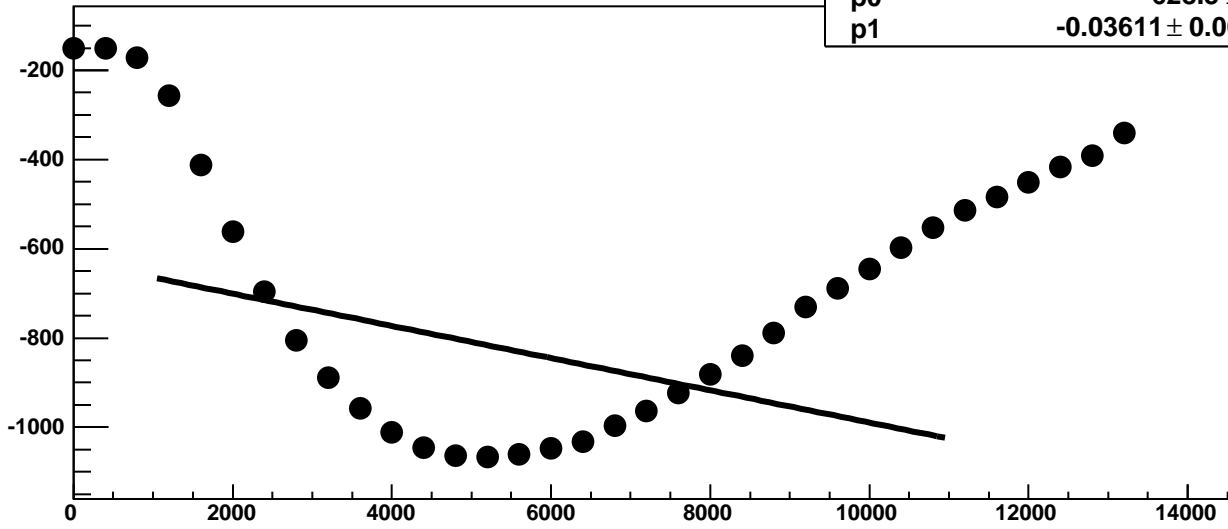


Chip 2, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC

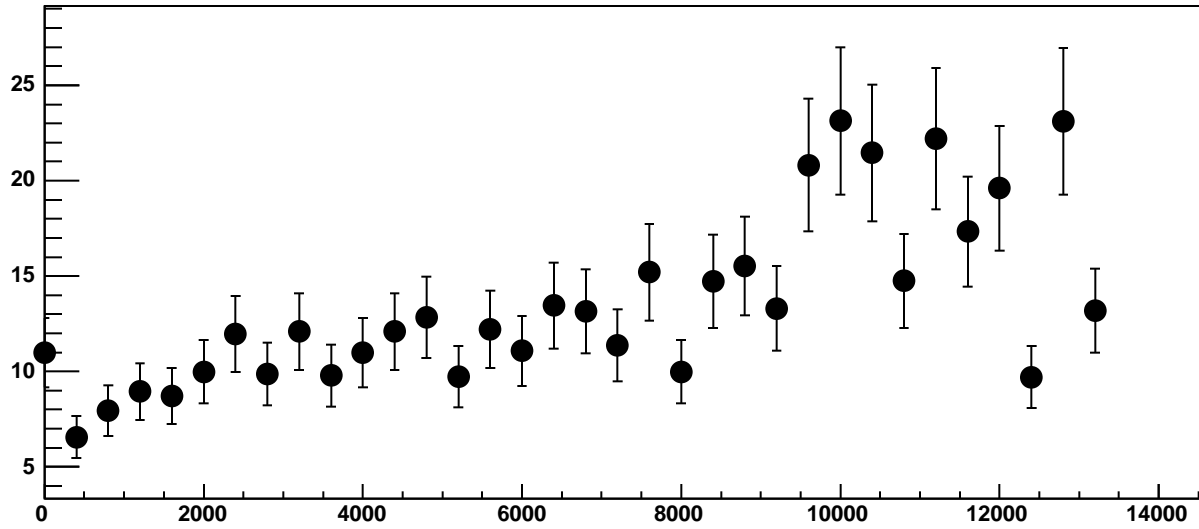


Chip 2, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

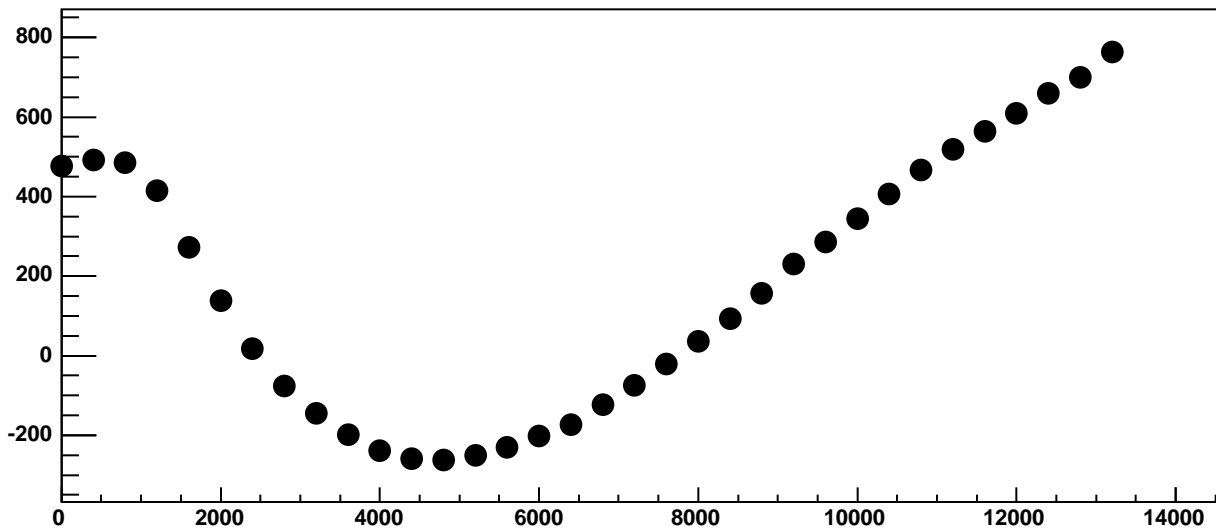
χ^2 / ndf 1.742e+05 / 23
p0 -628.3 ± 1.147
p1 -0.03611 ± 0.0002014



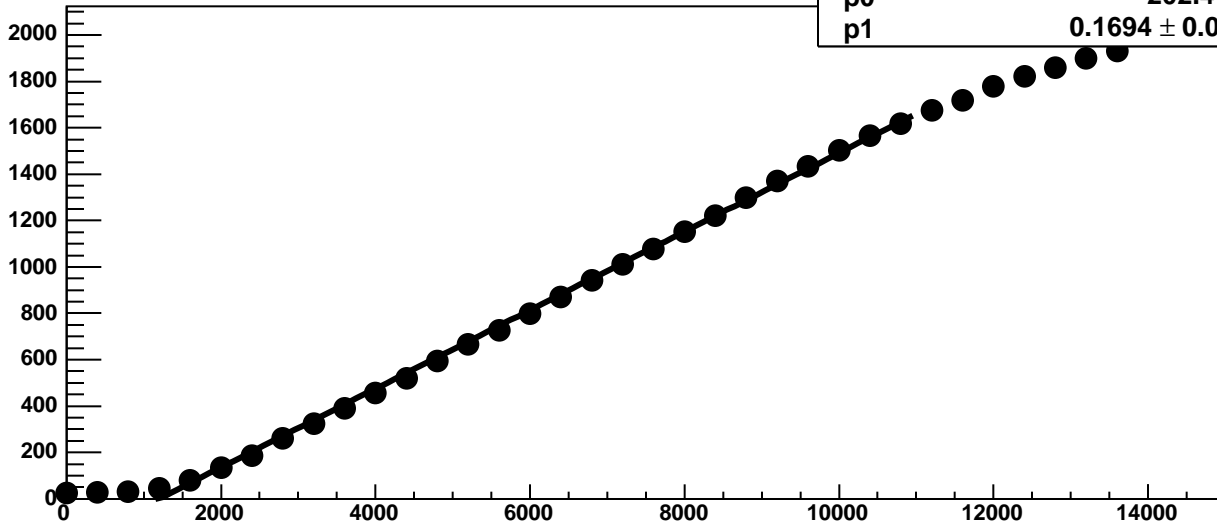
Chip 2, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

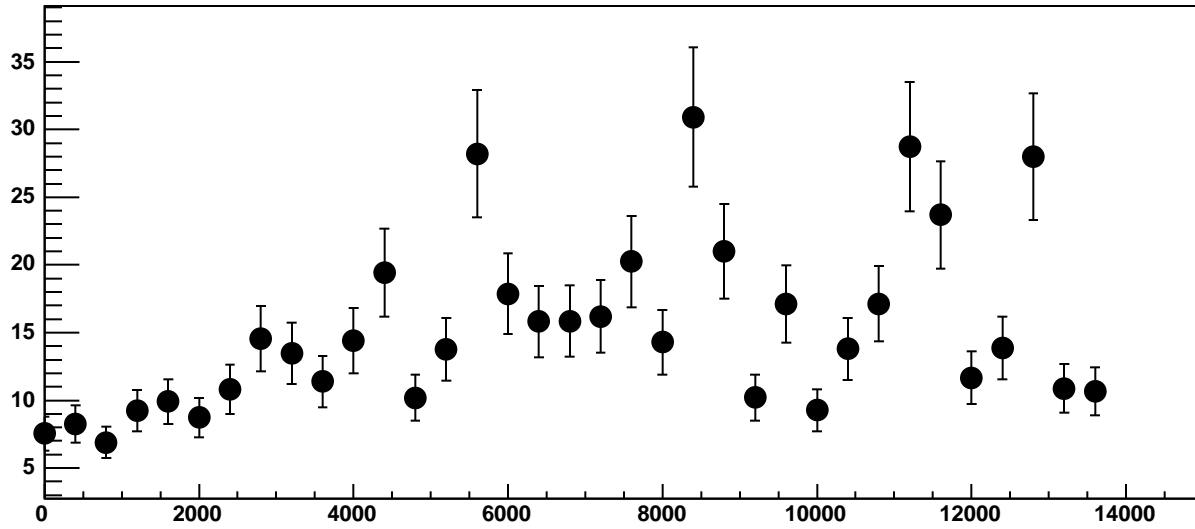


Chip 2, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

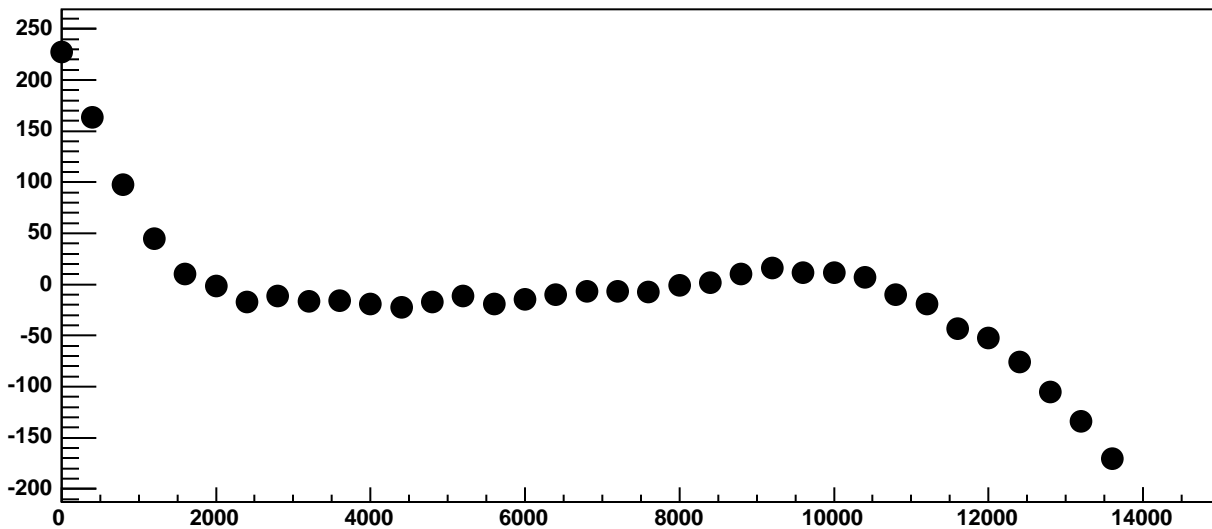


χ^2 / ndf 847.5 / 23
p0 -202.4 ± 1.171
p1 0.1694 ± 0.0001894

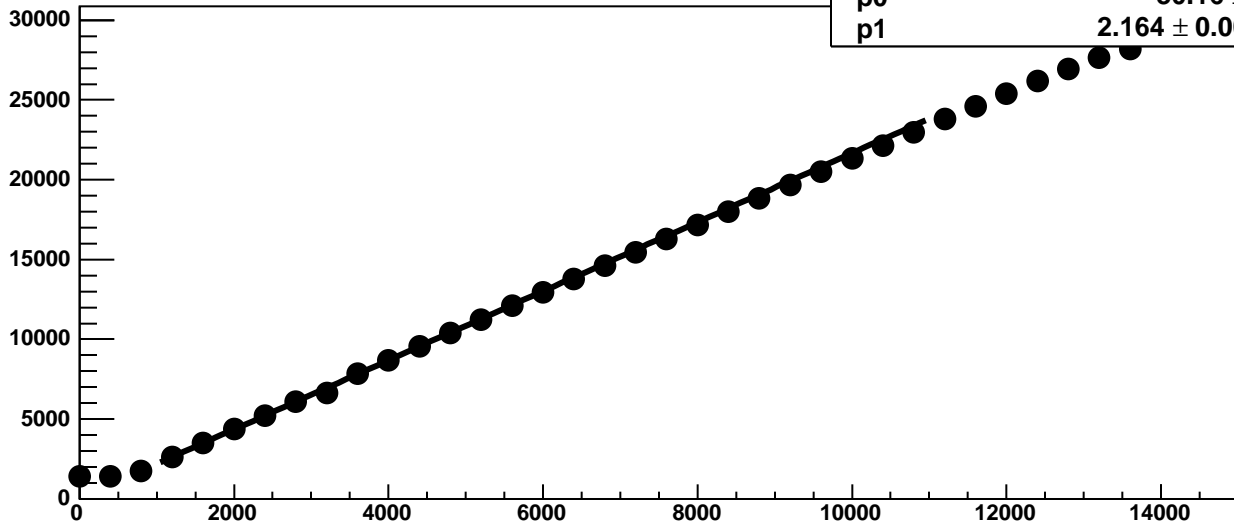
Chip 2, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

560.4 / 23

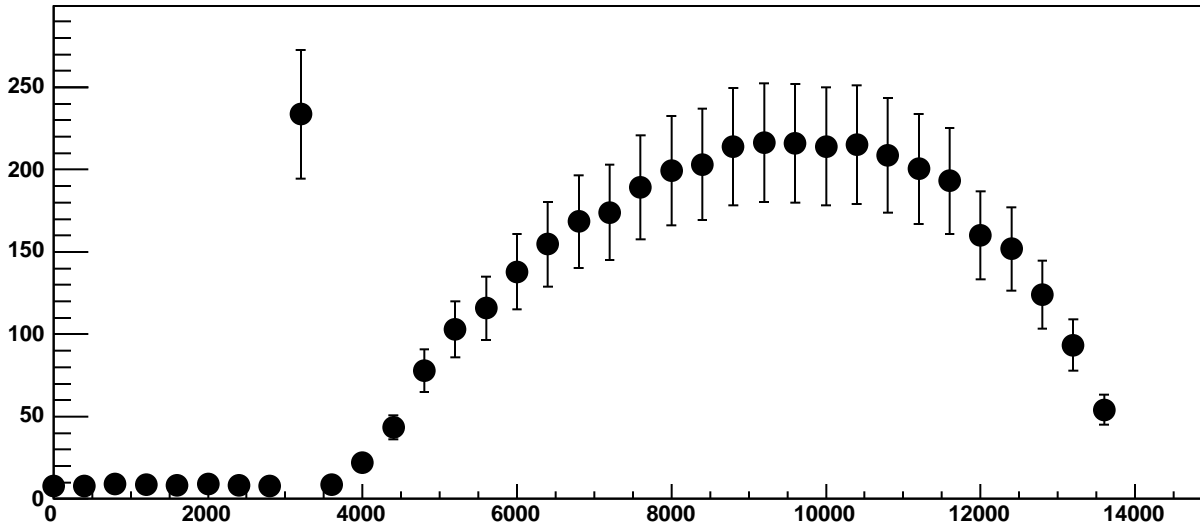
p0

30.16 ± 2.142

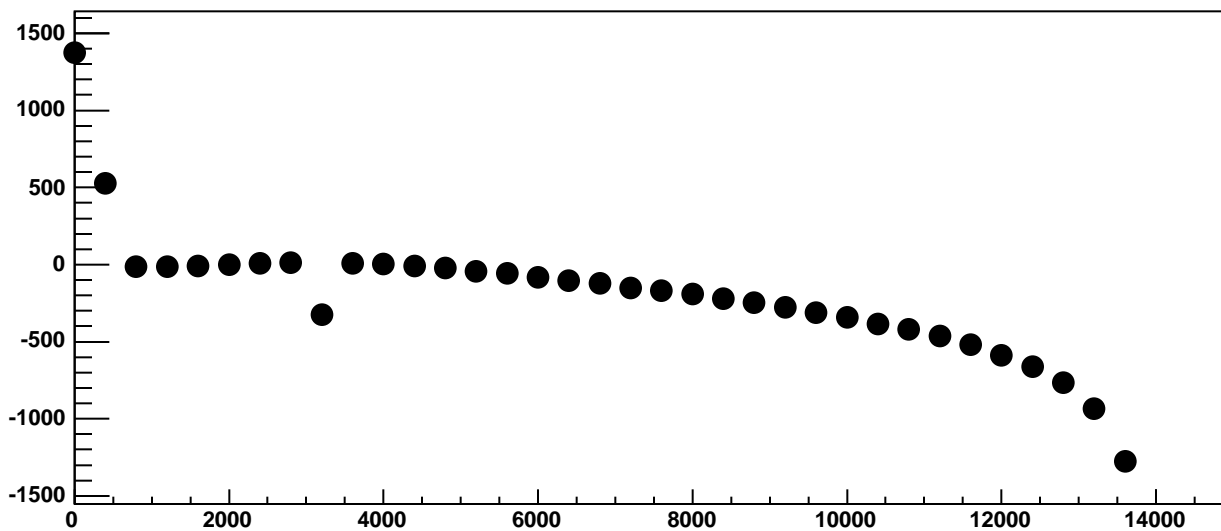
p1

2.164 ± 0.0008437

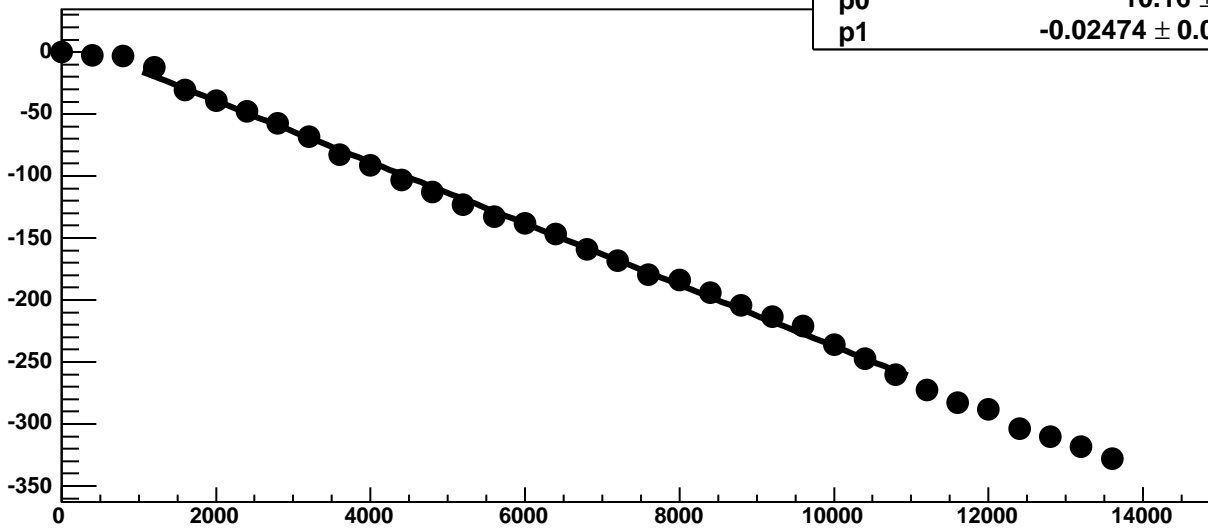
Chip 2, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

60.1 / 23

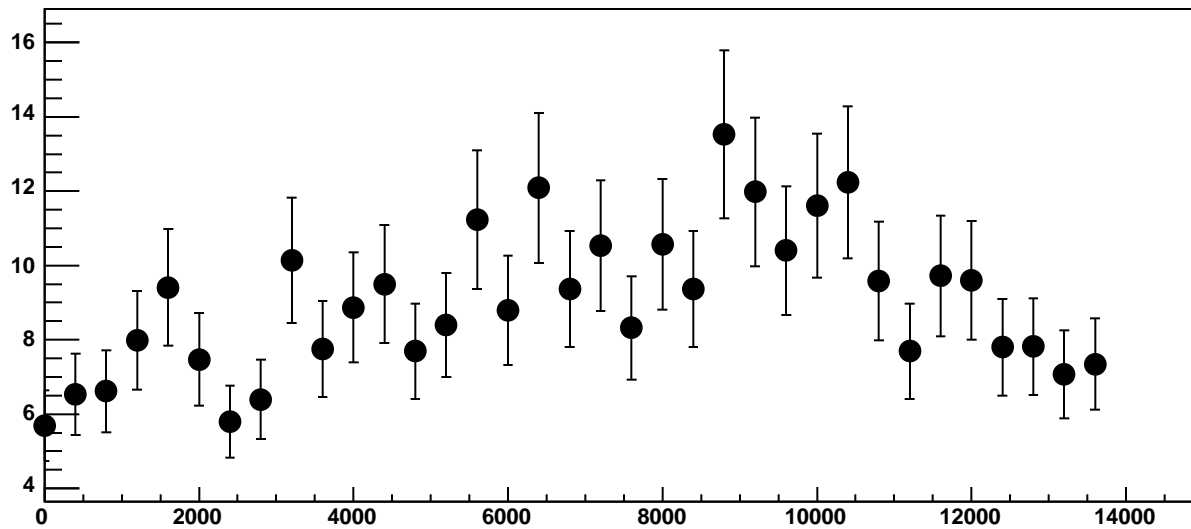
p0

10.16 ± 0.8609

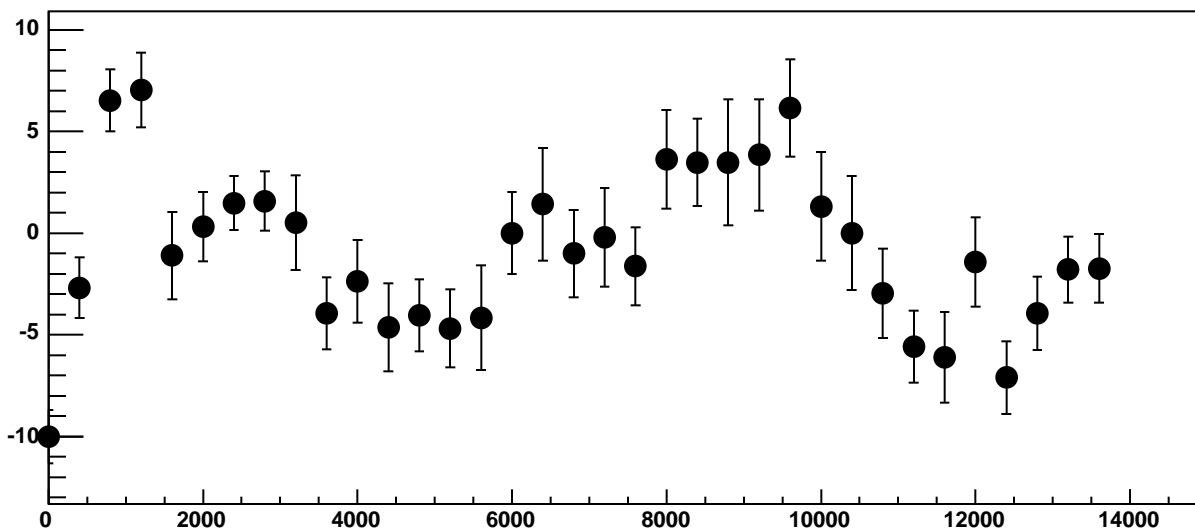
p1

-0.02474 ± 0.0001459

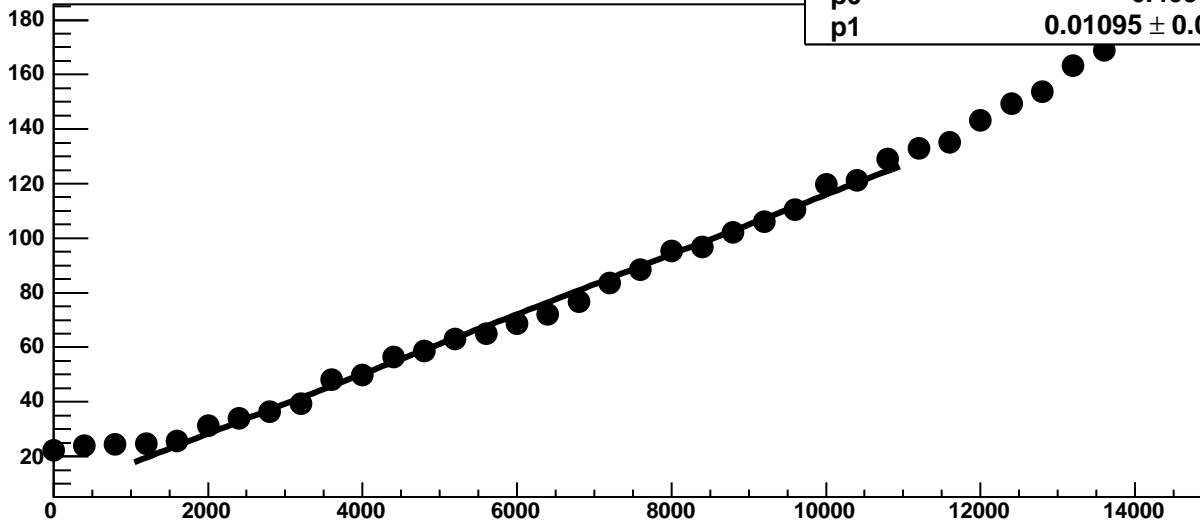
Chip 2, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



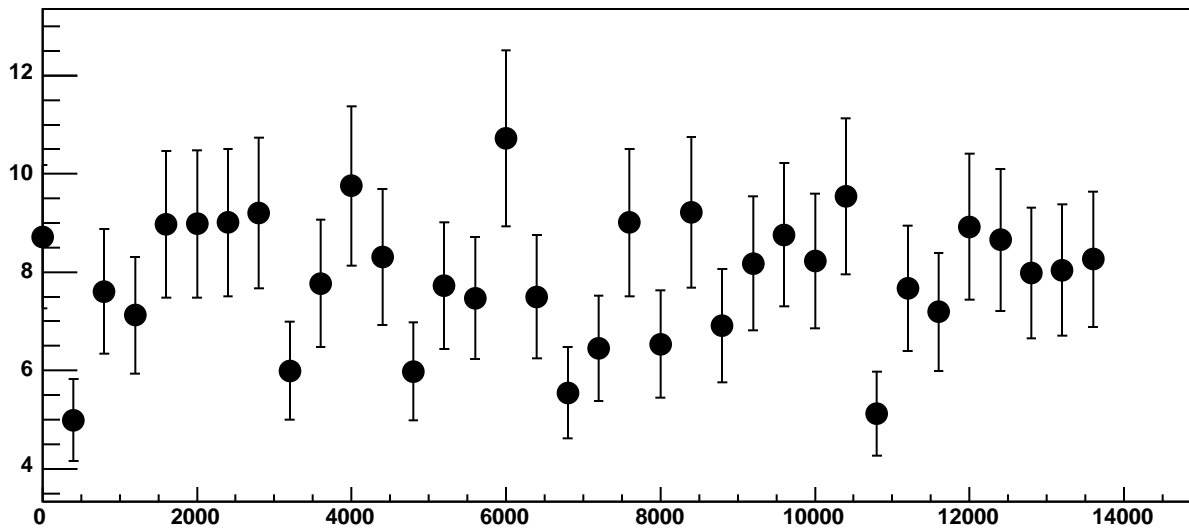
Chip 2, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



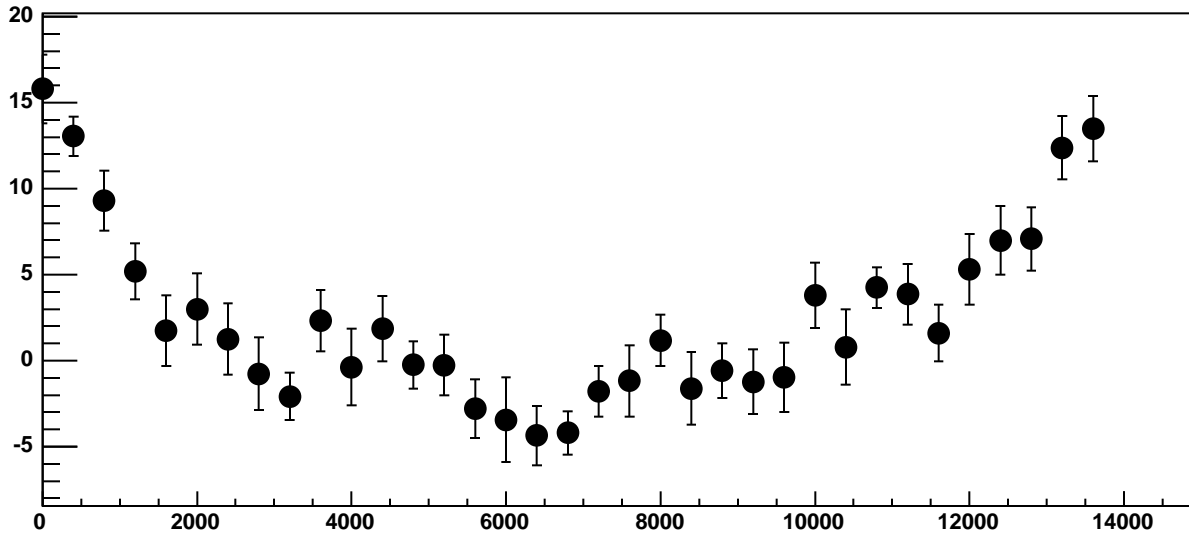
Chip 2, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



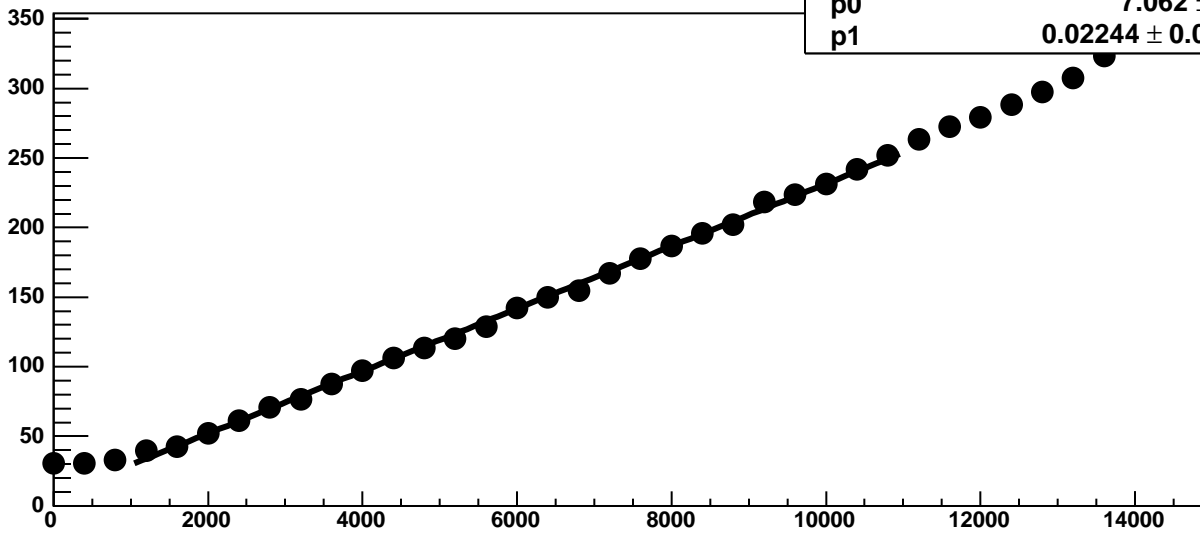
Chip 2, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

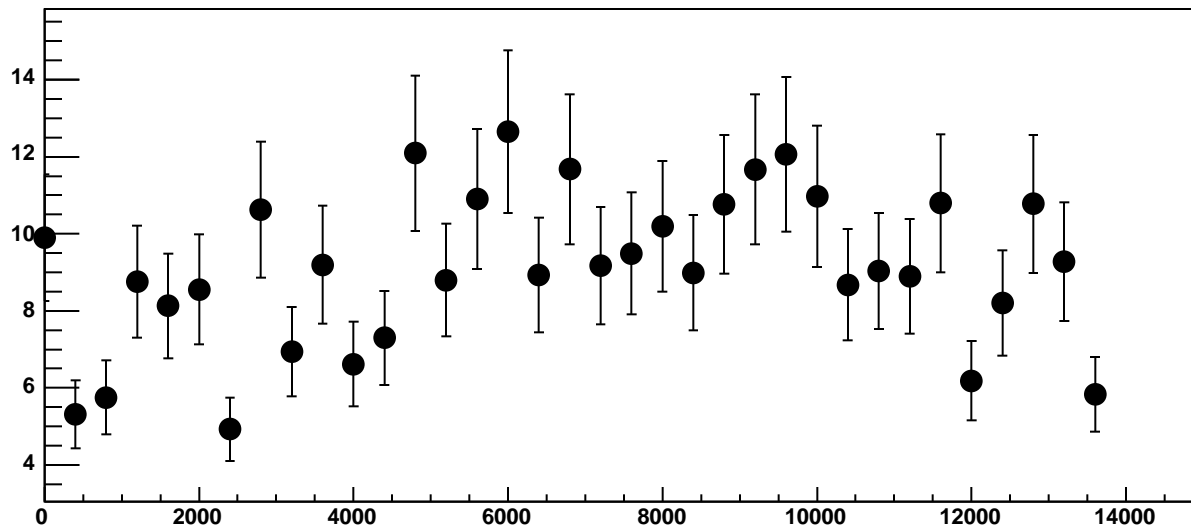


Chip 2, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

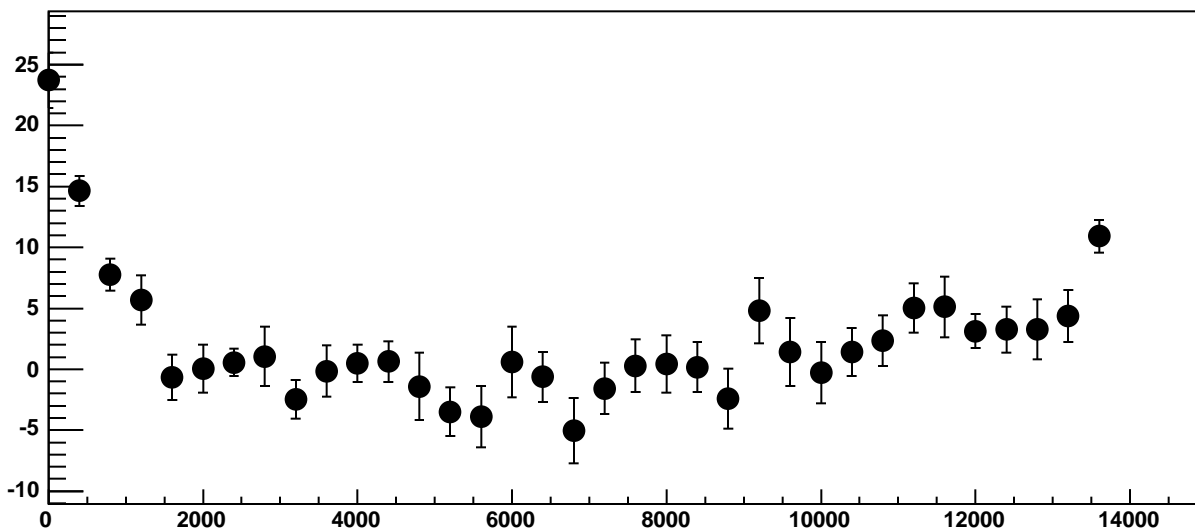


χ^2 / ndf 27.34 / 23
p0 7.062 ± 0.8381
p1 0.02244 ± 0.0001389

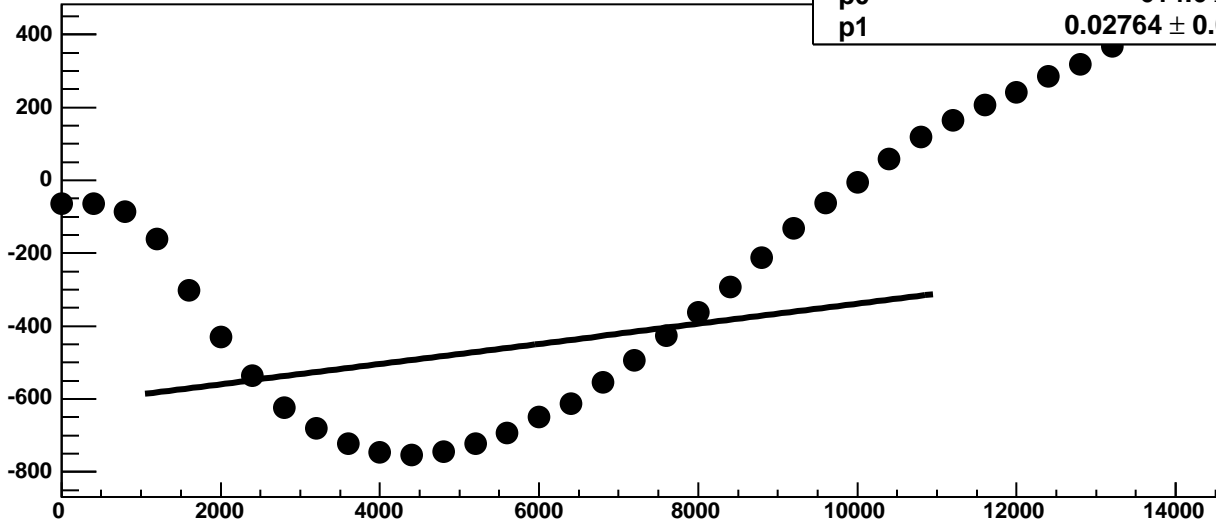
Chip 2, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC

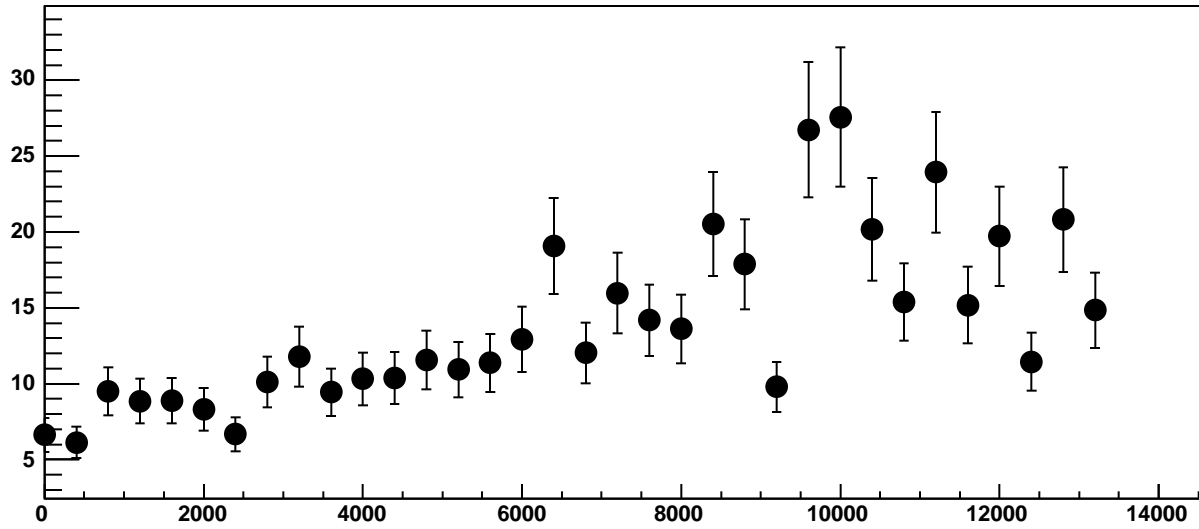


Chip 2, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

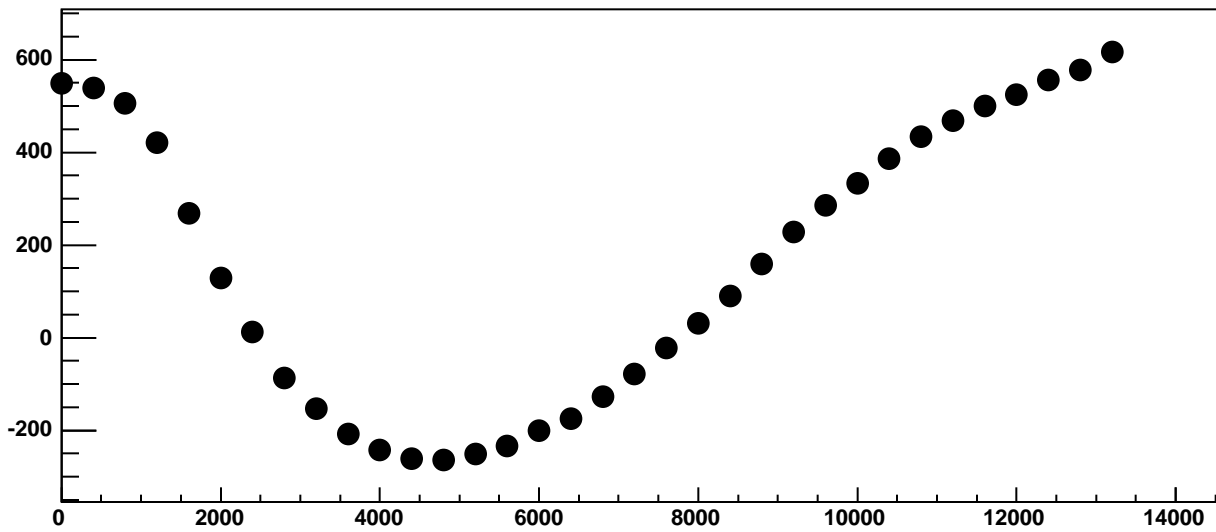


χ^2 / ndf 1.772e+05 / 23
p0 -614.6 ± 1.046
p1 0.02764 ± 0.000197

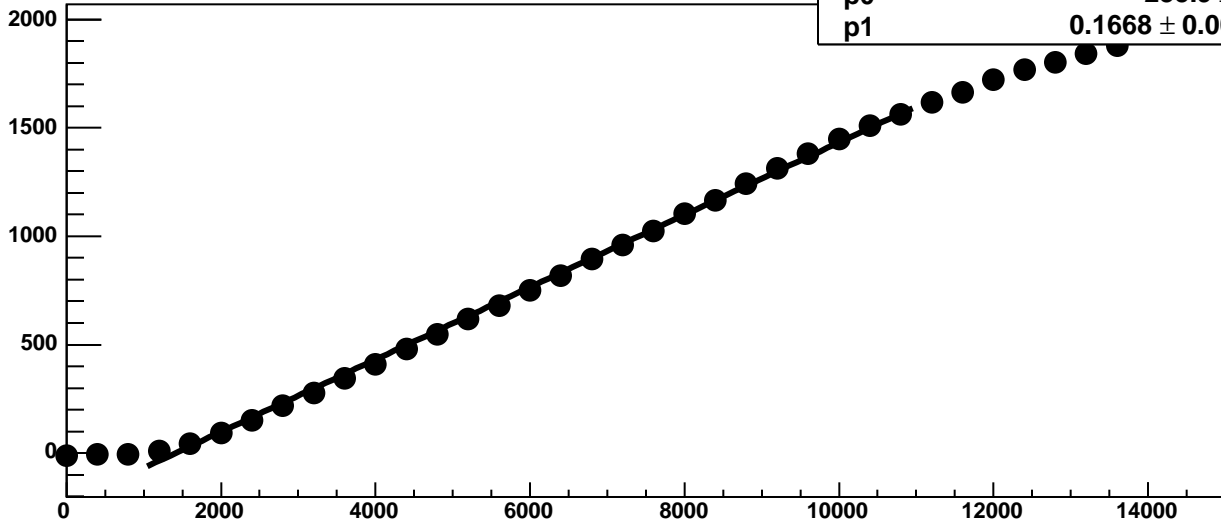
Chip 2, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



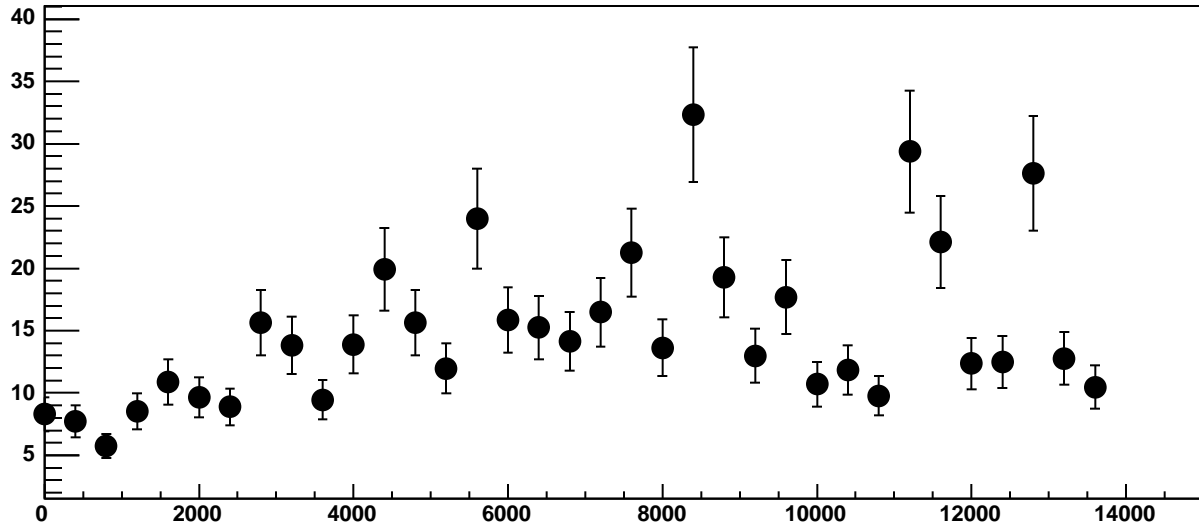
Chip 2, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



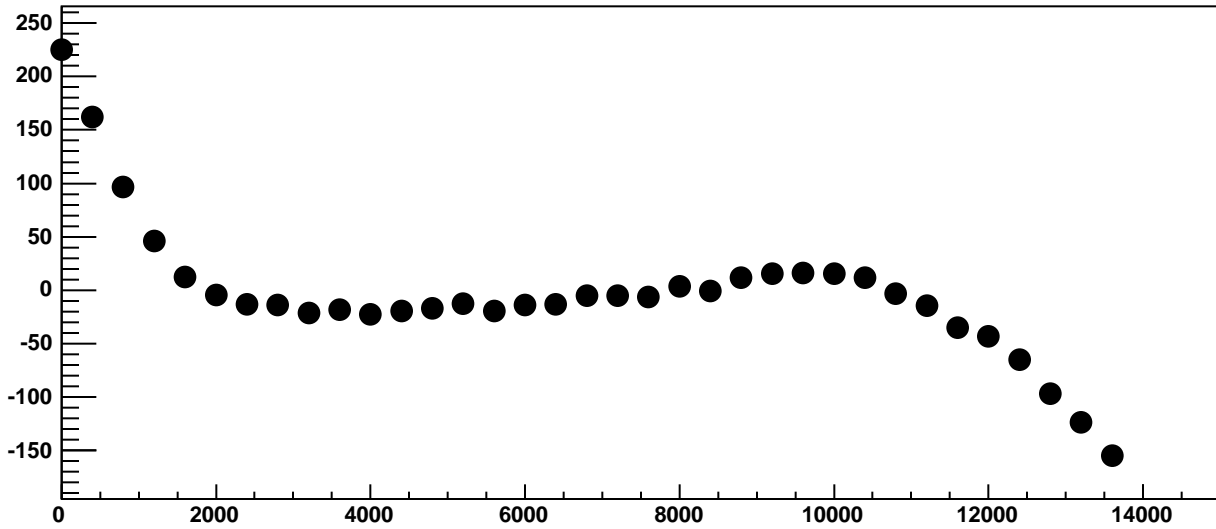
Chip 2, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



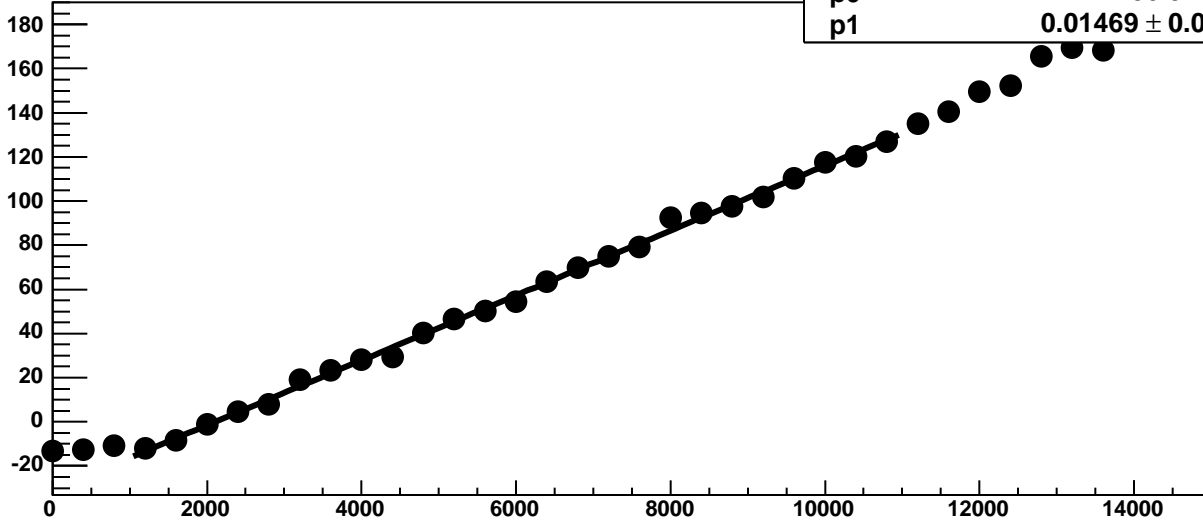
Chip 2, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

34.22 / 23

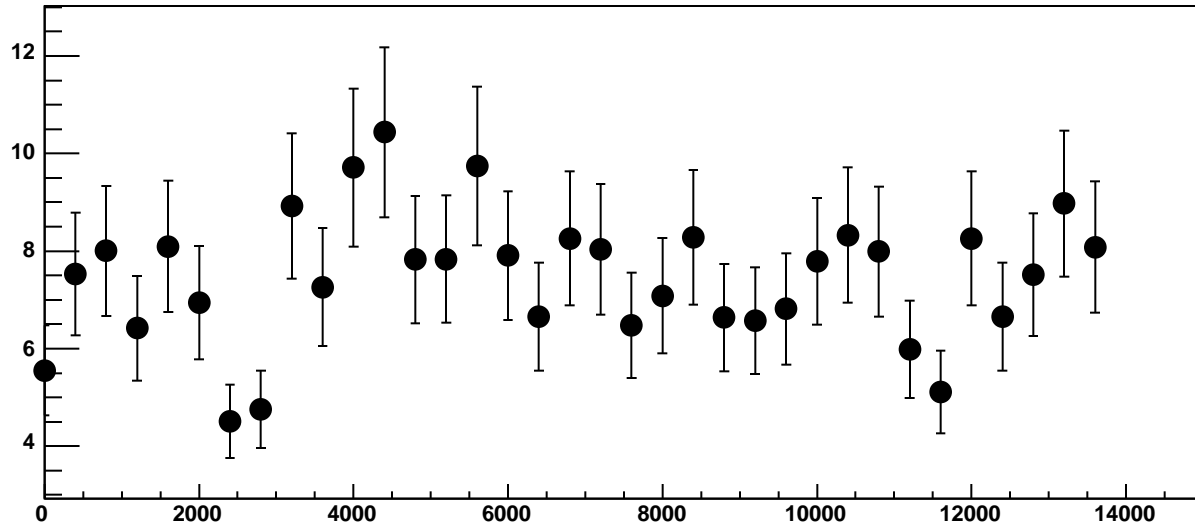
p0

-30.9 ± 0.7014

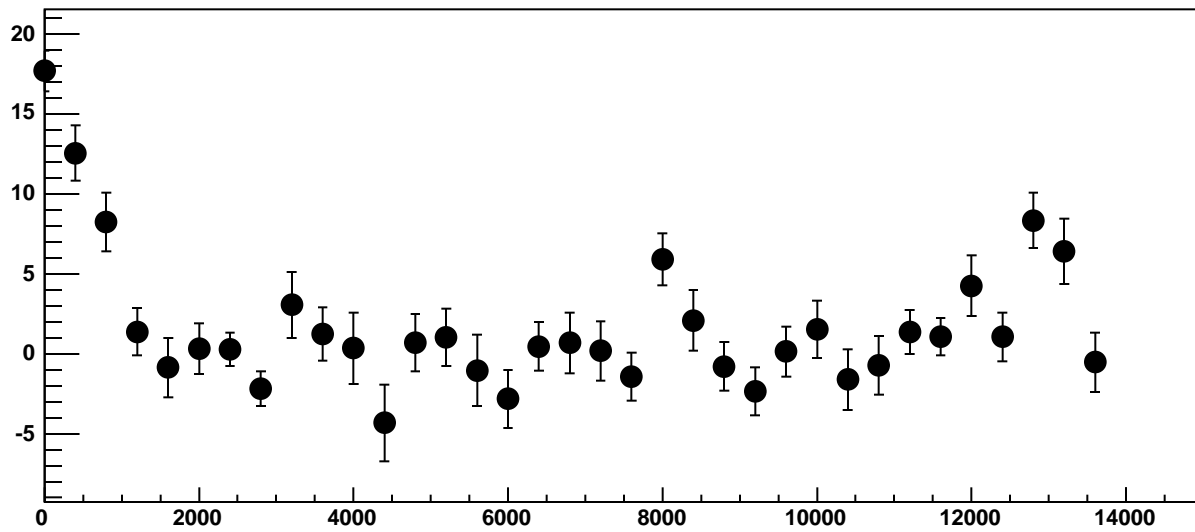
p1

0.01469 ± 0.0001095

Chip 2, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

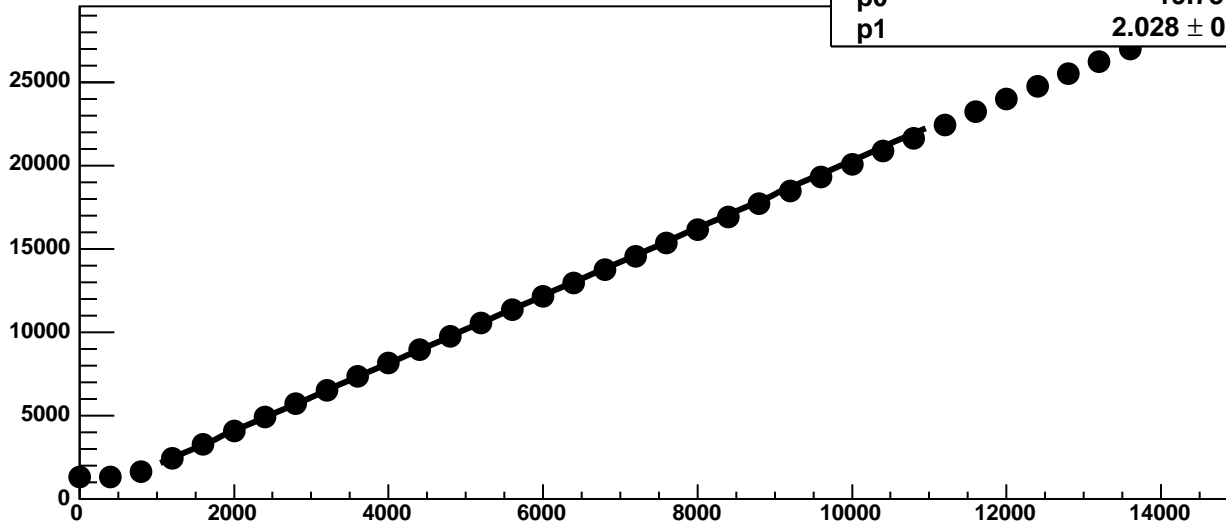


Chip 2, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

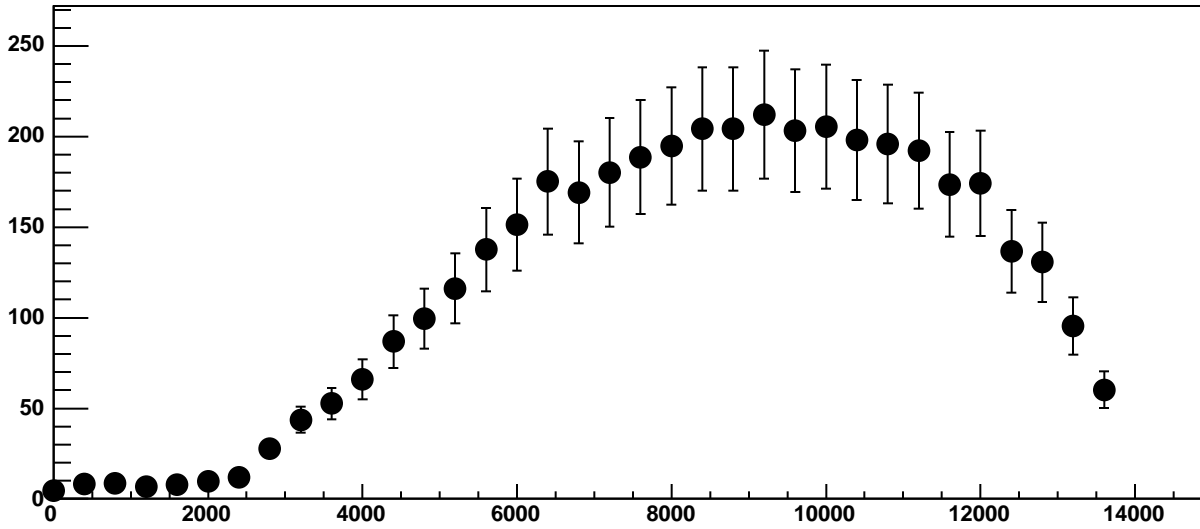


Chip 2, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC

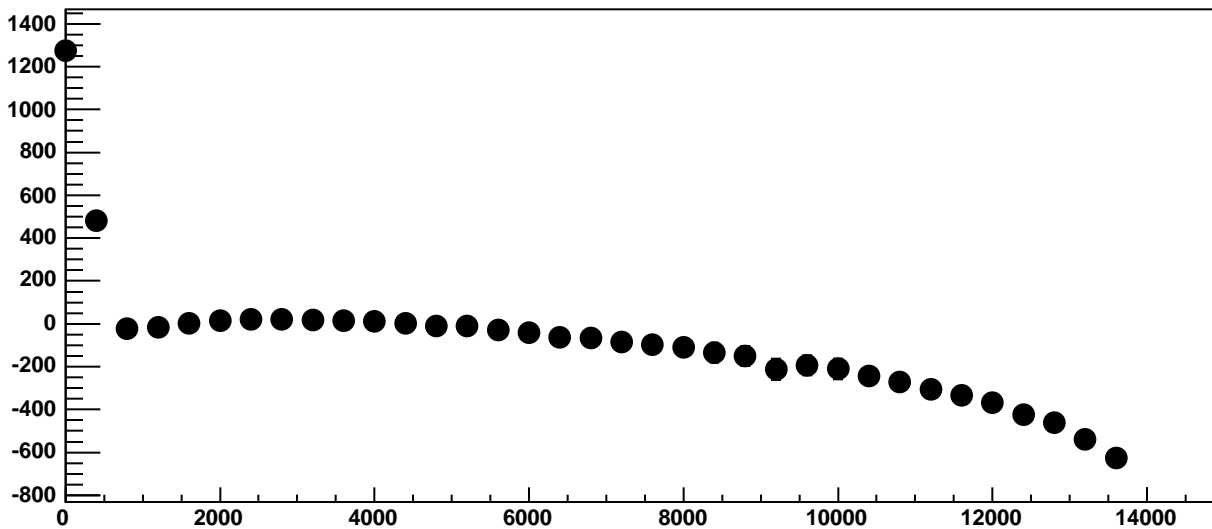
χ^2 / ndf 376.8 / 23
p0 19.75 ± 2.322
p1 2.028 ± 0.001224



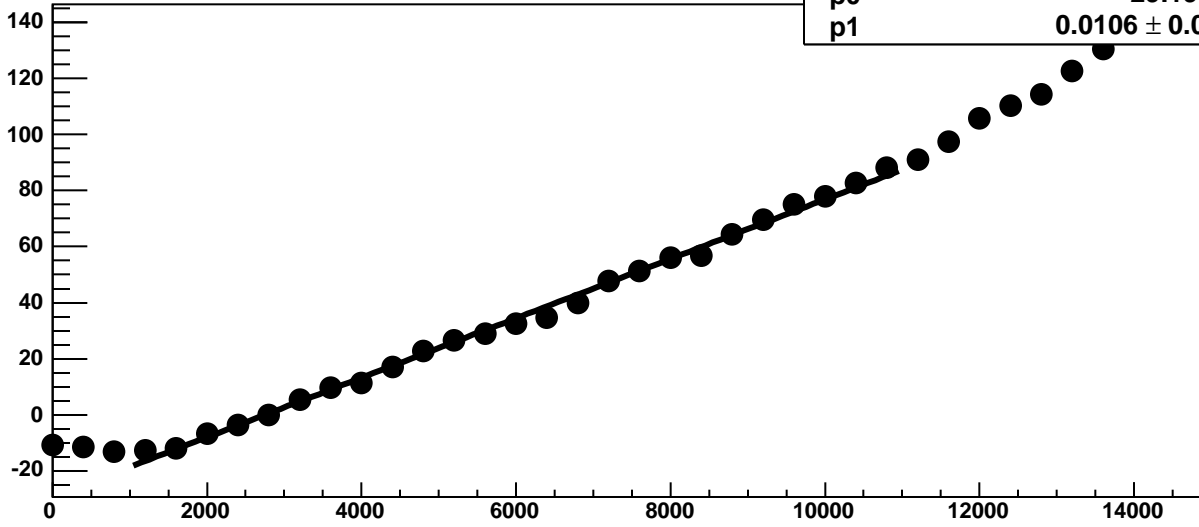
Chip 2, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



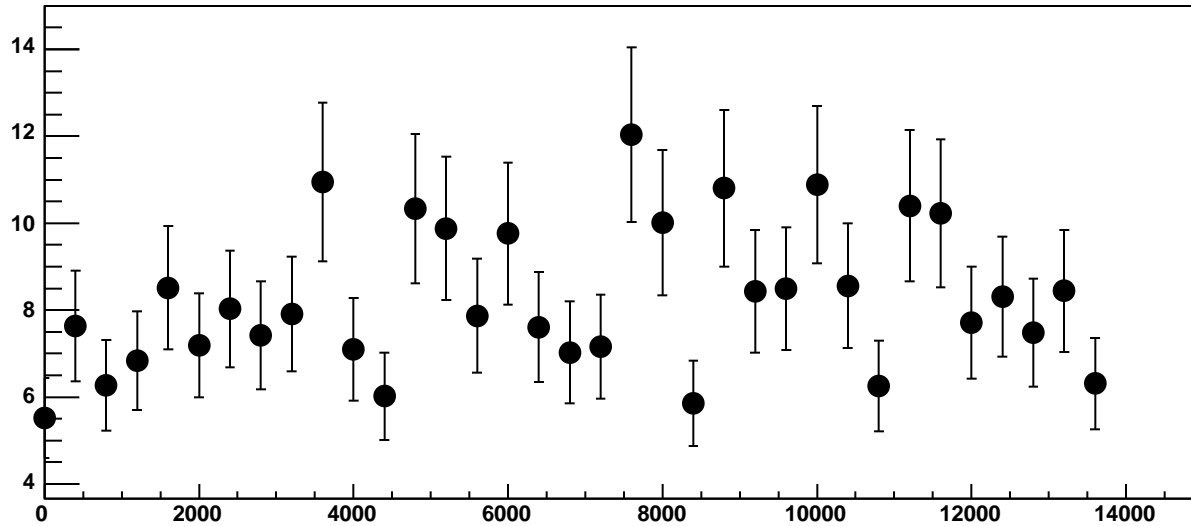
Chip 2, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC



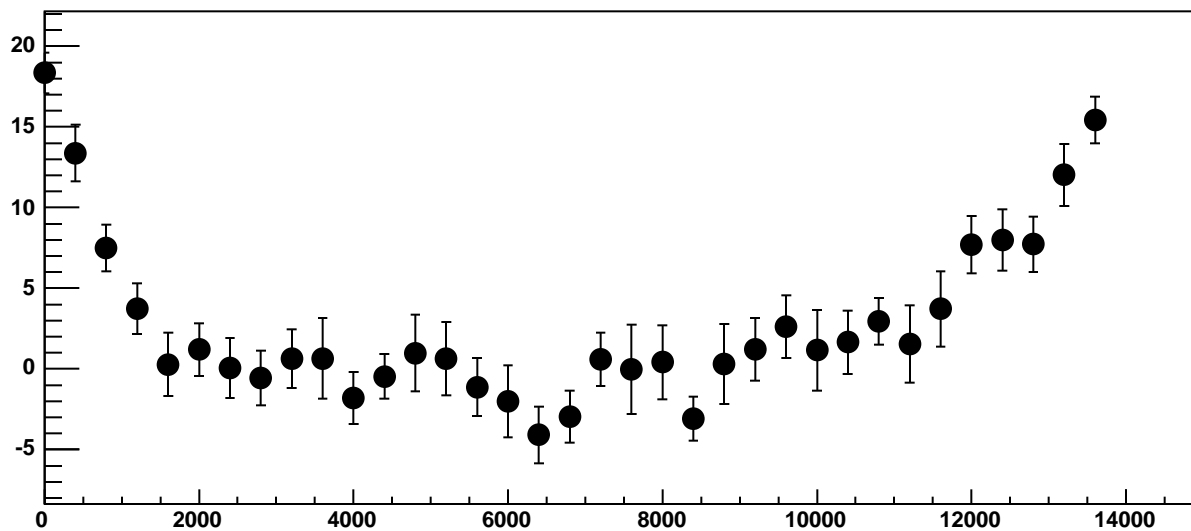
Chip 2, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



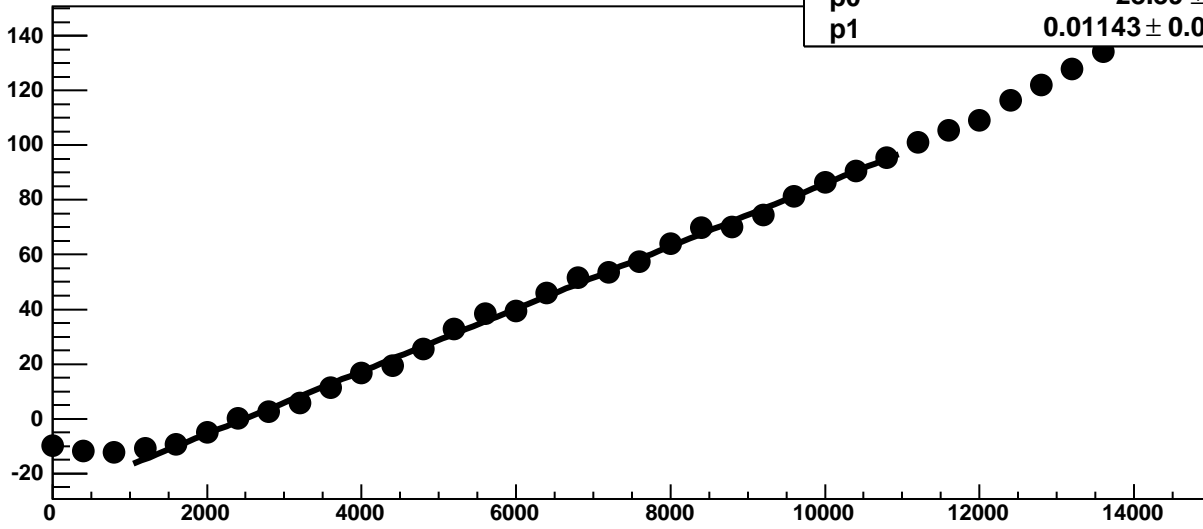
Chip 2, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

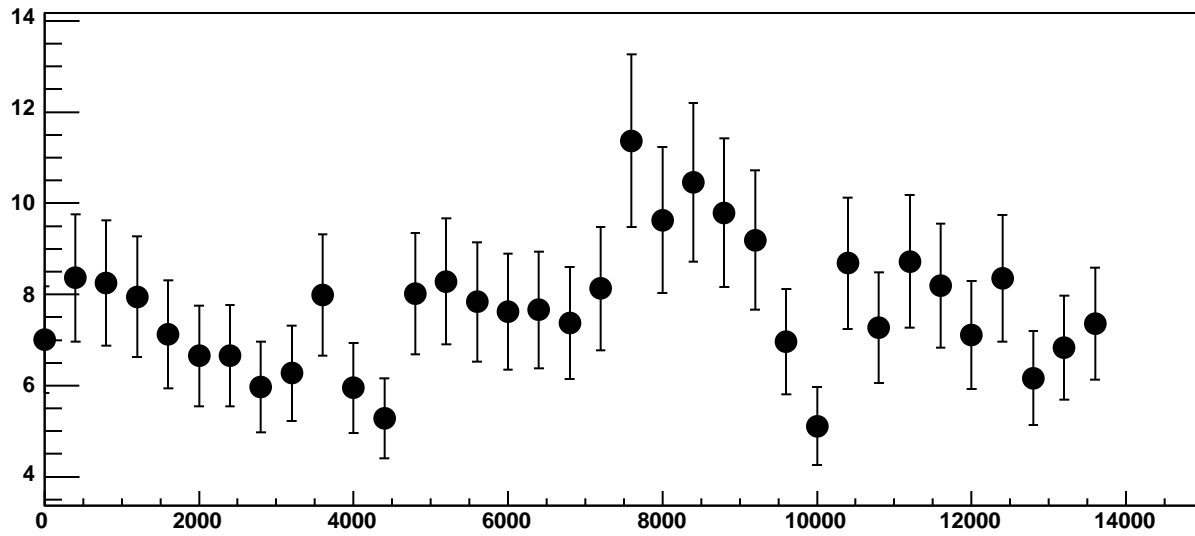


Chip 2, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

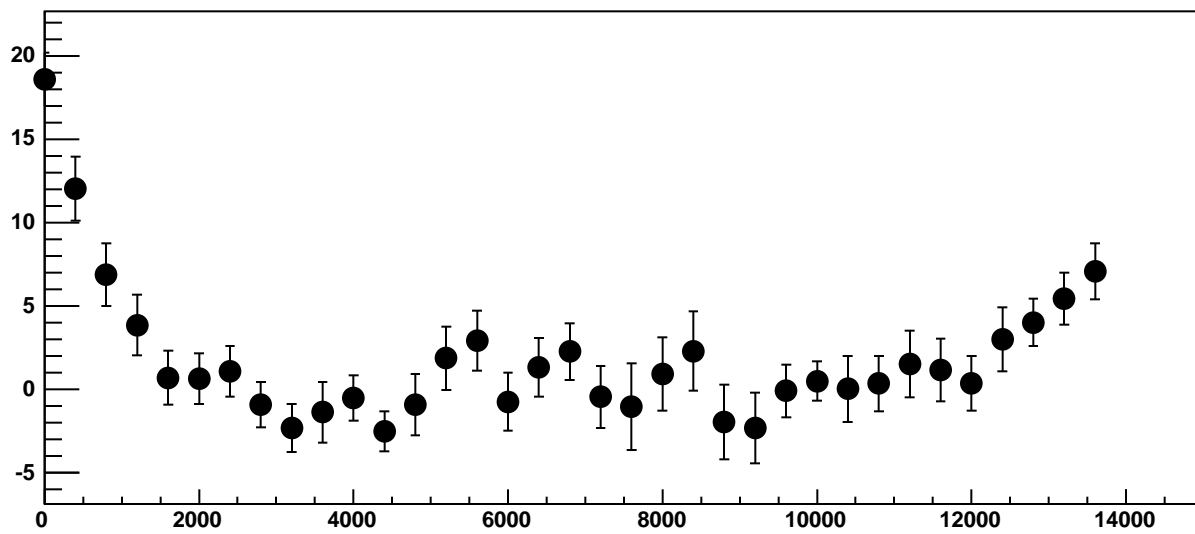


χ^2 / ndf 23.29 / 23
p0 -28.39 ± 0.7266
p1 0.01143 ± 0.0001133

Chip 2, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

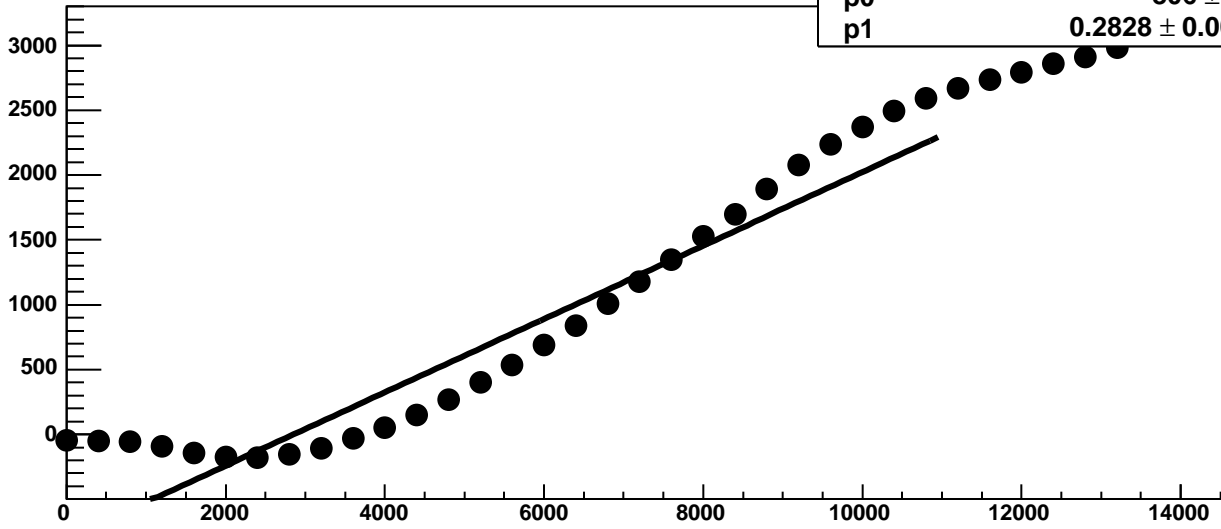


Chip 2, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

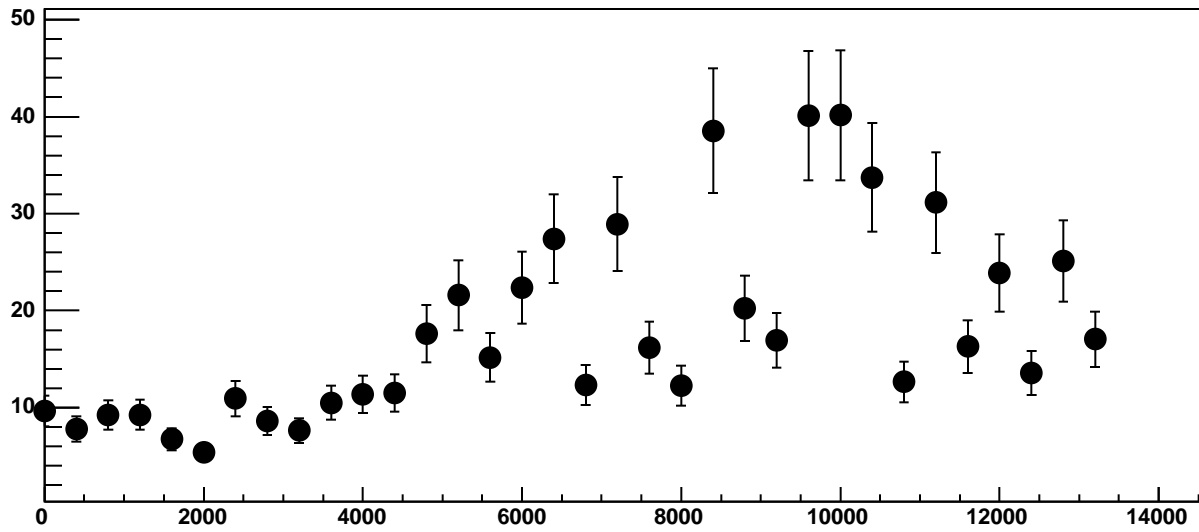


Chip 2, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC

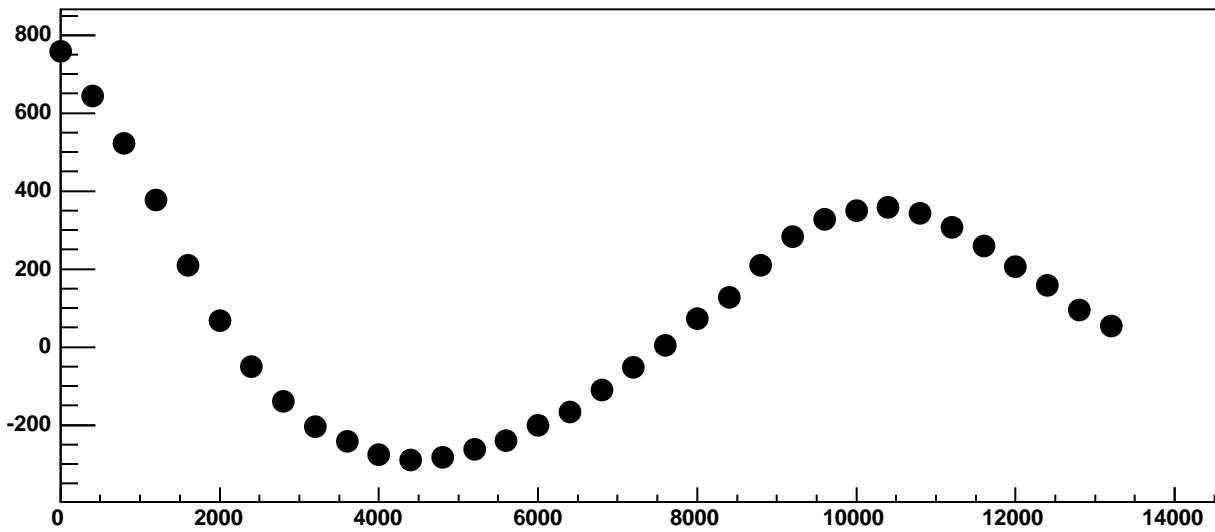
χ^2 / ndf 1.486e+05 / 23
p0 -806 ± 0.9753
p1 0.2828 ± 0.0002106



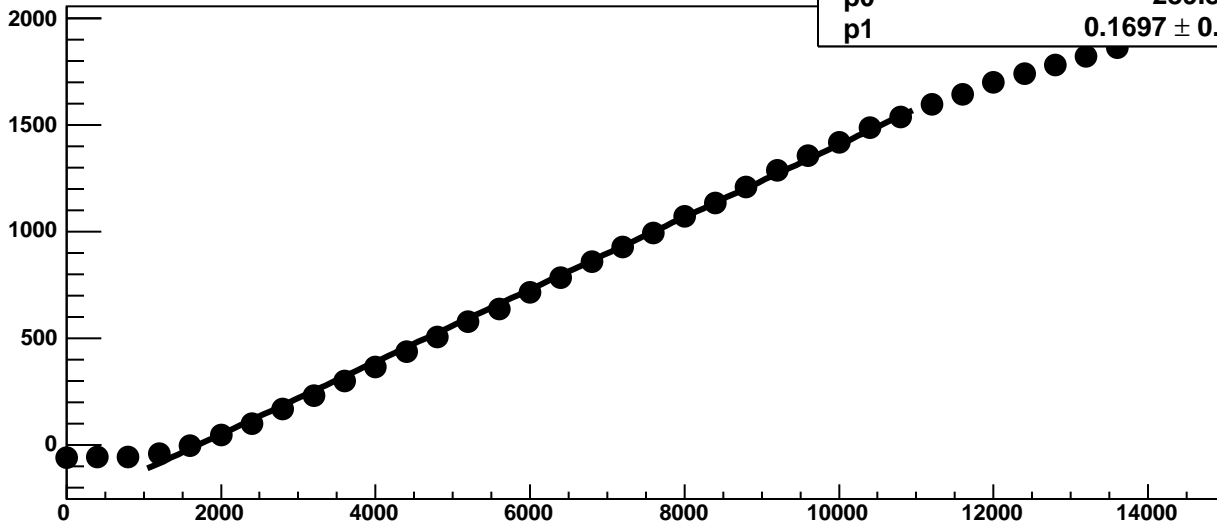
Chip 2, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC

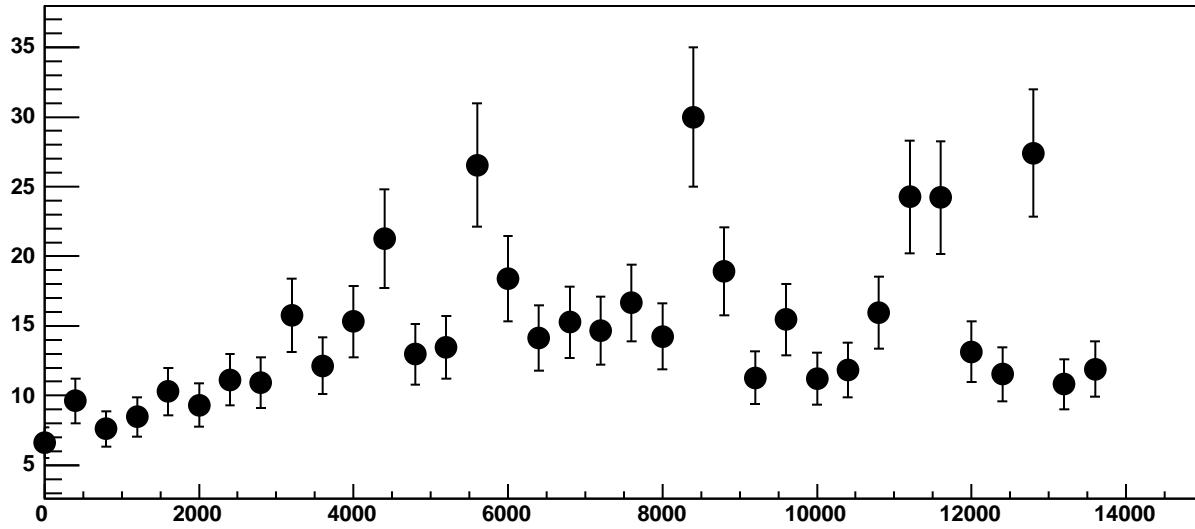


Chip 2, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

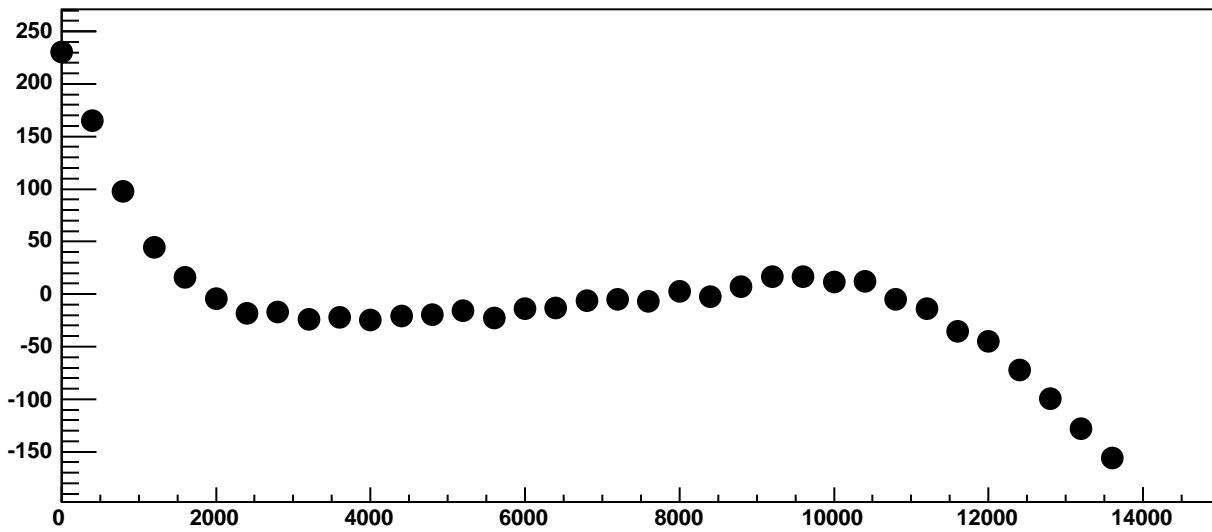


χ^2 / ndf 1070 / 23
p0 -289.8 ± 1.17
p1 0.1697 ± 0.000189

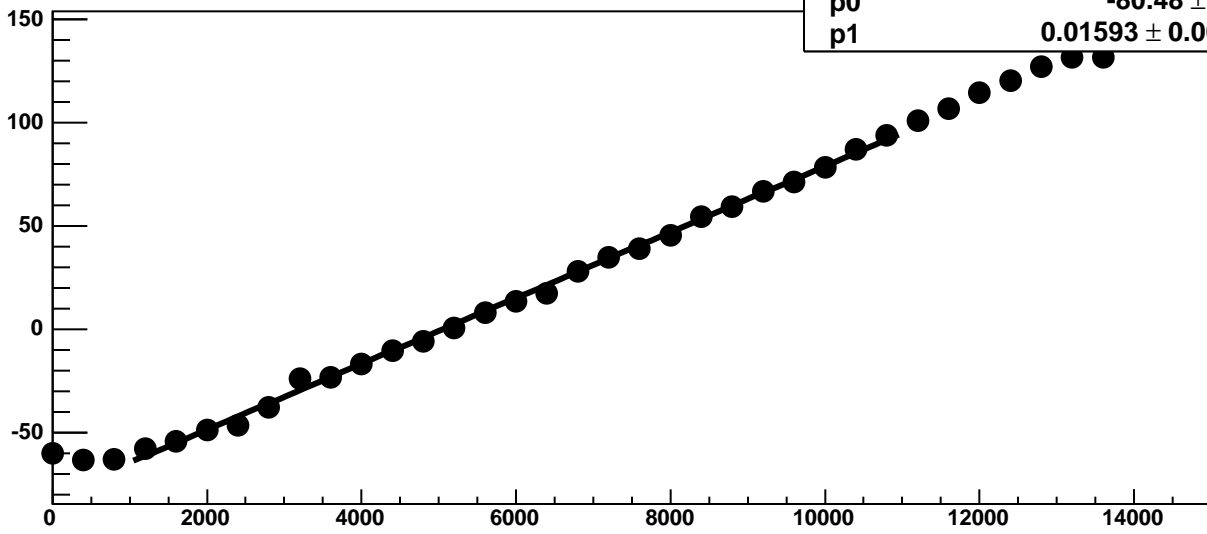
Chip 2, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

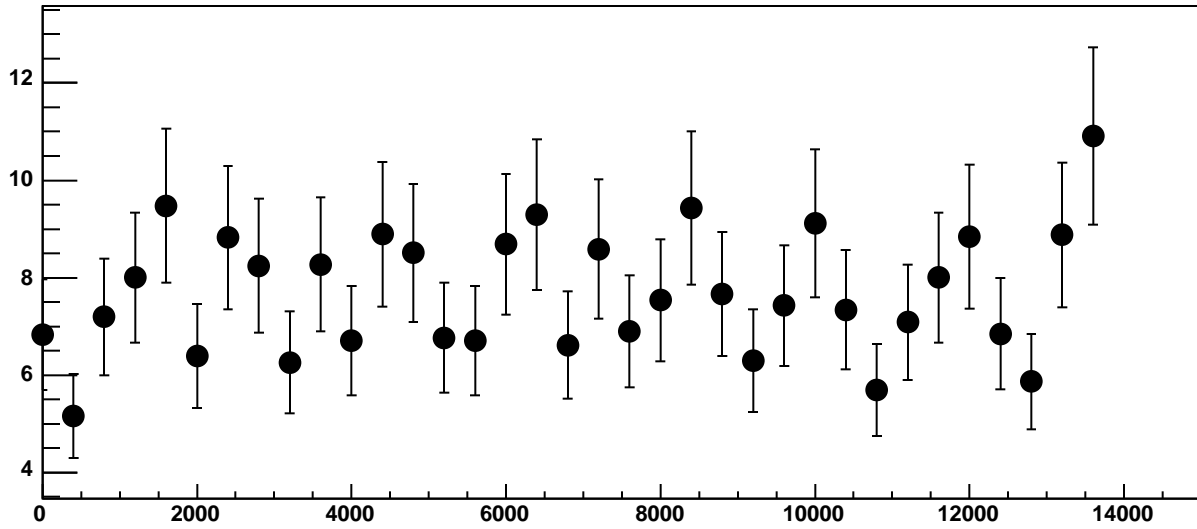


Chip 2, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

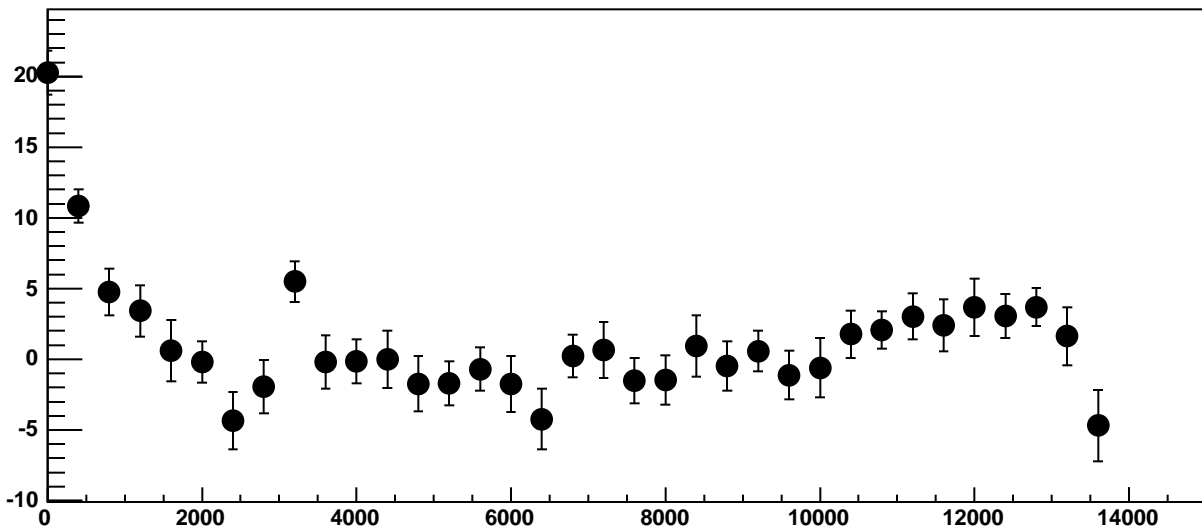


χ^2 / ndf 37.04 / 23
p0 -80.48 ± 0.8016
p1 0.01593 ± 0.0001173

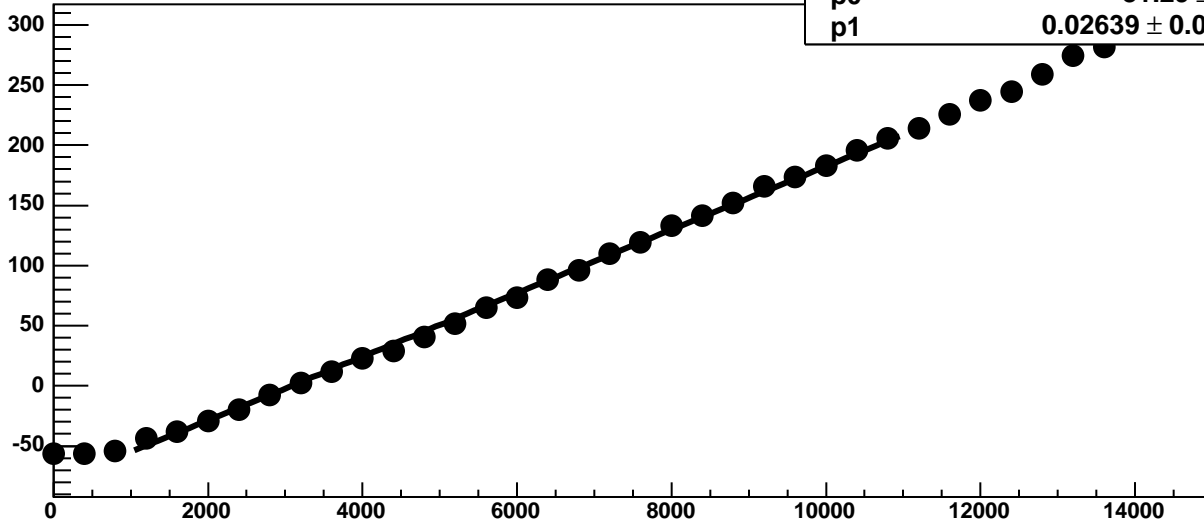
Chip 2, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

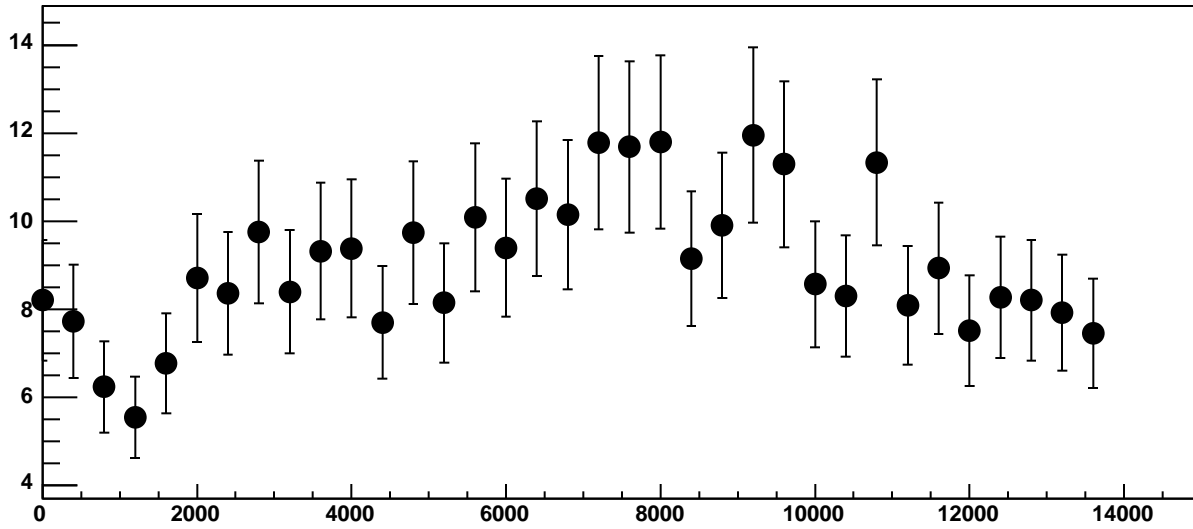


Chip 2, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

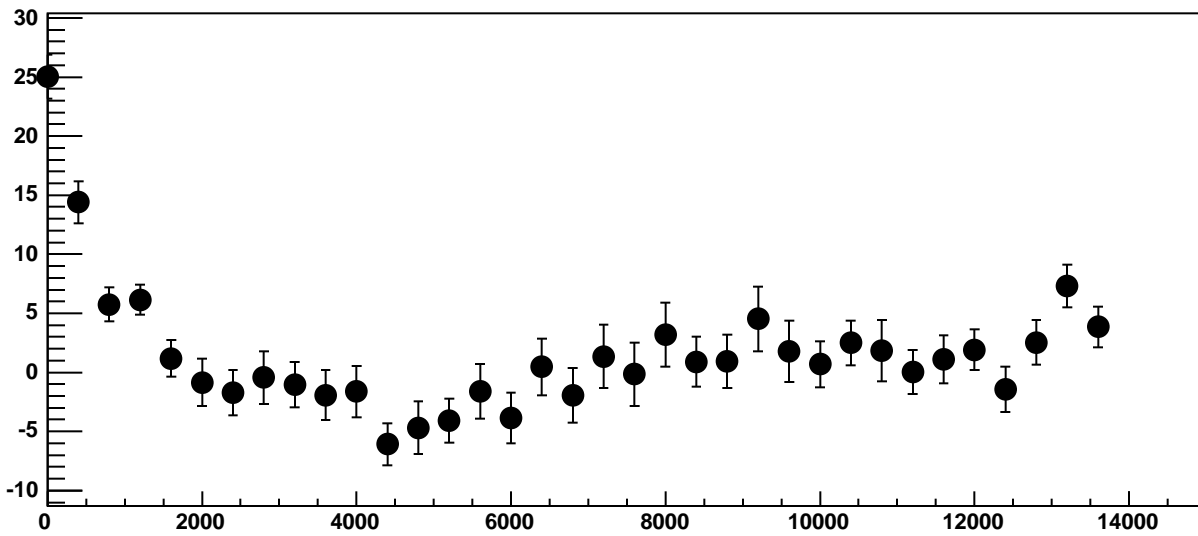


χ^2 / ndf 59.53 / 23
p0 -81.29 ± 0.8251
p1 0.02639 ± 0.0001355

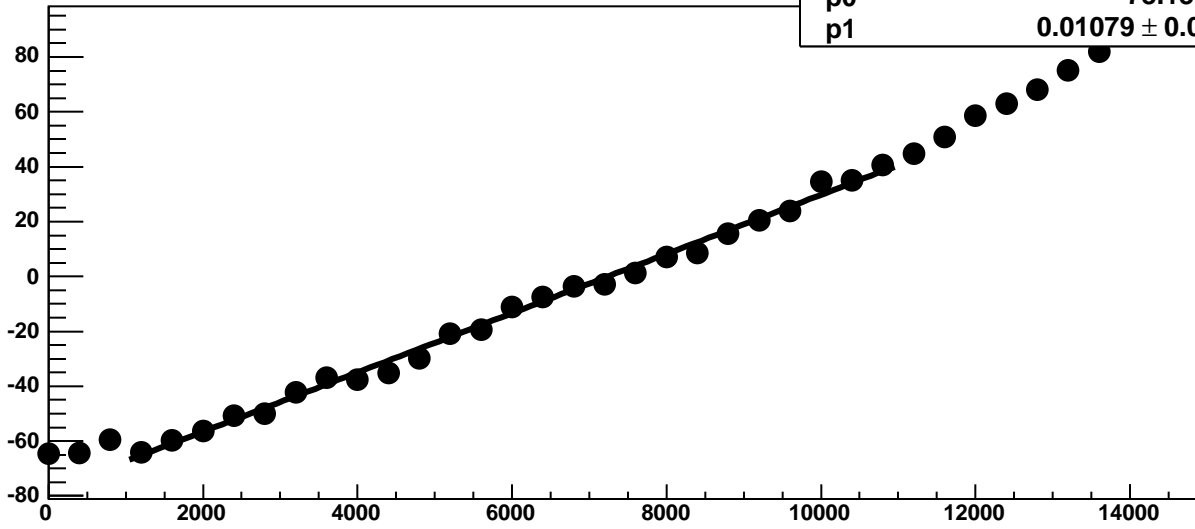
Chip 2, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

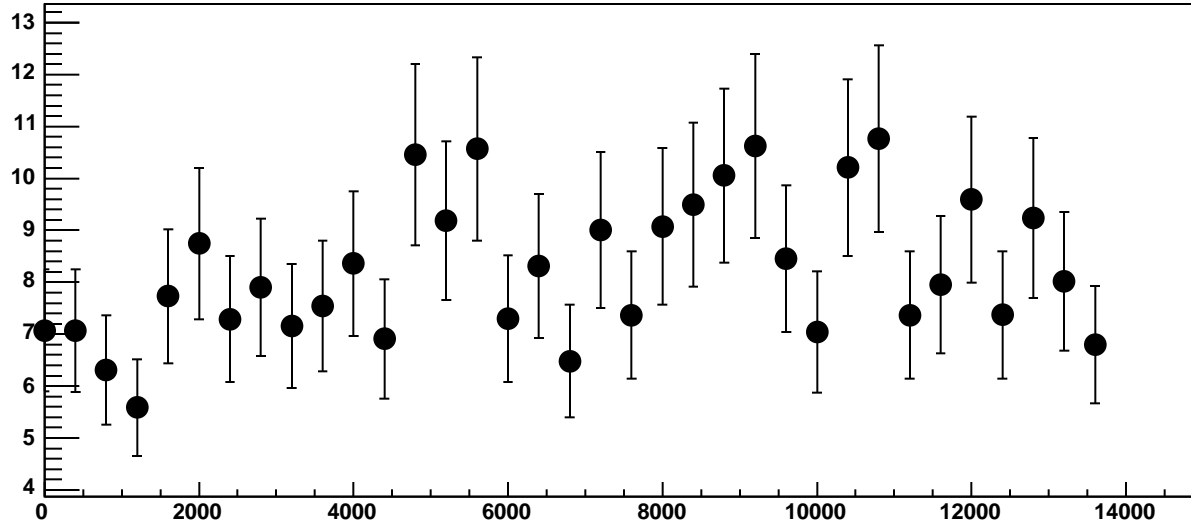


Chip 2, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

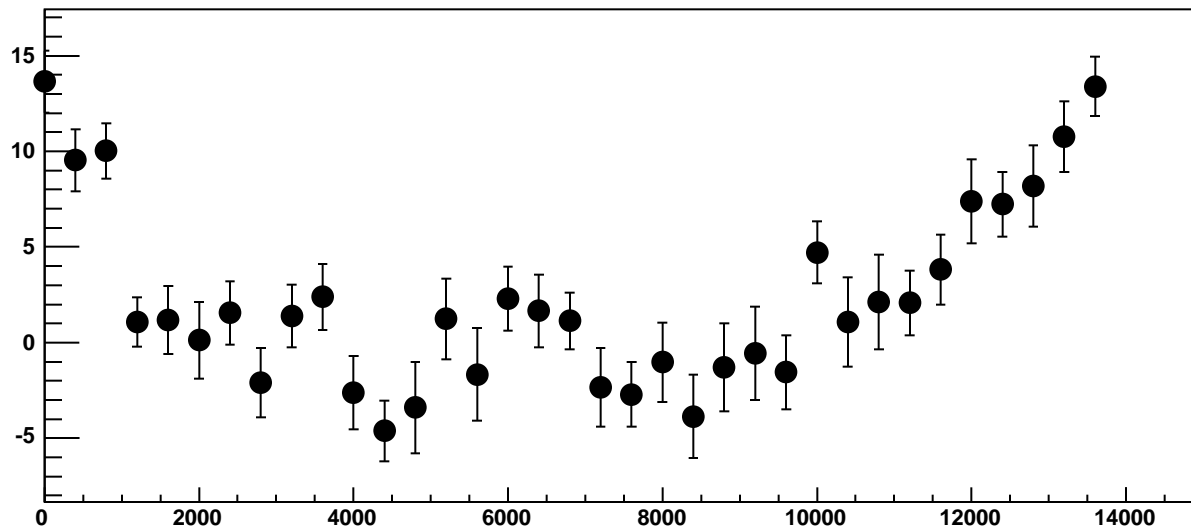


χ^2 / ndf 39.99 / 23
p0 -78.13 ± 0.795
p1 0.01079 ± 0.0001278

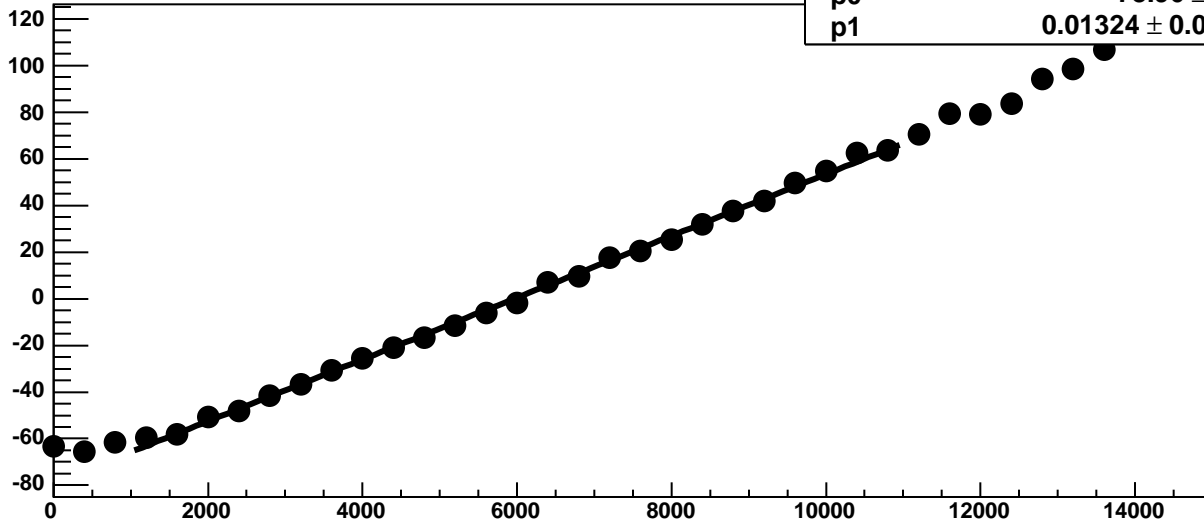
Chip 2, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

15.32 / 23

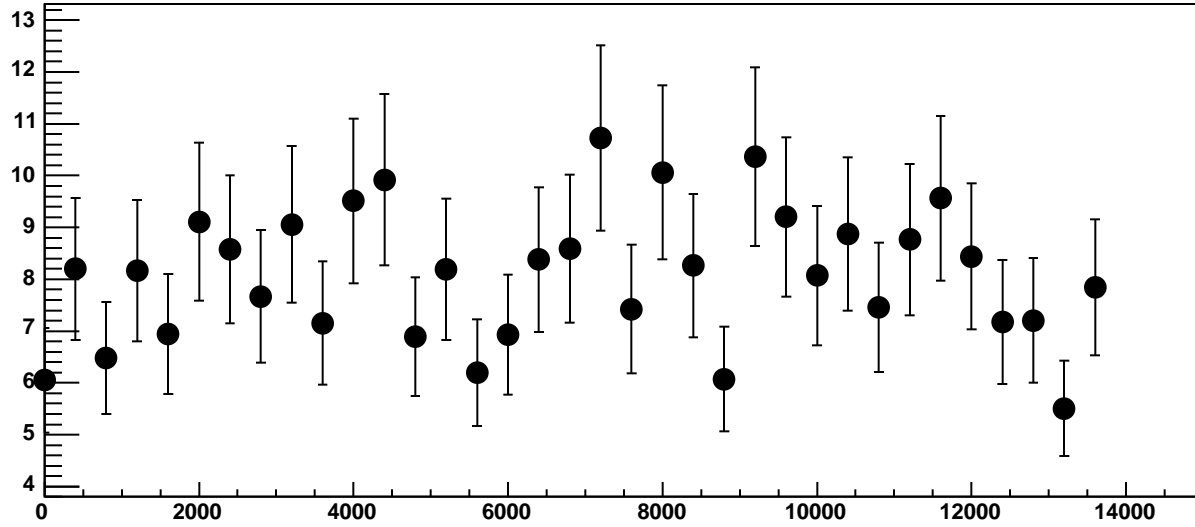
p0

-78.96 ± 0.8505

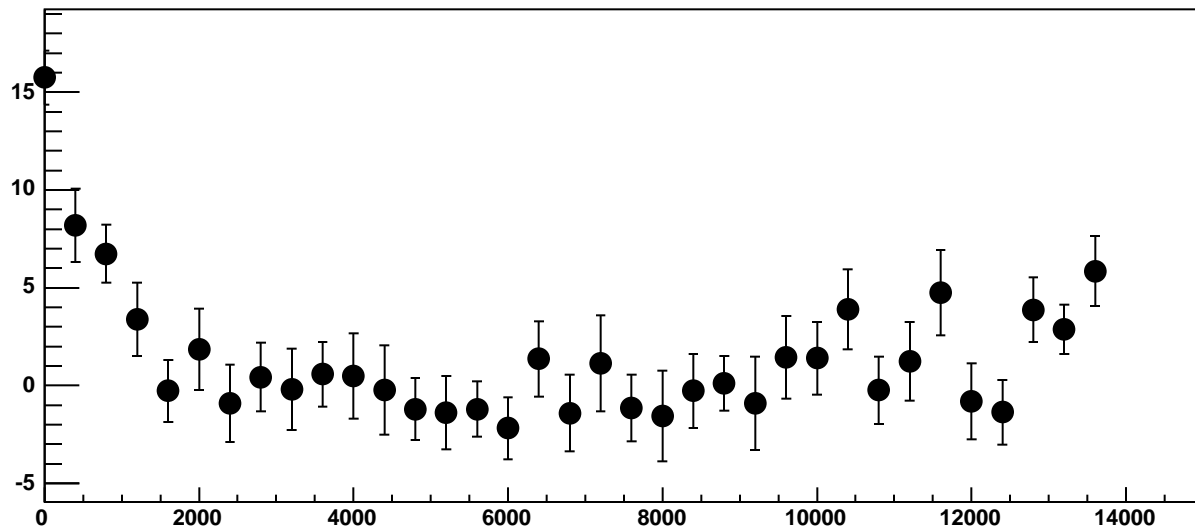
p1

0.01324 ± 0.0001288

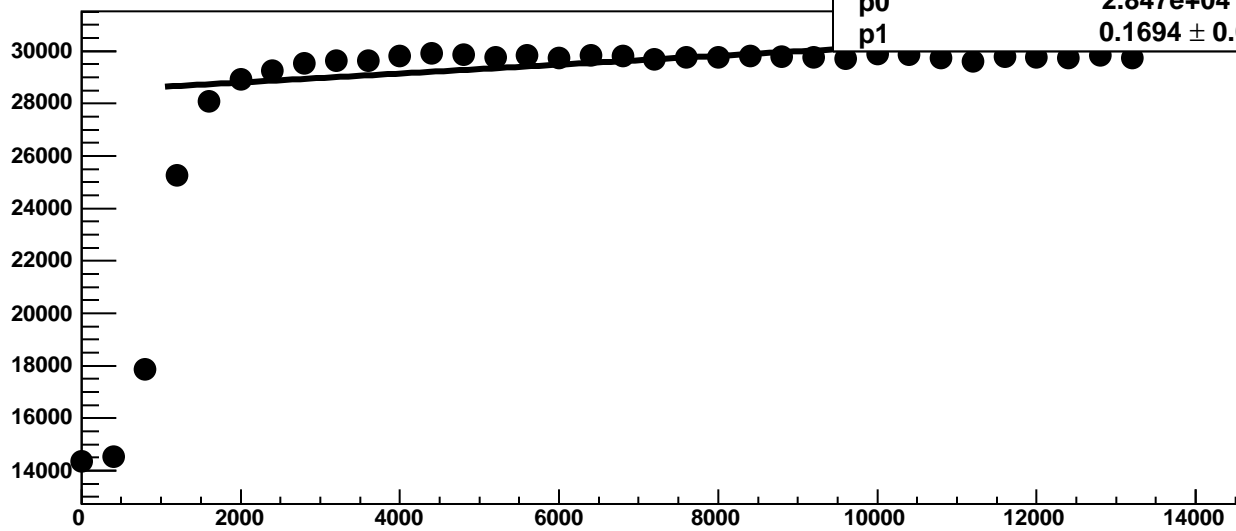
Chip 2, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



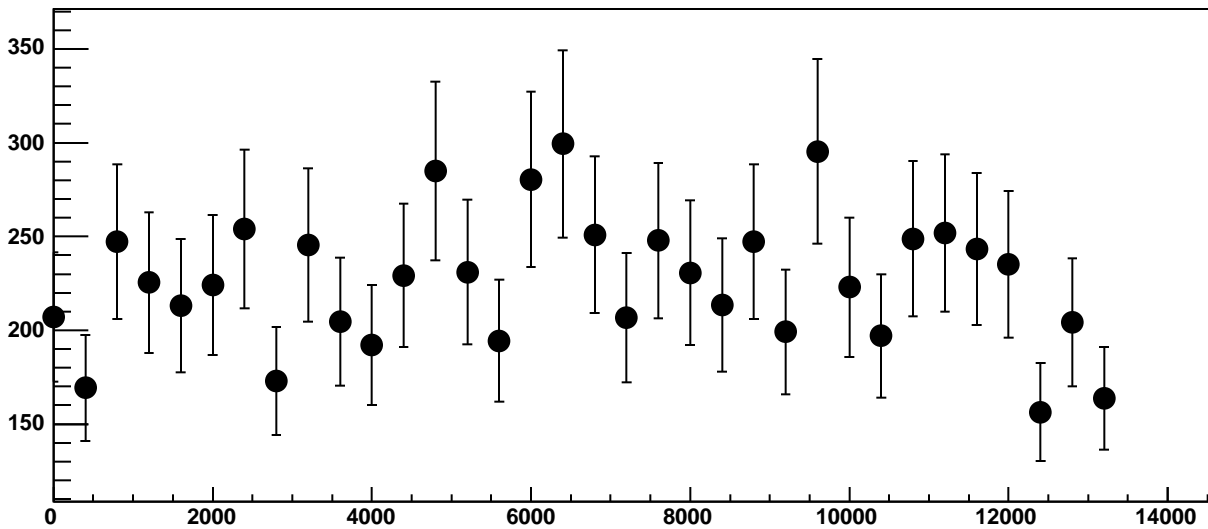
Chip 2, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



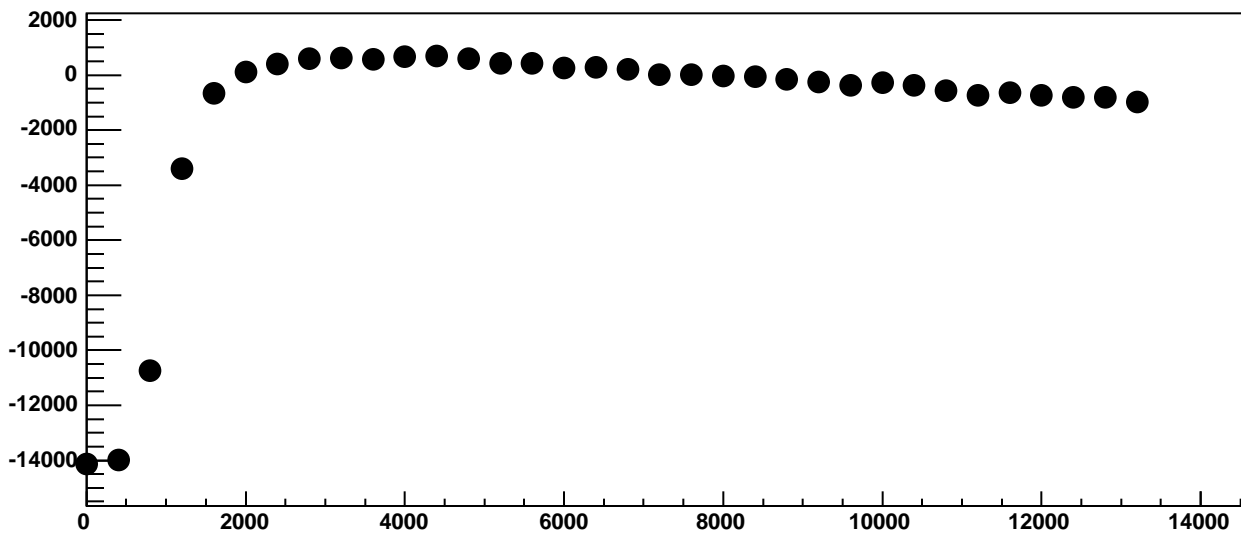
Chip 2, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC



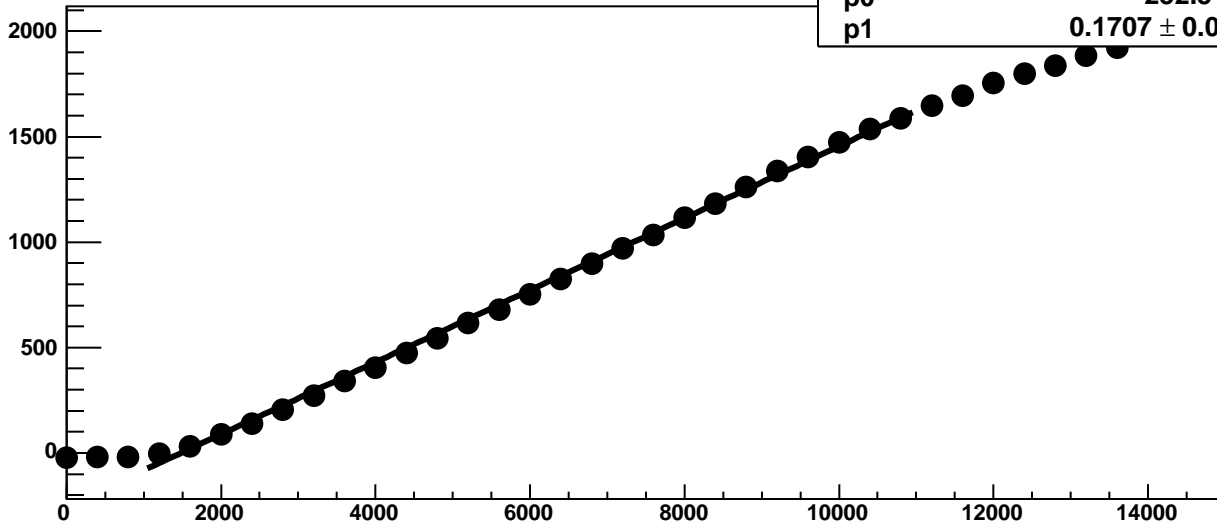
Chip 2, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC



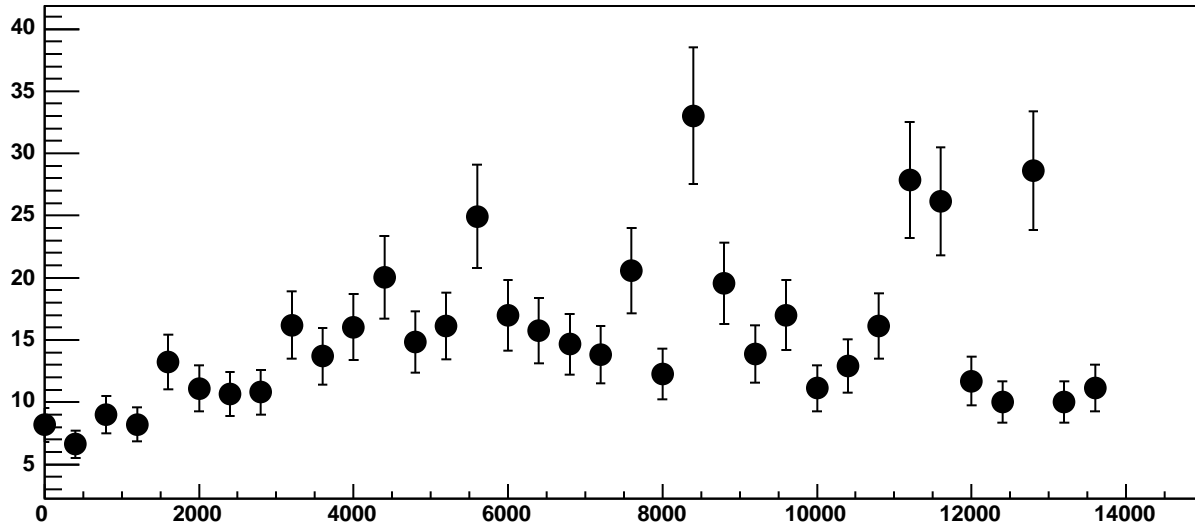
Chip 2, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC



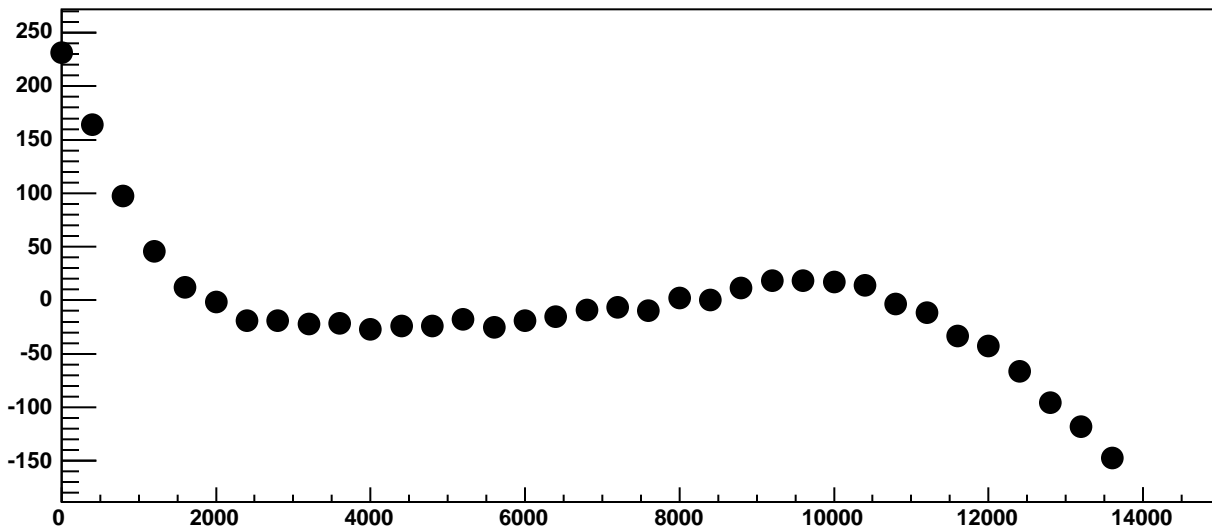
Chip 2, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



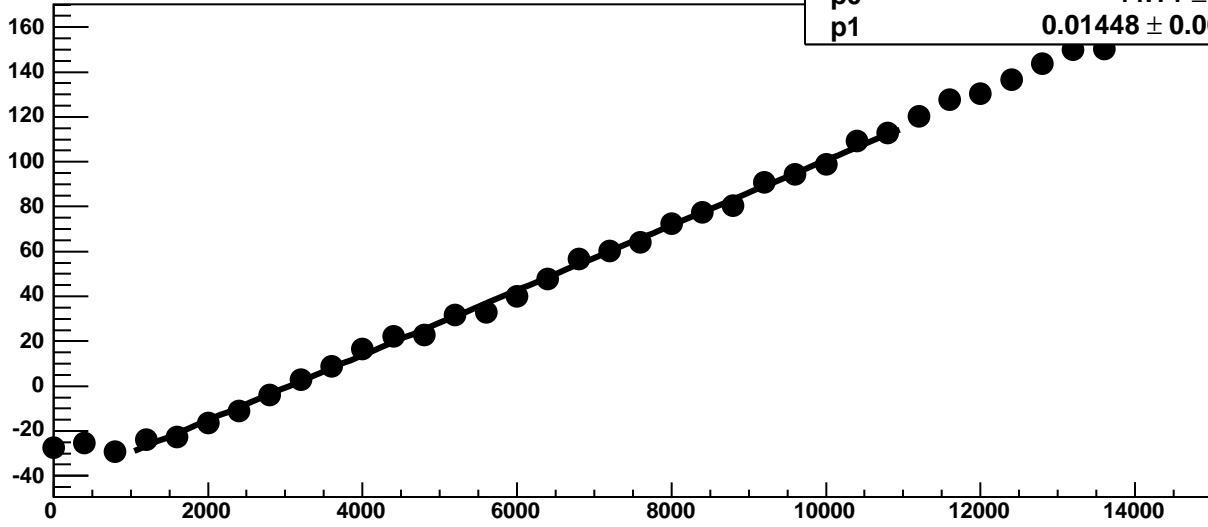
Chip 2, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



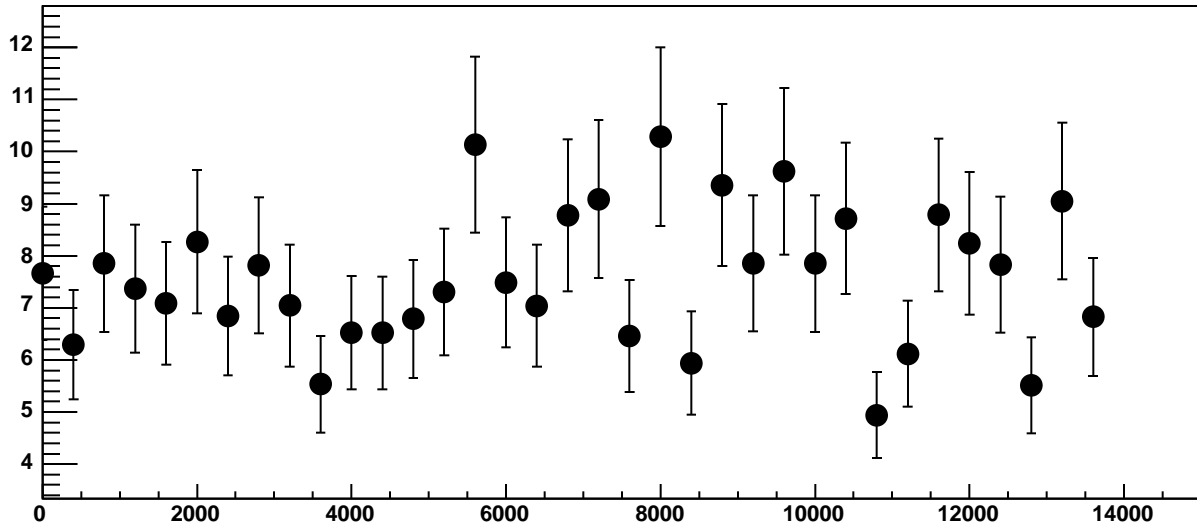
Chip 2, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



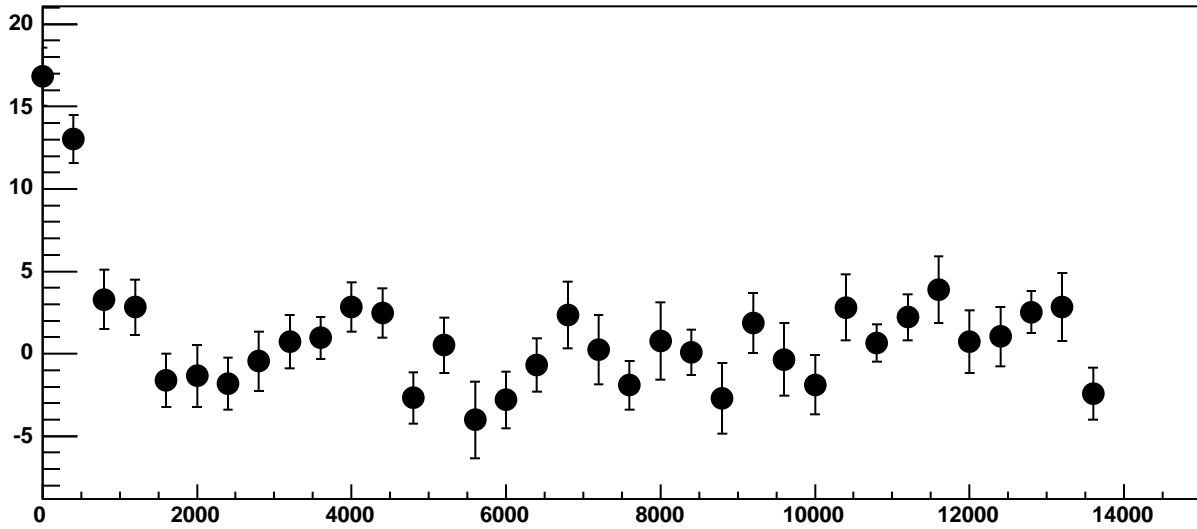
Chip 2, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



Chip 2, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

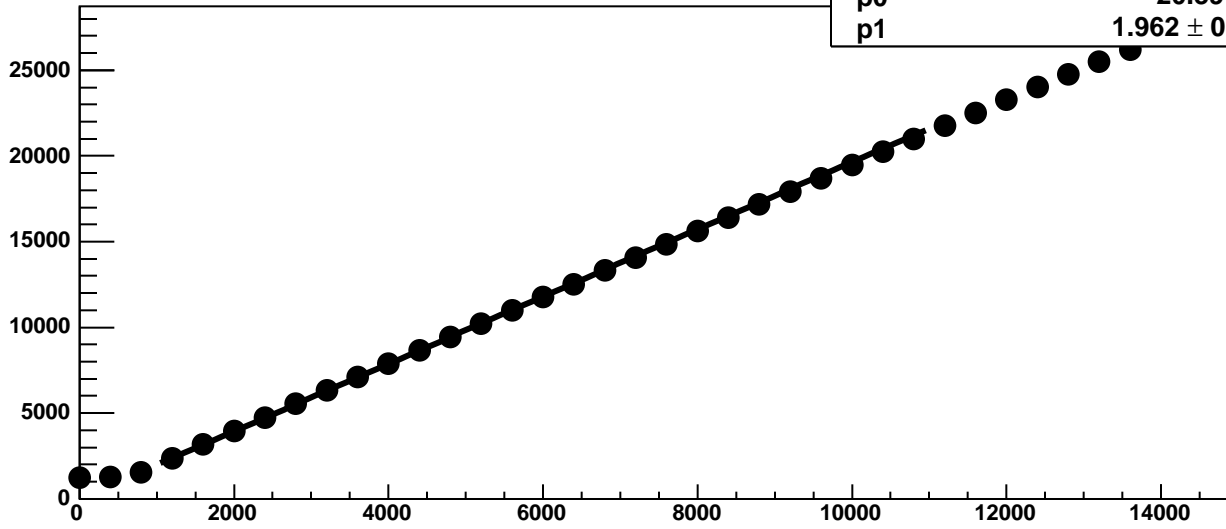


Chip 2, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

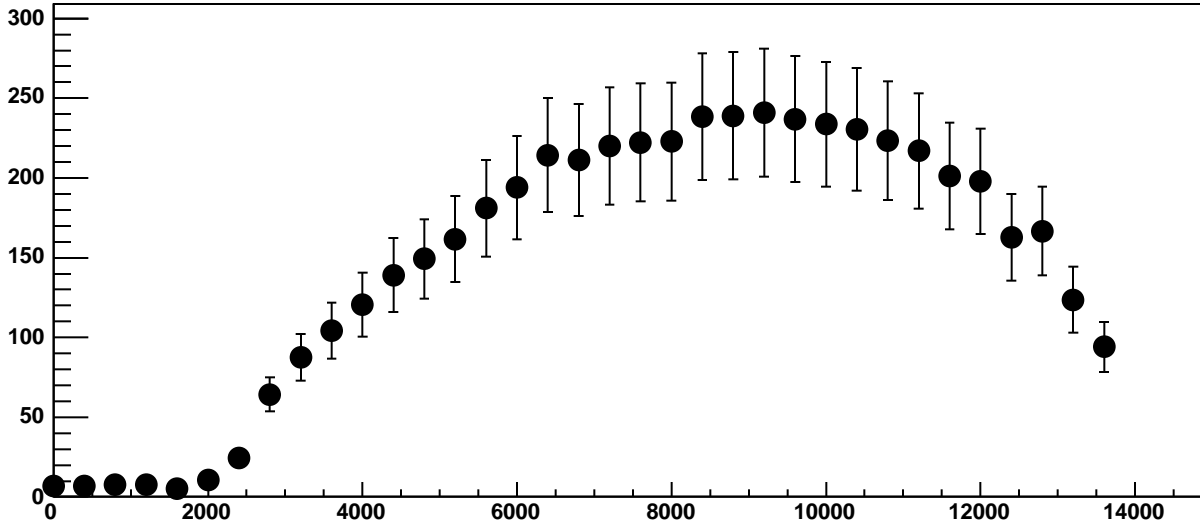


Chip 2, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC

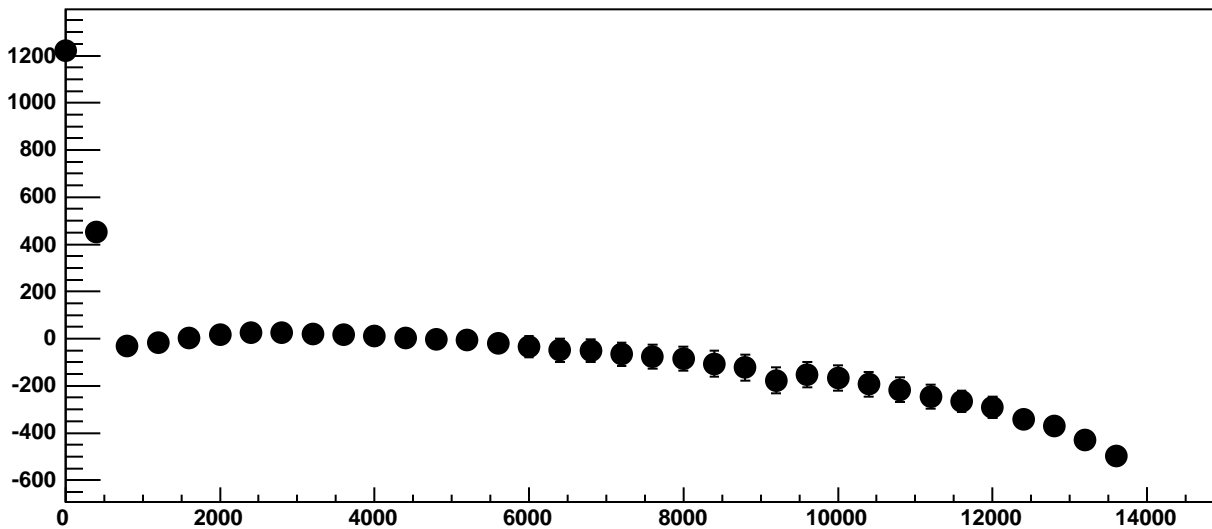
χ^2 / ndf 240.6 / 23
p0 20.39 ± 2.769
p1 1.962 ± 0.001607



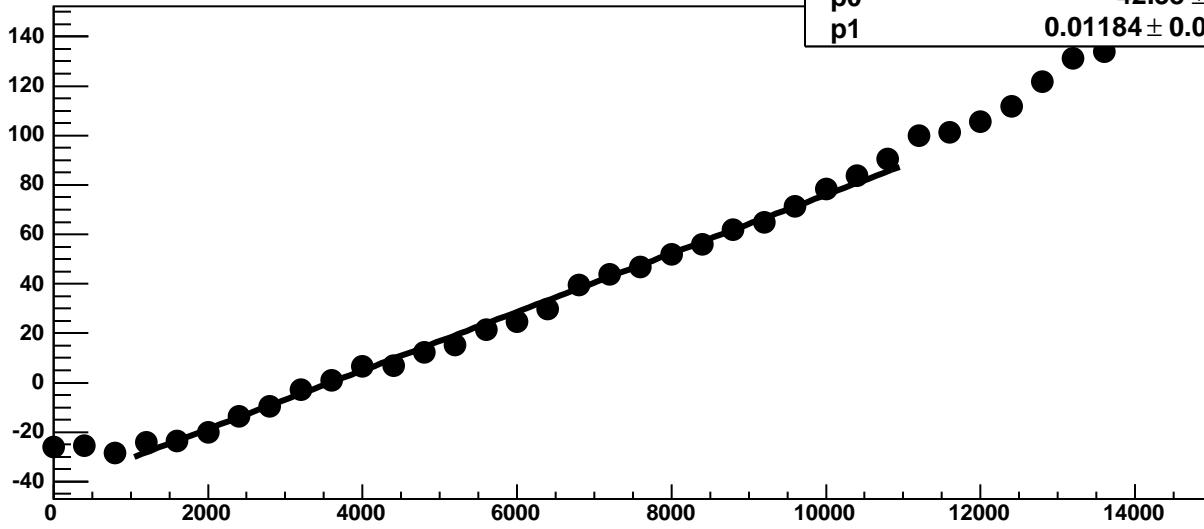
Chip 2, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC

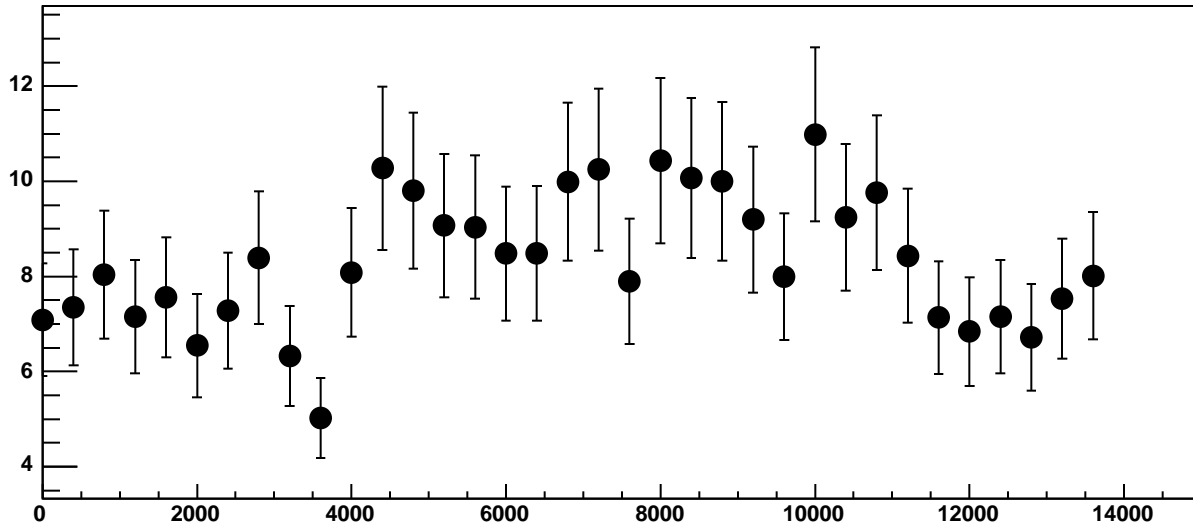


Chip 2, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

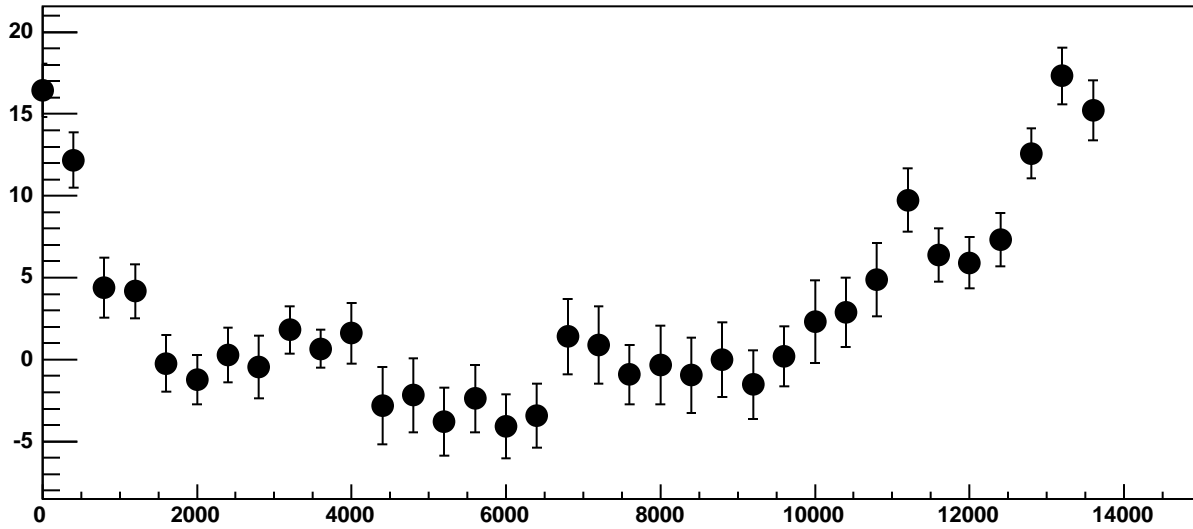


χ^2 / ndf 33.41 / 23
p0 -42.38 ± 0.7893
p1 0.01184 ± 0.0001313

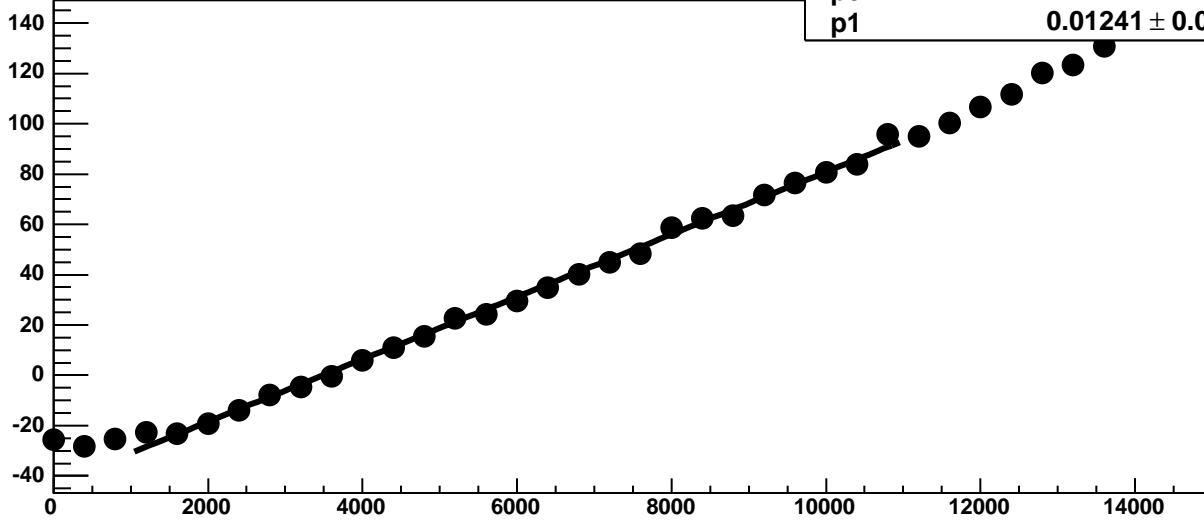
Chip 2, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



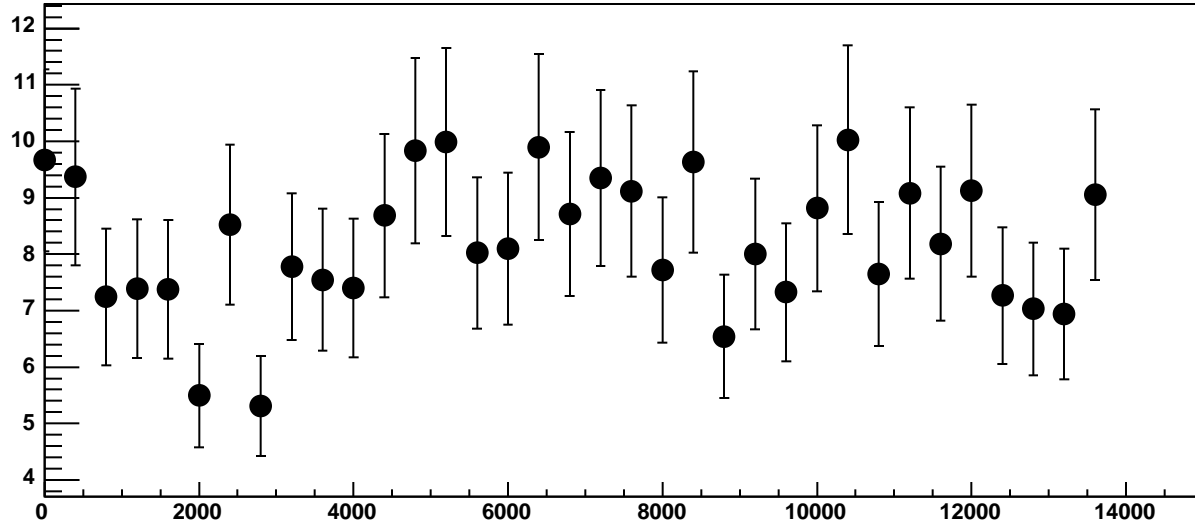
Chip 2, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



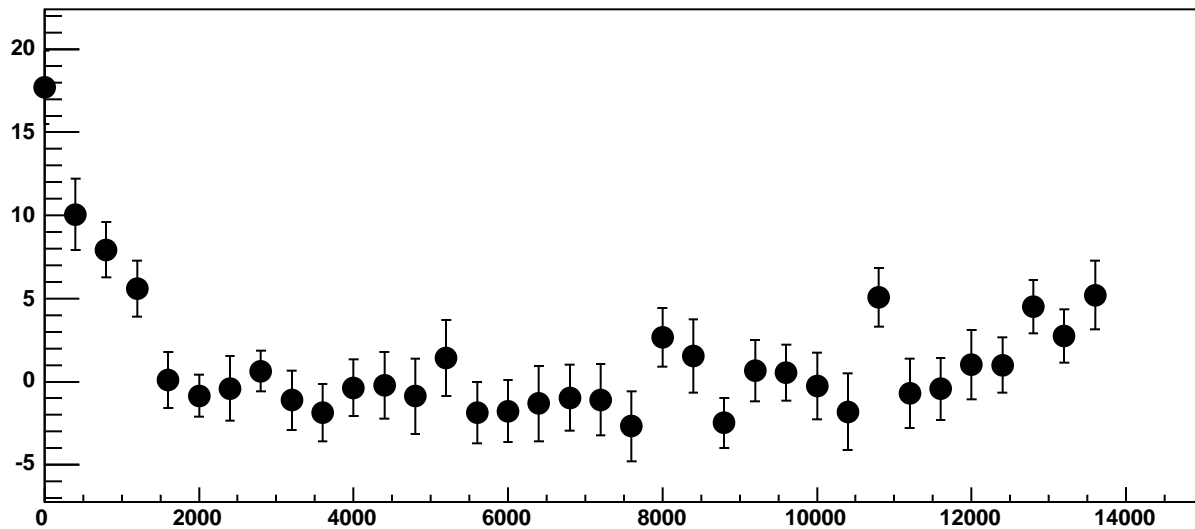
Chip 2, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



Chip 2, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

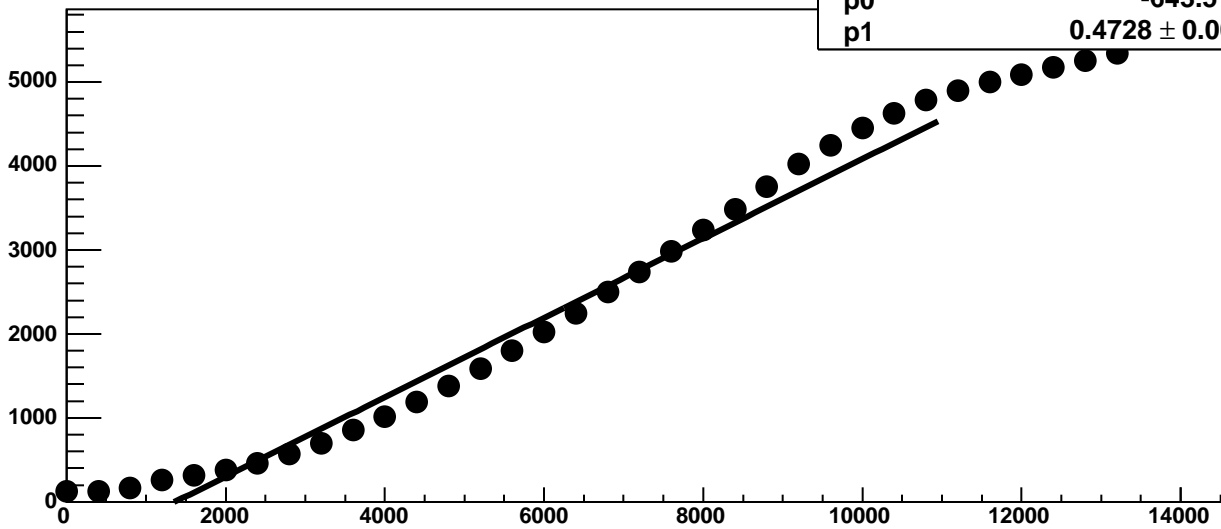


Chip 2, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

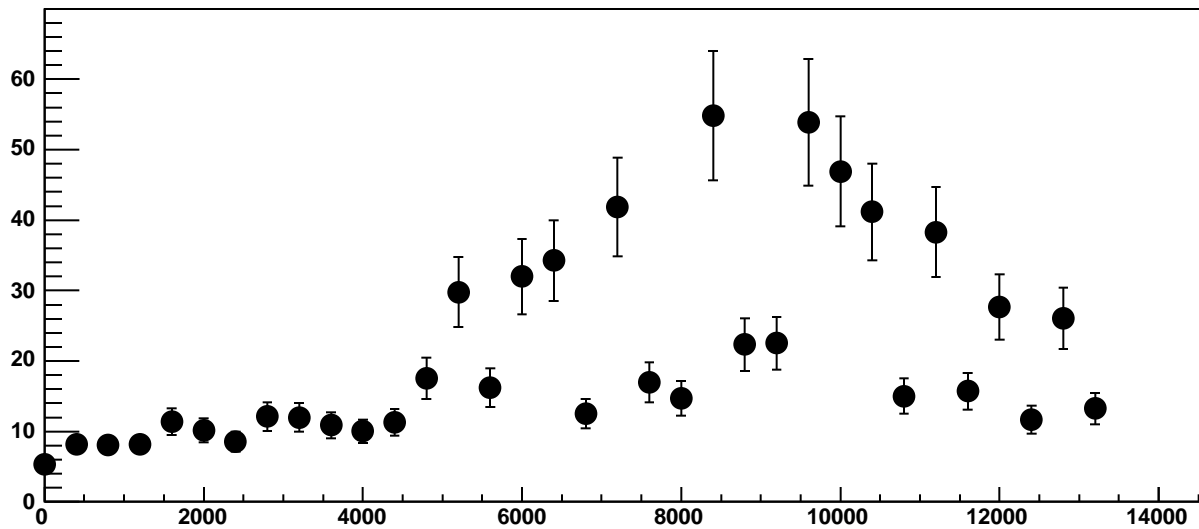


Chip 2, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC

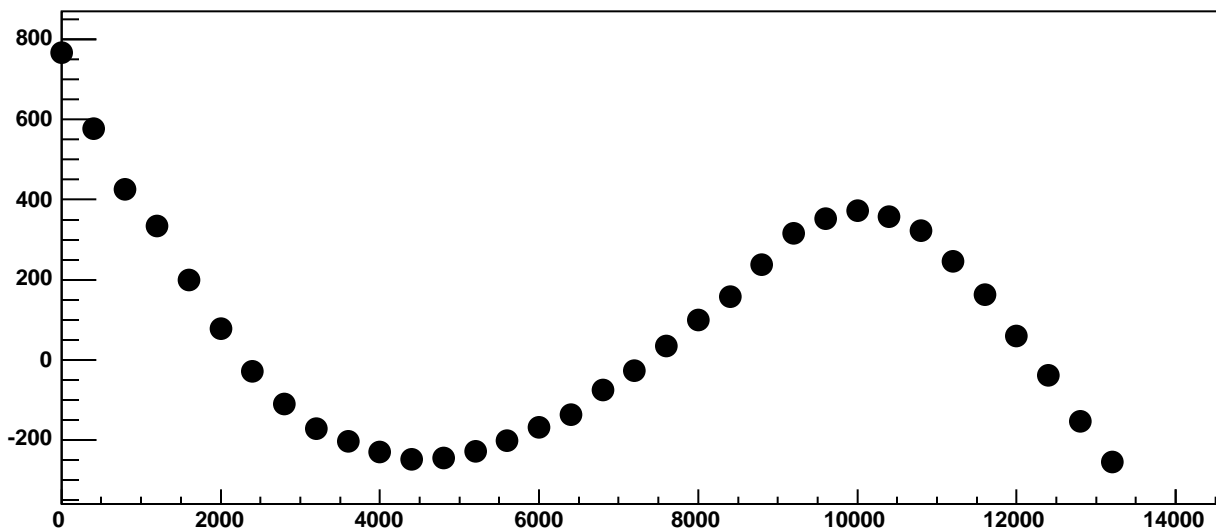
χ^2 / ndf 9.847e+04 / 23
p0 -643.5 ± 1.231
p1 0.4728 ± 0.0002549



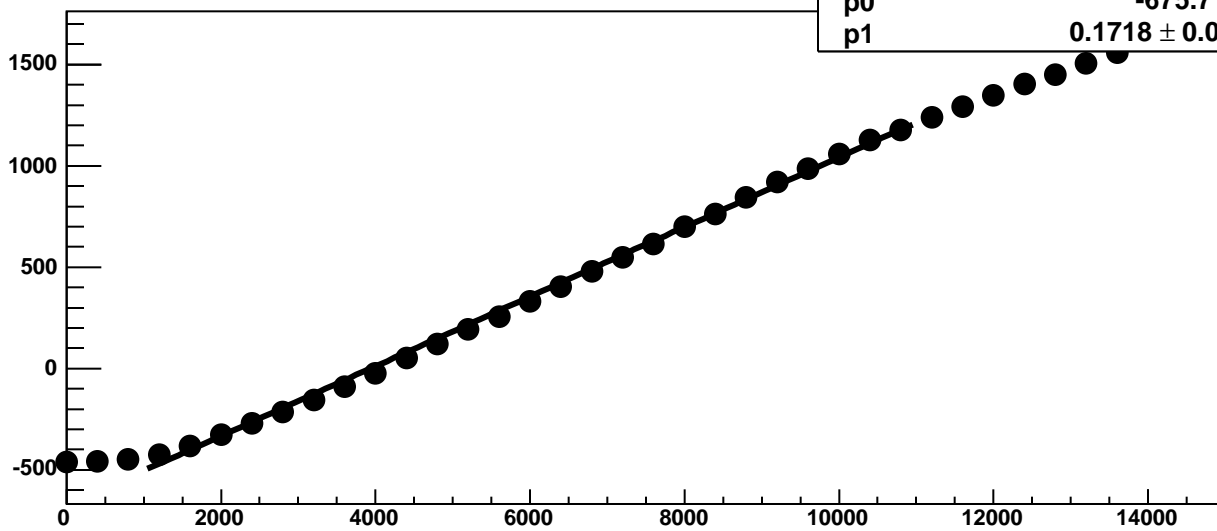
Chip 2, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC



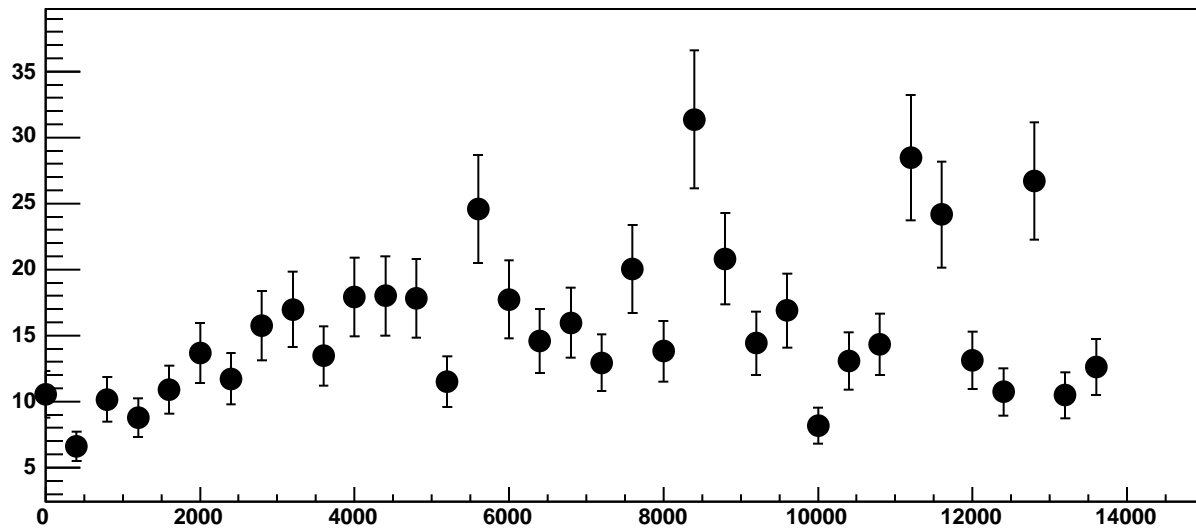
Chip 2, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC



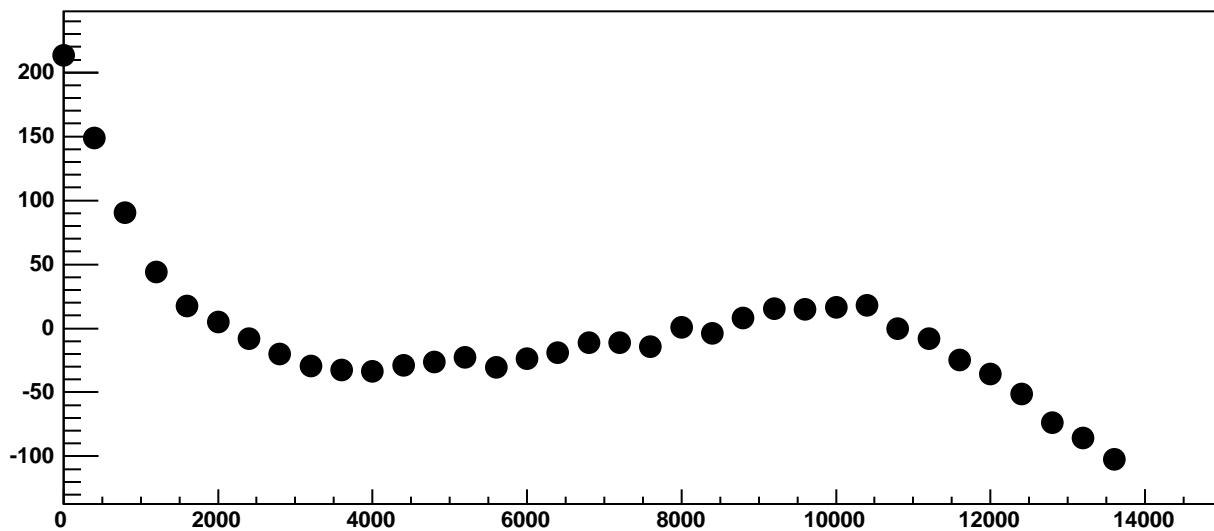
Chip 3, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC



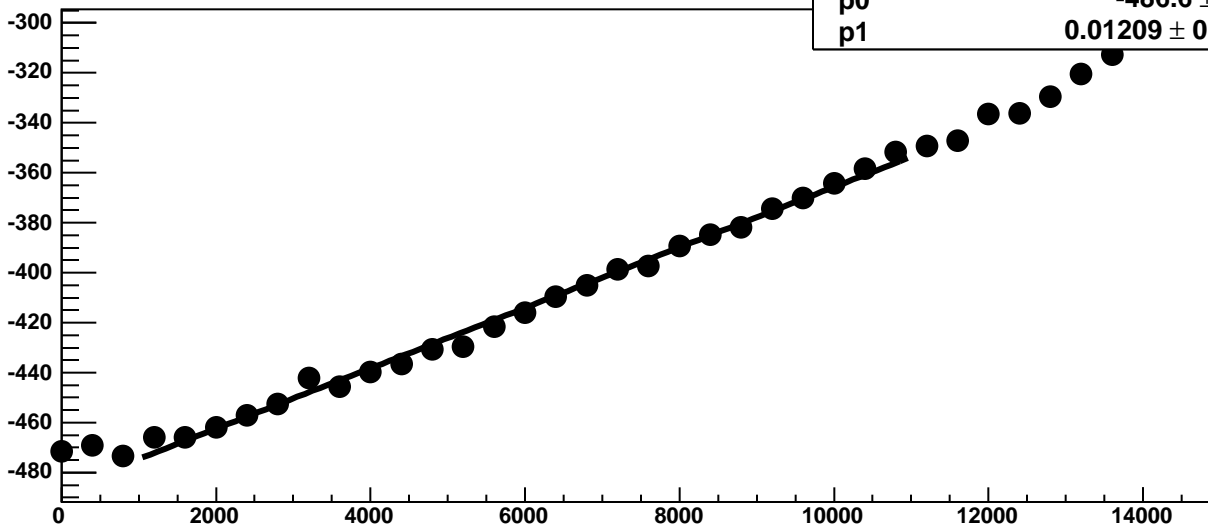
Chip 3, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

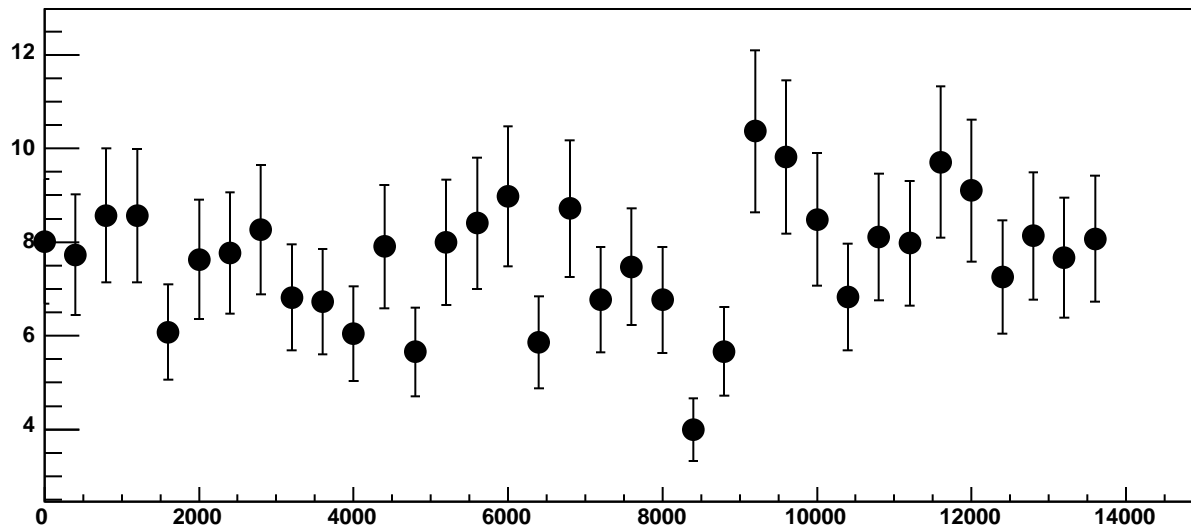


Chip 3, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

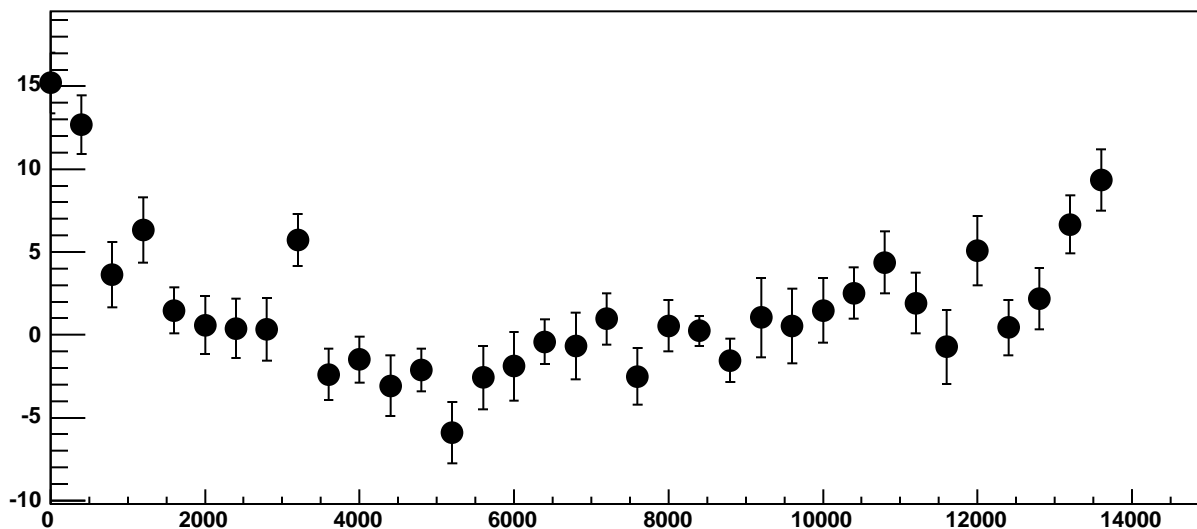


χ^2 / ndf 60.31 / 23
p0 -486.6 ± 0.7683
p1 0.01209 ± 0.000115

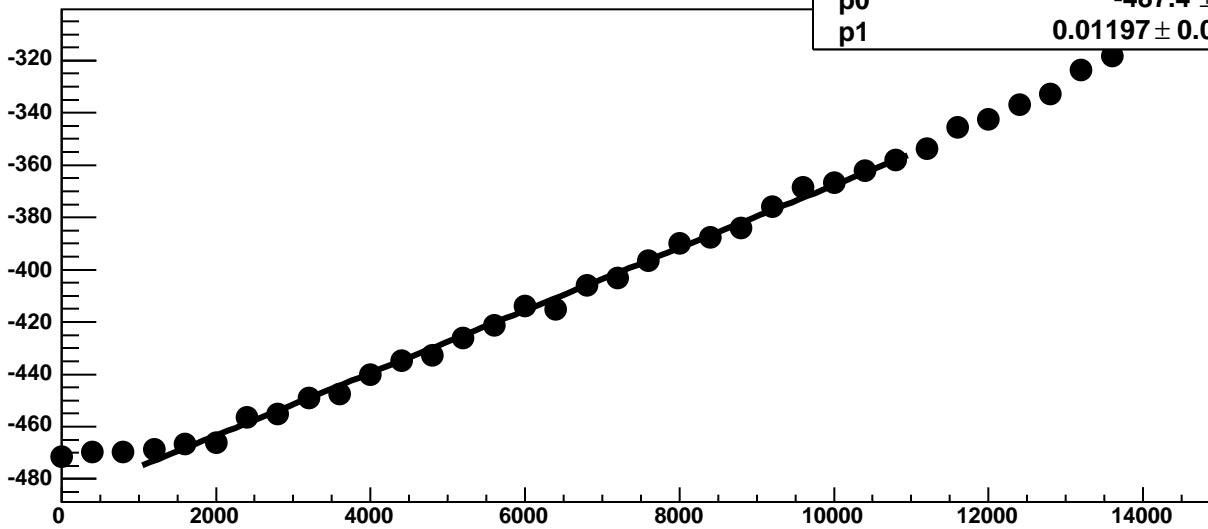
Chip 3, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

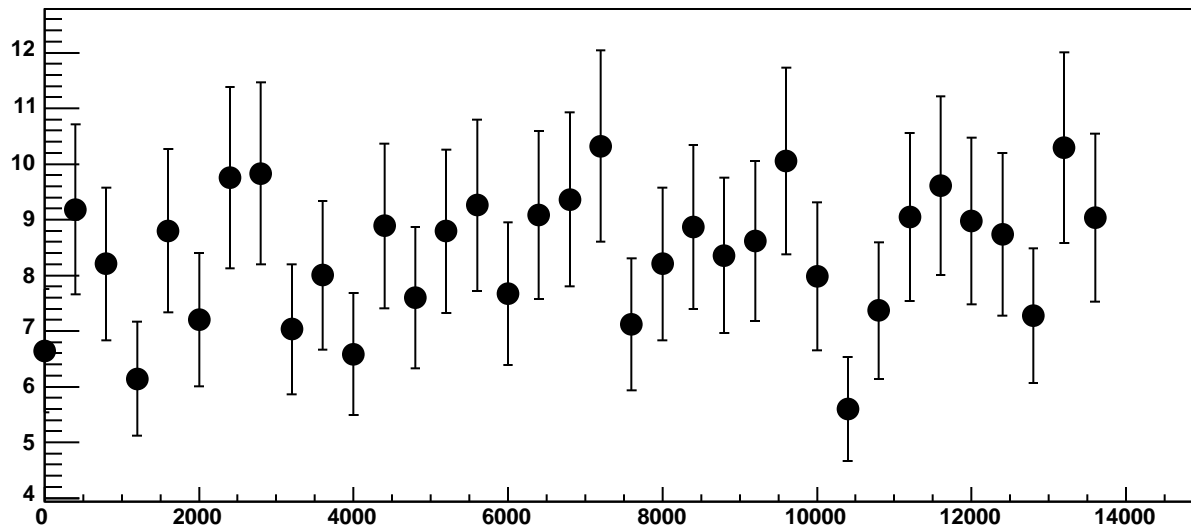


Chip 3, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

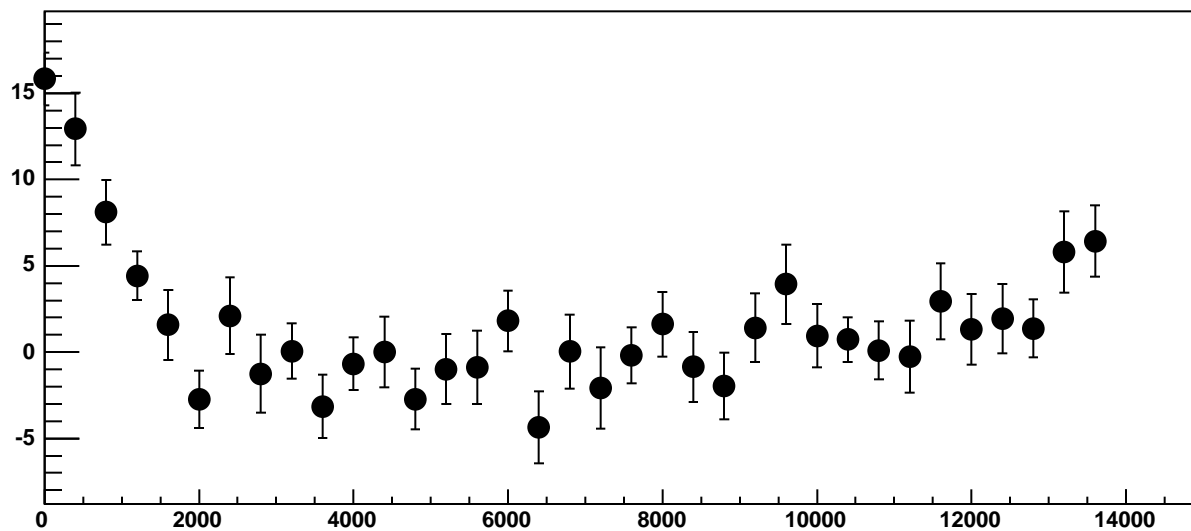


χ^2 / ndf 32.52 / 23
p0 -487.4 ± 0.8036
p1 0.01197 ± 0.0001193

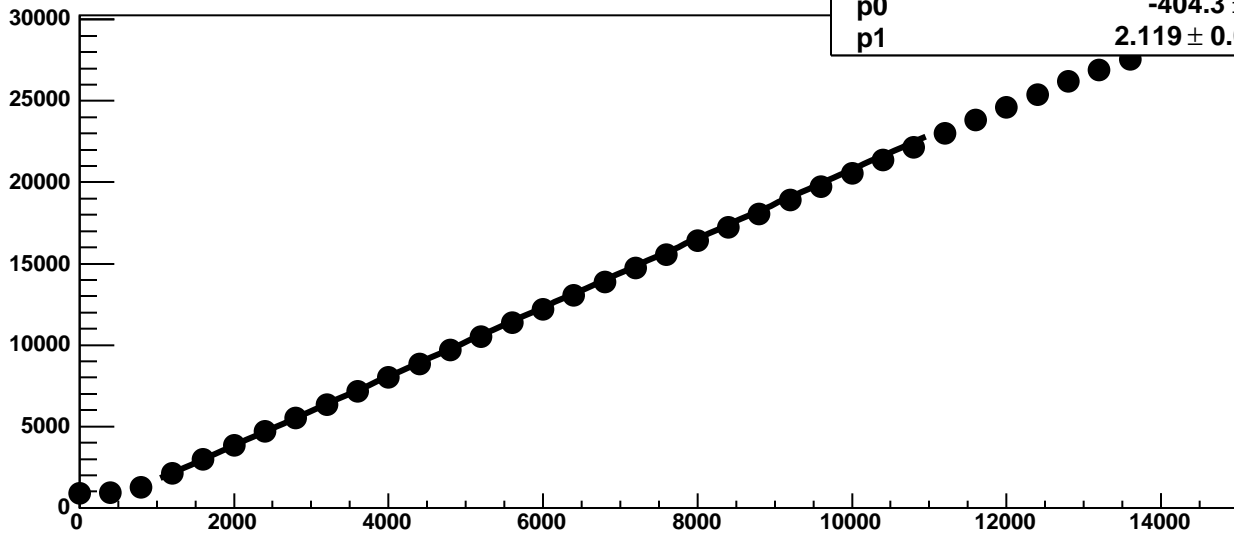
Chip 3, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



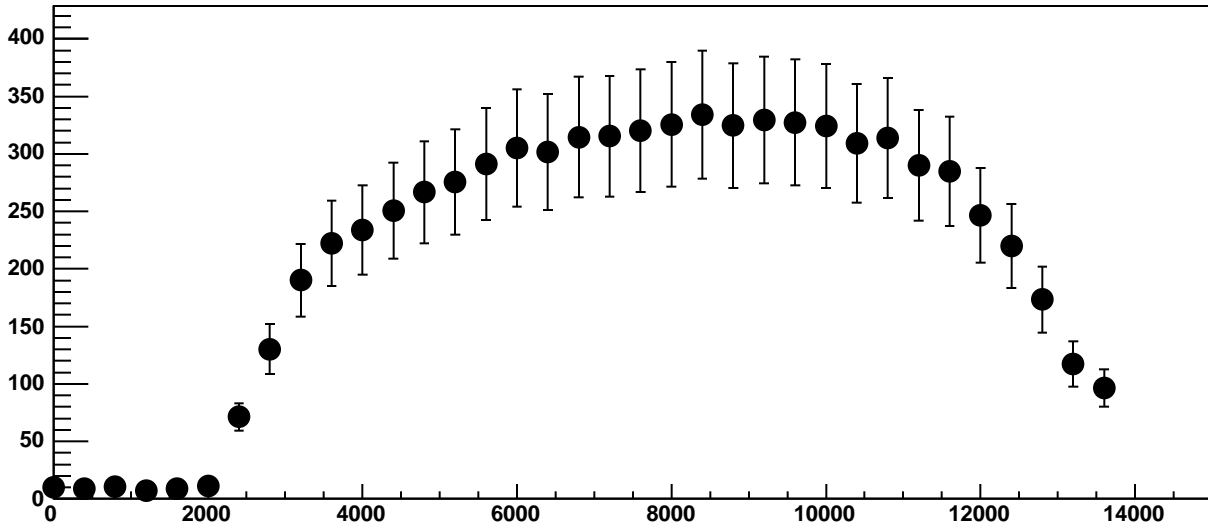
Chip 3, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



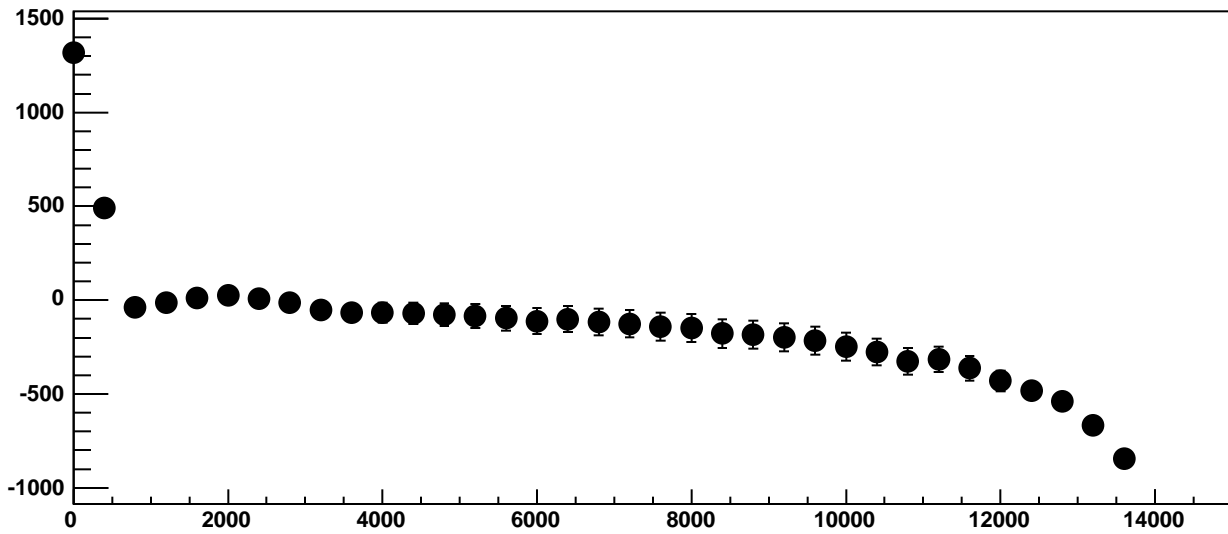
Chip 3, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC



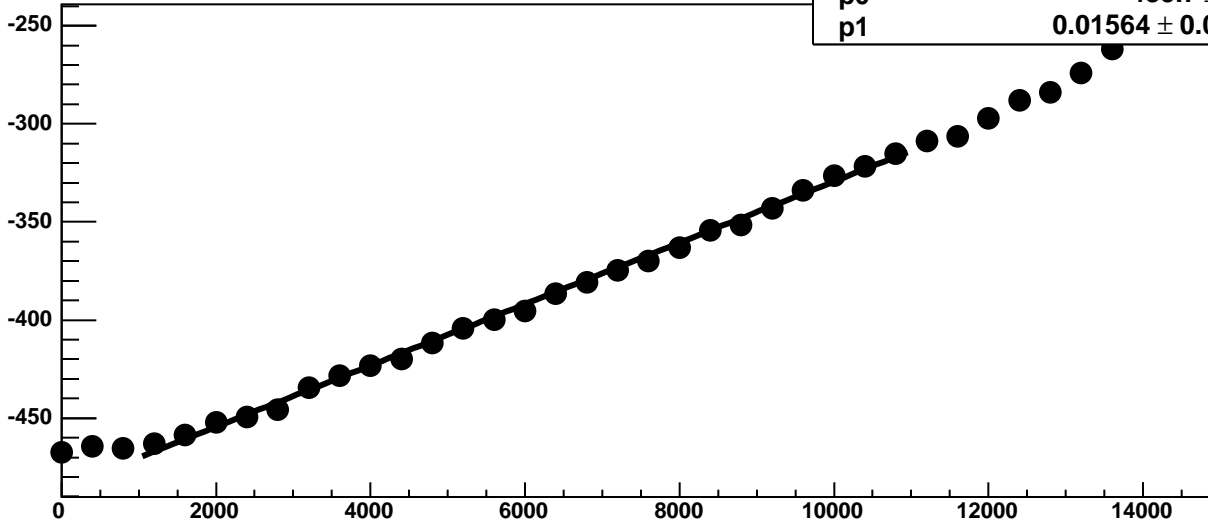
Chip 3, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC

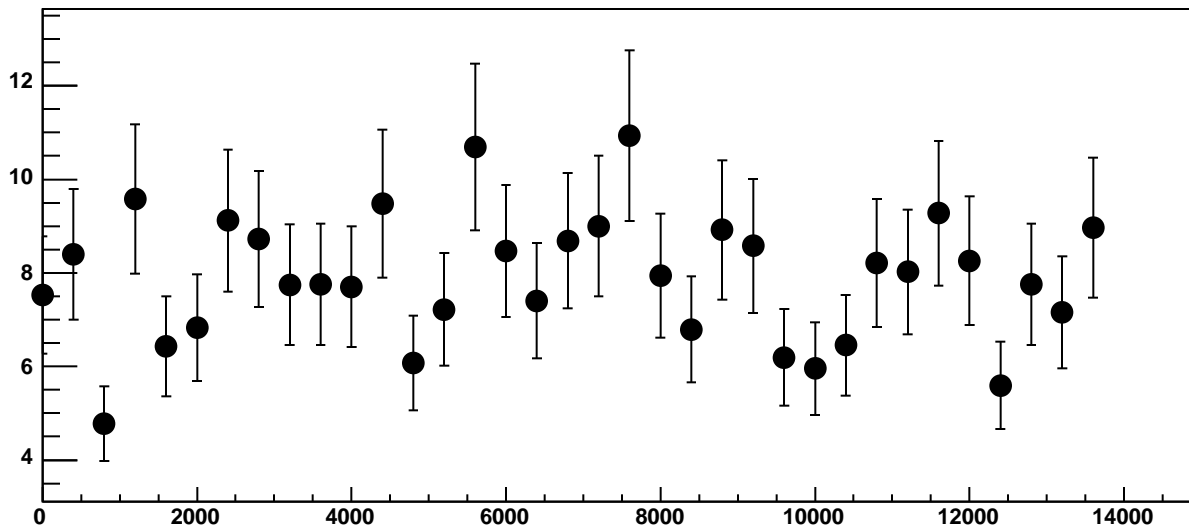


Chip 3, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

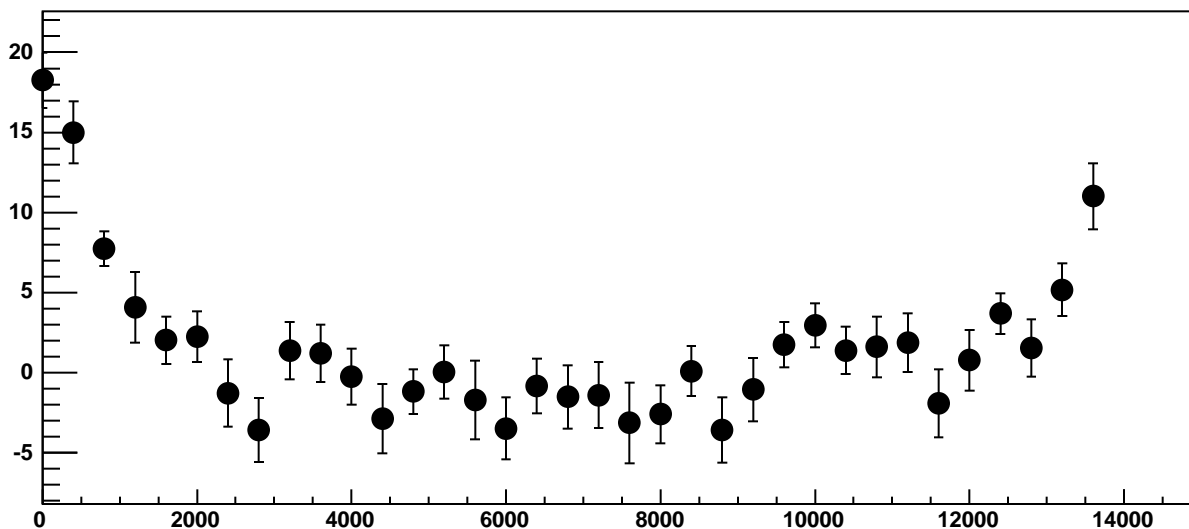


χ^2 / ndf 34.21 / 23
p0 -485.7 ± 0.8091
p1 0.01564 ± 0.0001179

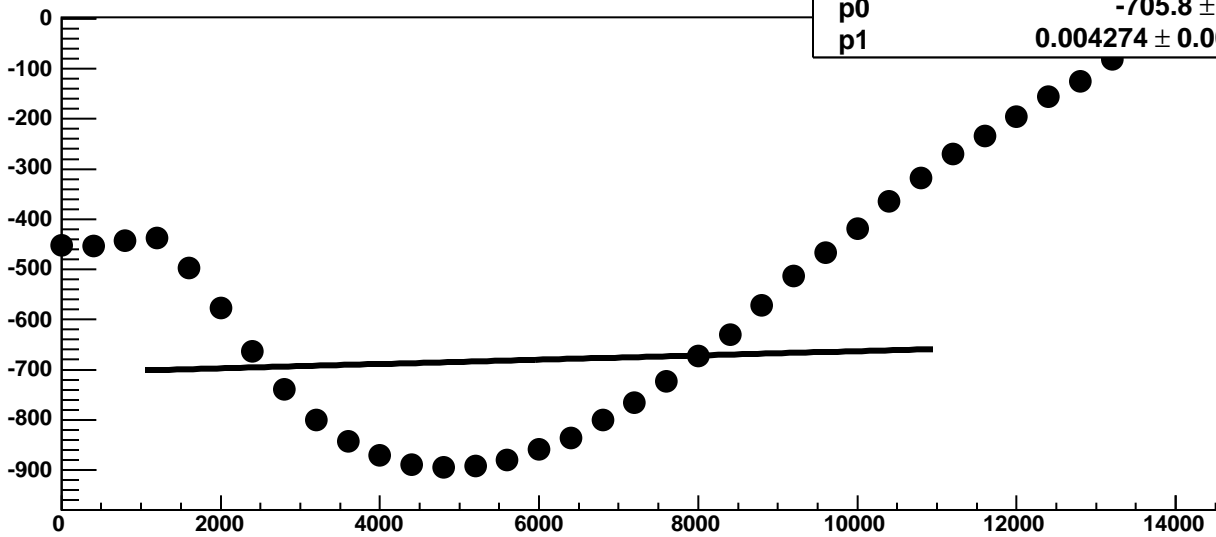
Chip 3, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

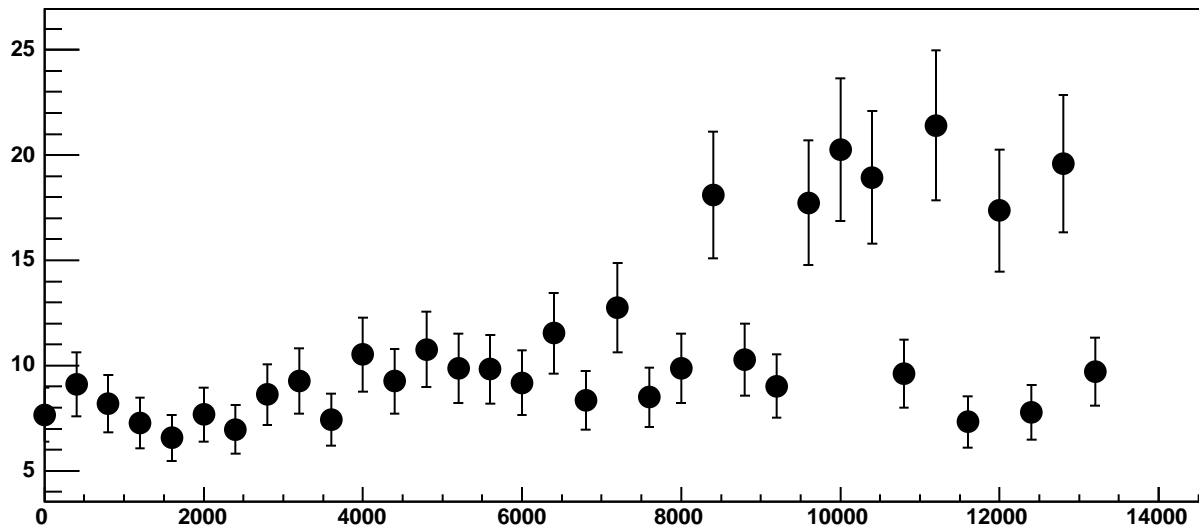


Chip 3, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

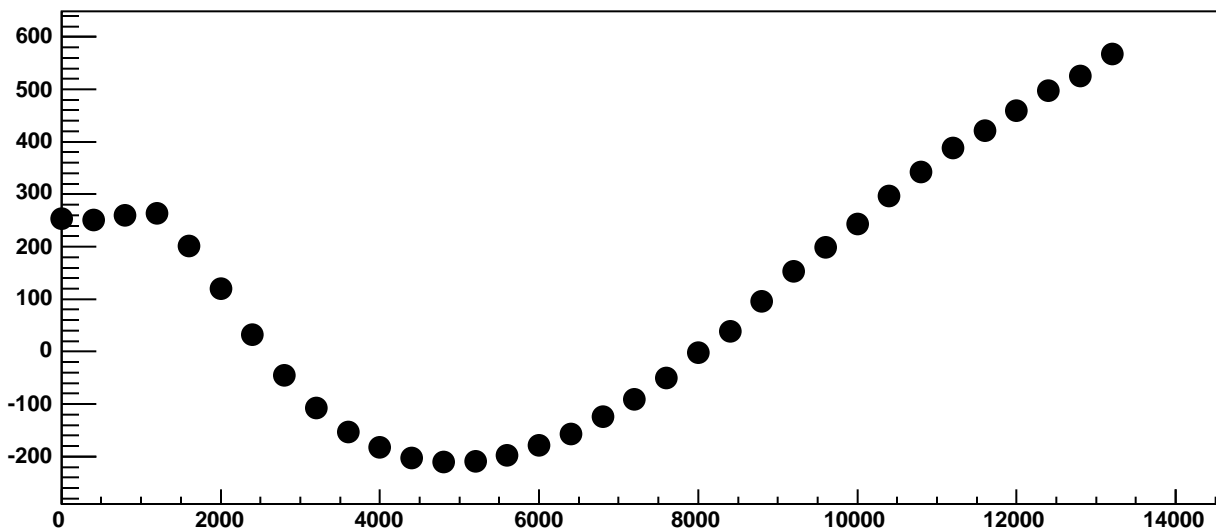


χ^2 / ndf 1.552e+05 / 23
p0 -705.8 ± 0.8757
p1 0.004274 ± 0.0001533

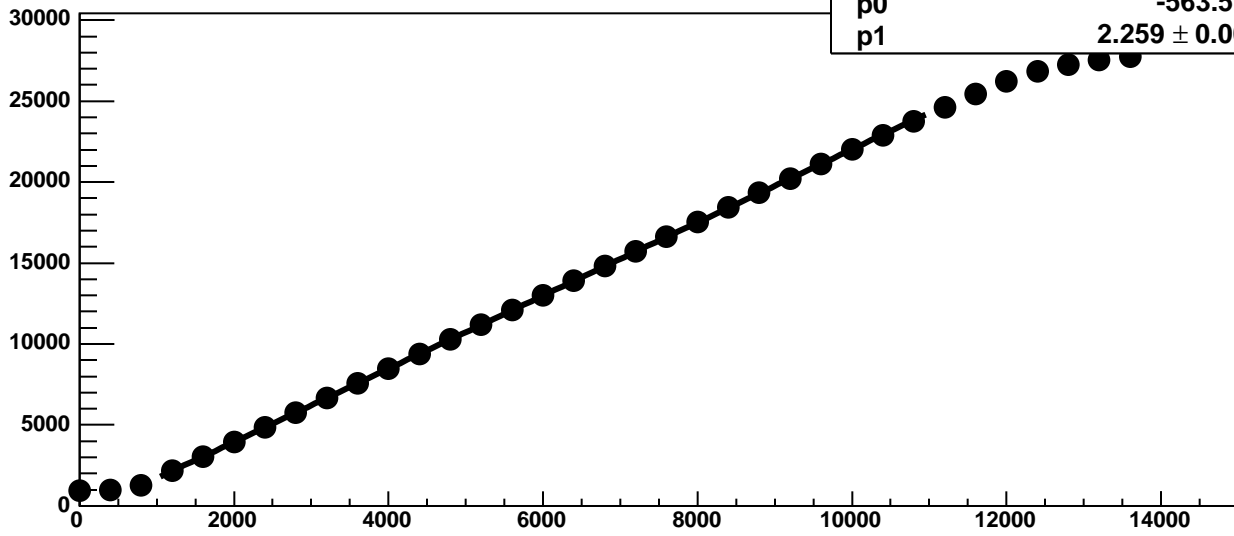
Chip 3, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



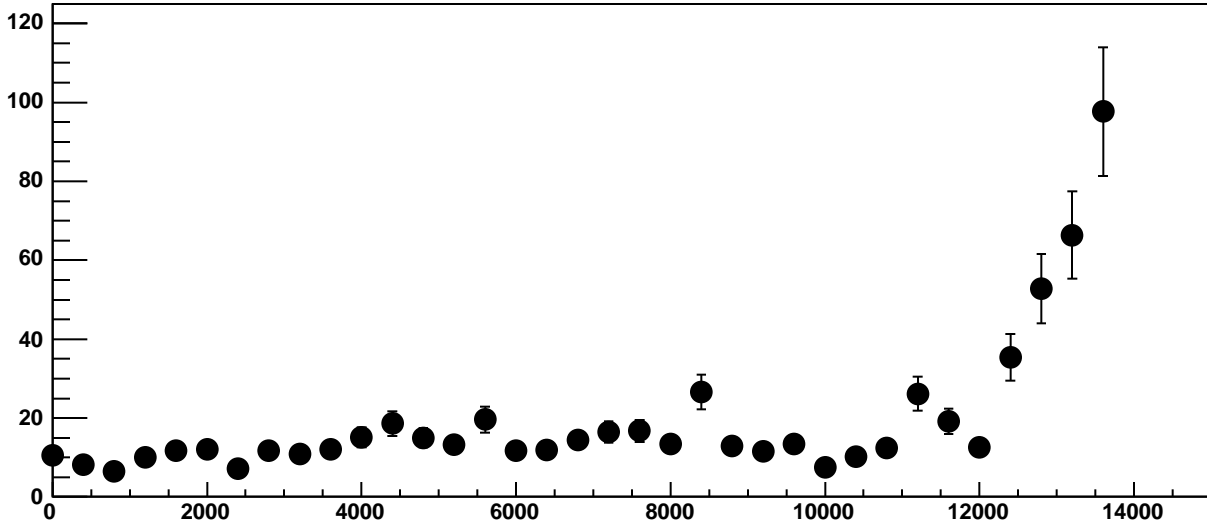
Chip 3, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



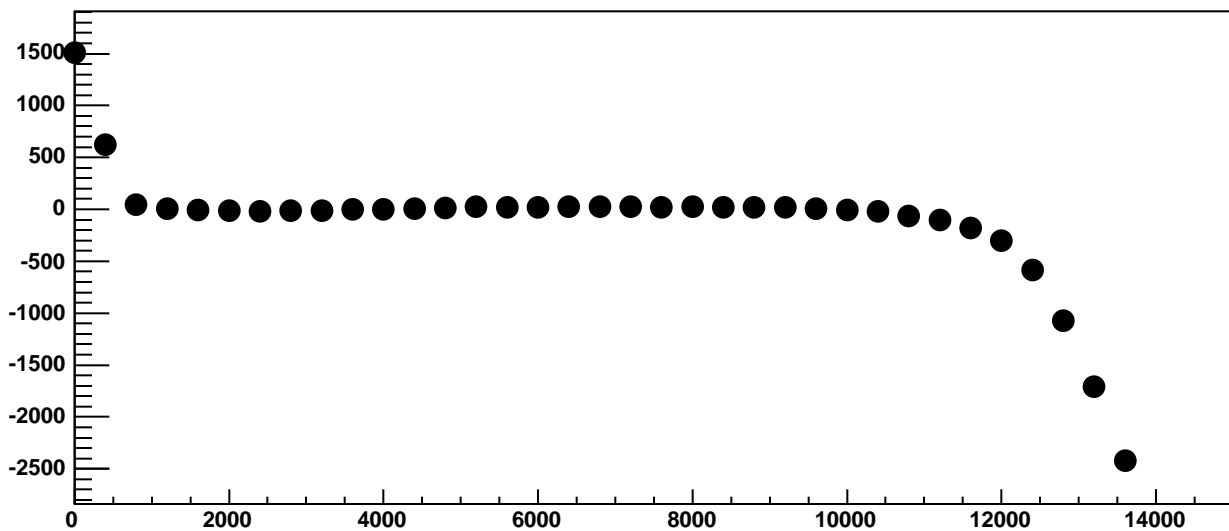
Chip 3, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



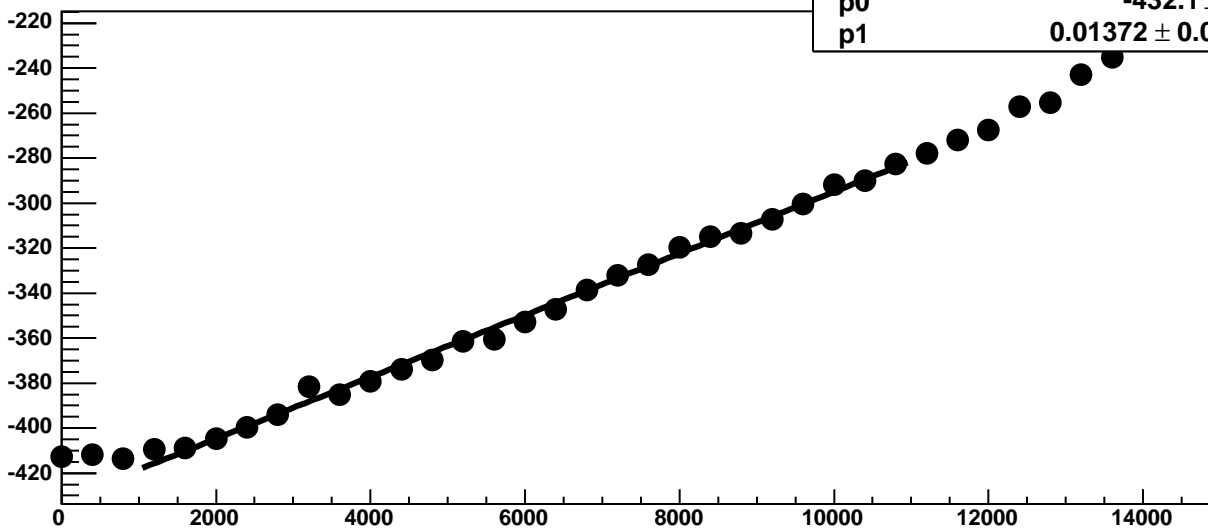
Chip 3, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



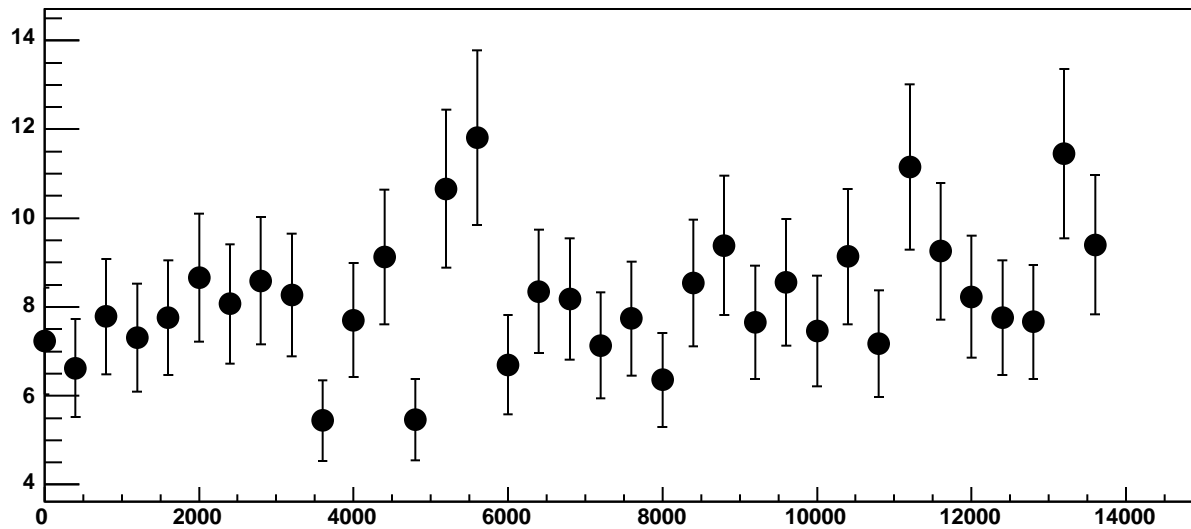
Chip 3, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC



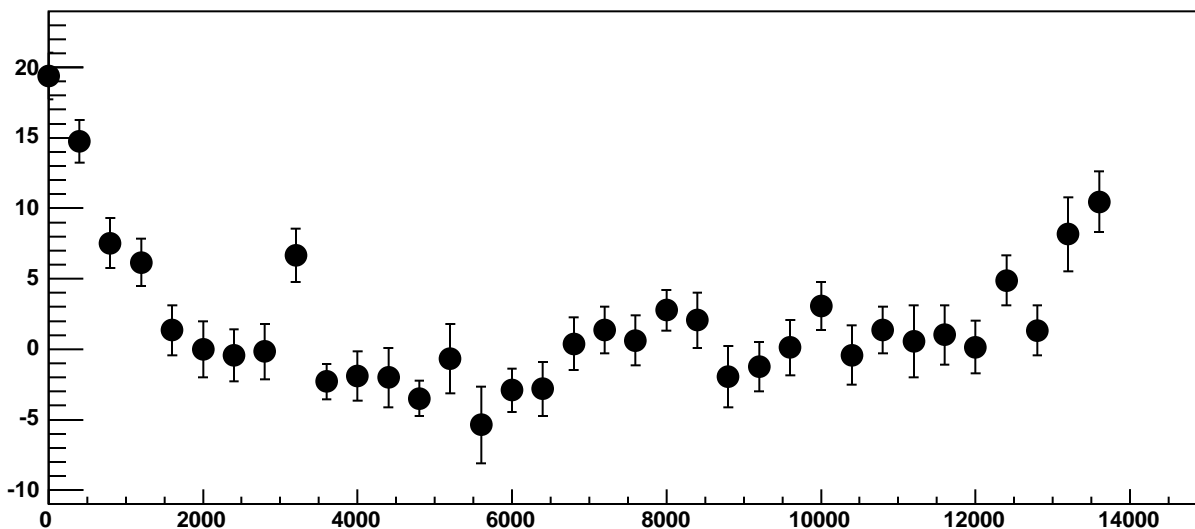
Chip 3, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



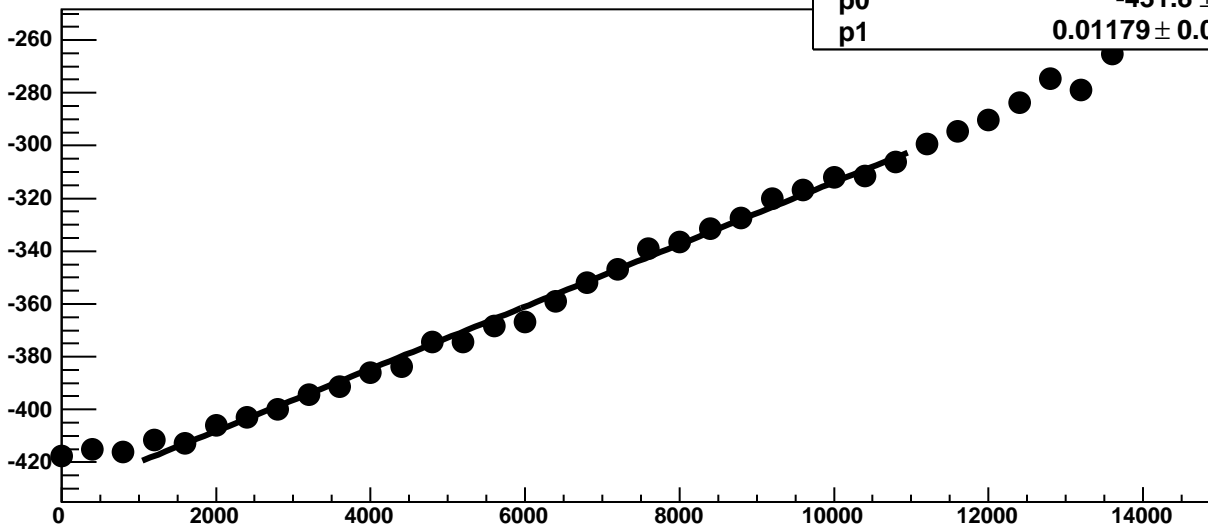
Chip 3, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



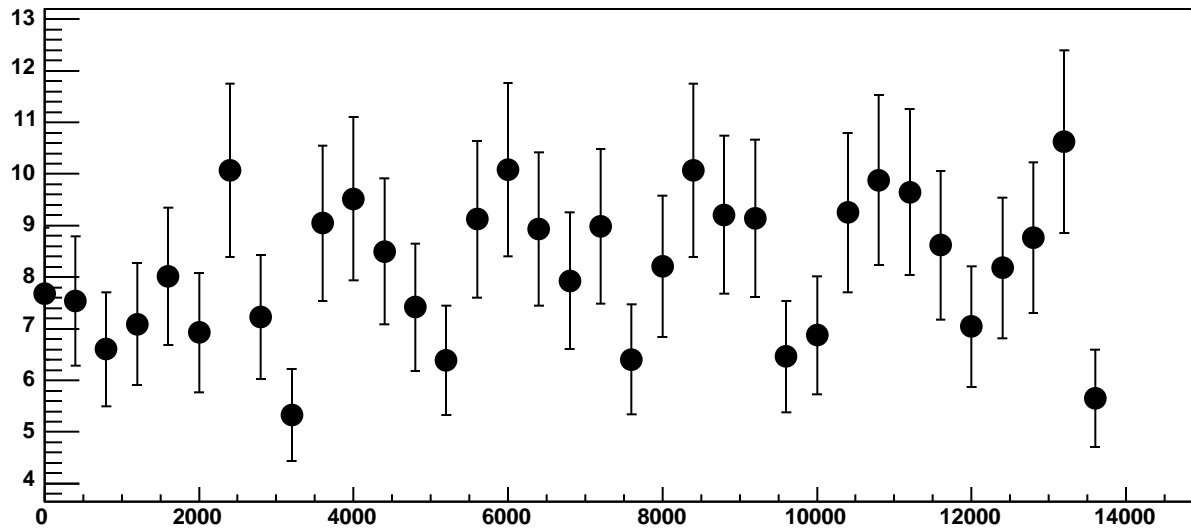
Chip 3, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



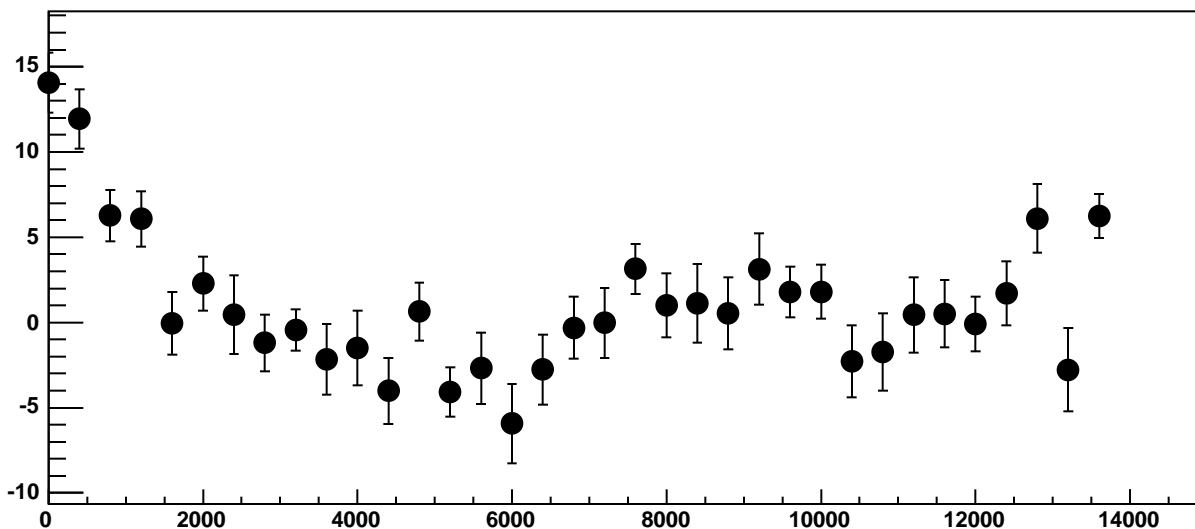
Chip 3, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



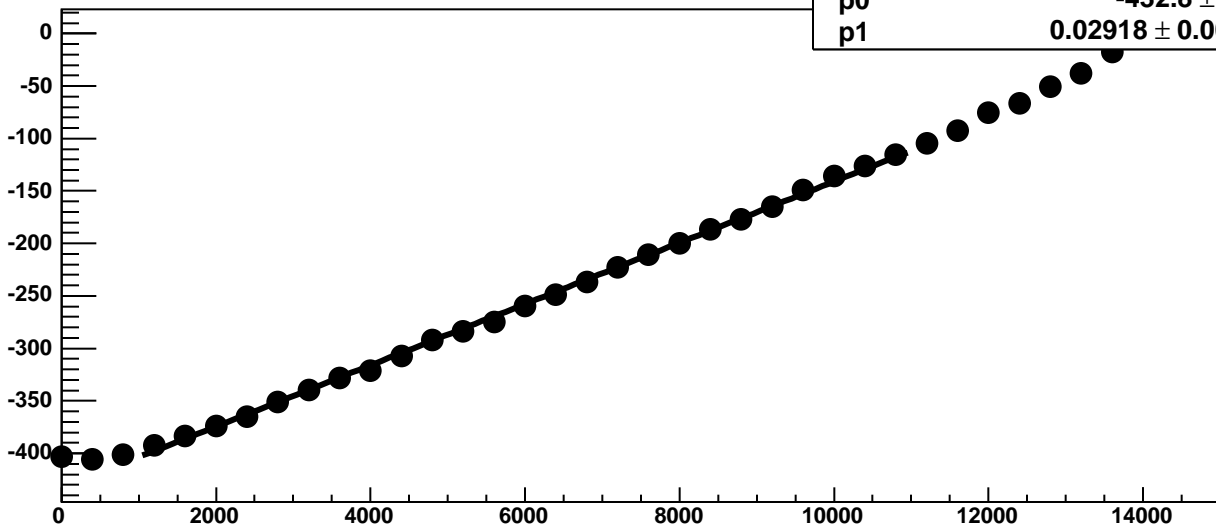
Chip 3, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



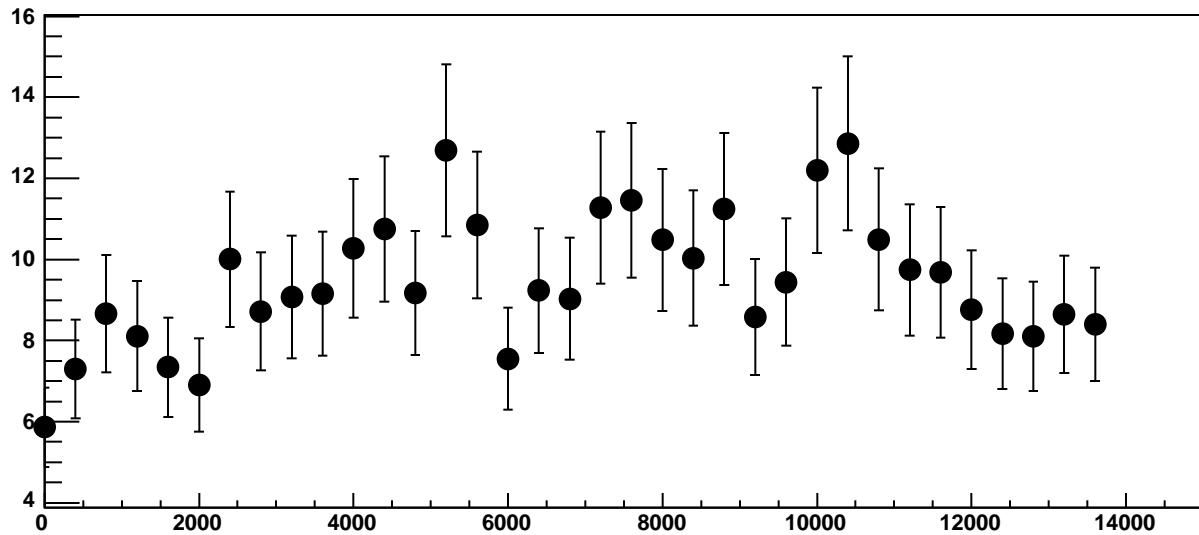
Chip 3, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



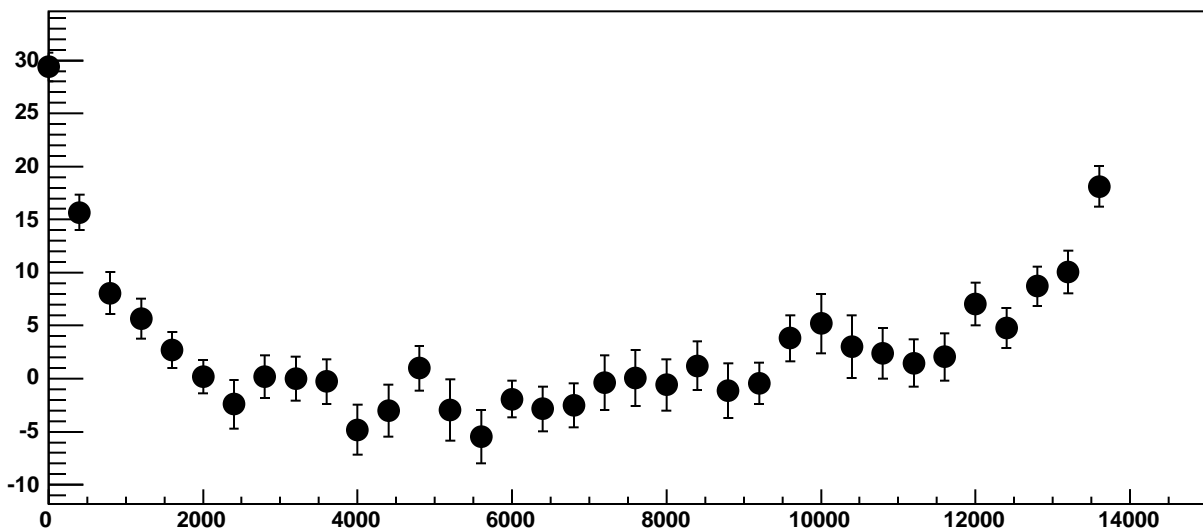
Chip 3, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC



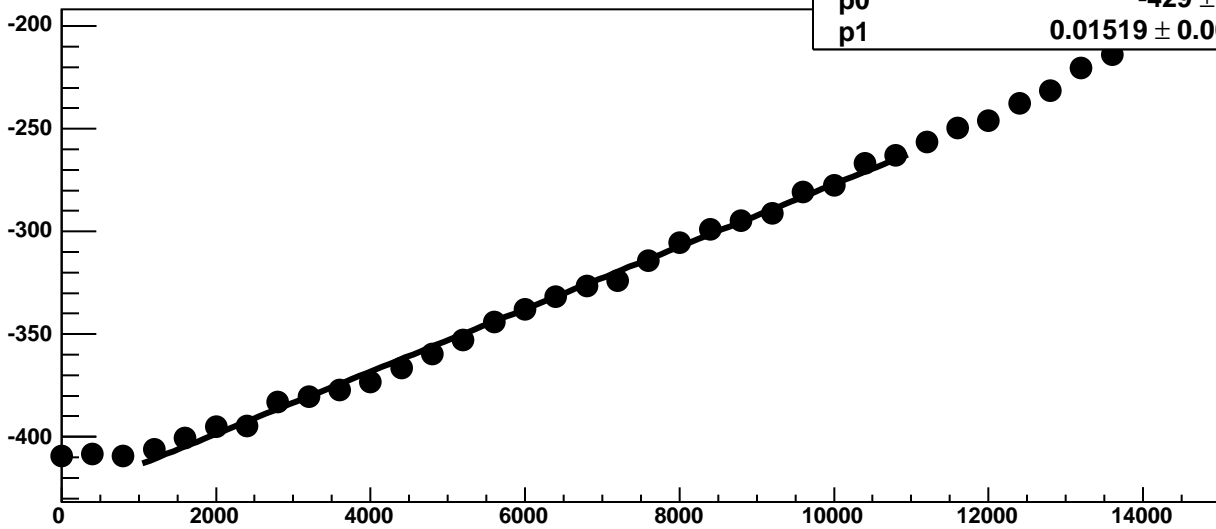
Chip 3, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC

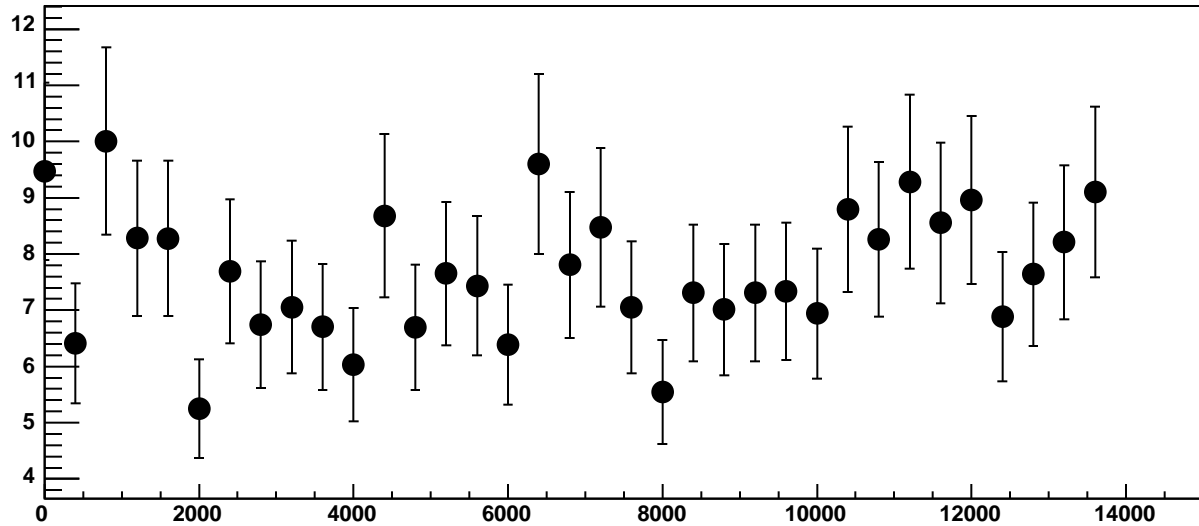


Chip 3, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

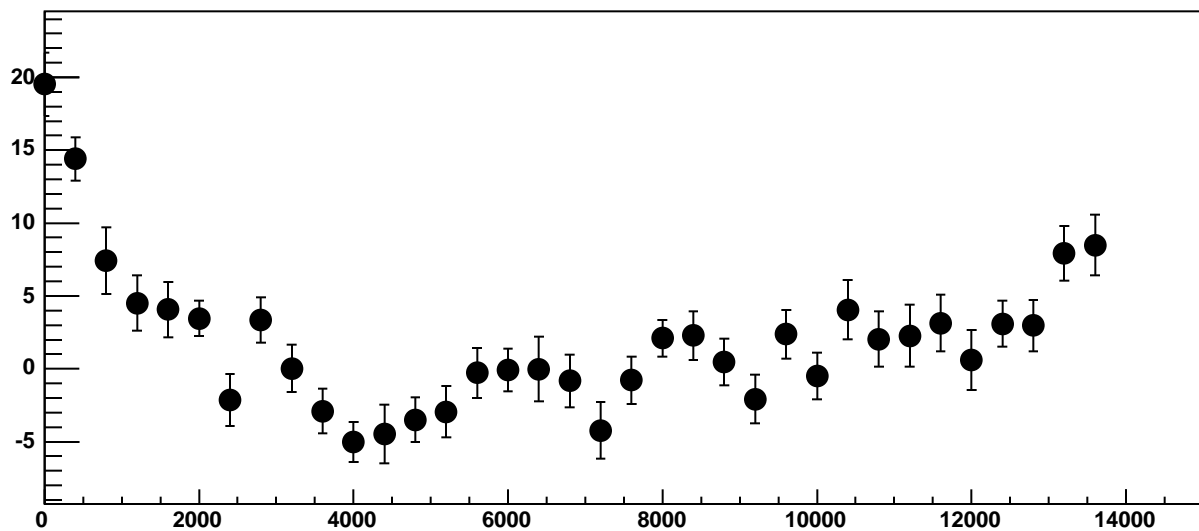


χ^2 / ndf 73.14 / 23
p0 -429 ± 0.7514
p1 0.01519 ± 0.0001155

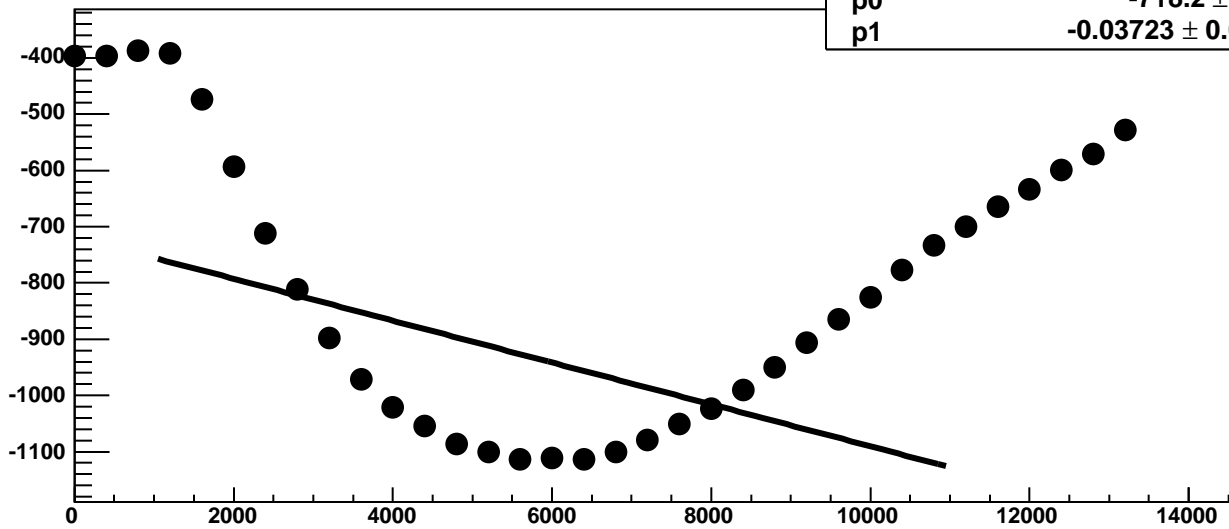
Chip 3, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

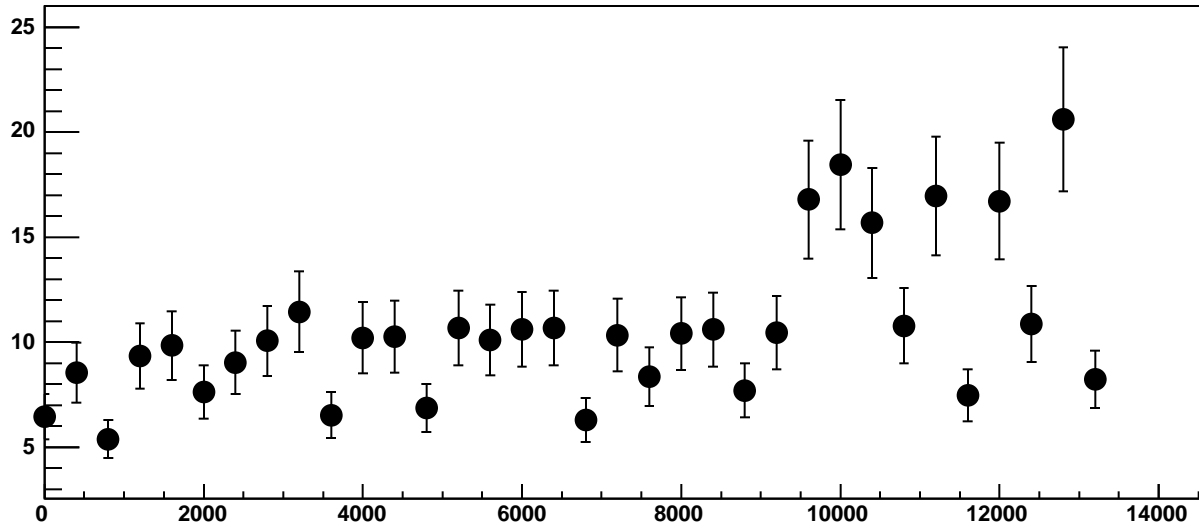


Chip 3, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

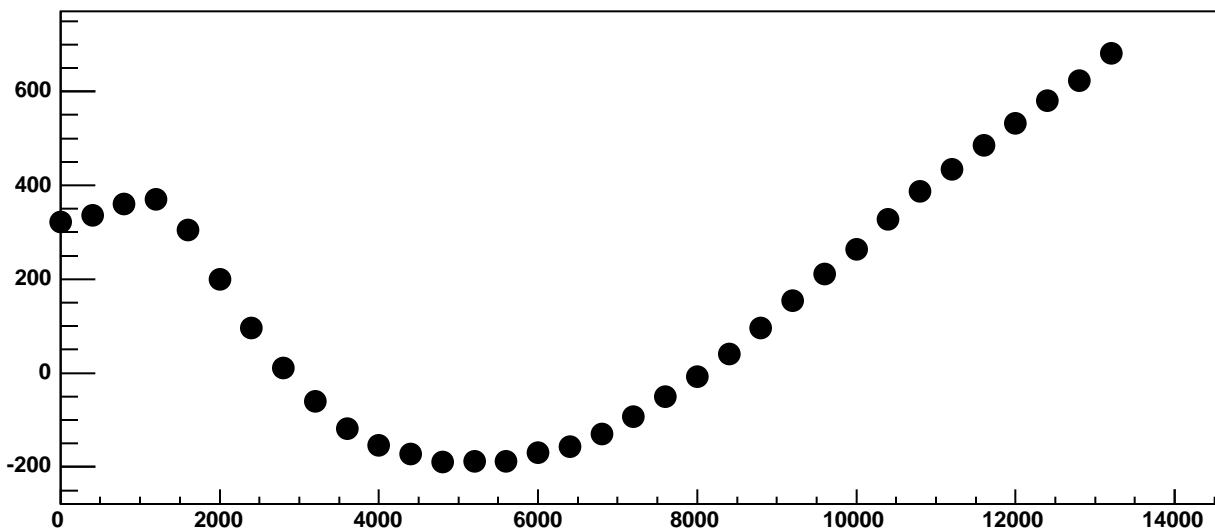


χ^2 / ndf 1.733e+05 / 23
p0 -718.2 ± 0.9978
p1 -0.03723 ± 0.000163

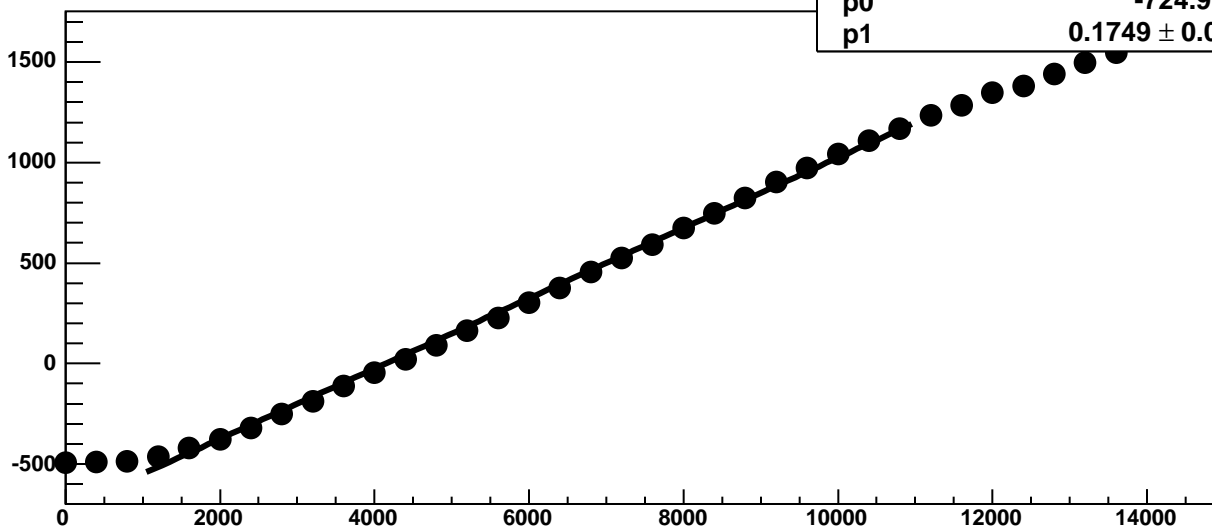
Chip 3, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



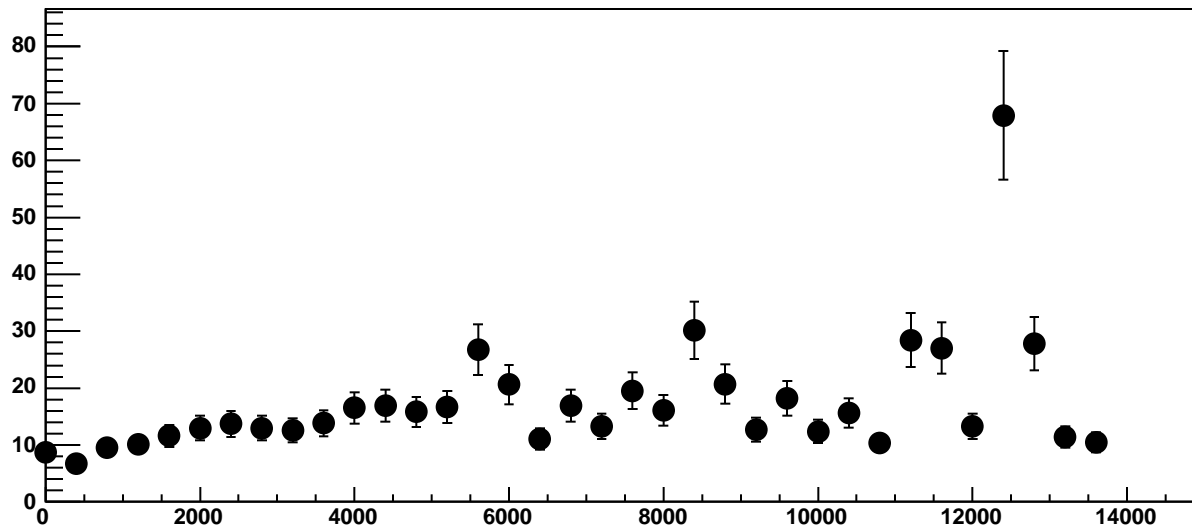
Chip 3, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



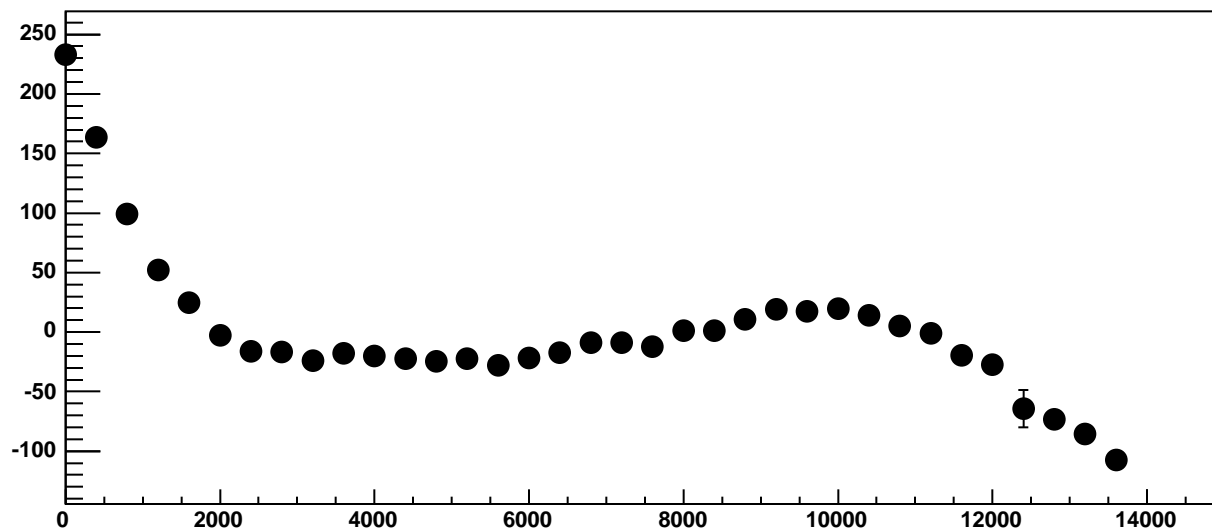
Chip 3, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC



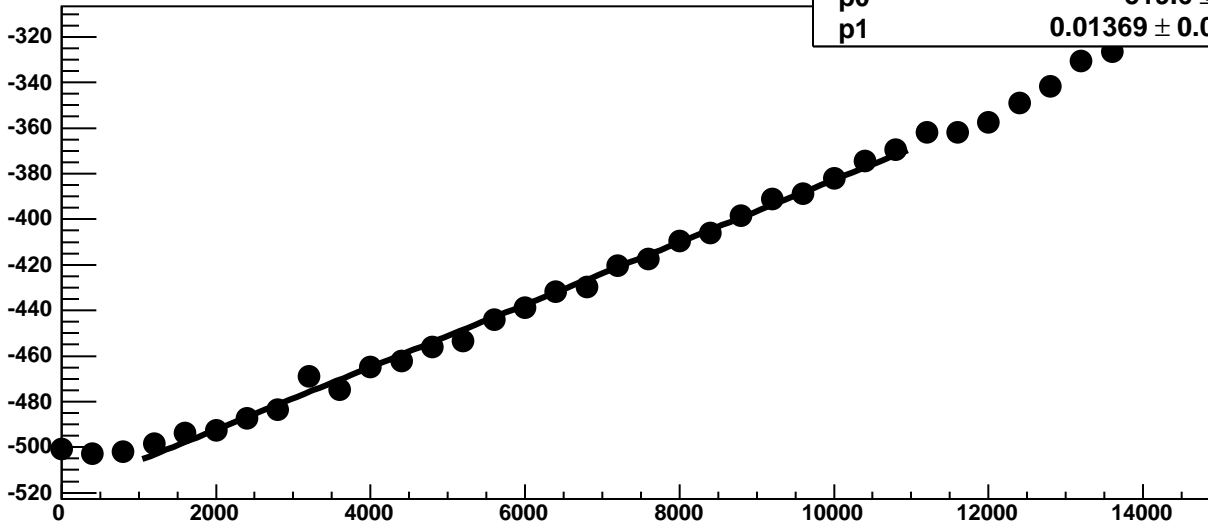
Chip 3, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

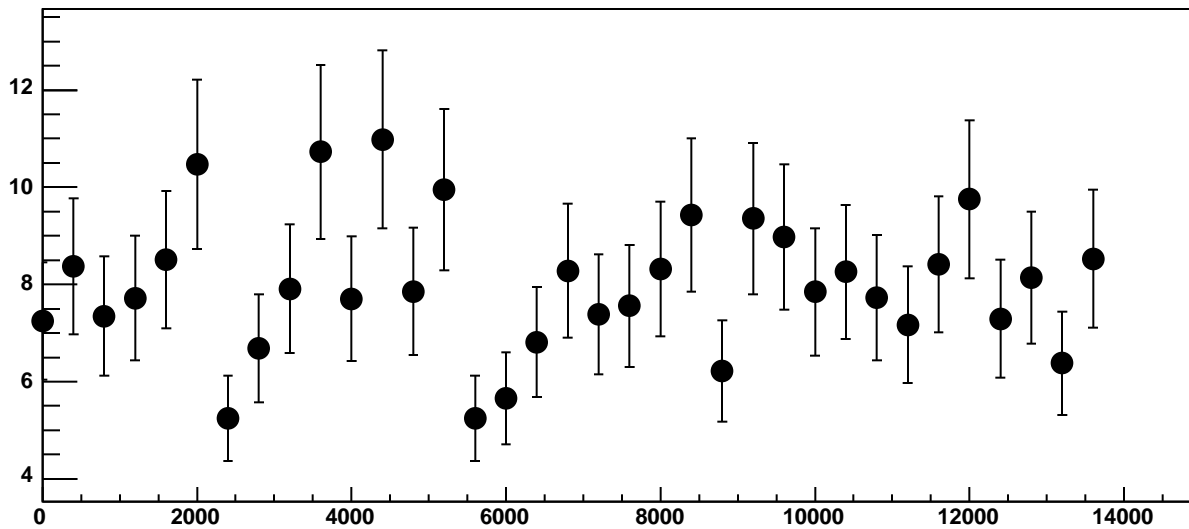


Chip 3, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

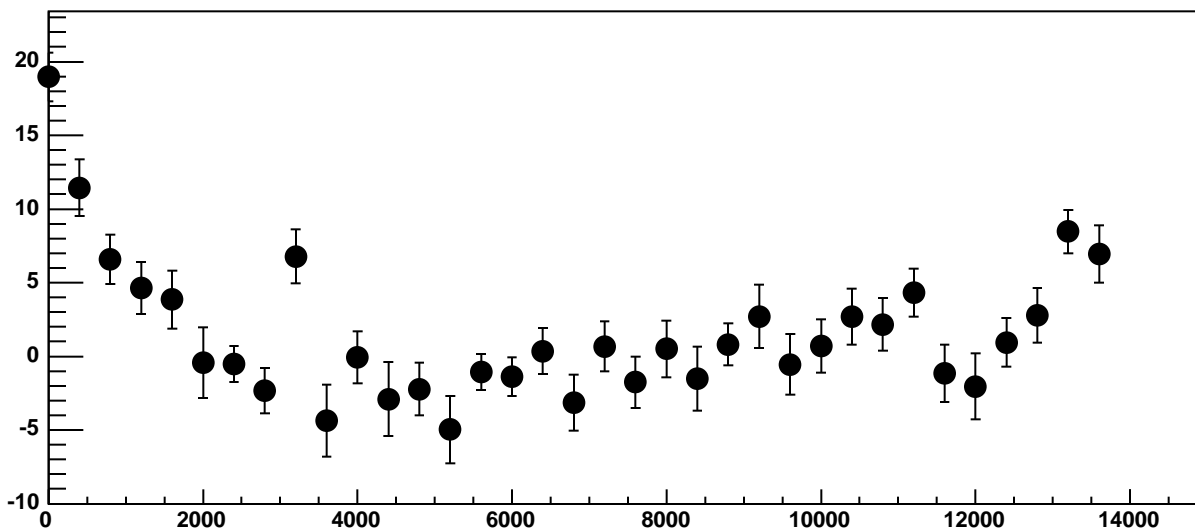


χ^2 / ndf 50.02 / 23
p0 -519.6 ± 0.8106
p1 0.01369 ± 0.0001242

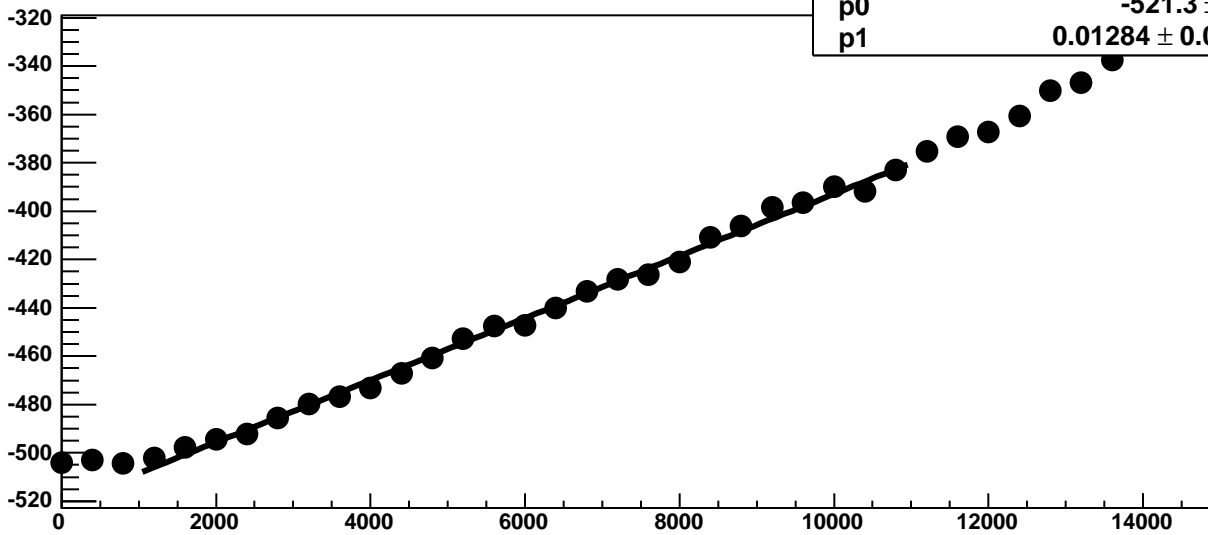
Chip 3, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

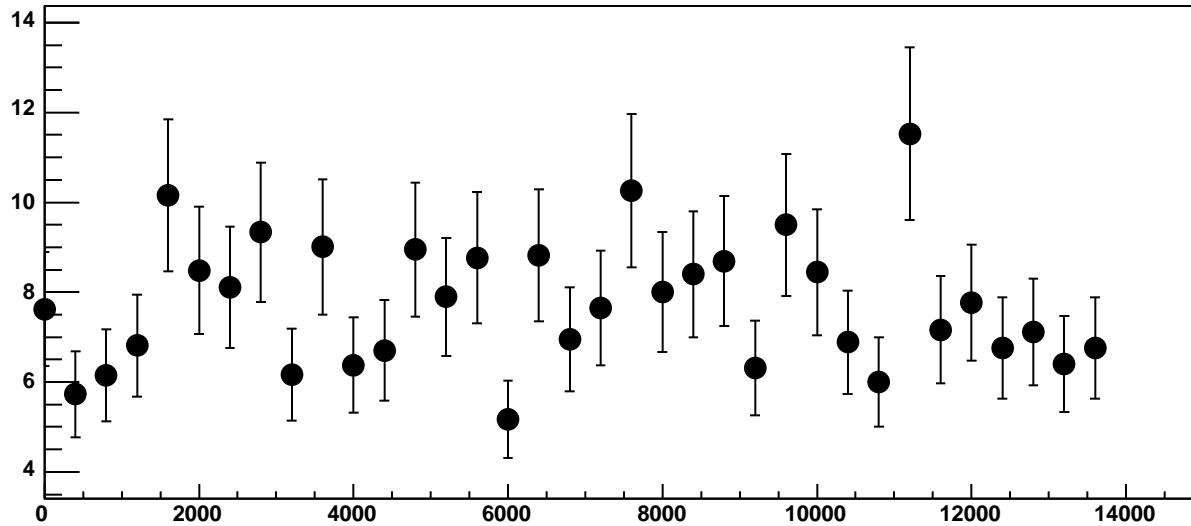


Chip 3, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

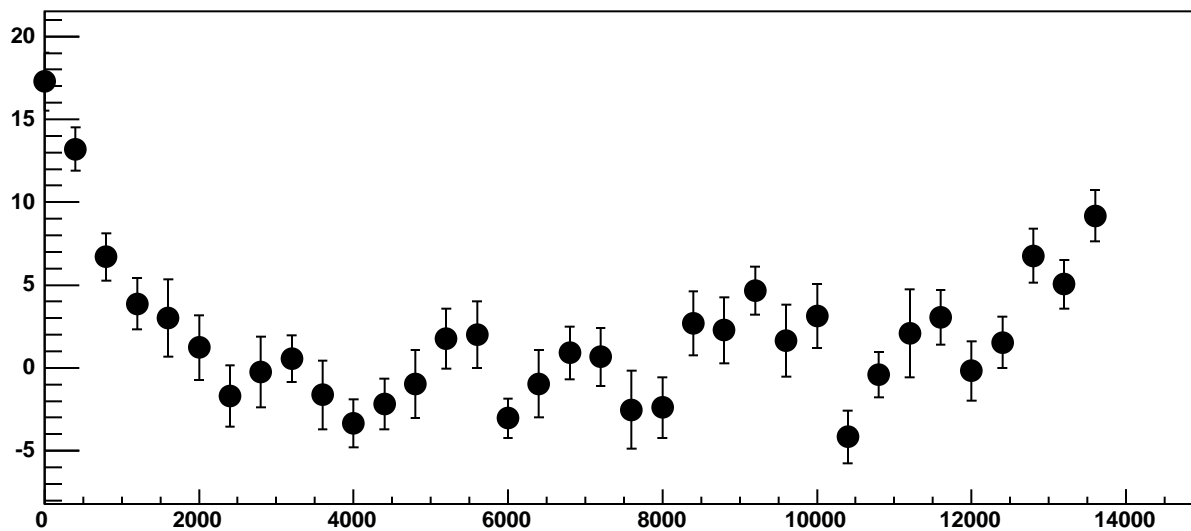


χ^2 / ndf 53.11 / 23
p0 -521.3 ± 0.8101
p1 0.01284 ± 0.0001199

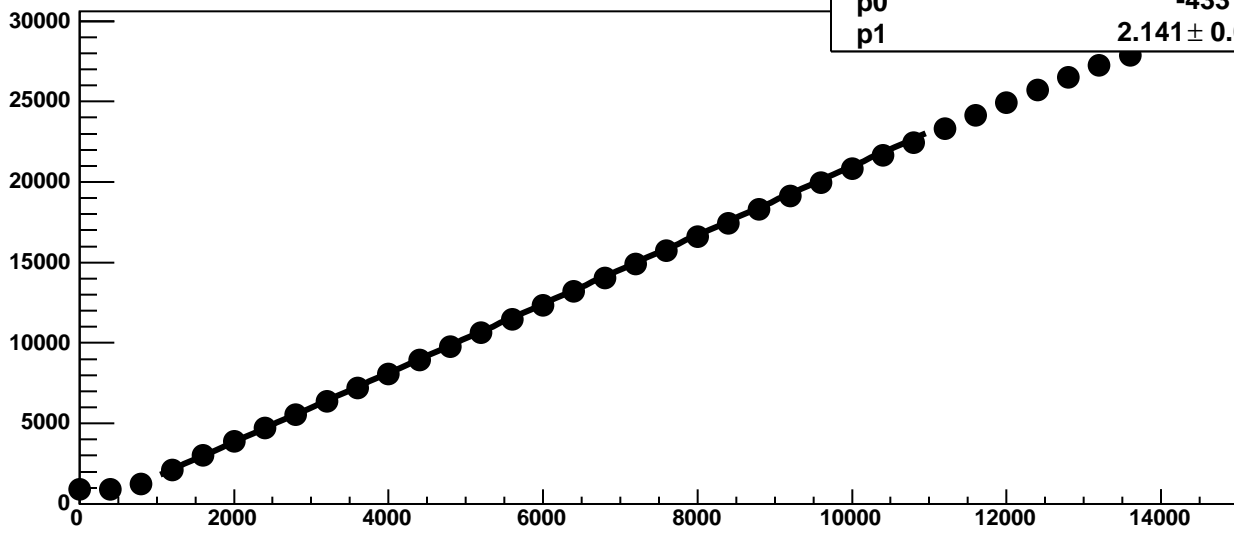
Chip 3, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



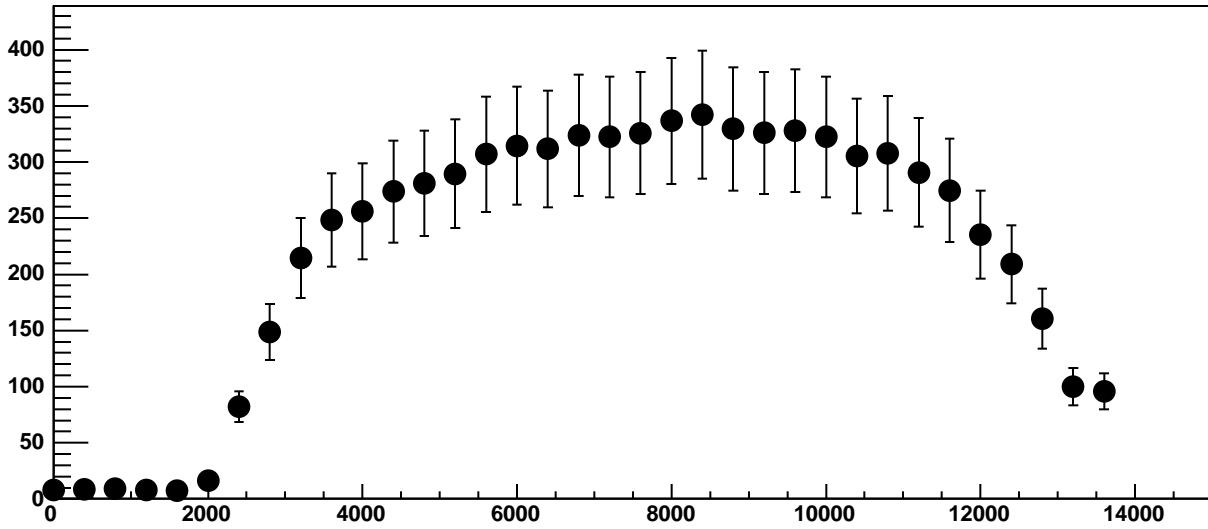
Chip 3, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



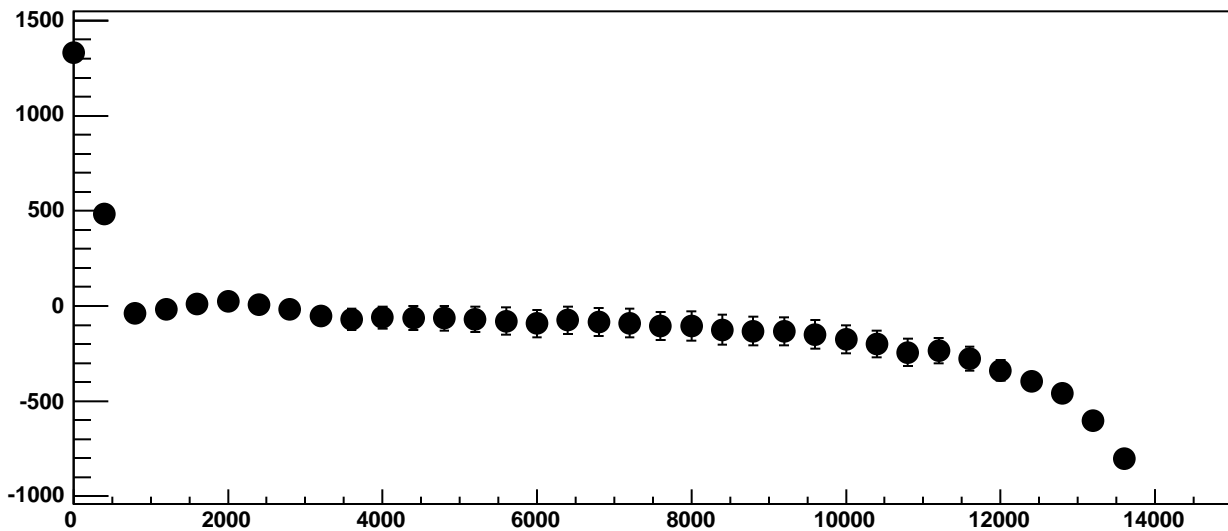
Chip 3, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC



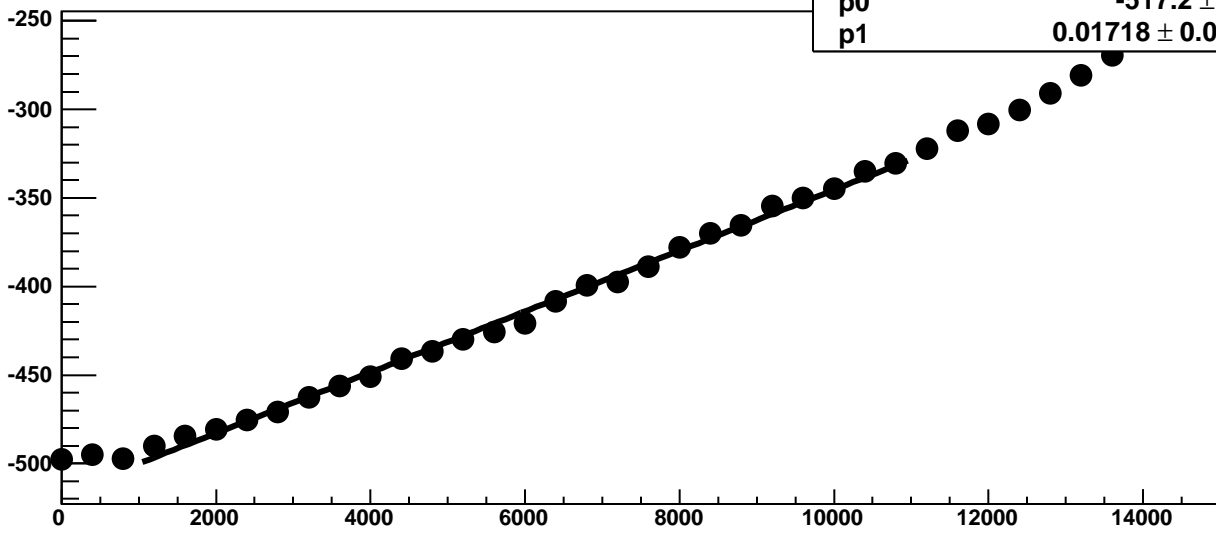
Chip 3, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC

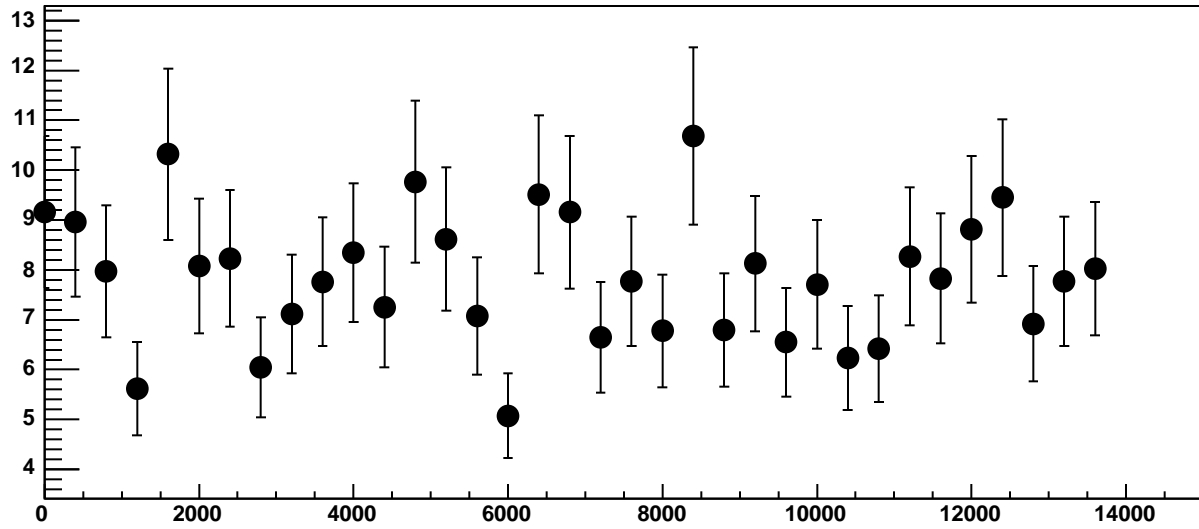


Chip 3, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

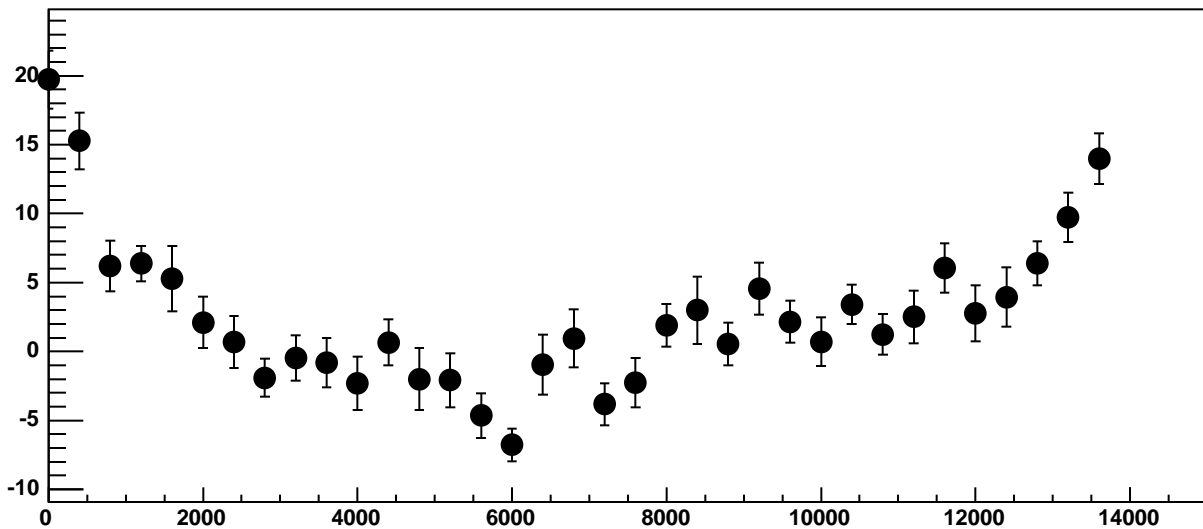


χ^2 / ndf 104.7 / 23
p0 -517.2 ± 0.7602
p1 0.01718 ± 0.0001122

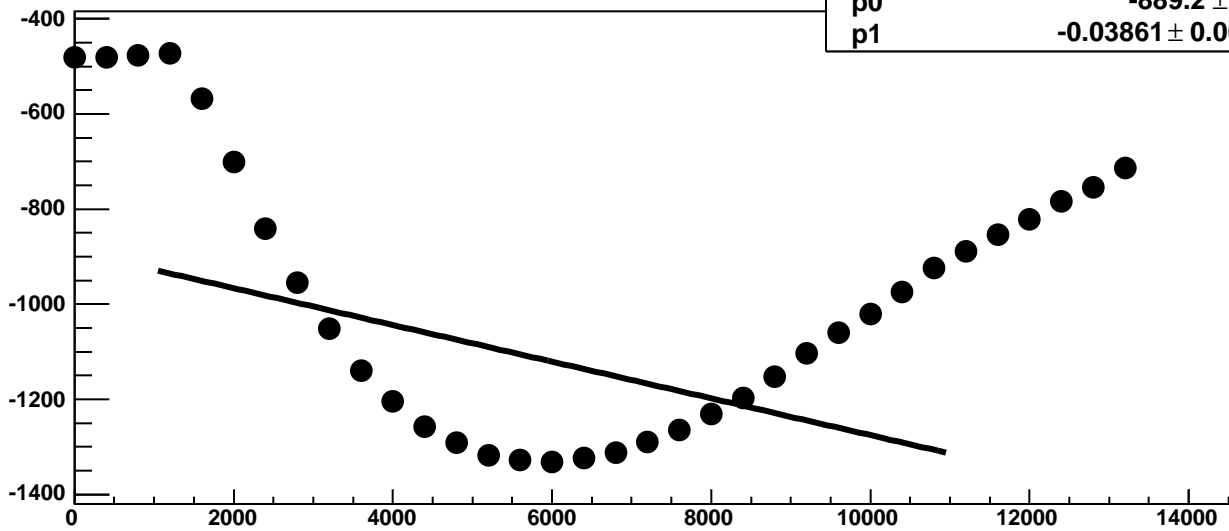
Chip 3, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

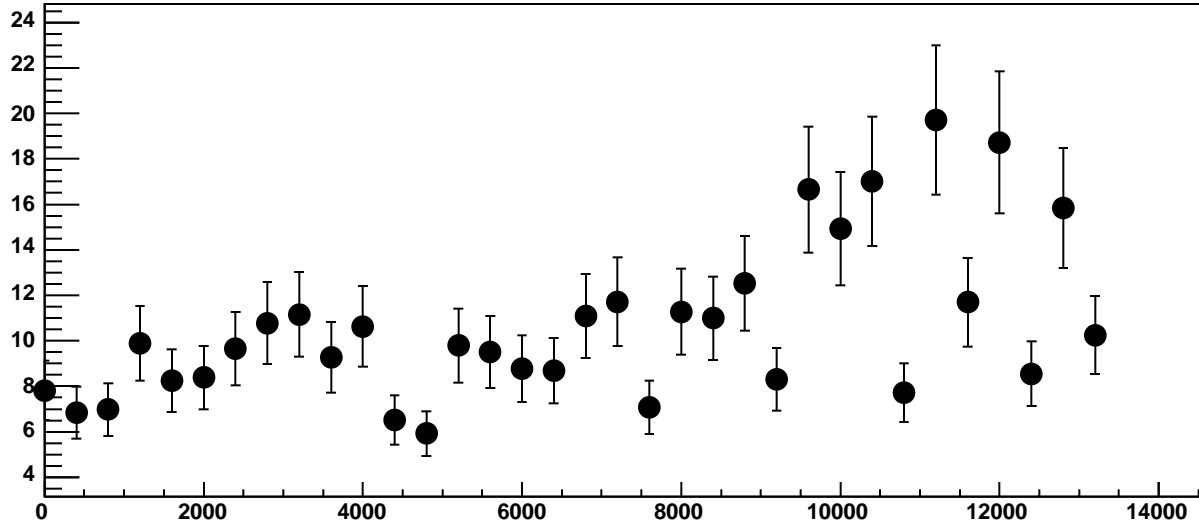


Chip 3, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

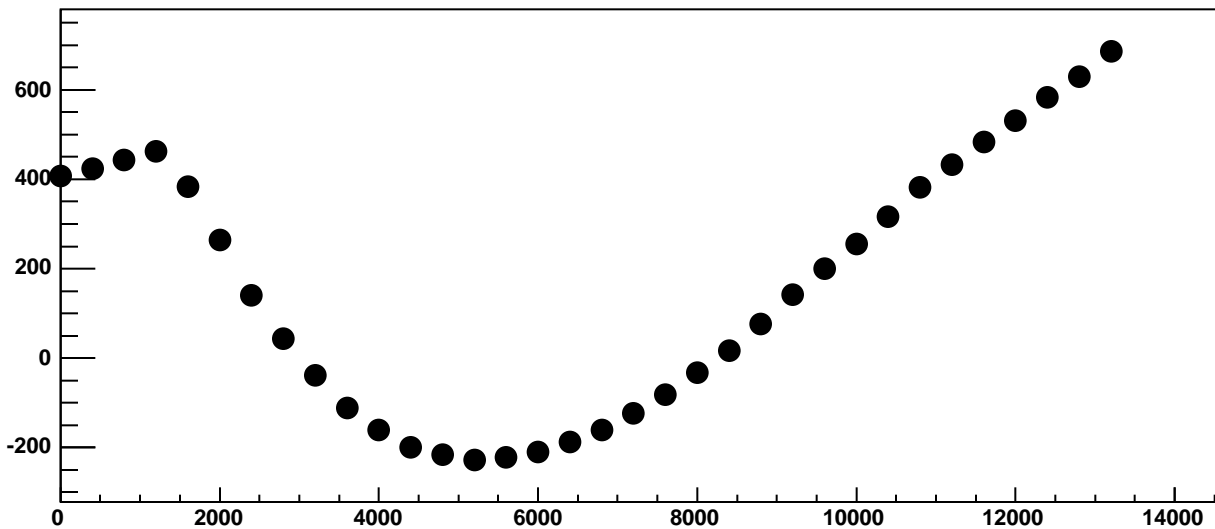


χ^2 / ndf 2.733e+05 / 23
p0 -889.2 ± 0.9817
p1 -0.03861 ± 0.0001574

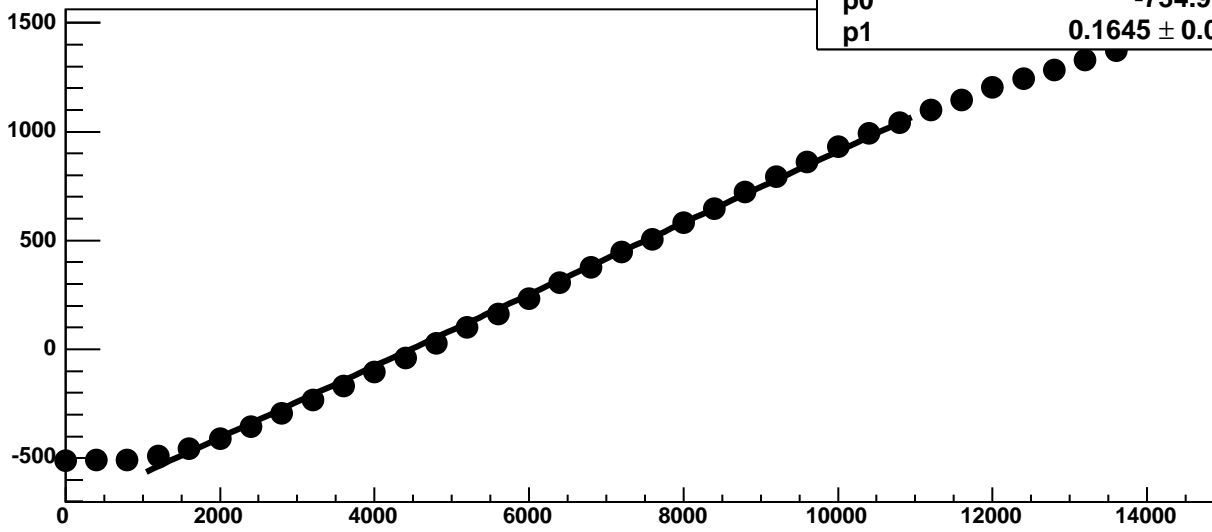
Chip 3, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



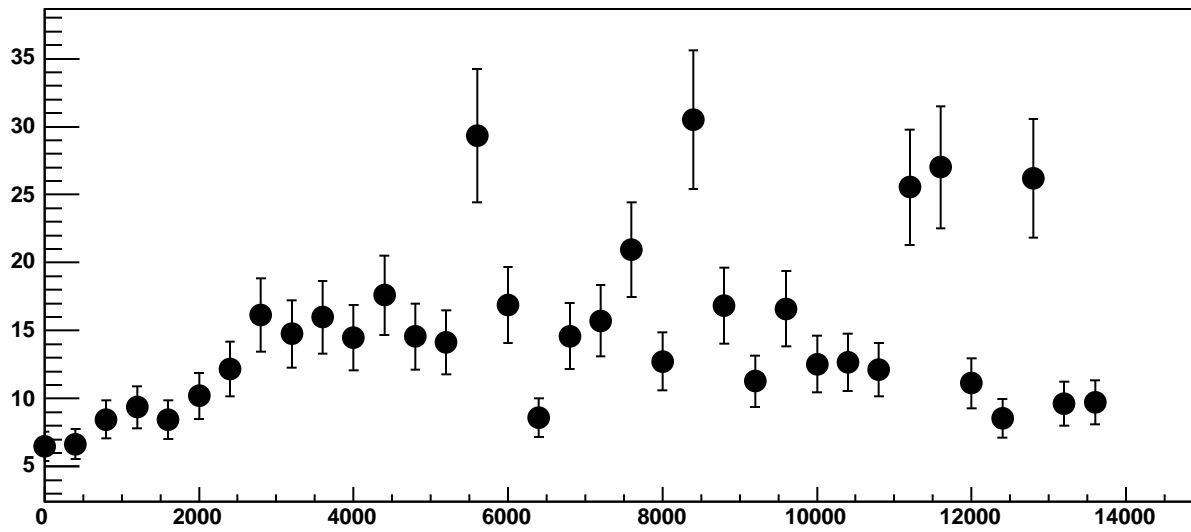
Chip 3, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



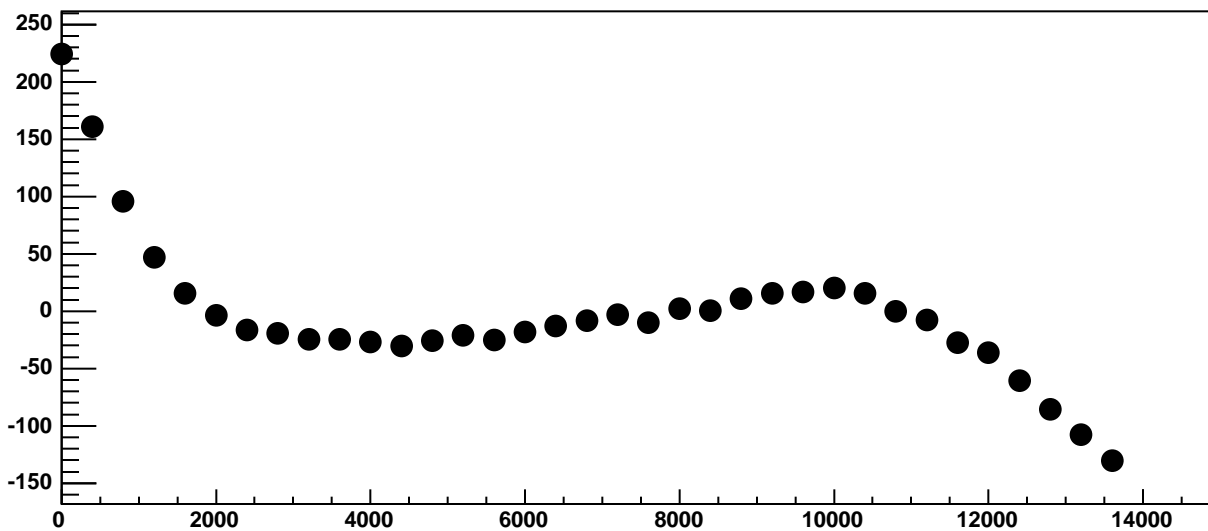
Chip 3, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



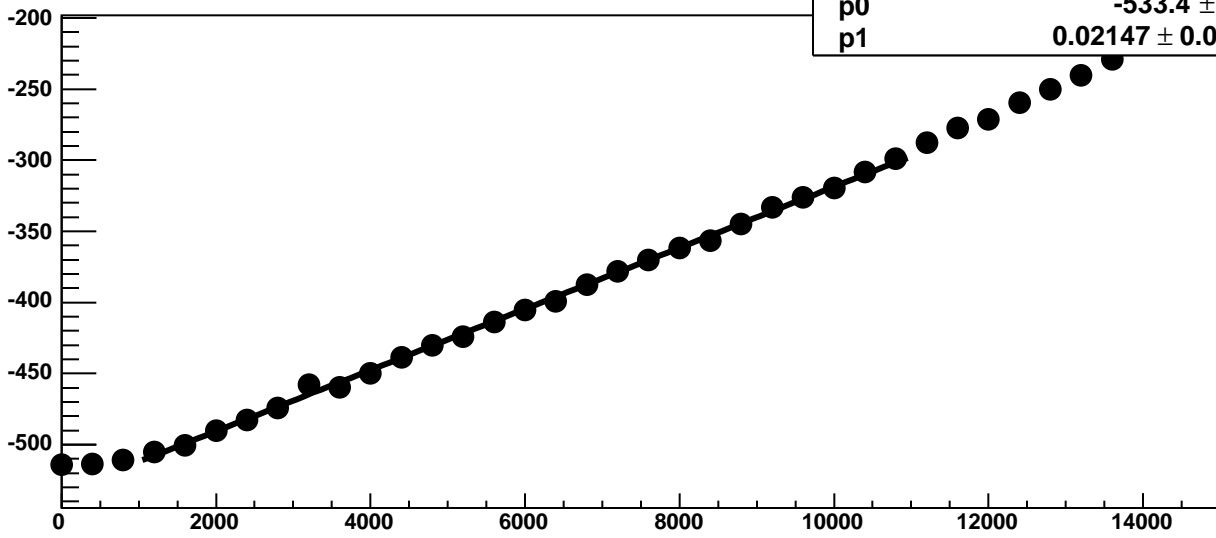
Chip 3, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

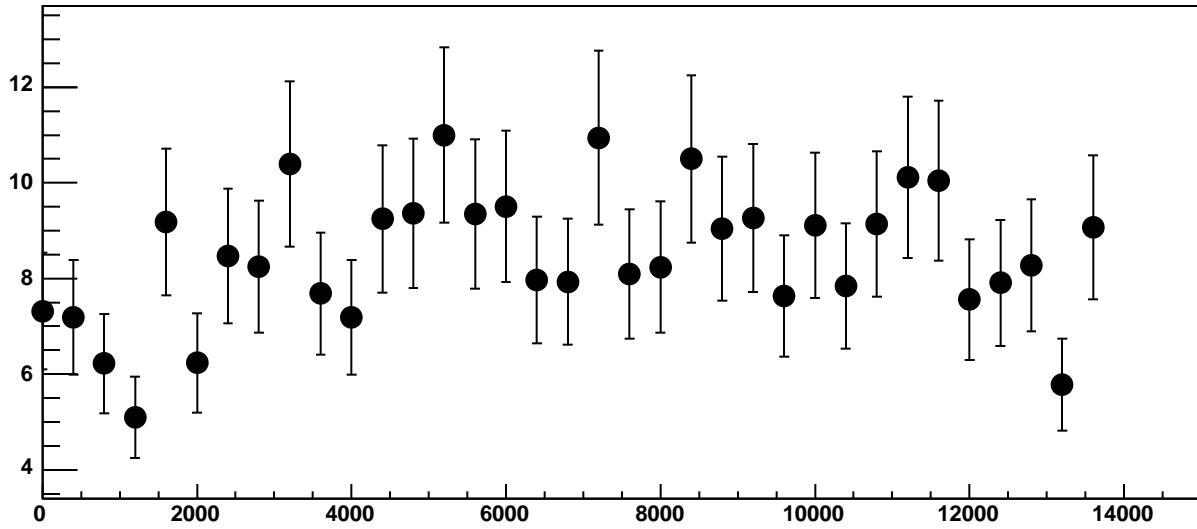


Chip 3, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC

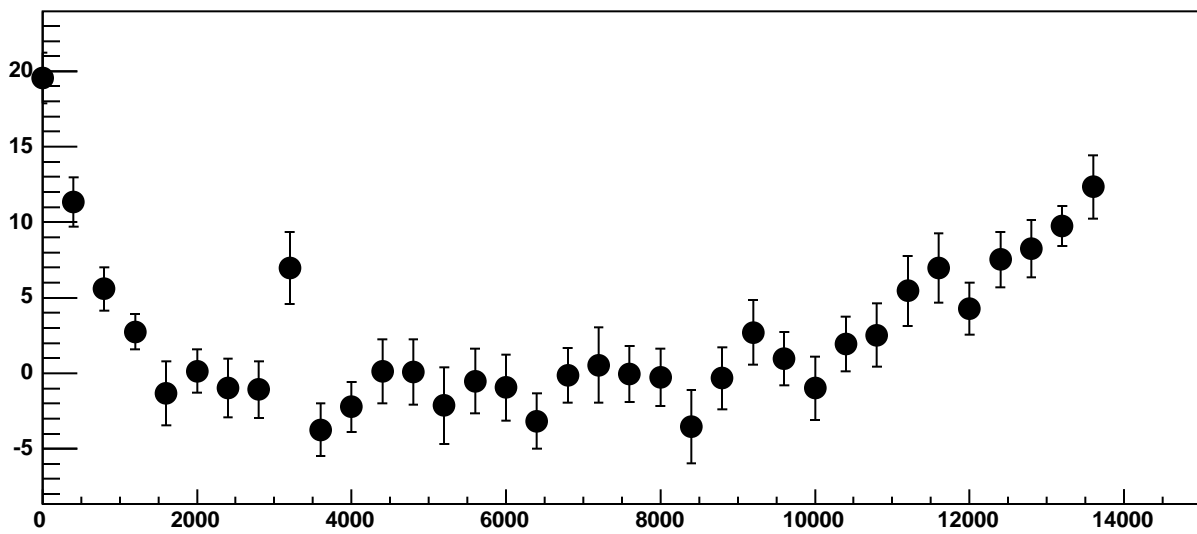


χ^2 / ndf 32.29 / 23
p0 -533.4 ± 0.7796
p1 0.02147 ± 0.0001231

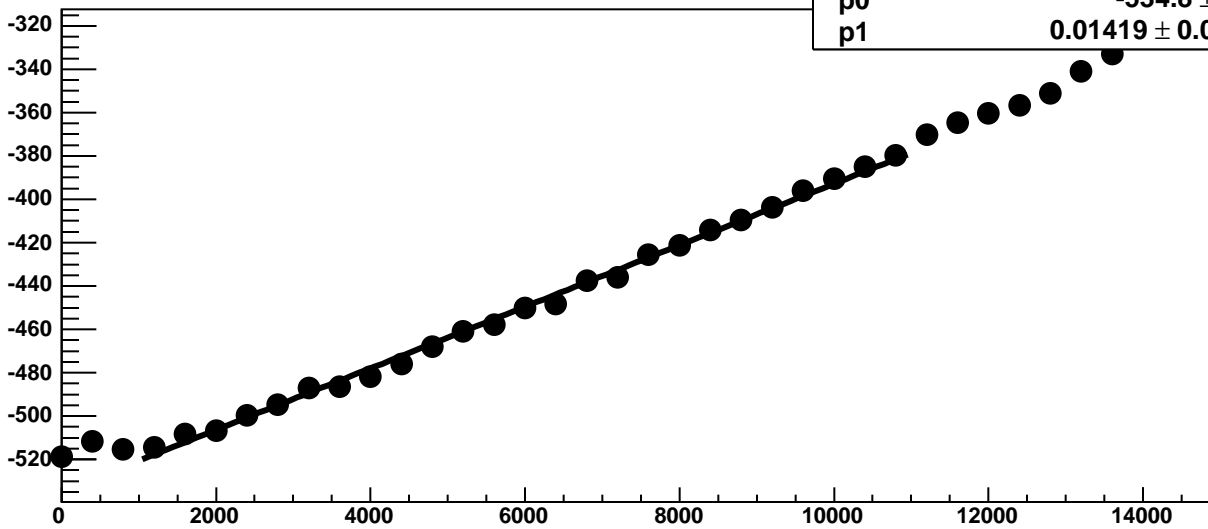
Chip 3, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

47 / 23

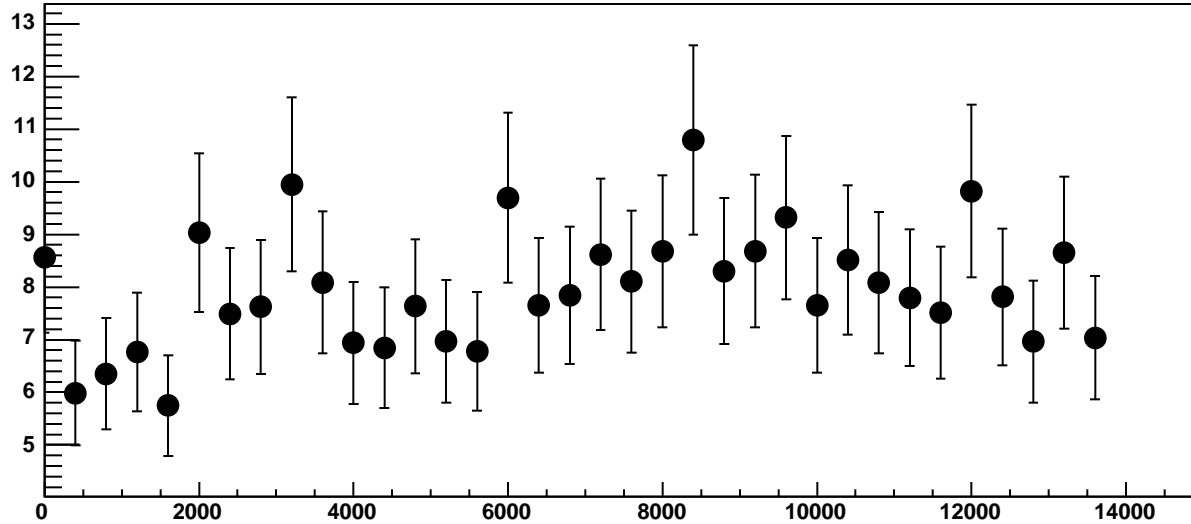
p0

-534.8 ± 0.7816

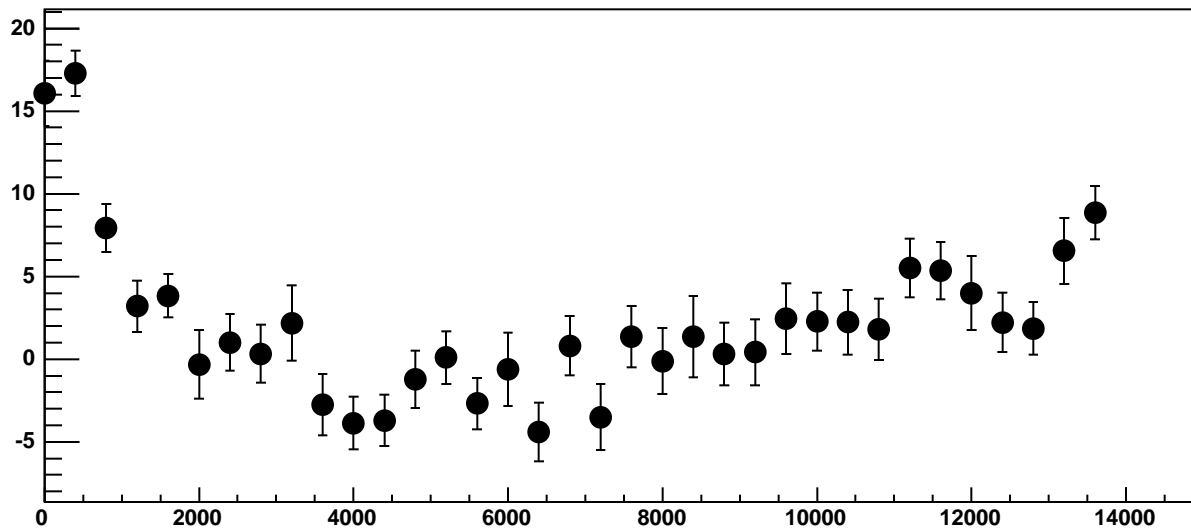
p1

0.01419 ± 0.0001237

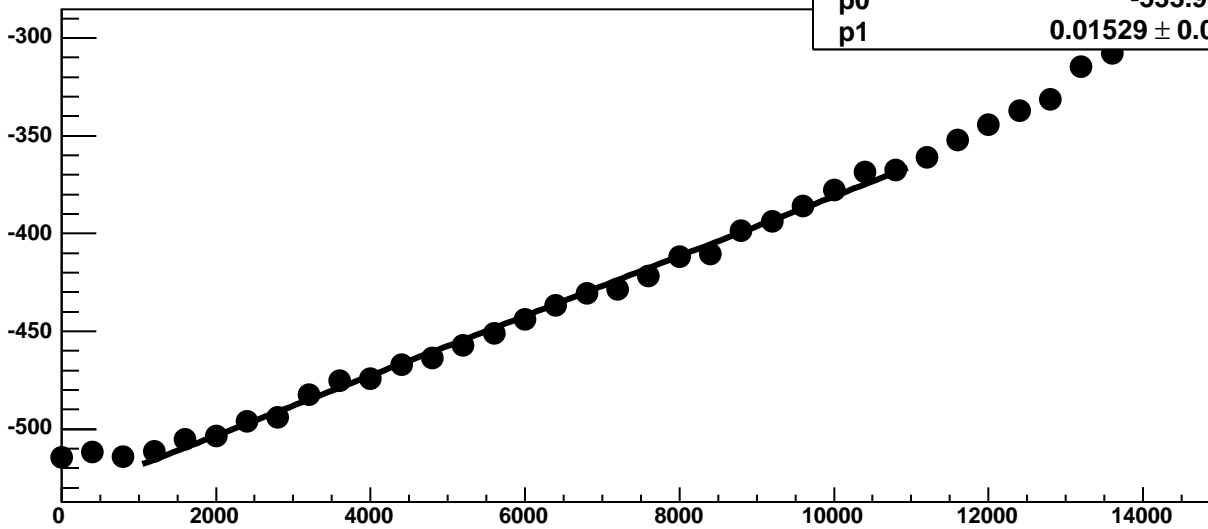
Chip 3, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

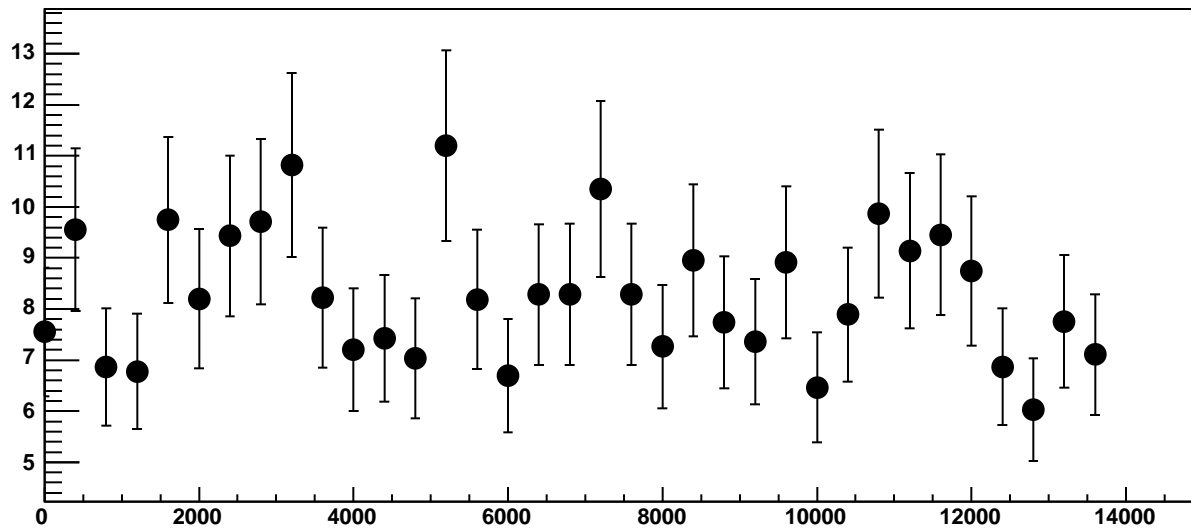


Chip 3, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

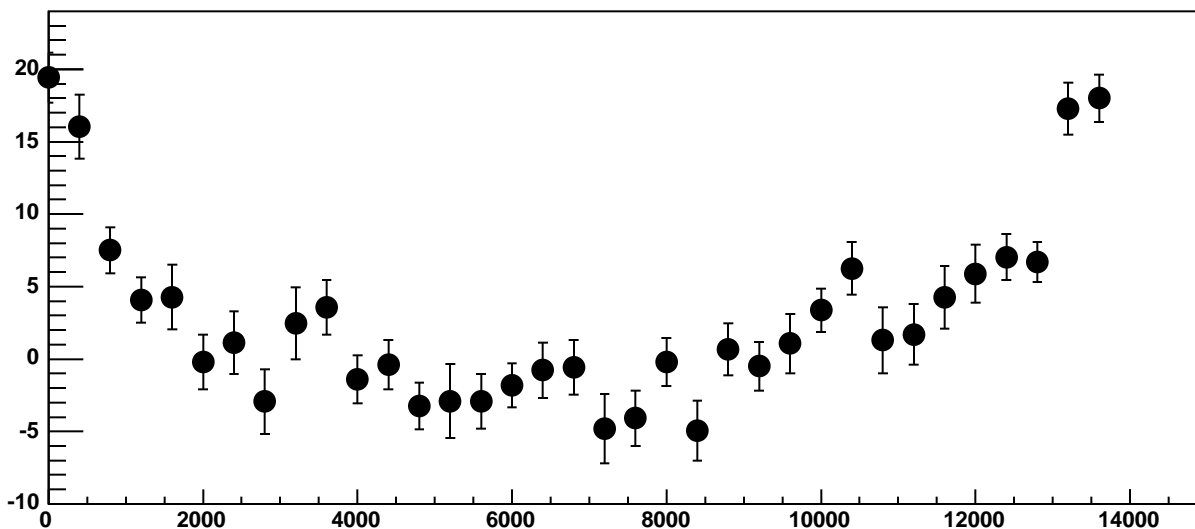


χ^2 / ndf 59.7 / 23
p0 -533.9 ± 0.875
p1 0.01529 ± 0.0001297

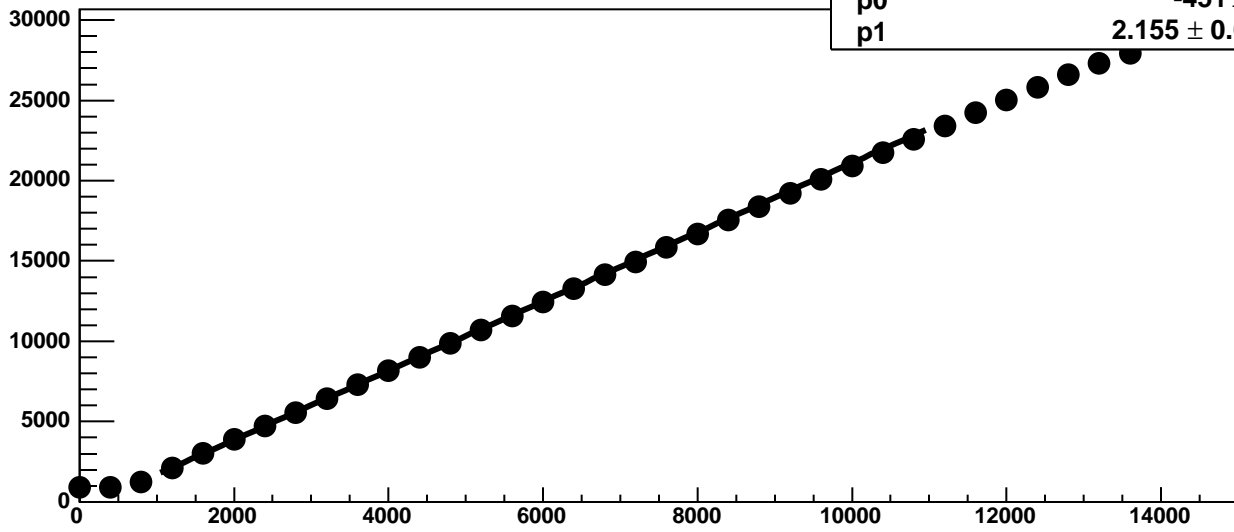
Chip 3, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

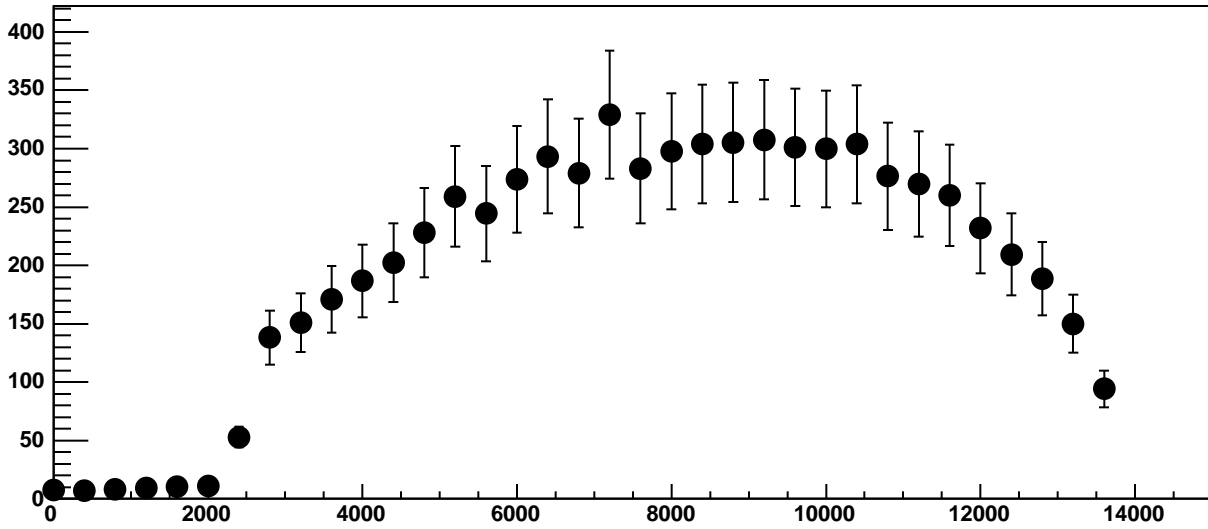


Chip 3, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC

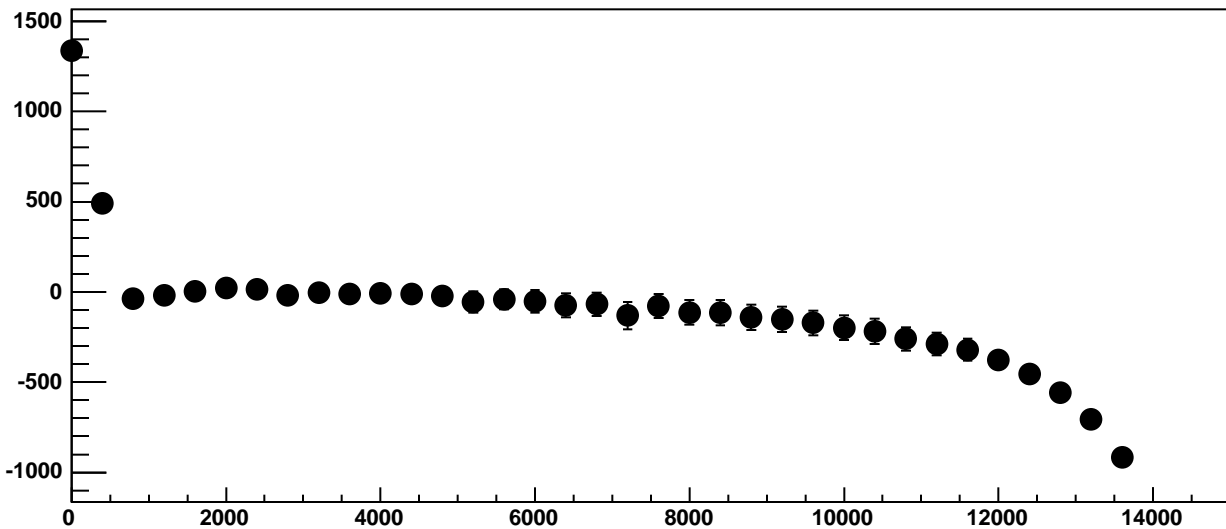


χ^2 / ndf 214.5 / 23
p0 -451 ± 3.635
p1 2.155 ± 0.002097

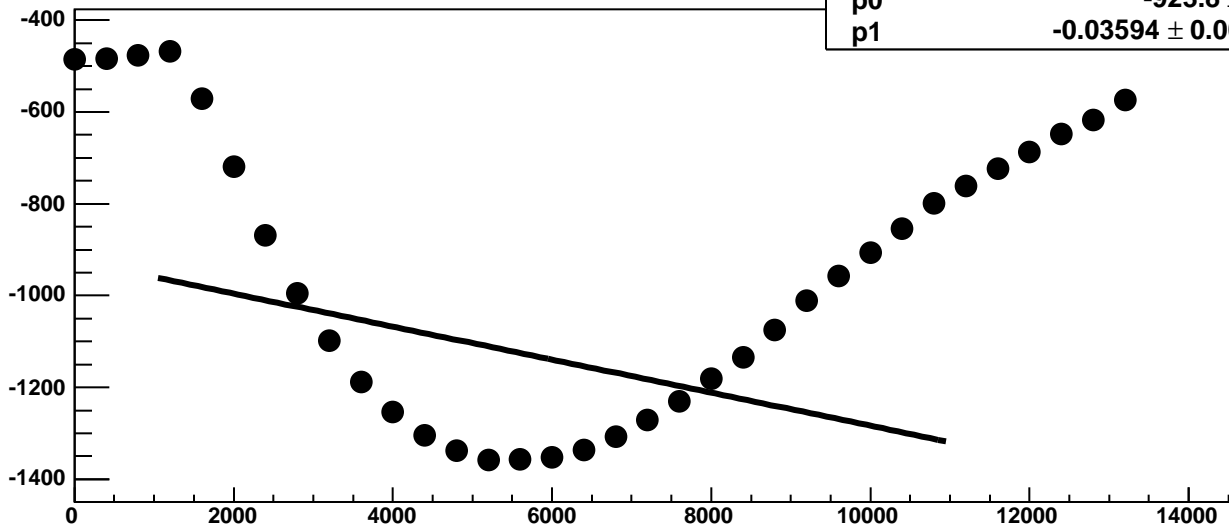
Chip 3, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC

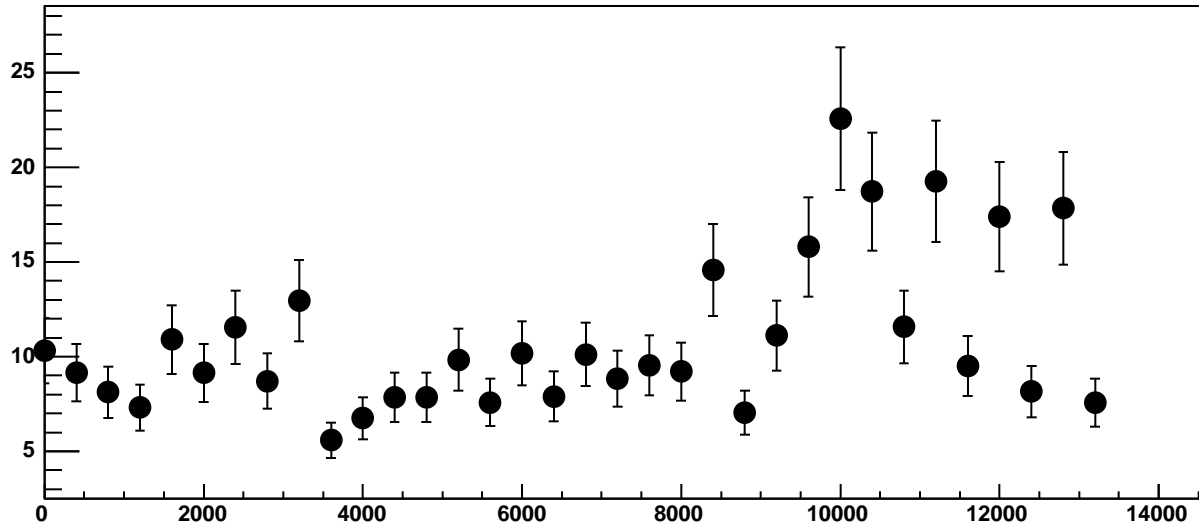


Chip 3, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

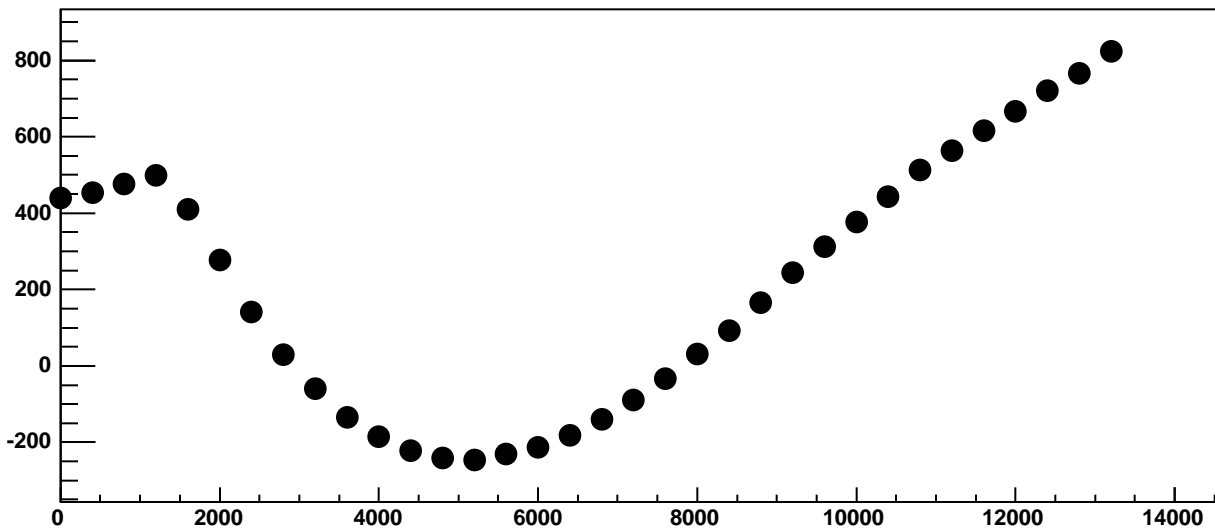


χ^2 / ndf 3.301e+05 / 23
p0 -923.8 ± 0.968
p1 -0.03594 ± 0.0001643

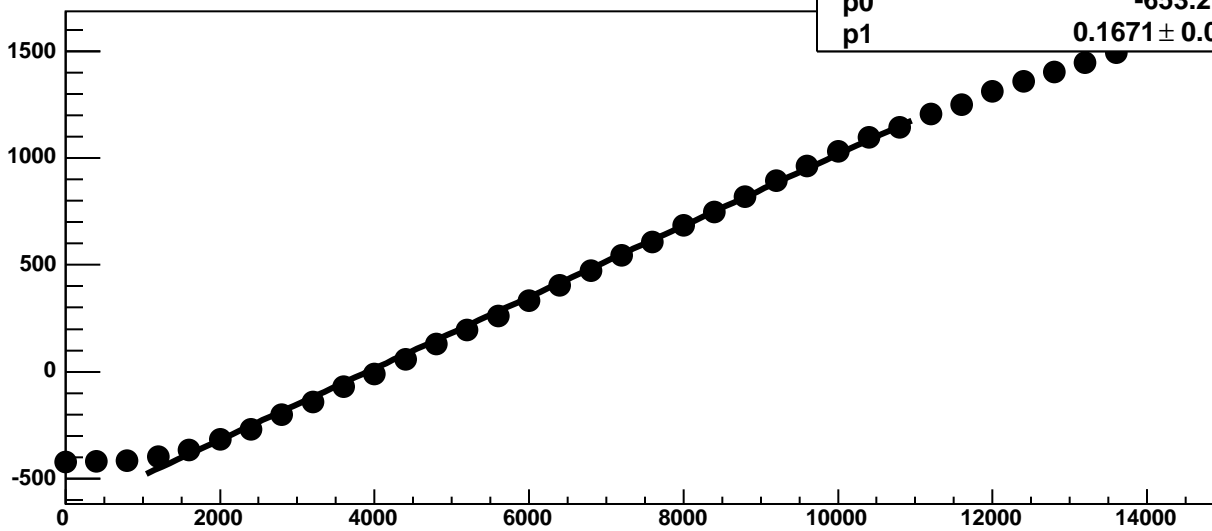
Chip 3, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



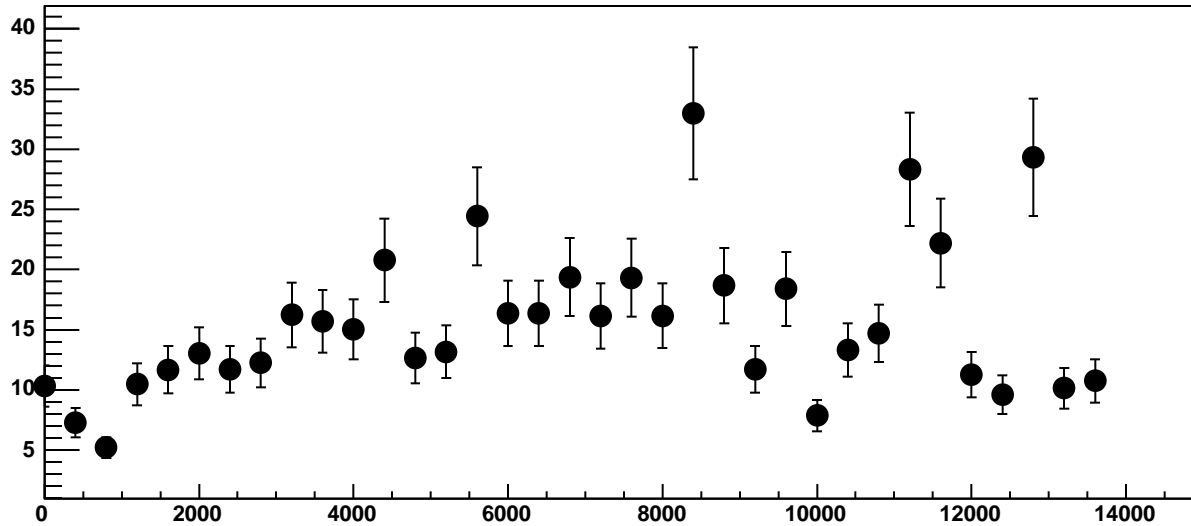
Chip 3, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



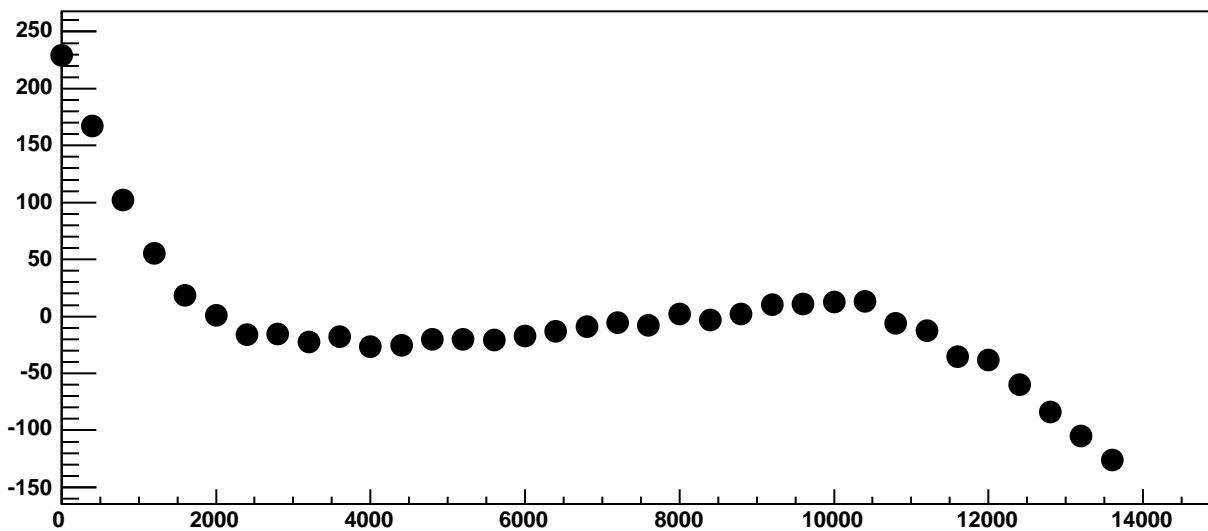
Chip 3, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



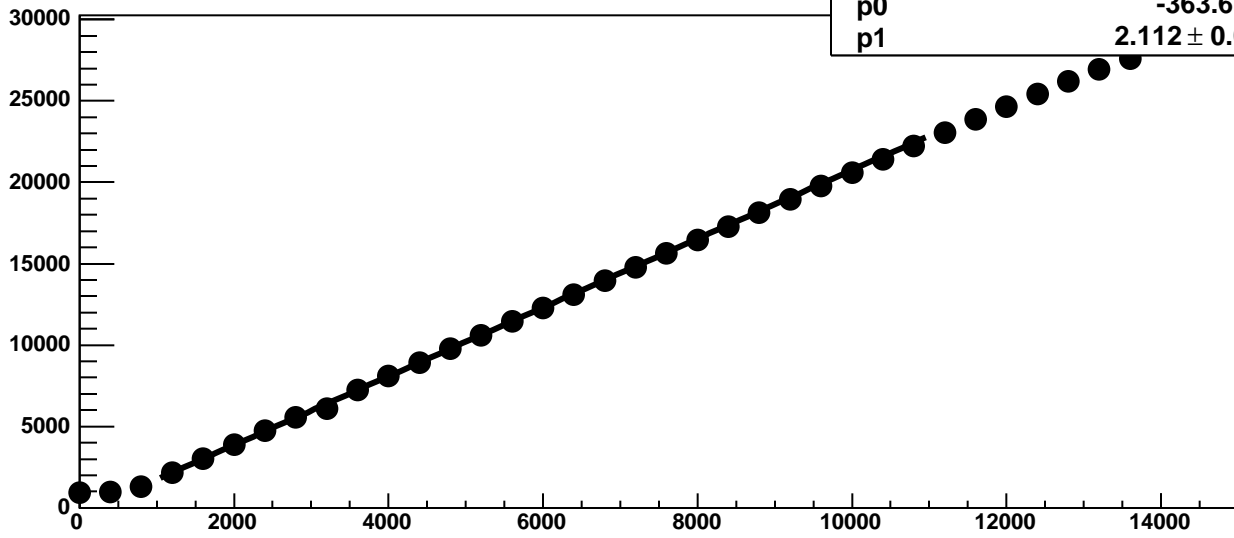
Chip 3, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



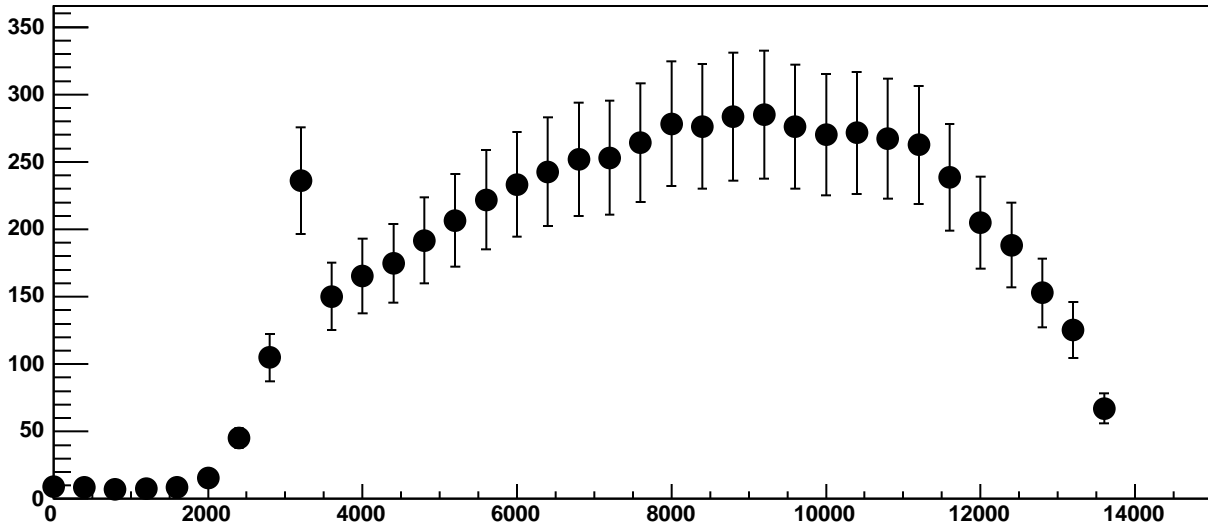
Chip 3, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



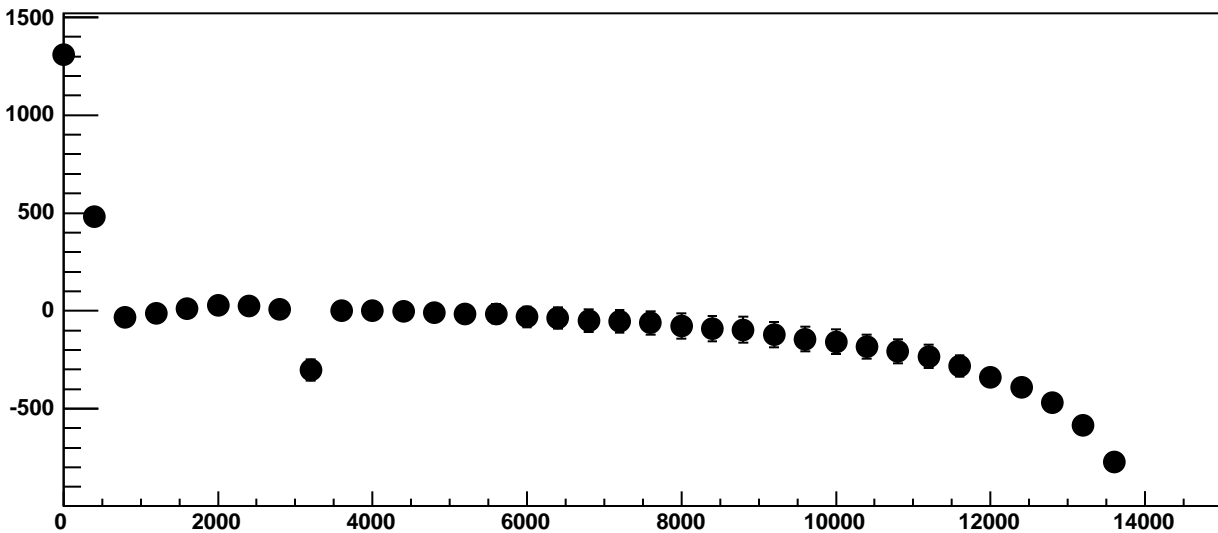
Chip 3, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC



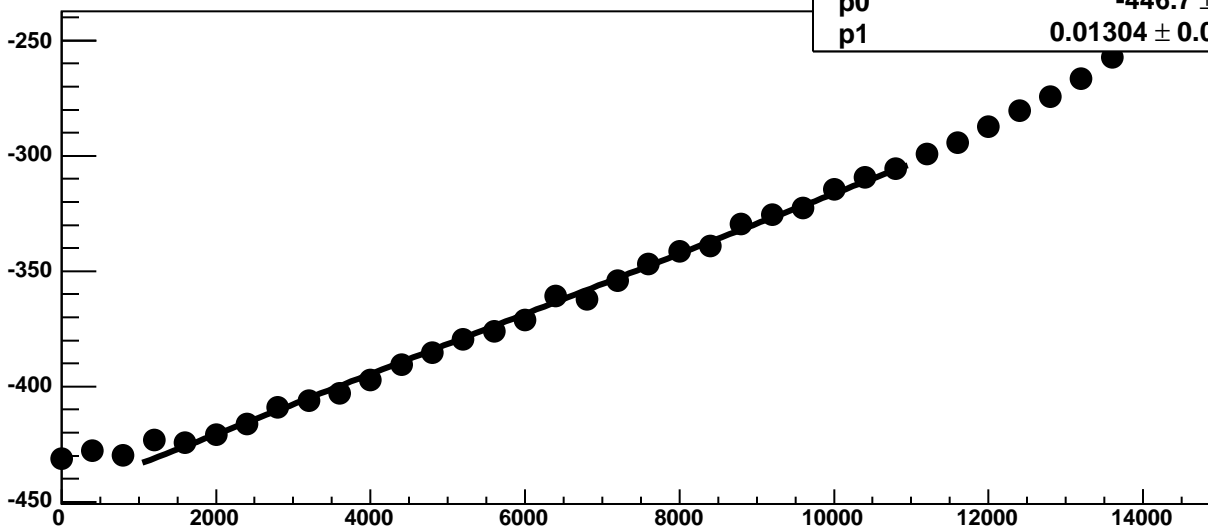
Chip 3, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



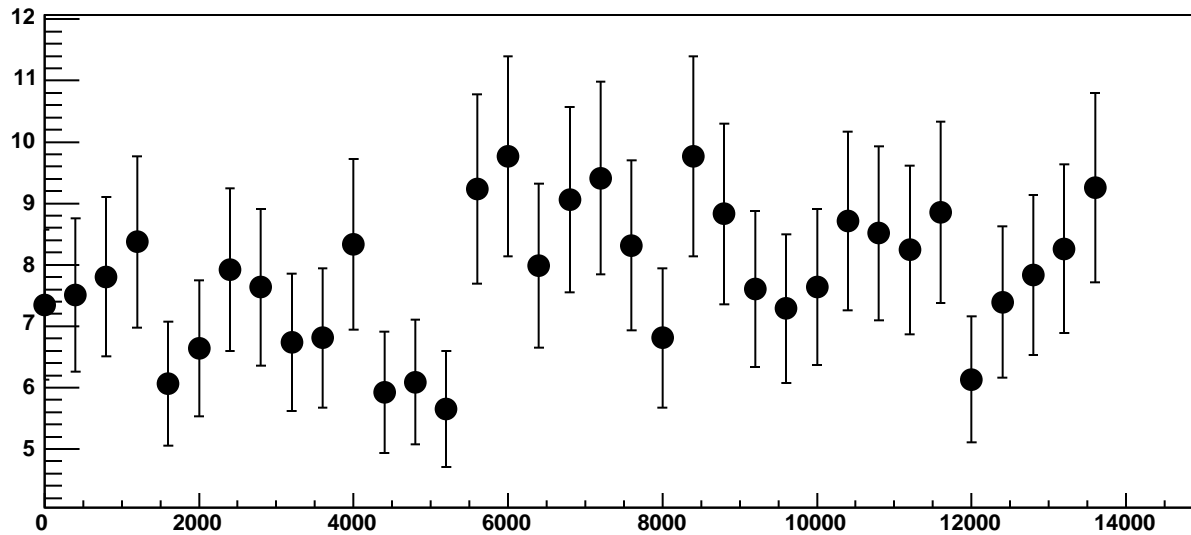
Chip 3, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC



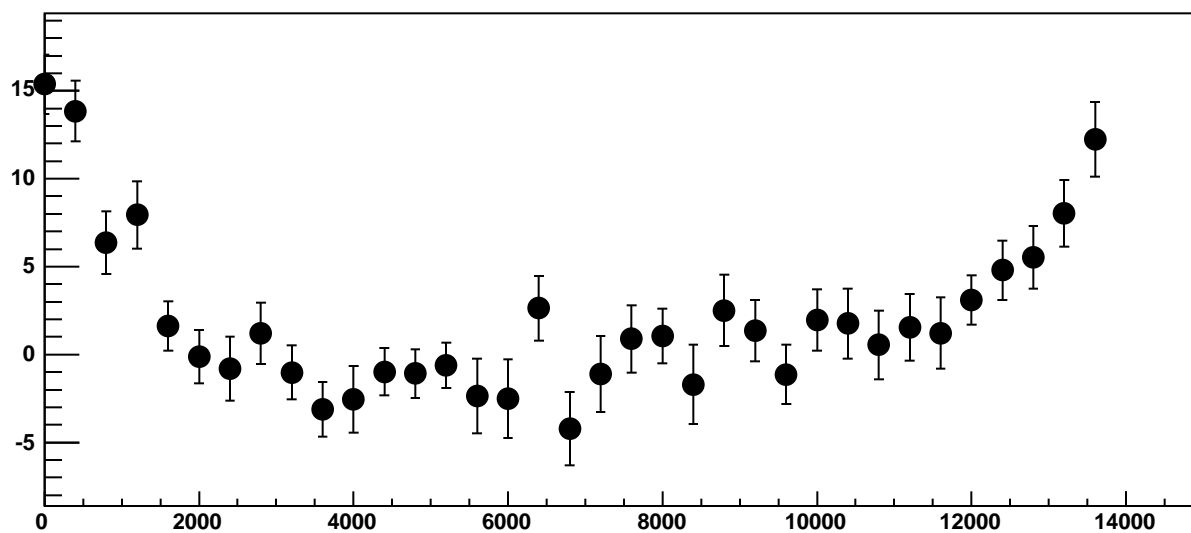
Chip 3, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



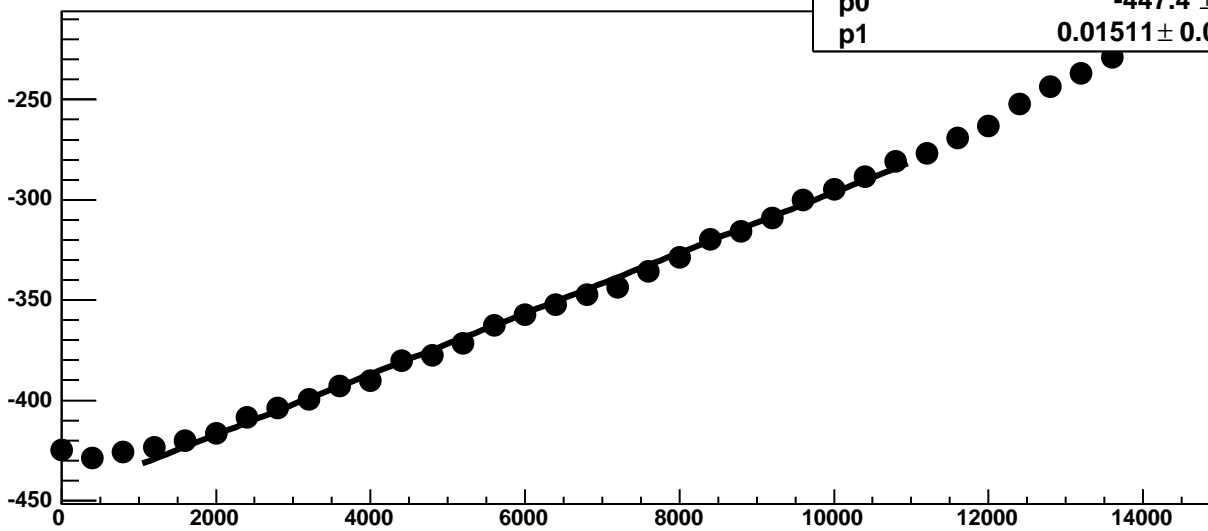
Chip 3, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

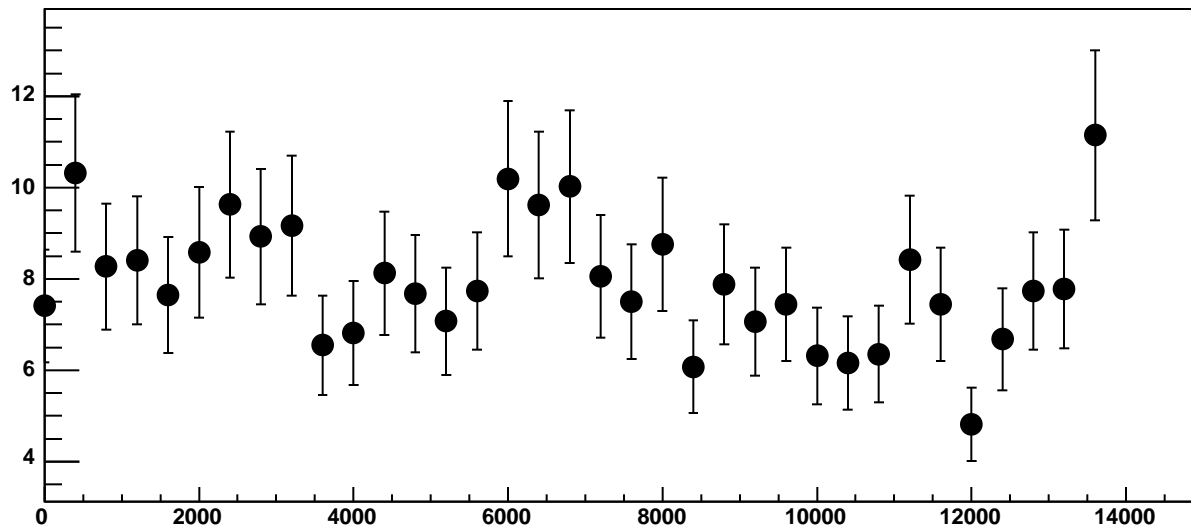


Chip 3, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

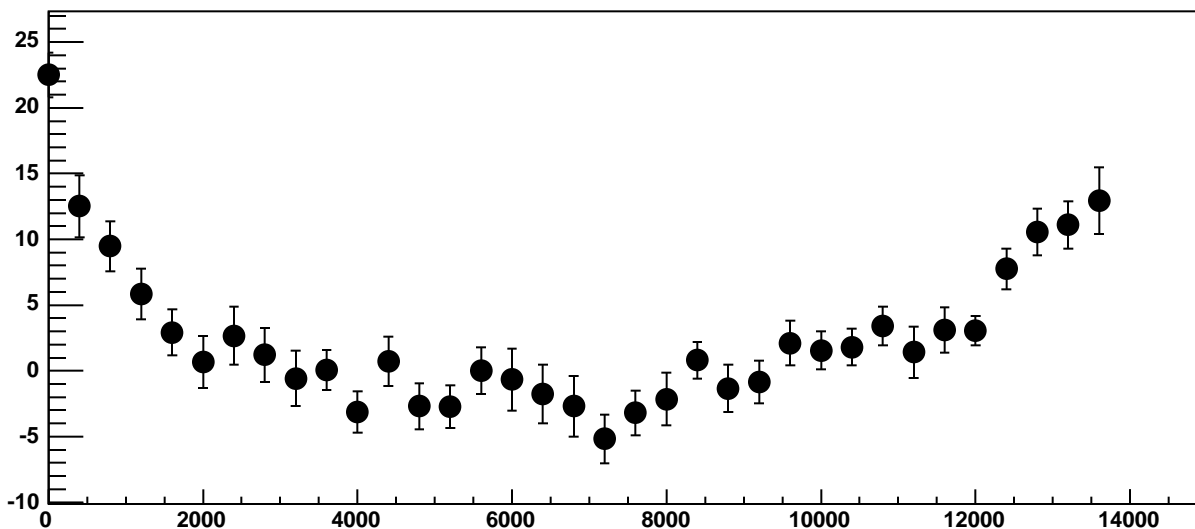


χ^2 / ndf 48.66 / 23
p0 -447.4 ± 0.8352
p1 0.01511 ± 0.0001179

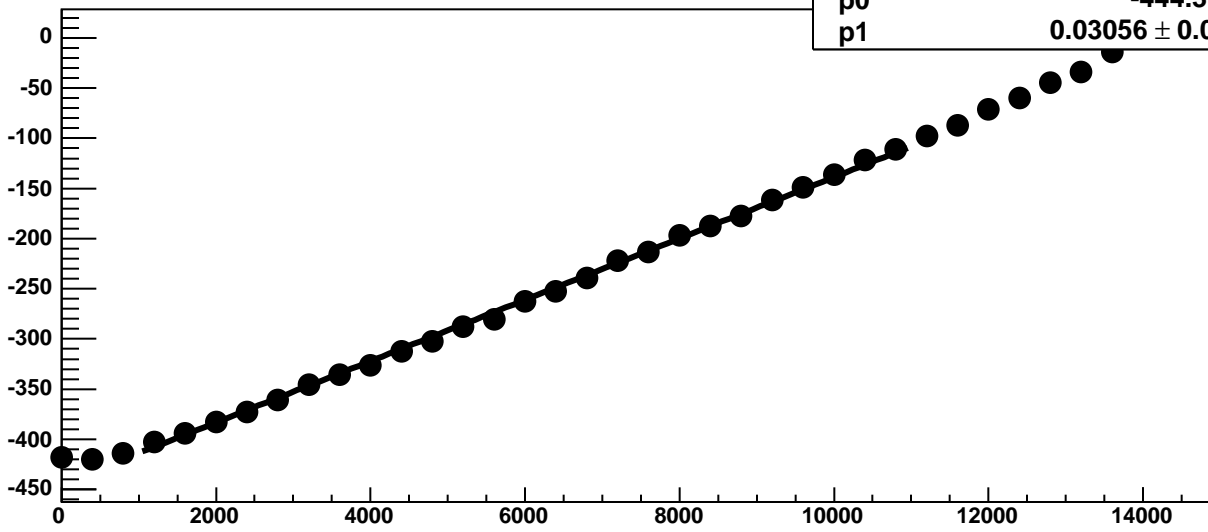
Chip 3, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

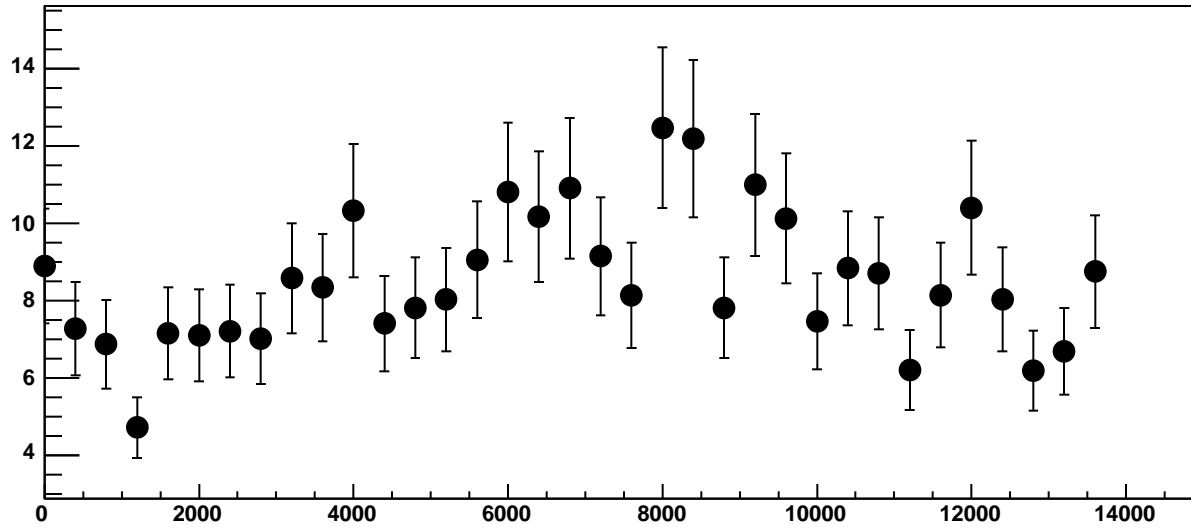


Chip 3, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

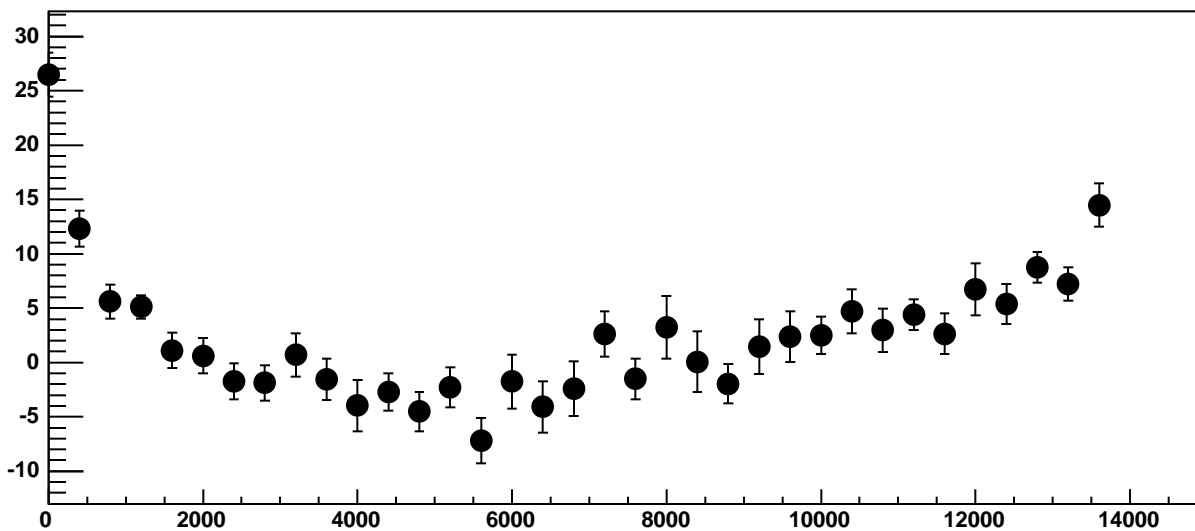


χ^2 / ndf 71.64 / 23
p0 -444.3 ± 0.736
p1 0.03056 ± 0.0001215

Chip 3, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC

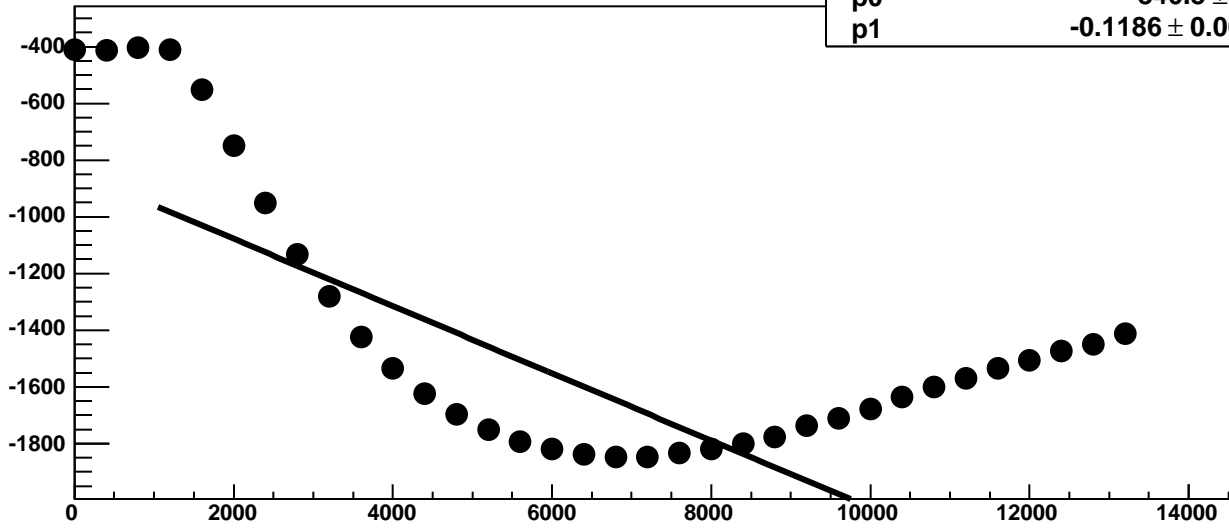


Chip 3, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC

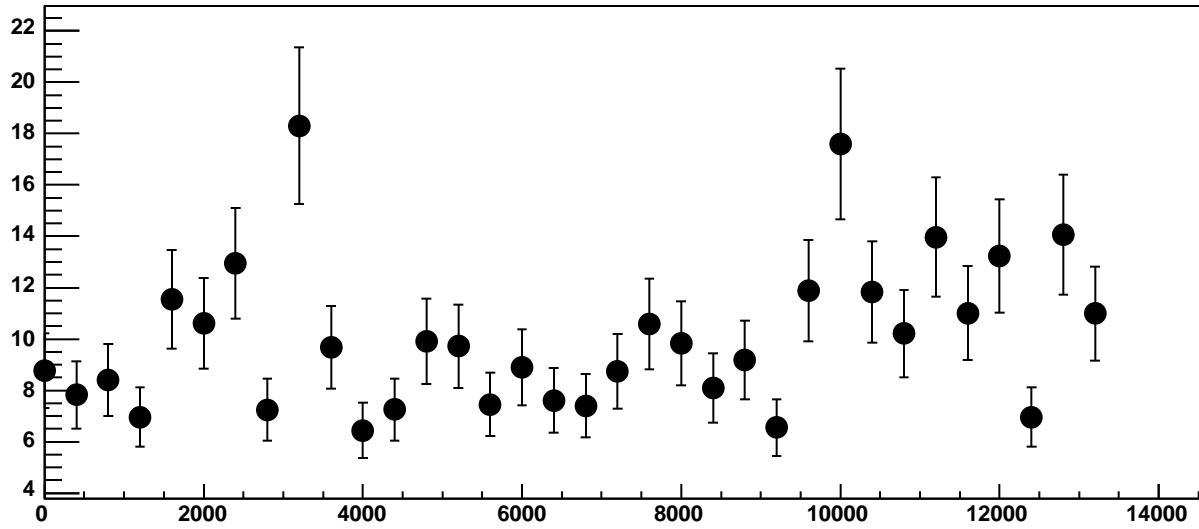


Chip 3, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

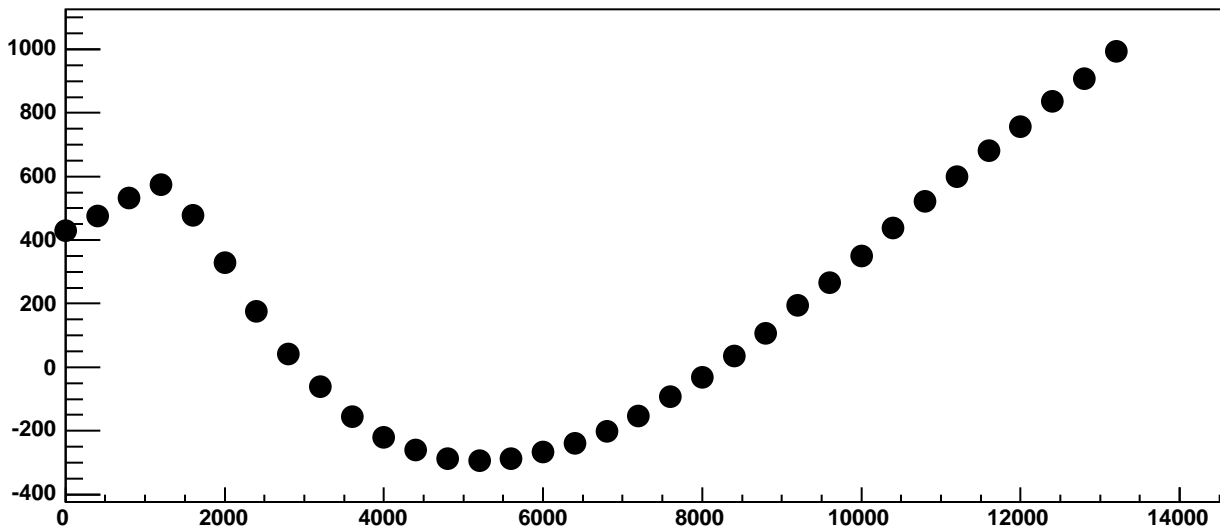
χ^2 / ndf 4.666e+05 / 23
p0 -840.8 ± 0.9702
p1 -0.1186 ± 0.0001513



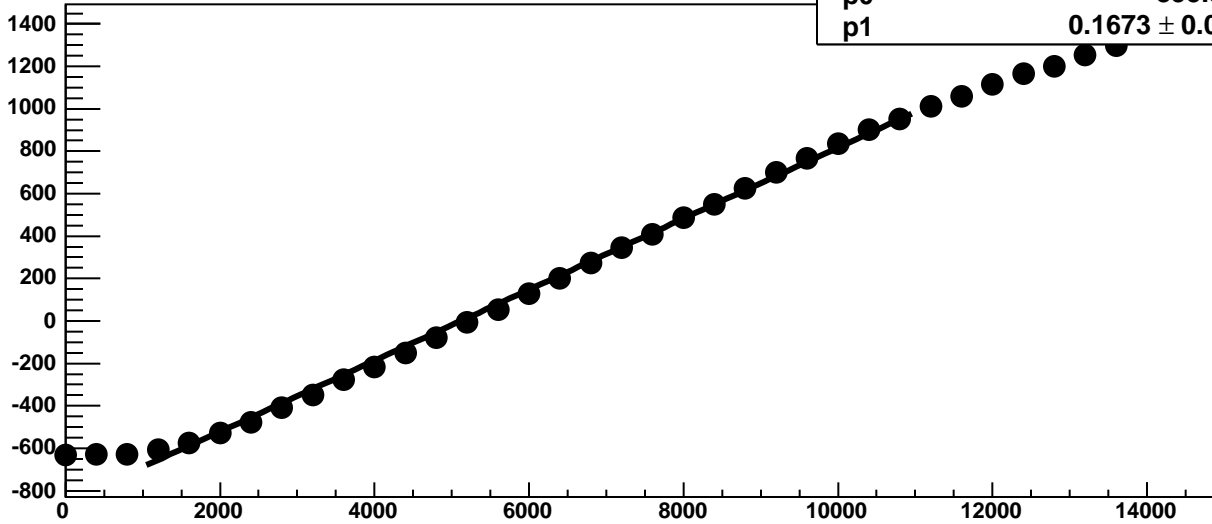
Chip 3, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1369 / 23

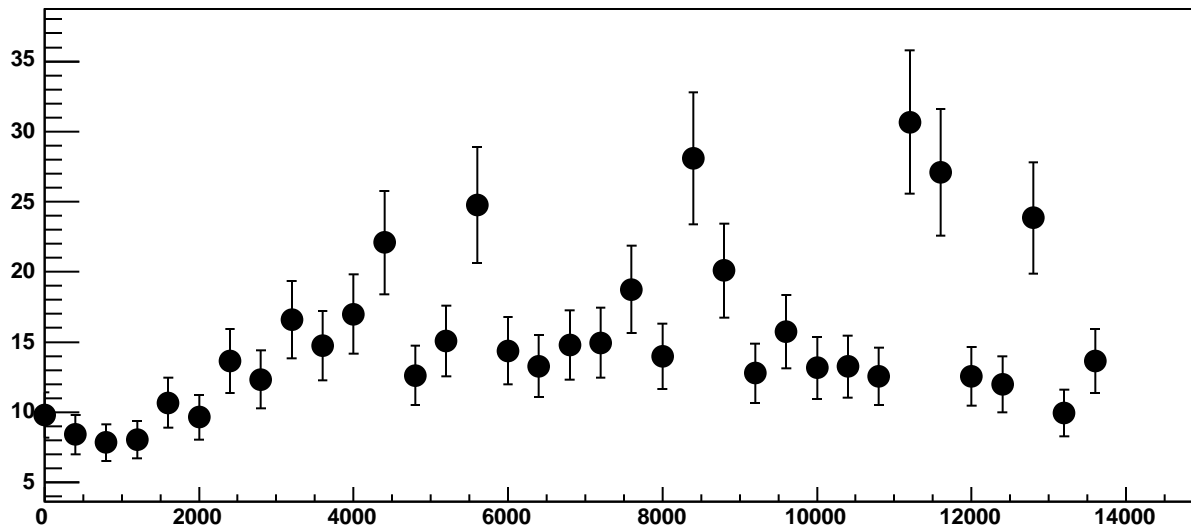
p0

-855.8 ± 1.21

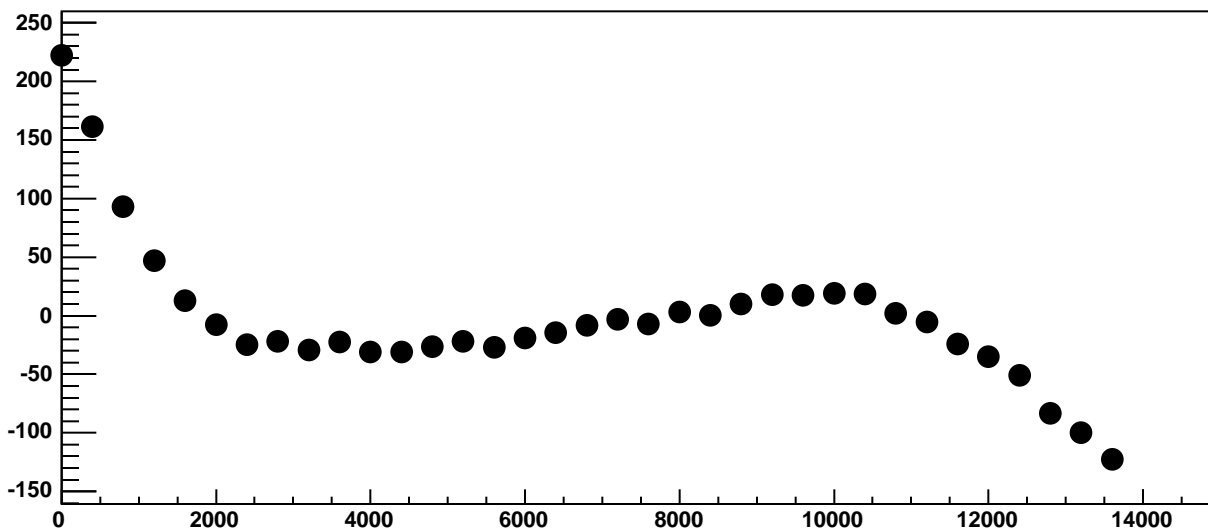
p1

0.1673 ± 0.0001939

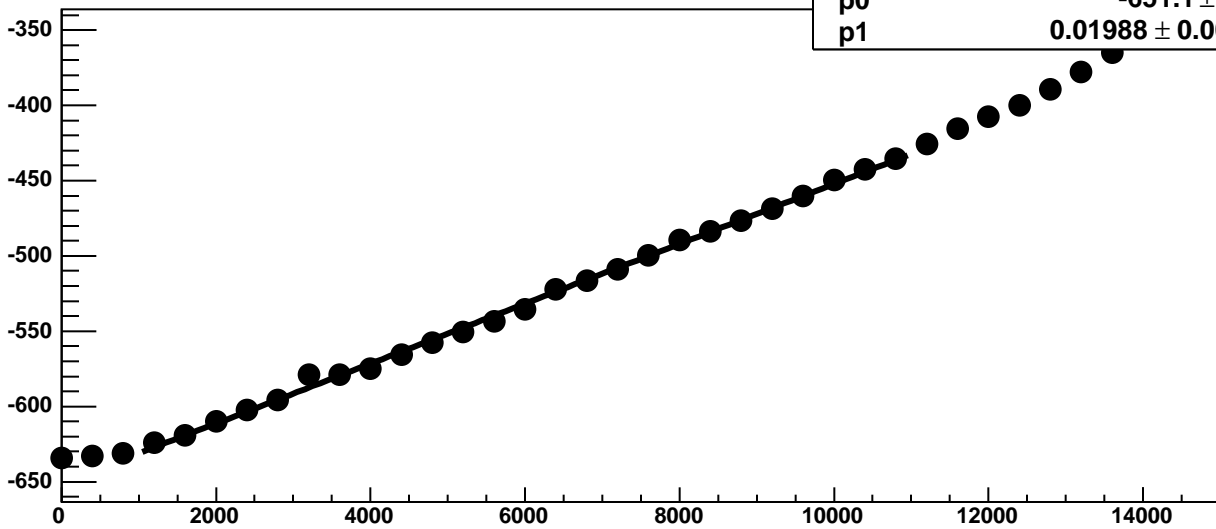
Chip 3, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

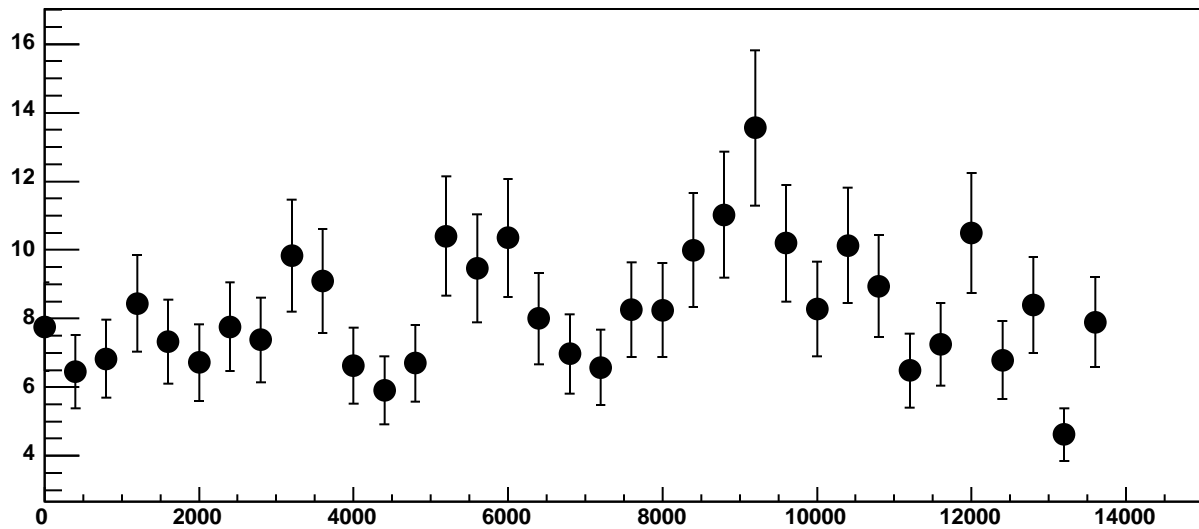


Chip 3, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

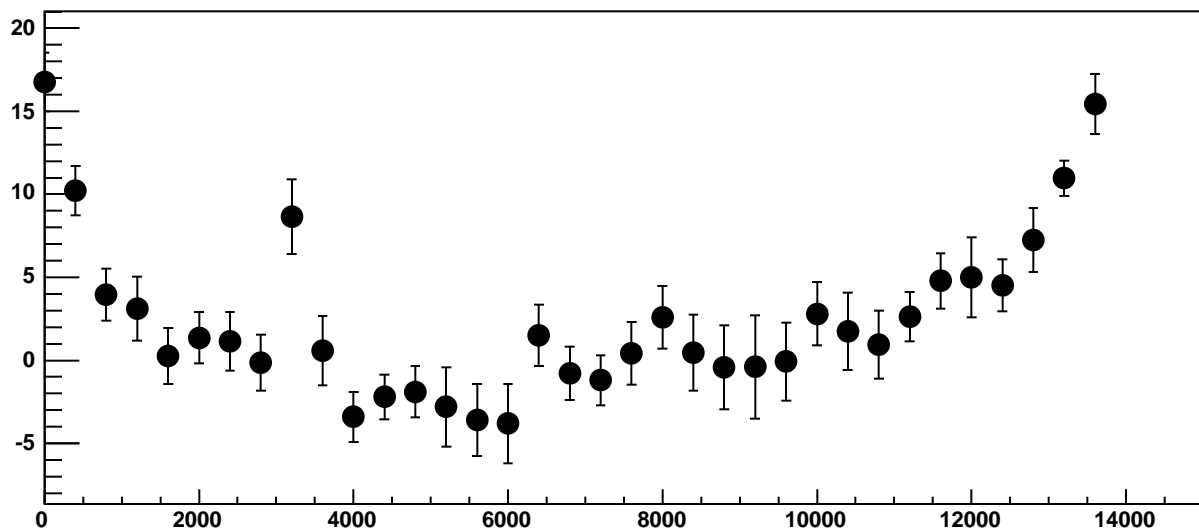


χ^2 / ndf 40.97 / 23
p0 -651.1 ± 0.8363
p1 0.01988 ± 0.0001355

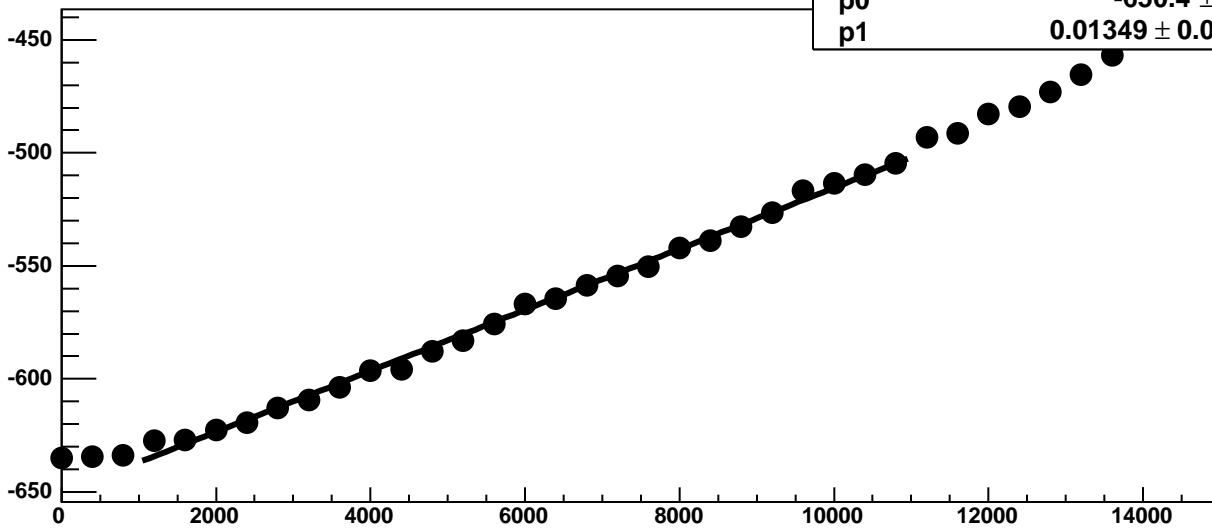
Chip 3, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



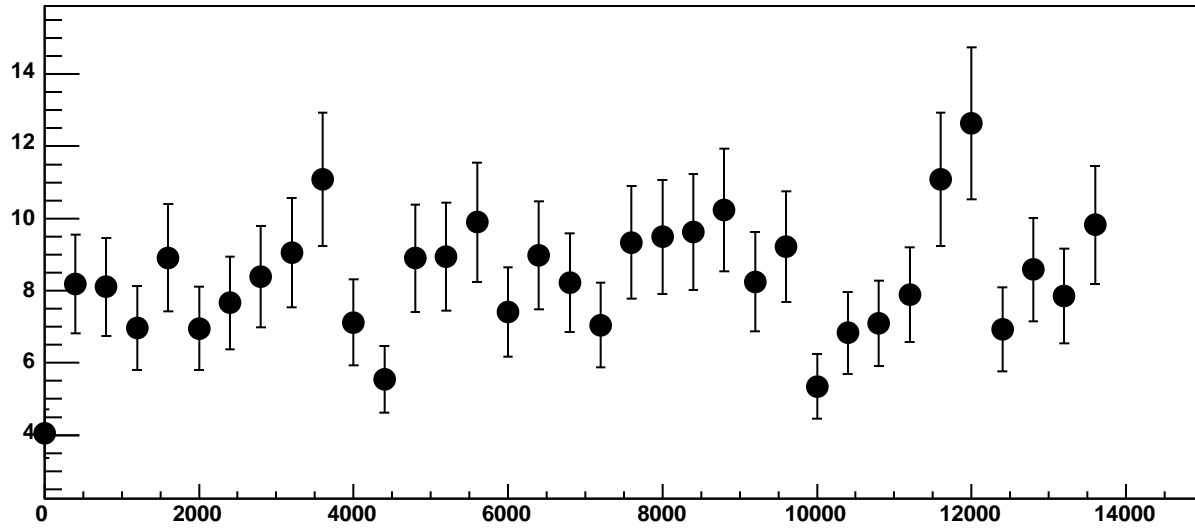
Chip 3, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC



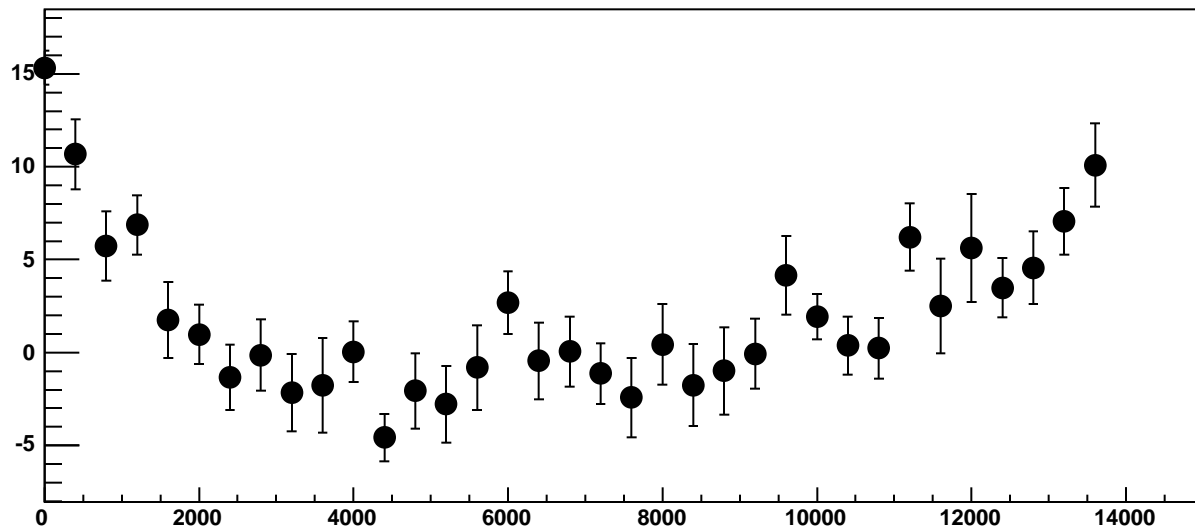
Chip 3, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



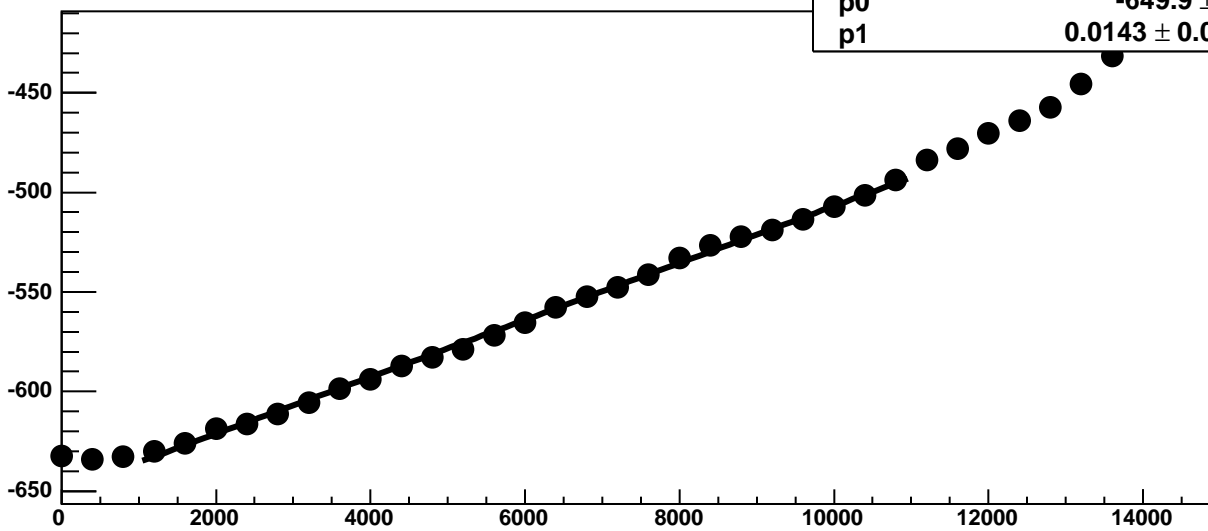
Chip 3, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

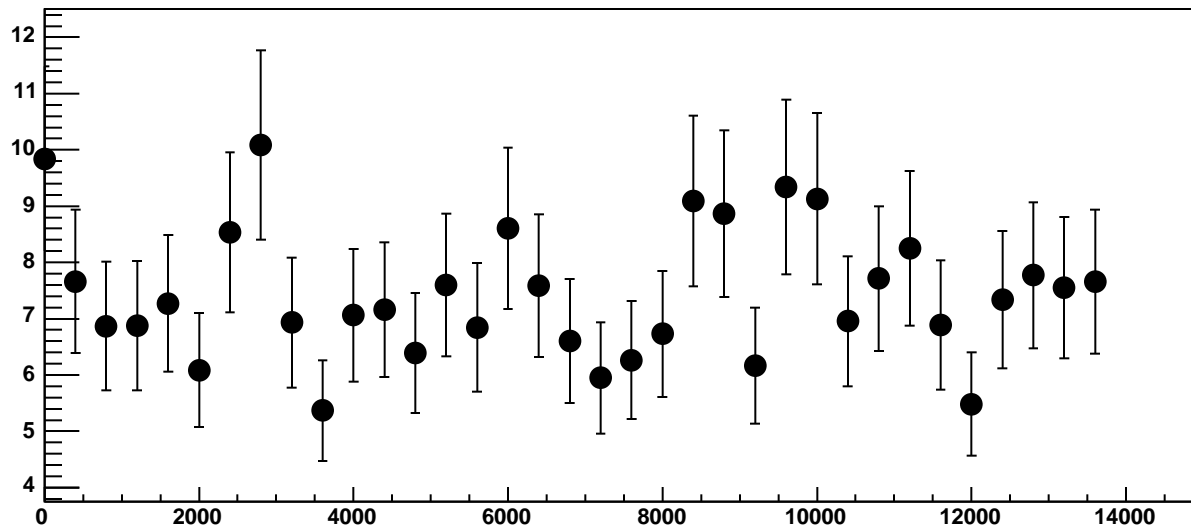


Chip 3, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

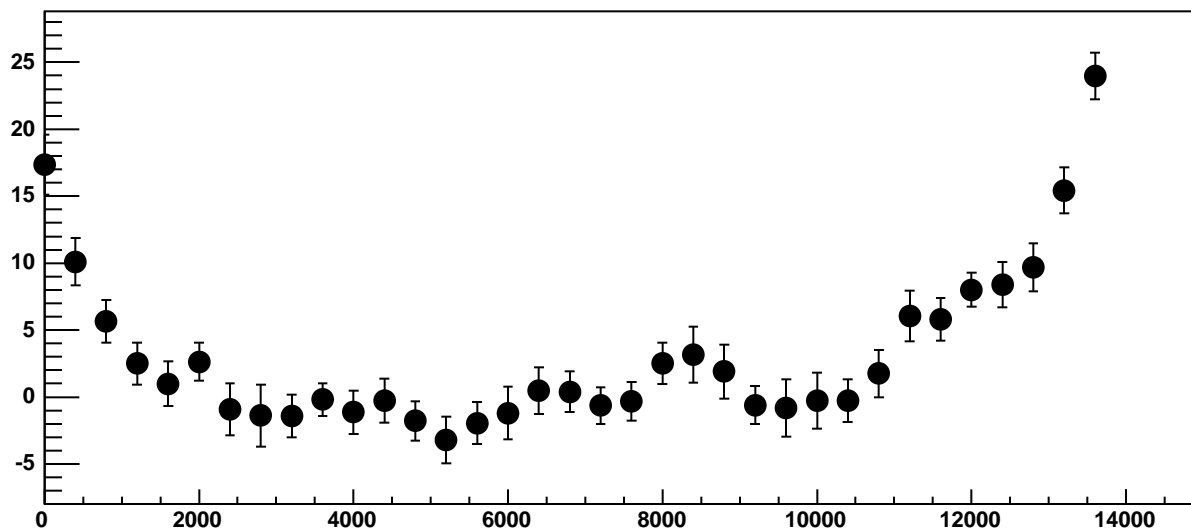


χ^2 / ndf 22.65 / 23
p0 -649.9 ± 0.7544
p1 0.0143 ± 0.0001166

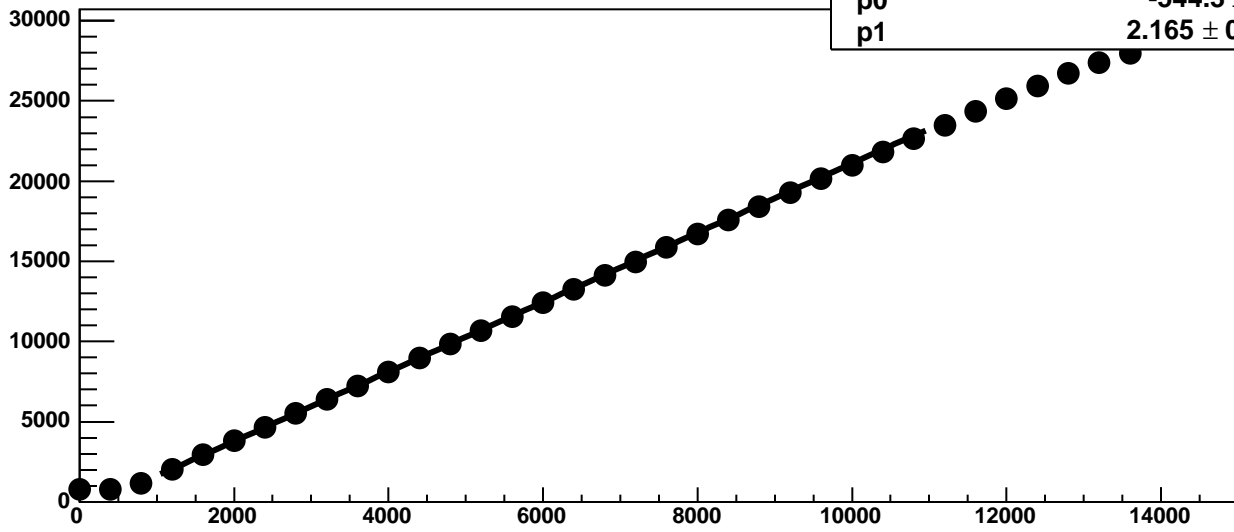
Chip 3, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

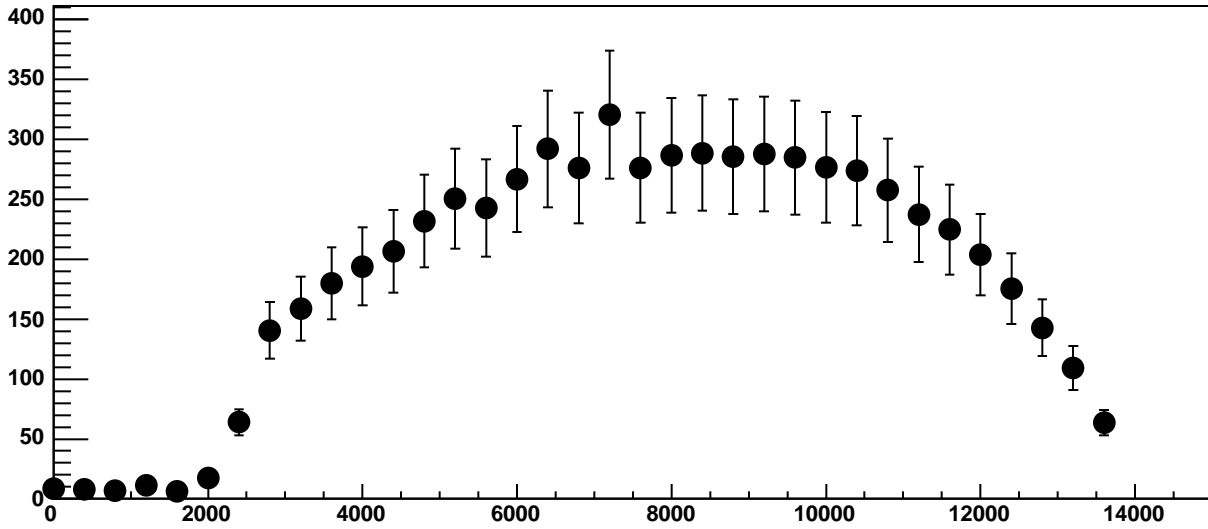


Chip 3, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC

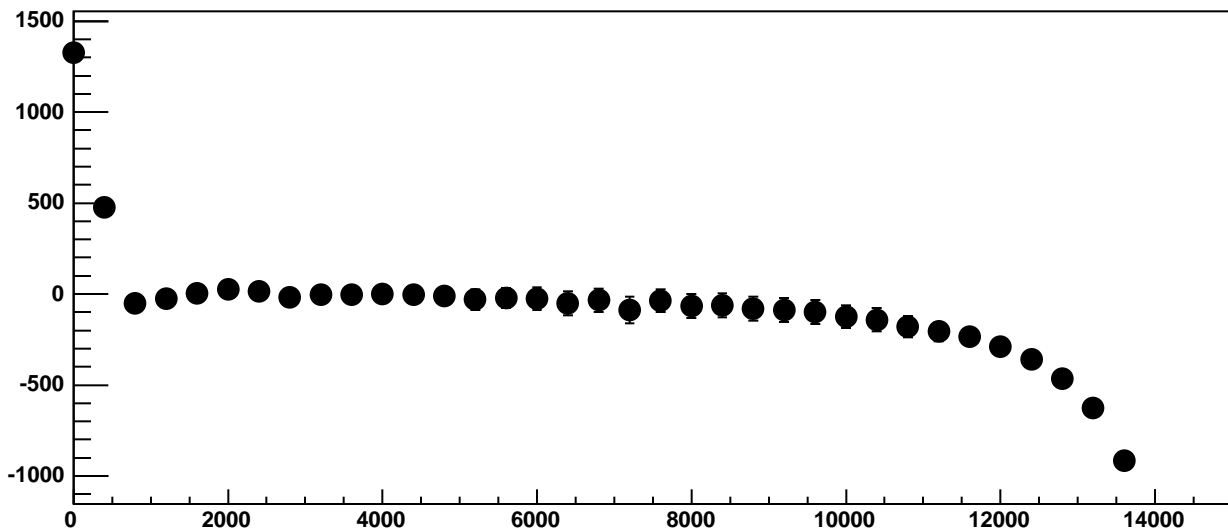


χ^2 / ndf	167.9 / 23
p0	-544.3 ± 3.668
p1	2.165 ± 0.00216

Chip 3, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC

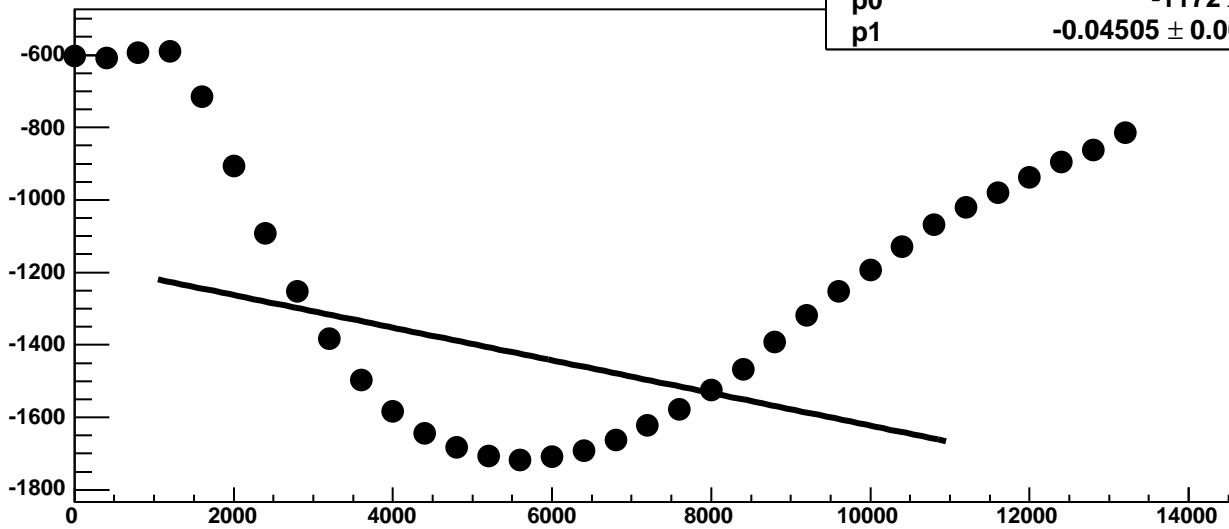


Chip 3, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC

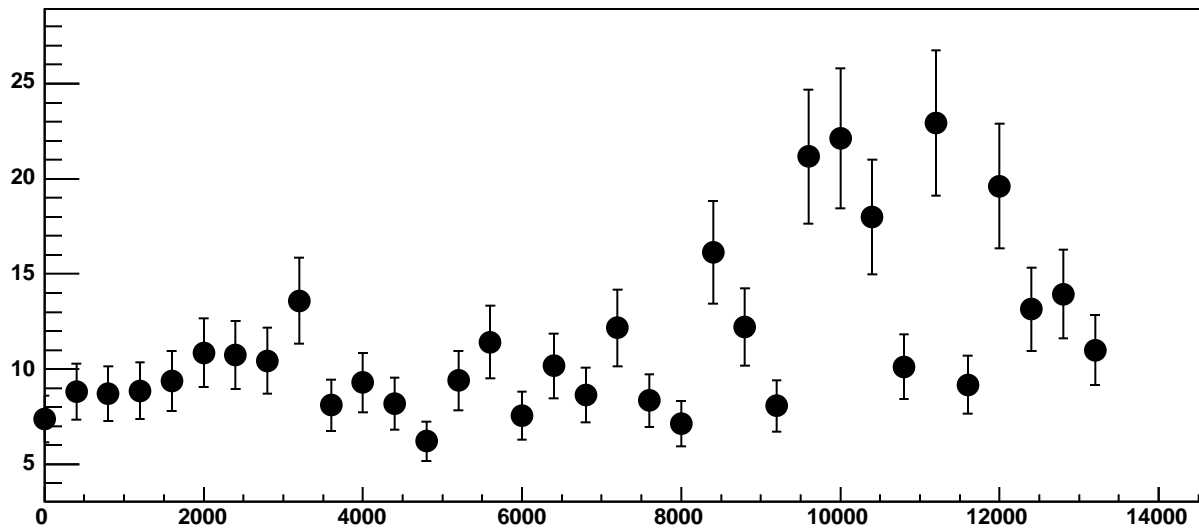


Chip 3, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

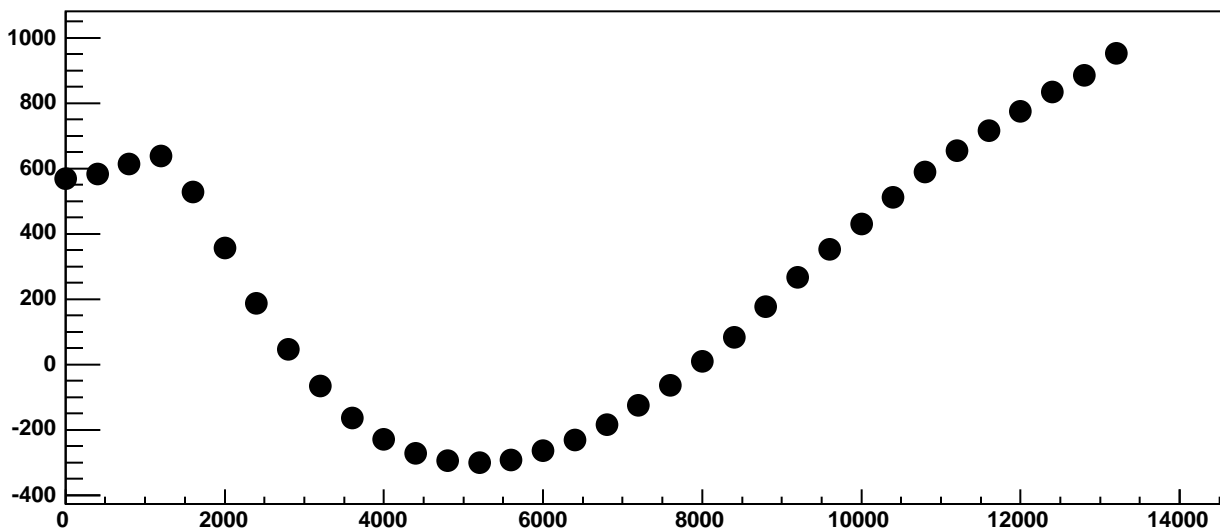
χ^2 / ndf 4.635e+05 / 23
p0 -1172 ± 1.058
p1 -0.04505 ± 0.0001717



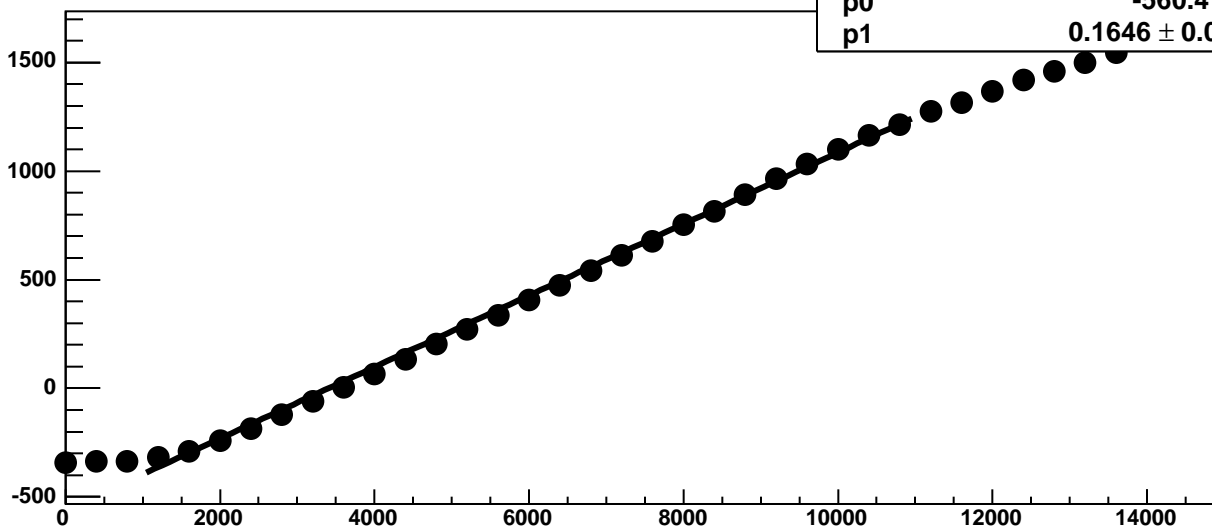
Chip 3, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



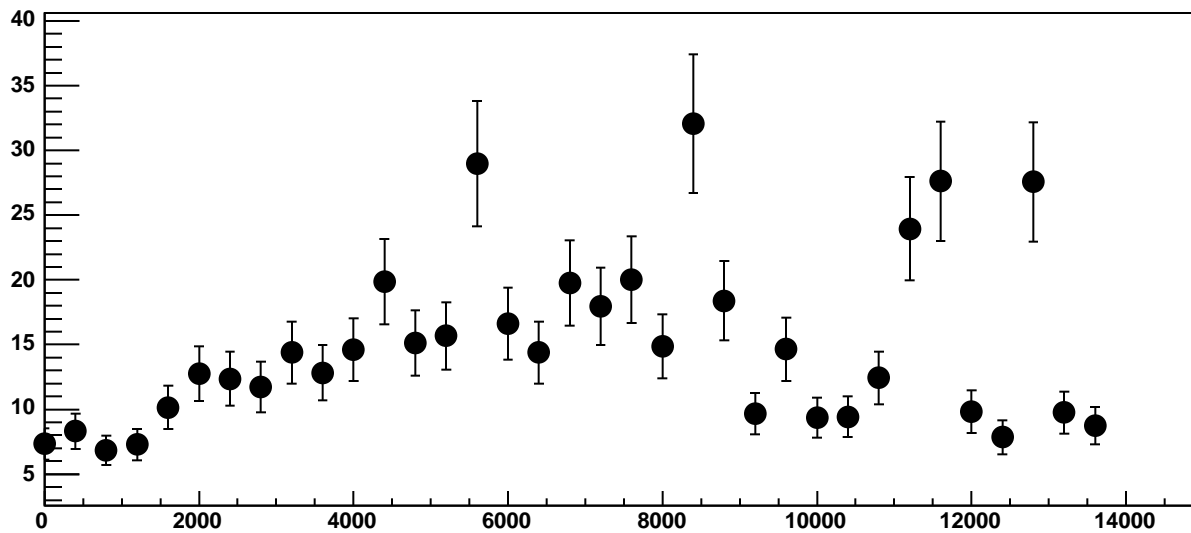
Chip 3, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



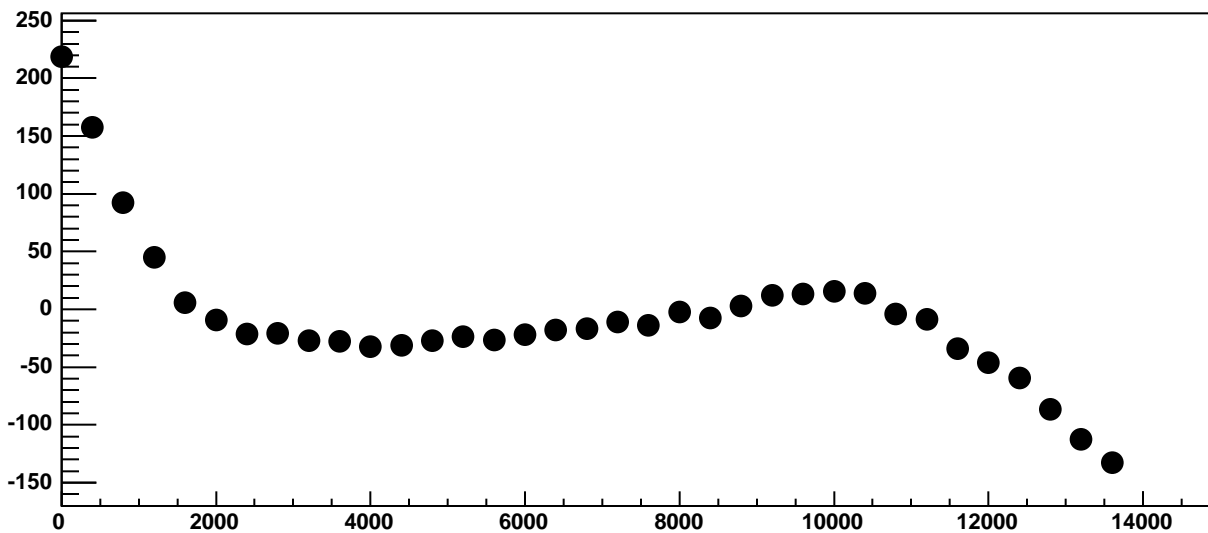
Chip 3, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



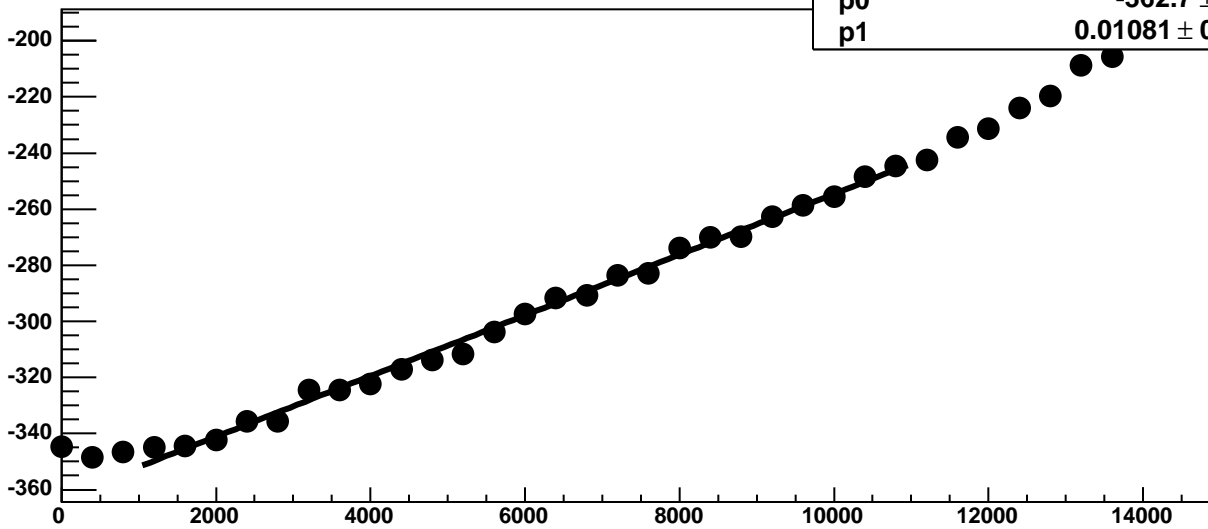
Chip 3, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

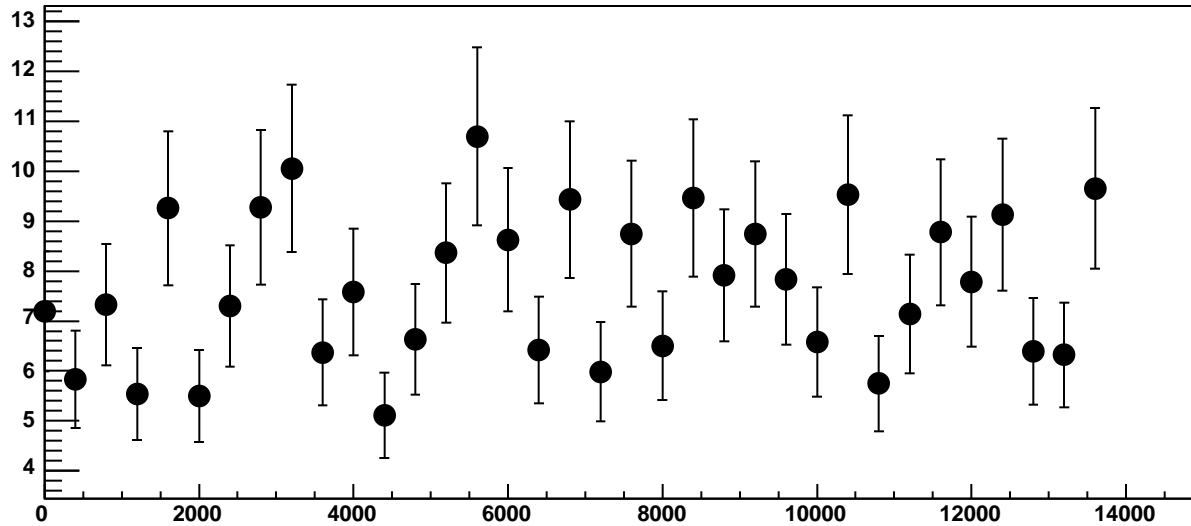


Chip 3, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

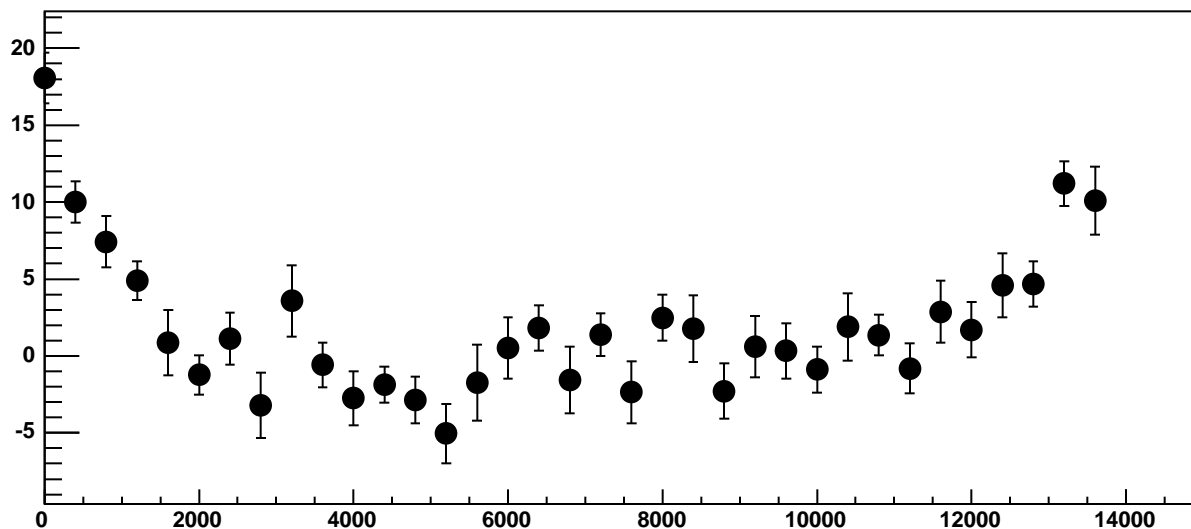


χ^2 / ndf 49.02 / 23
p0 -362.7 ± 0.7249
p1 0.01081 ± 0.000111

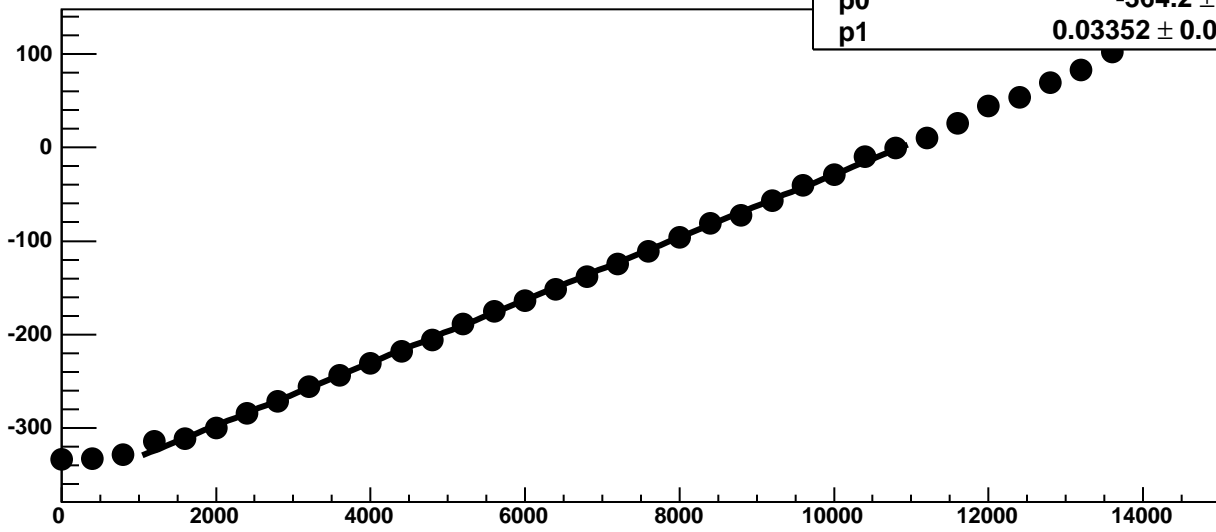
Chip 3, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

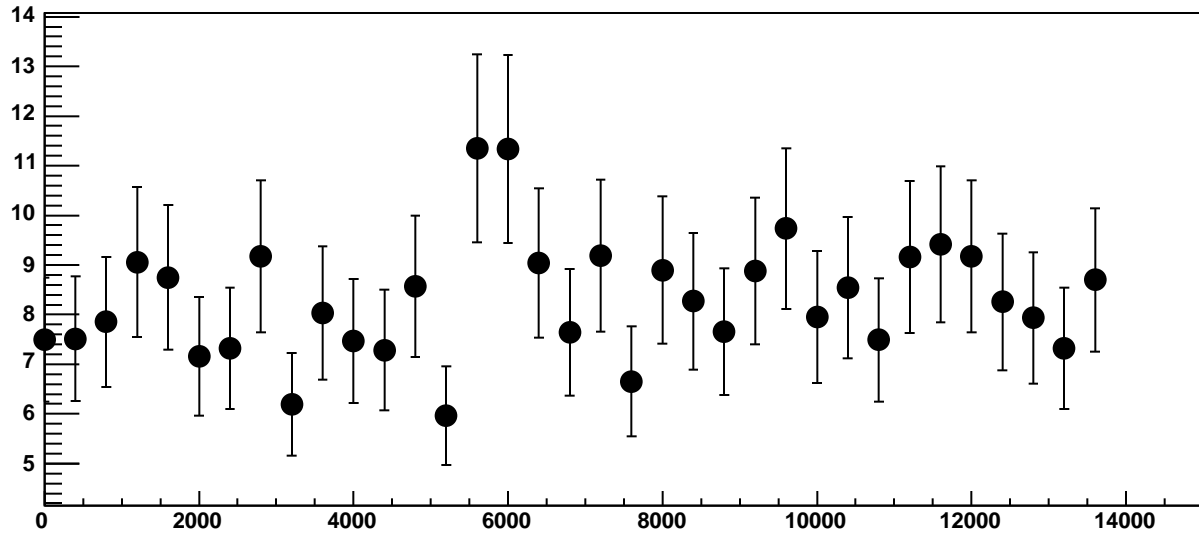


Chip 3, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC

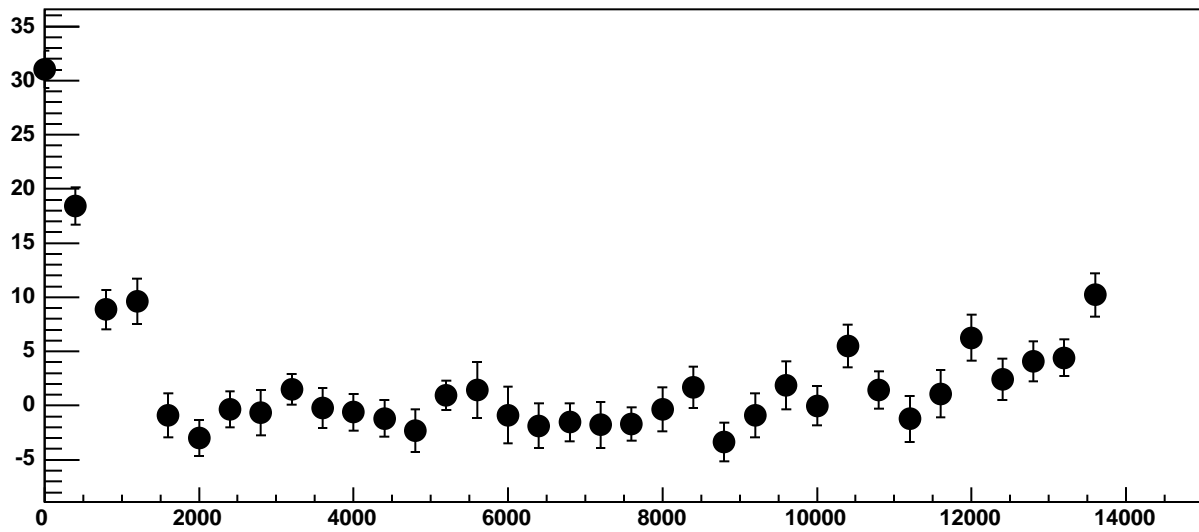


χ^2 / ndf 46.56 / 23
p0 -364.2 ± 0.8365
p1 0.03352 ± 0.0001281

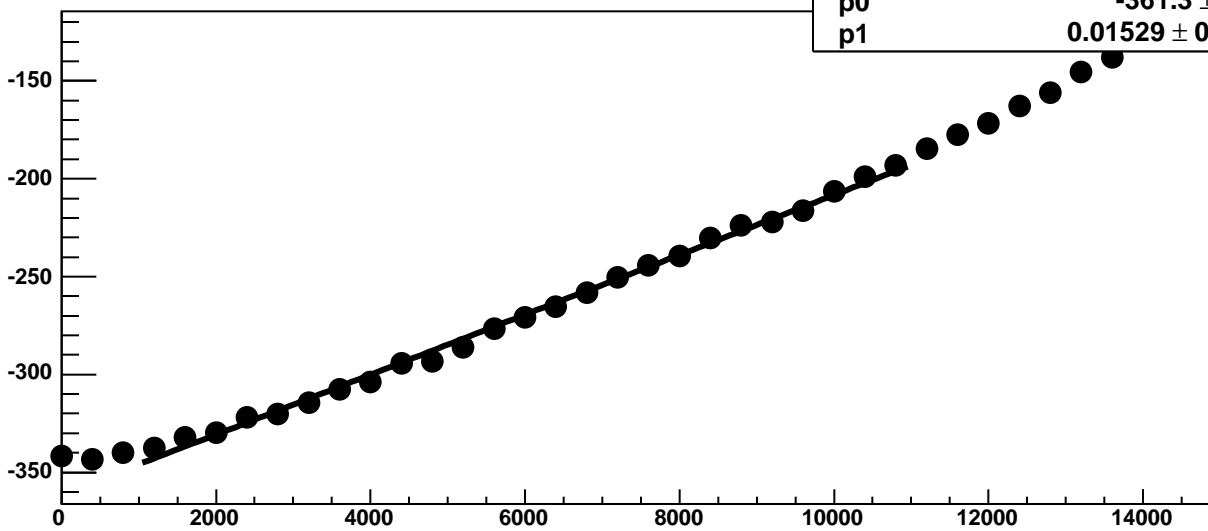
Chip 3, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC

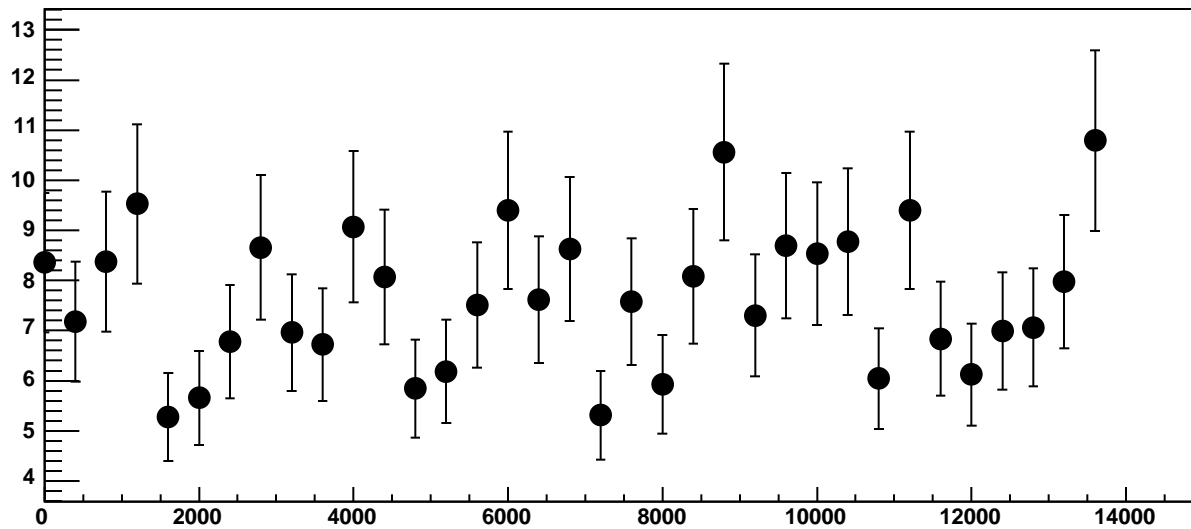


Chip 3, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

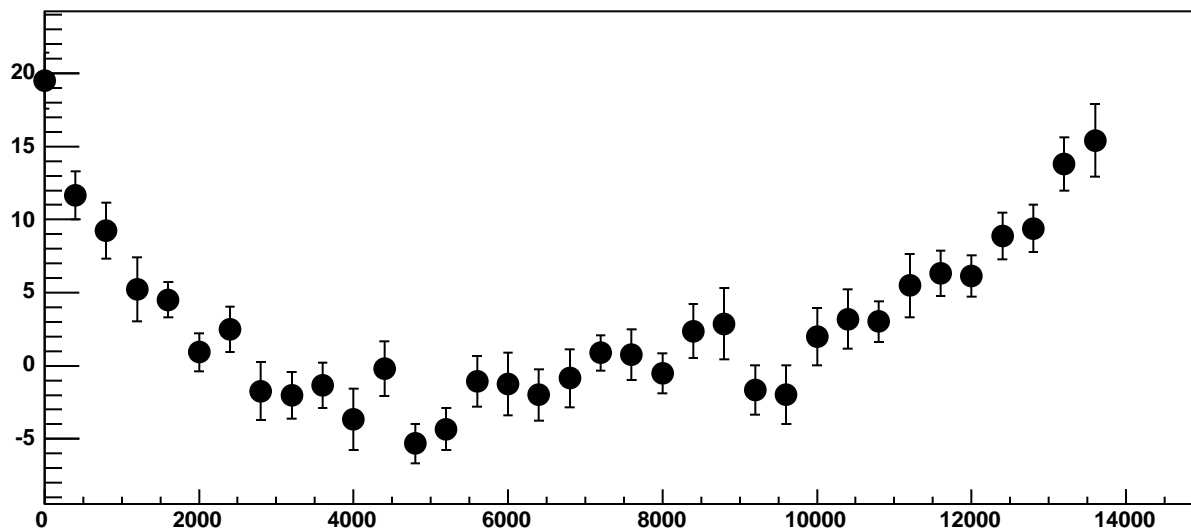


χ^2 / ndf 70.31 / 23
p0 -361.3 ± 0.7279
p1 0.01529 ± 0.000113

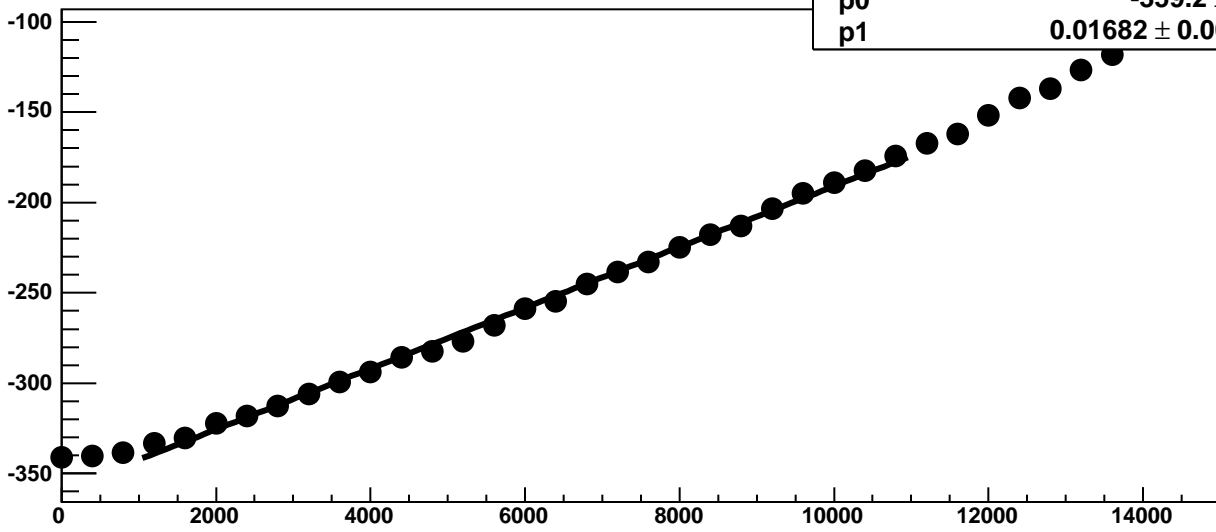
Chip 3, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

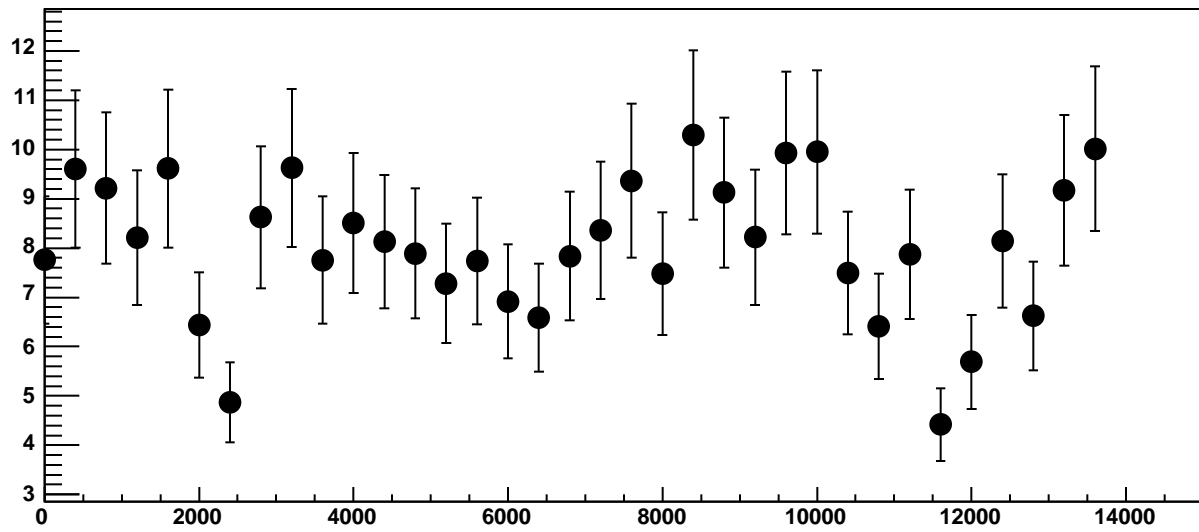


Chip 3, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

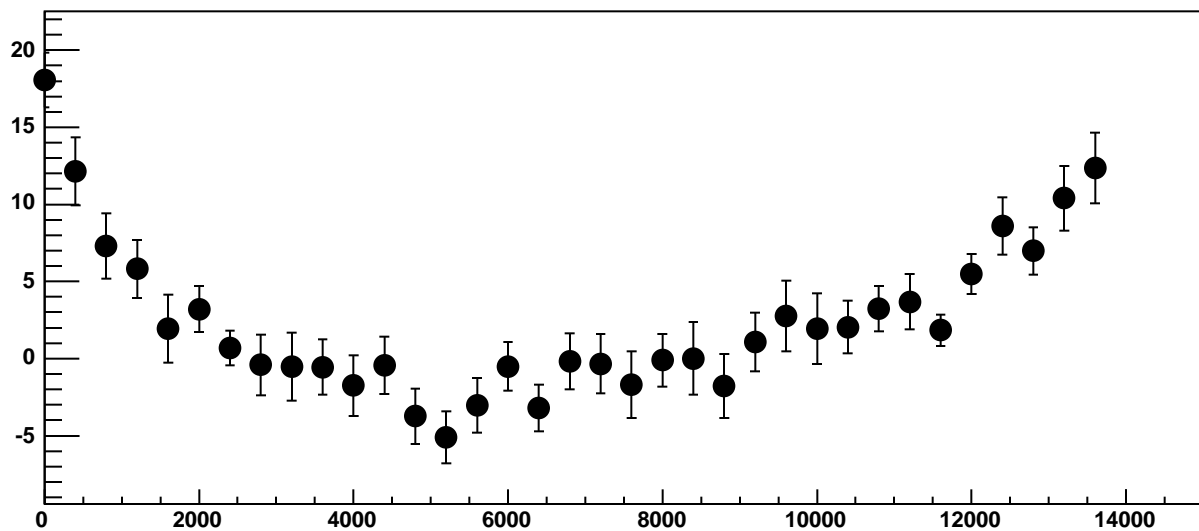


χ^2 / ndf 47.8 / 23
p0 -359.2 ± 0.786
p1 0.01682 ± 0.0001219

Chip 3, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

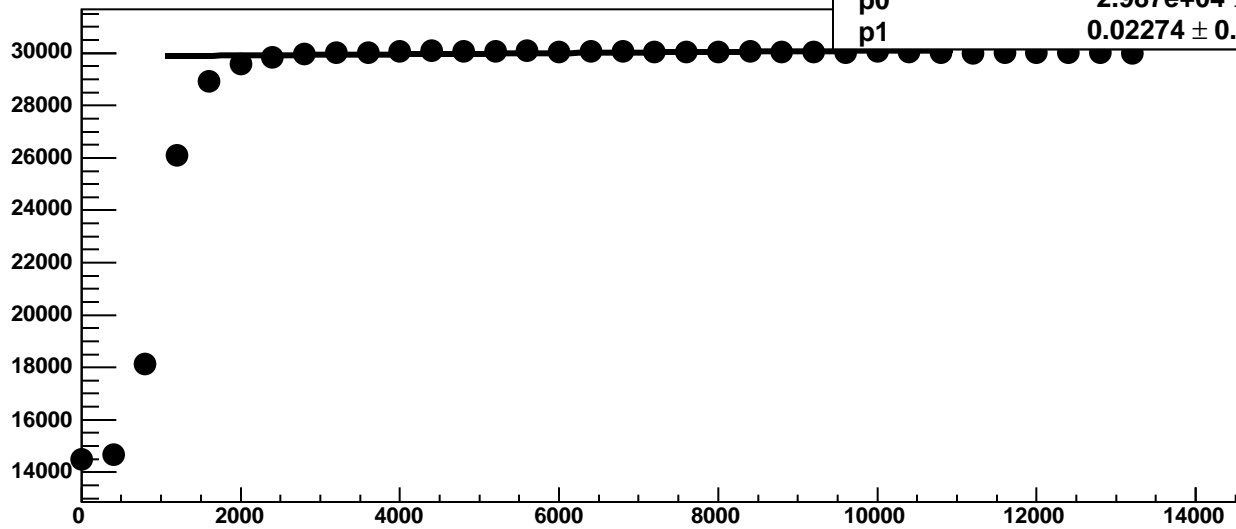
6993 / 23

p0

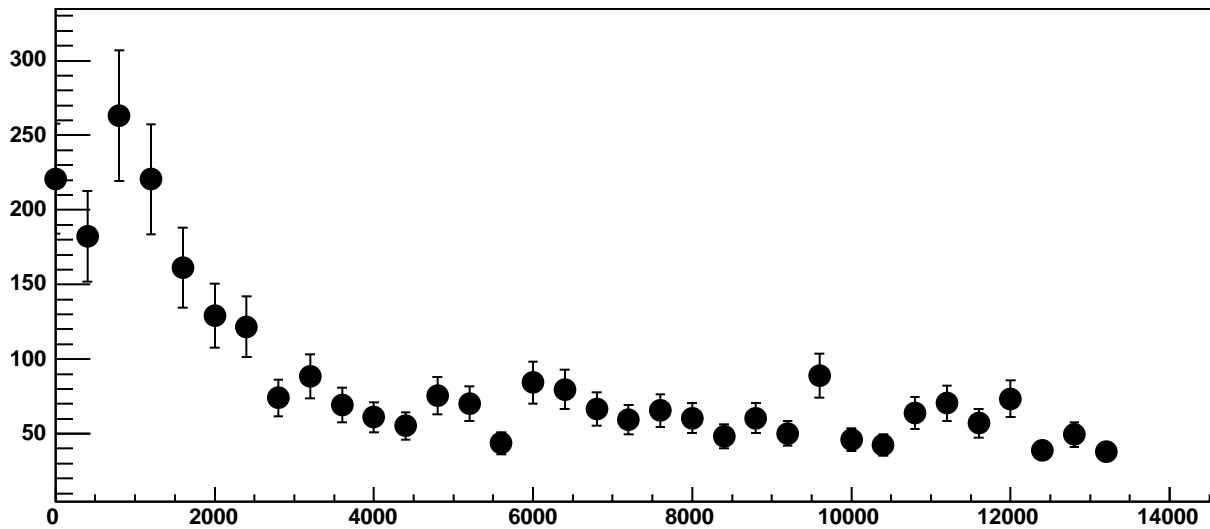
$2.987\text{e}+04 \pm 8.813$

p1

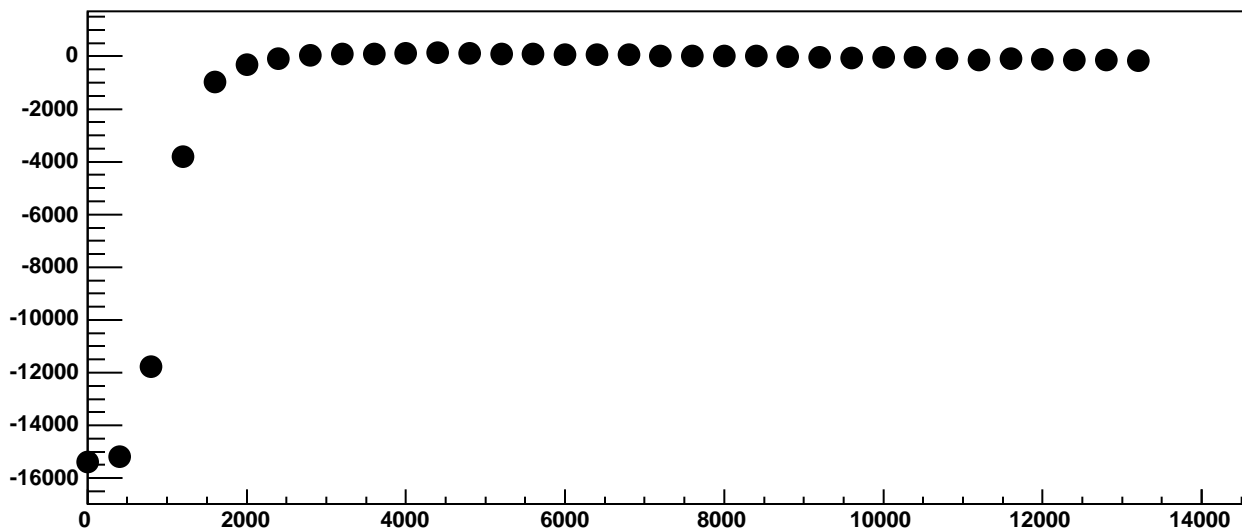
0.02274 ± 0.001167



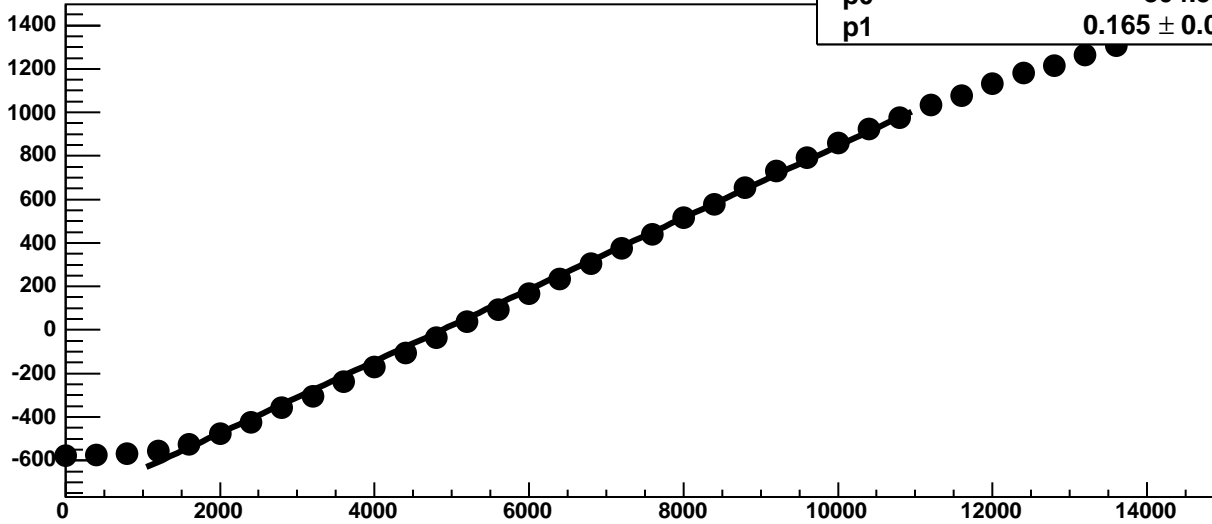
Chip 3, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC



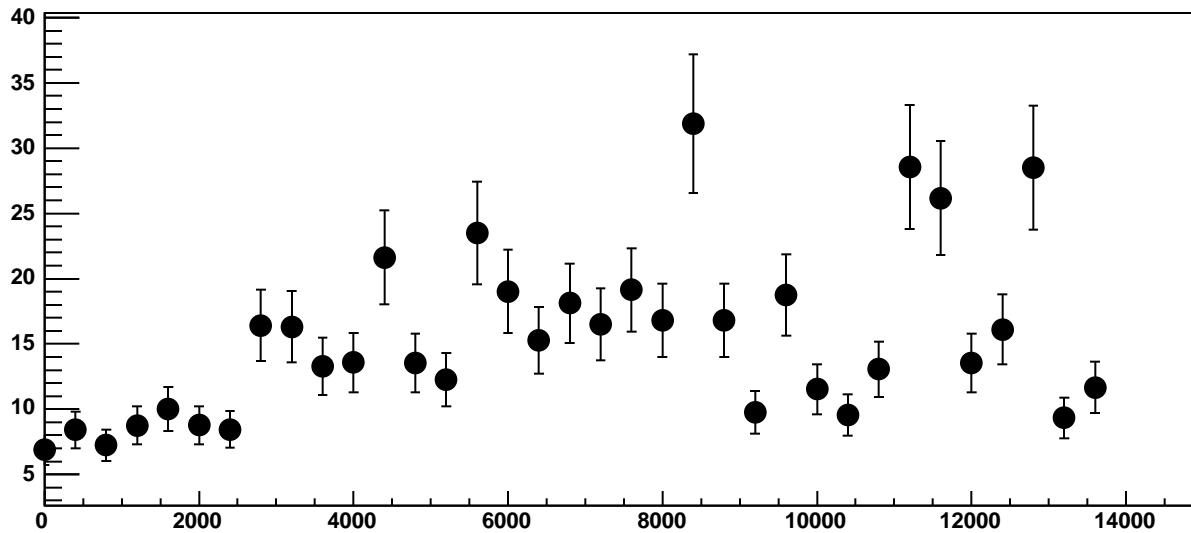
Chip 3, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC



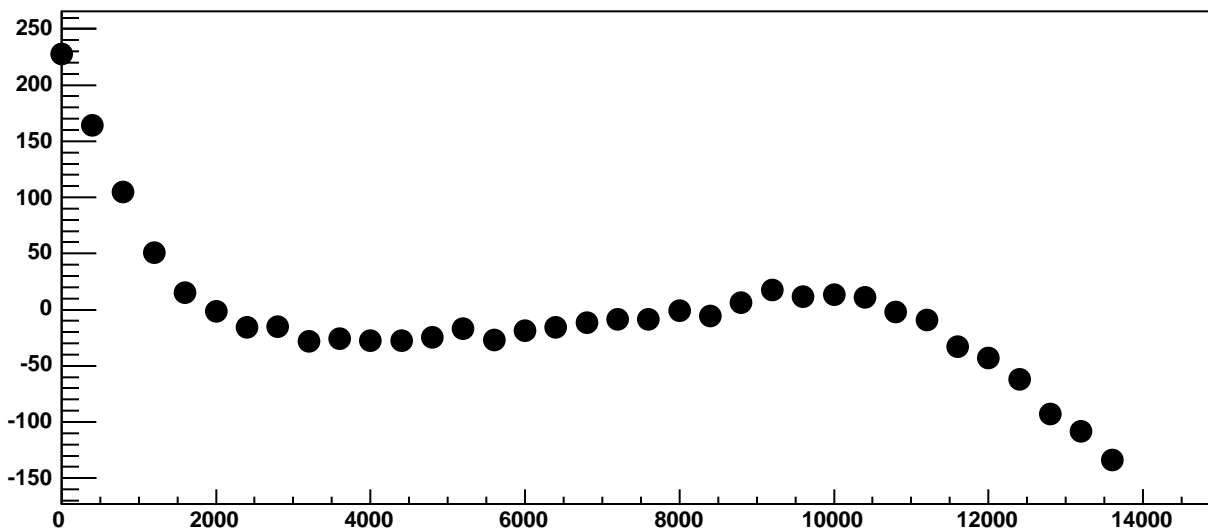
Chip 3, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



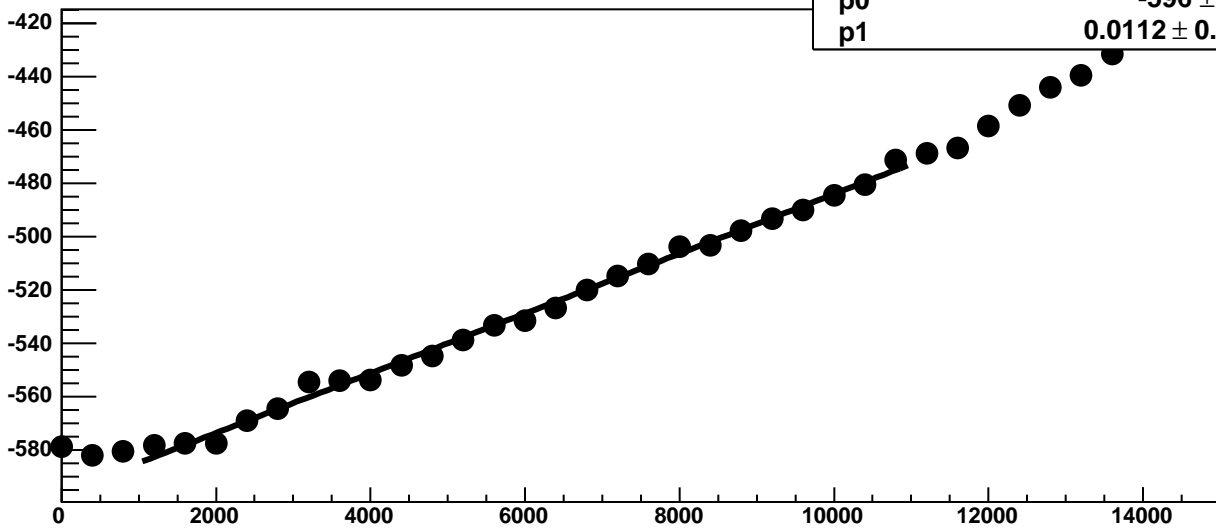
Chip 3, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



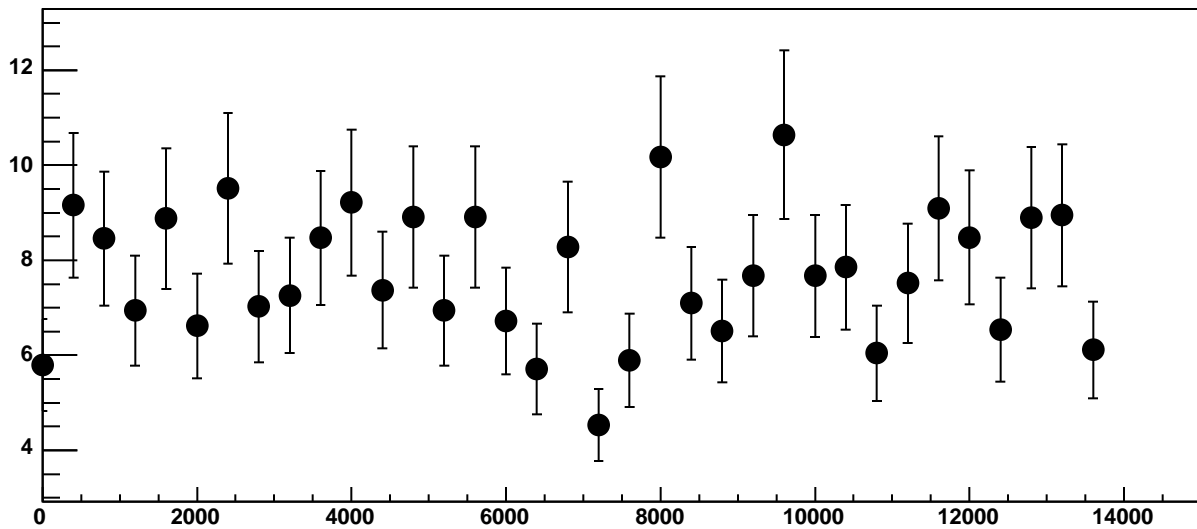
Chip 3, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



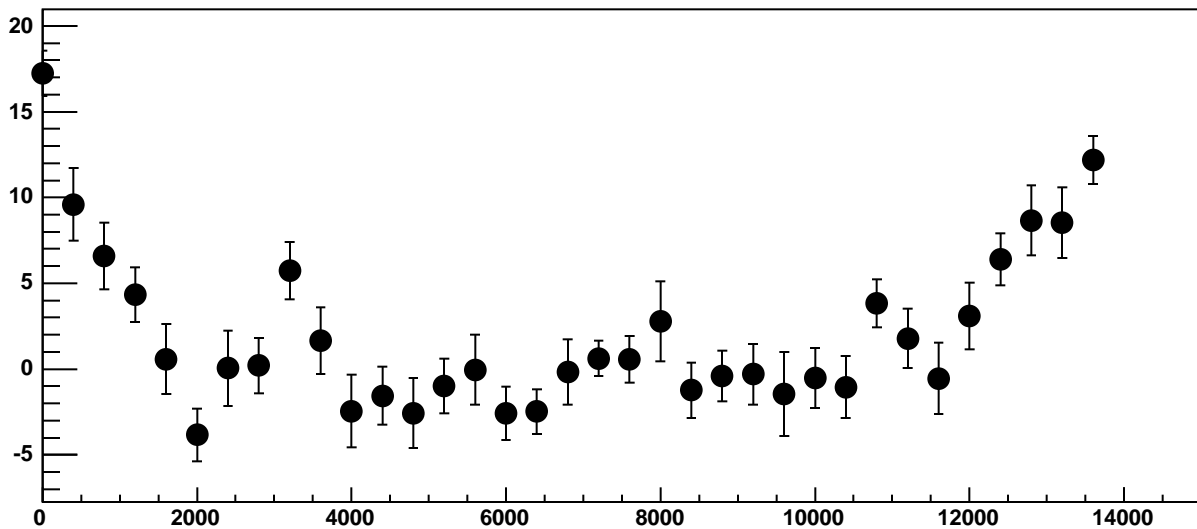
Chip 3, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



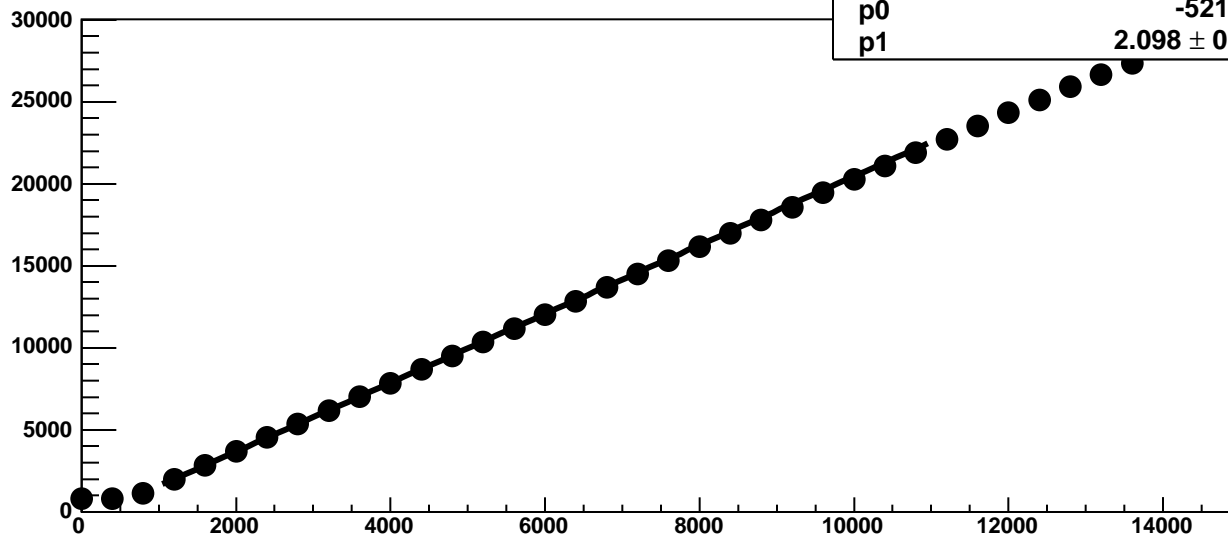
Chip 3, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



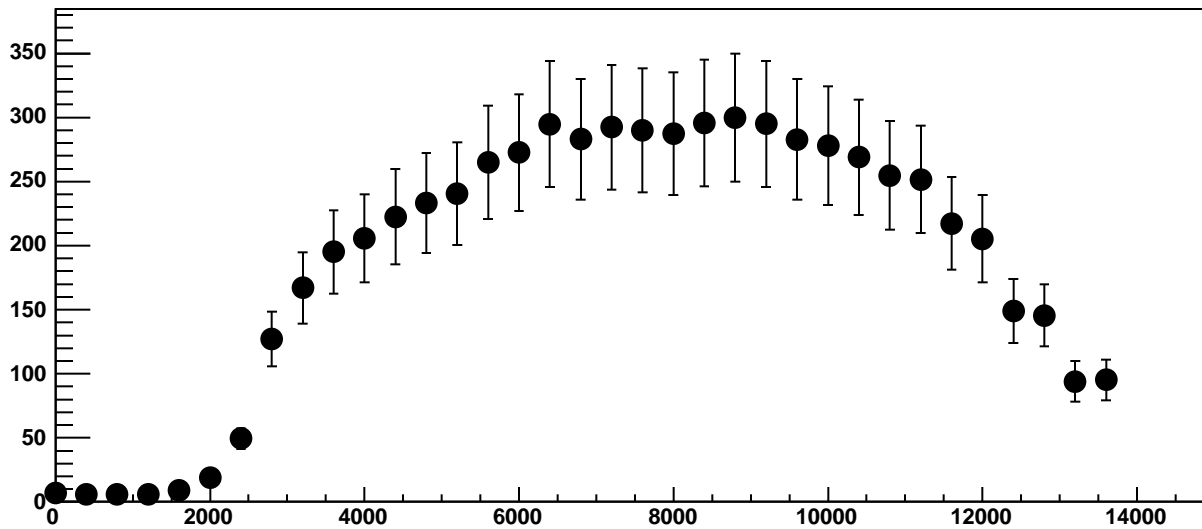
Chip 3, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



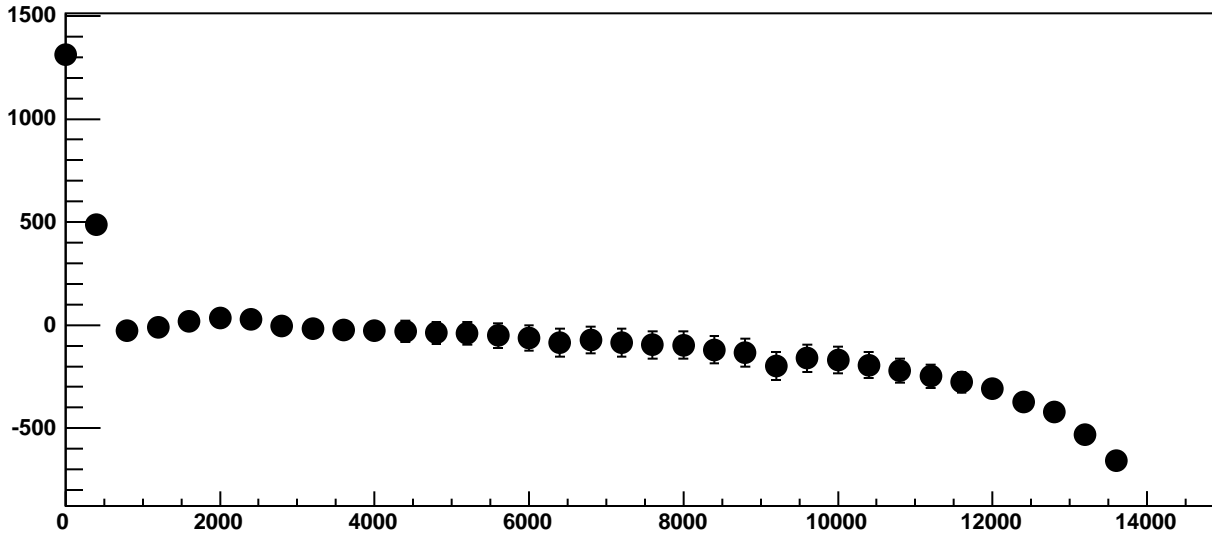
Chip 3, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC



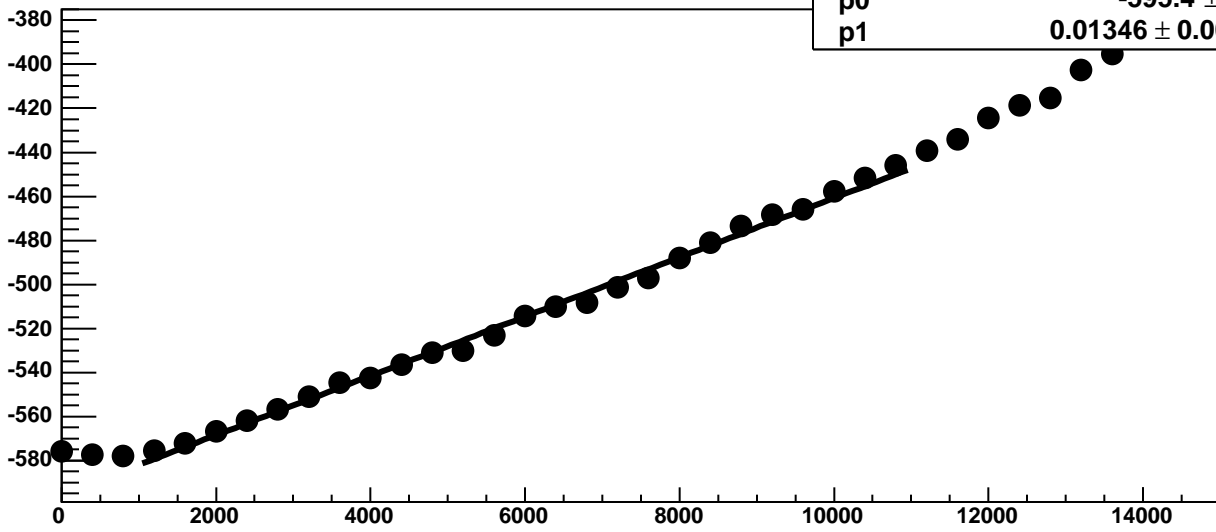
Chip 3, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC

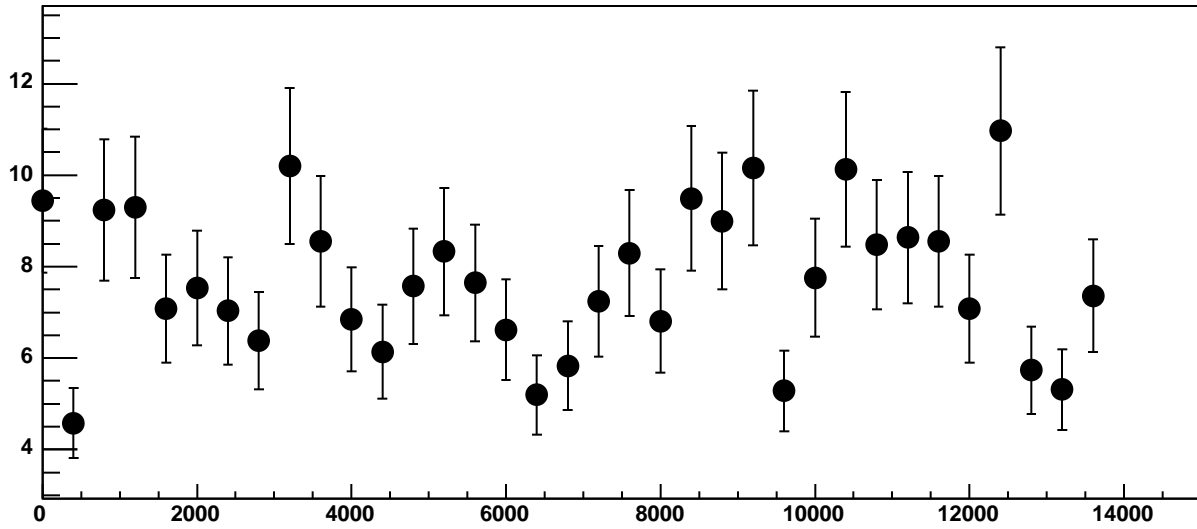


Chip 3, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

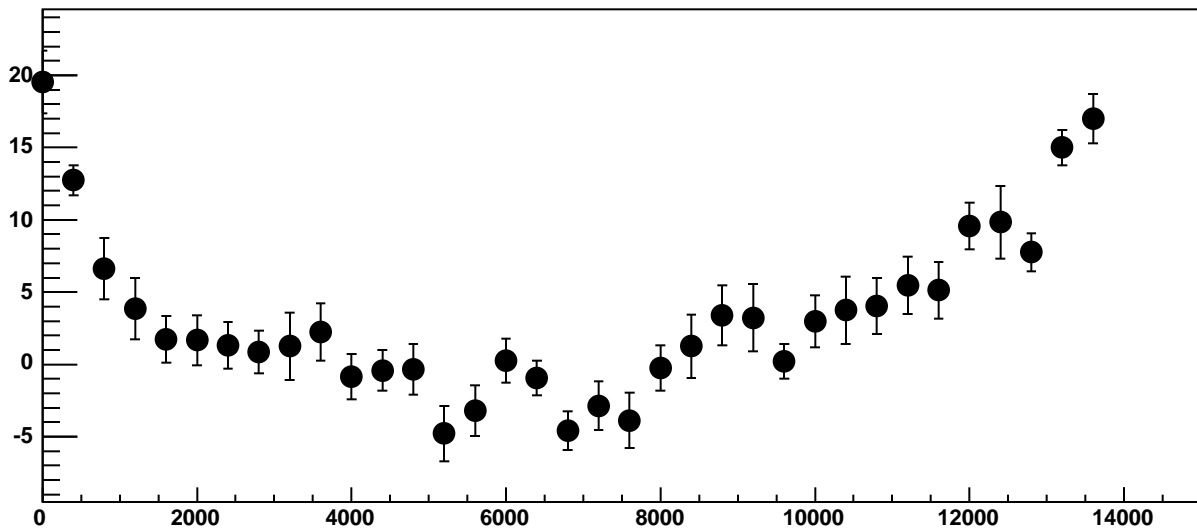


χ^2 / ndf 52.14 / 23
p0 -595.4 ± 0.8061
p1 0.01346 ± 0.0001232

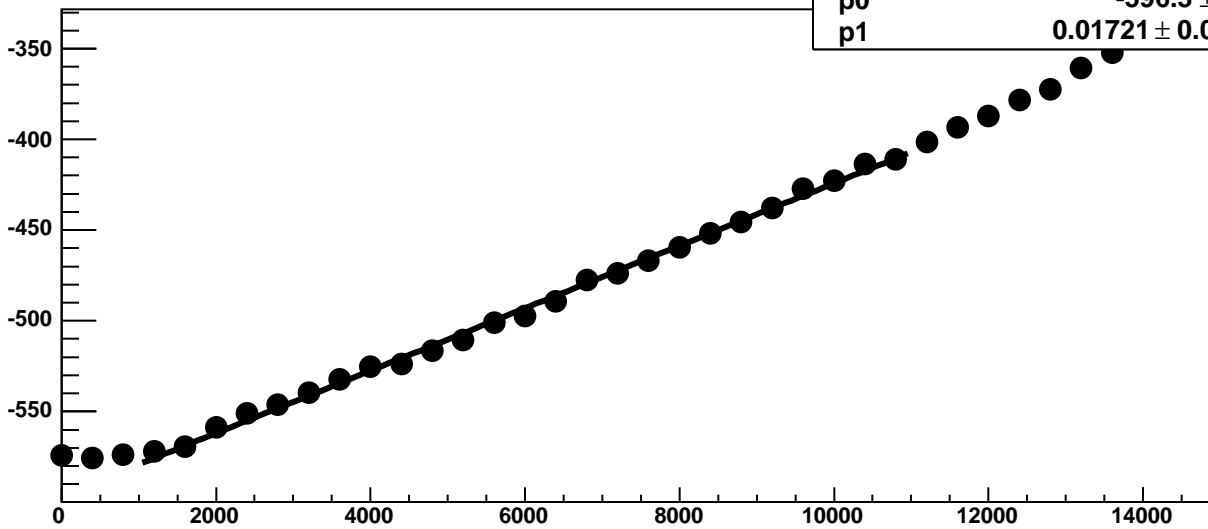
Chip 3, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

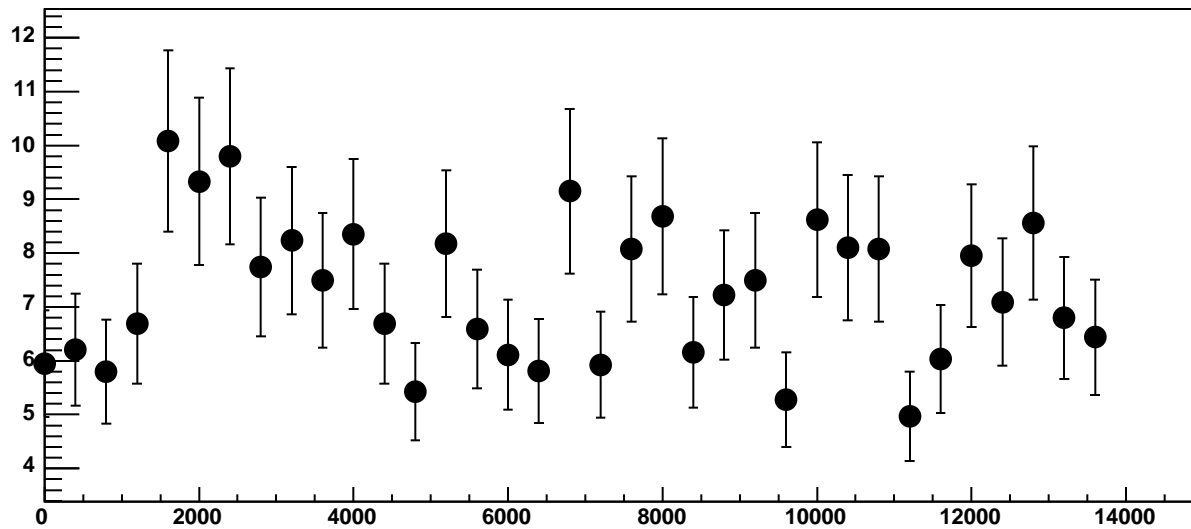


Chip 3, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

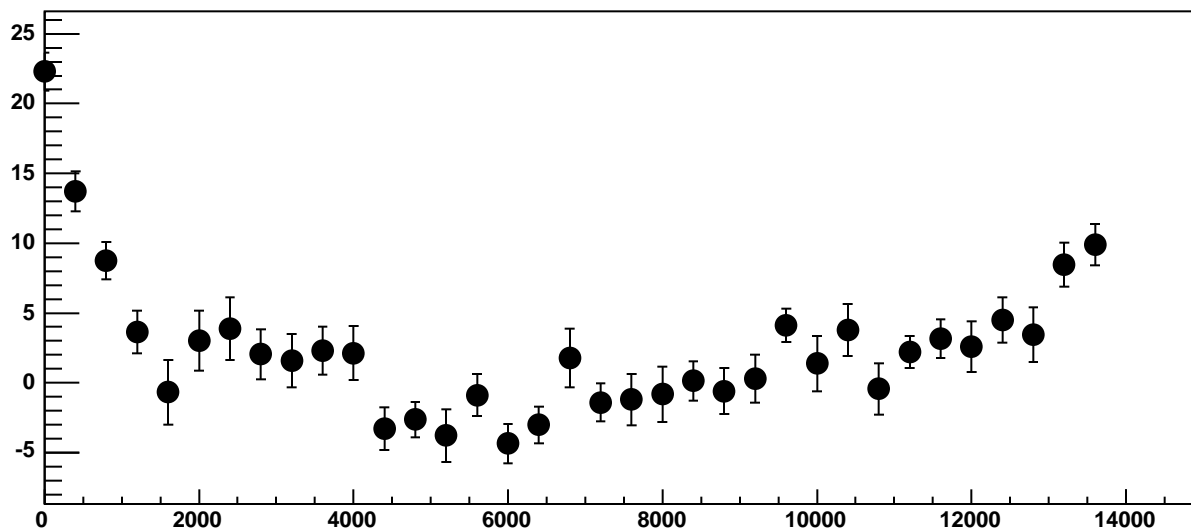


χ^2 / ndf 62.66 / 23
p0 -596.3 ± 0.8292
p1 0.01721 ± 0.0001222

Chip 3, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

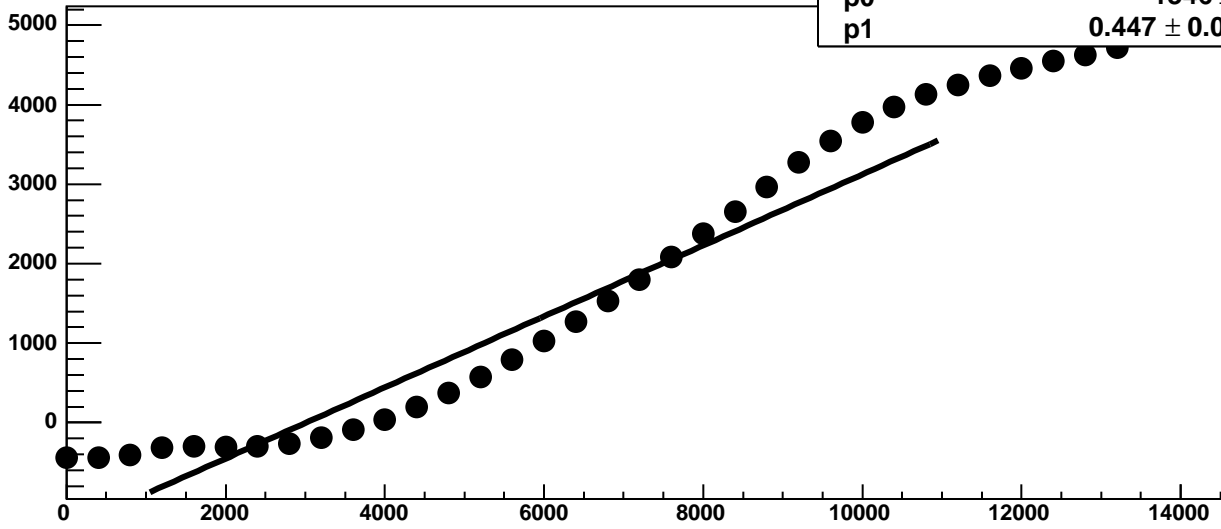


Chip 3, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

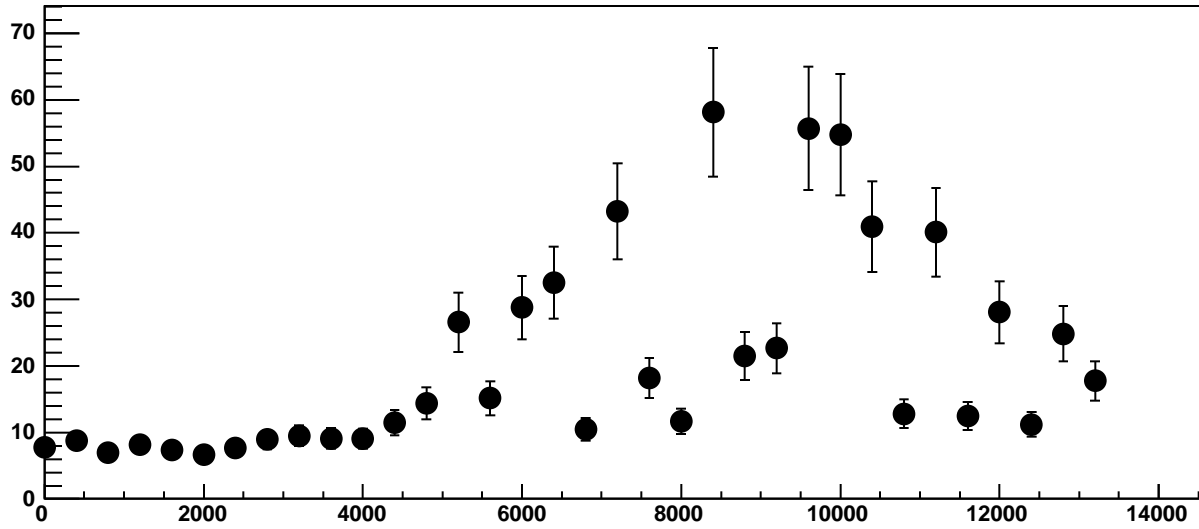


Chip 3, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC

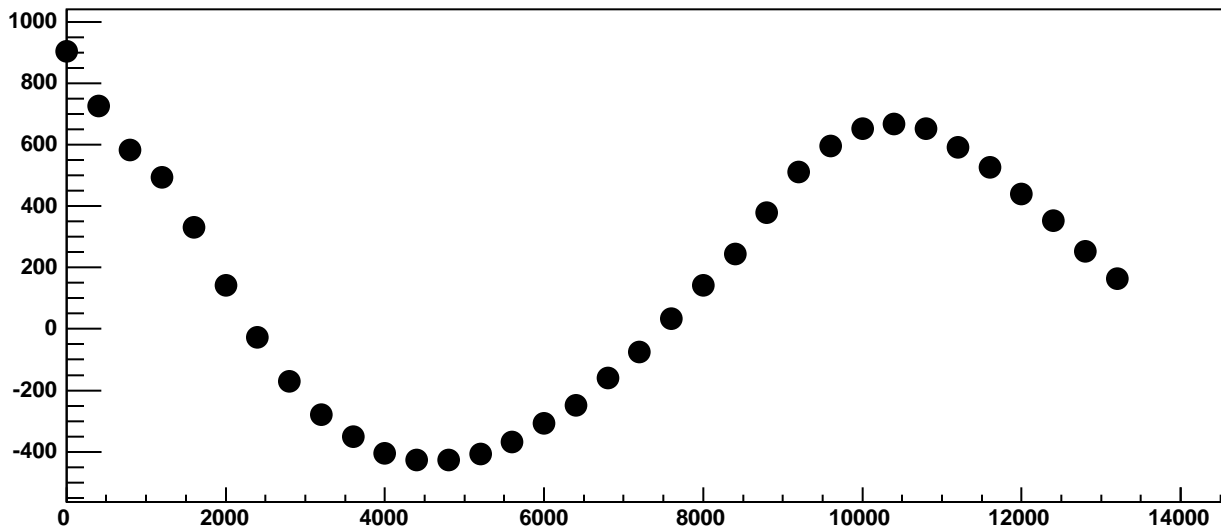
χ^2 / ndf 3.504e+05 / 23
p0 -1346 ± 1.012
p1 0.447 ± 0.0002211



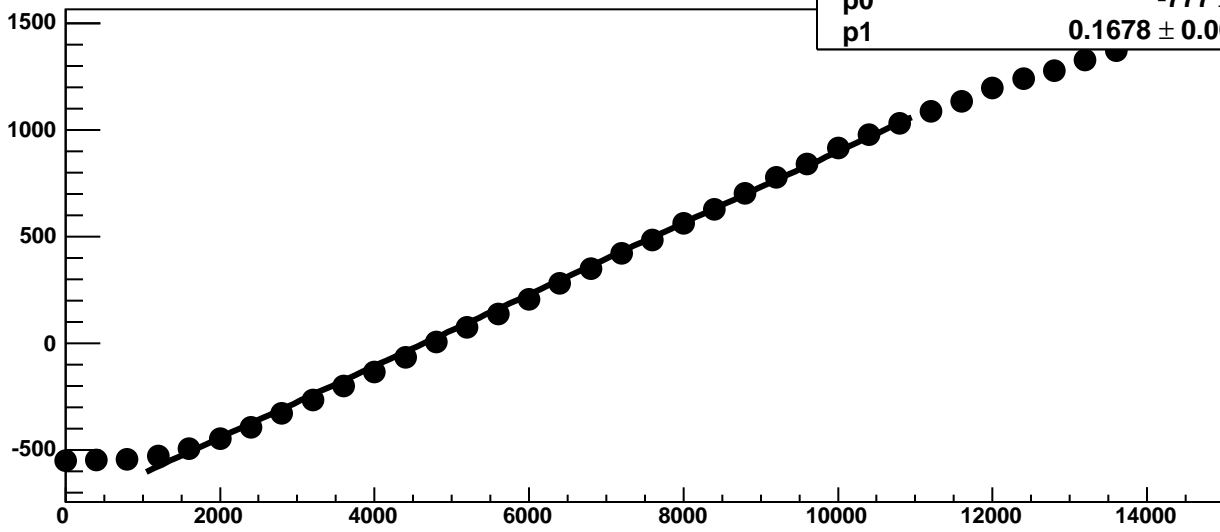
Chip 3, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



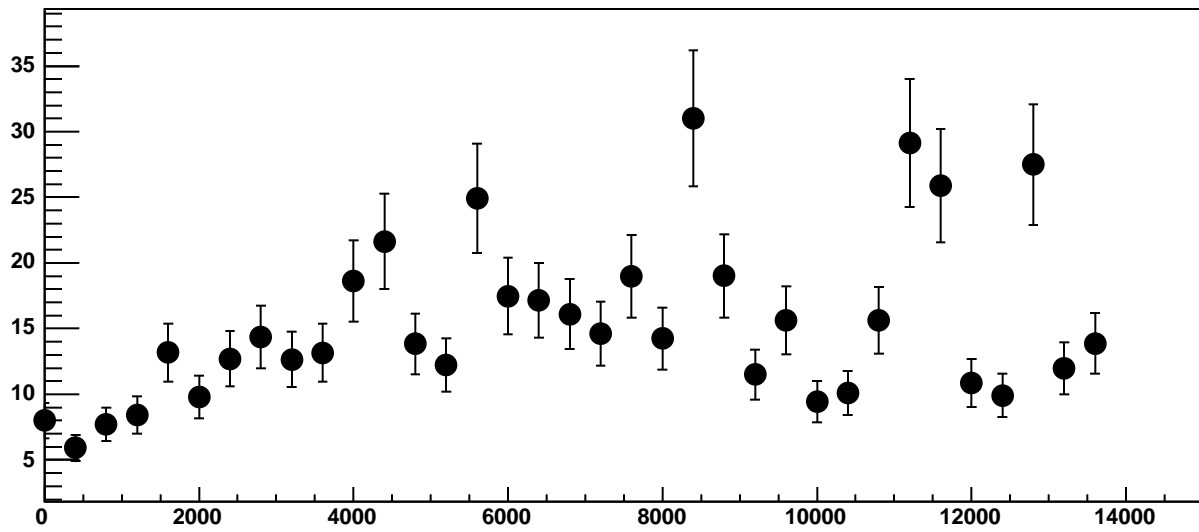
Chip 3, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC



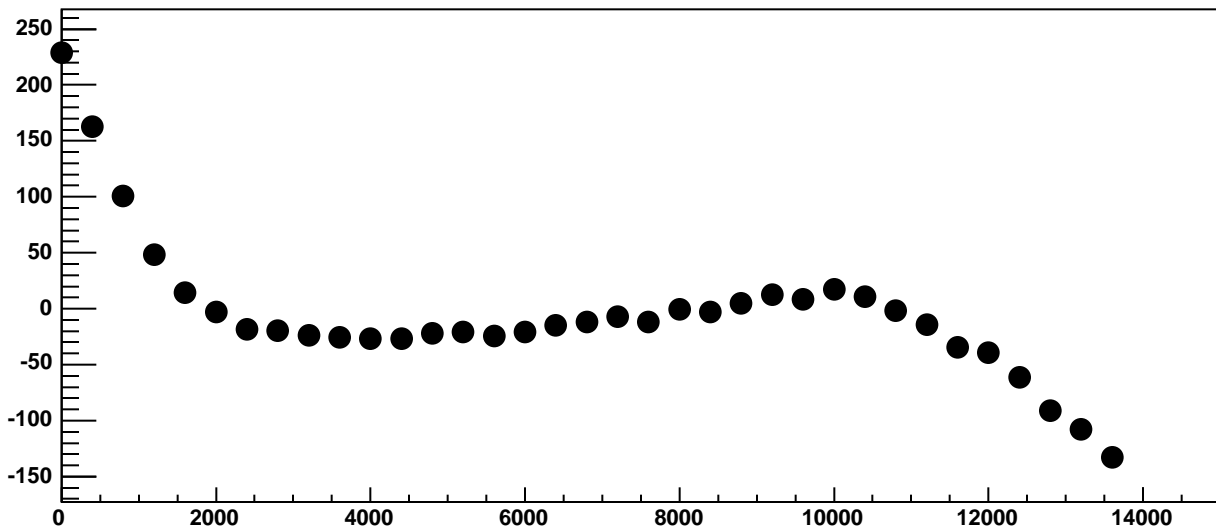
Chip 3, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



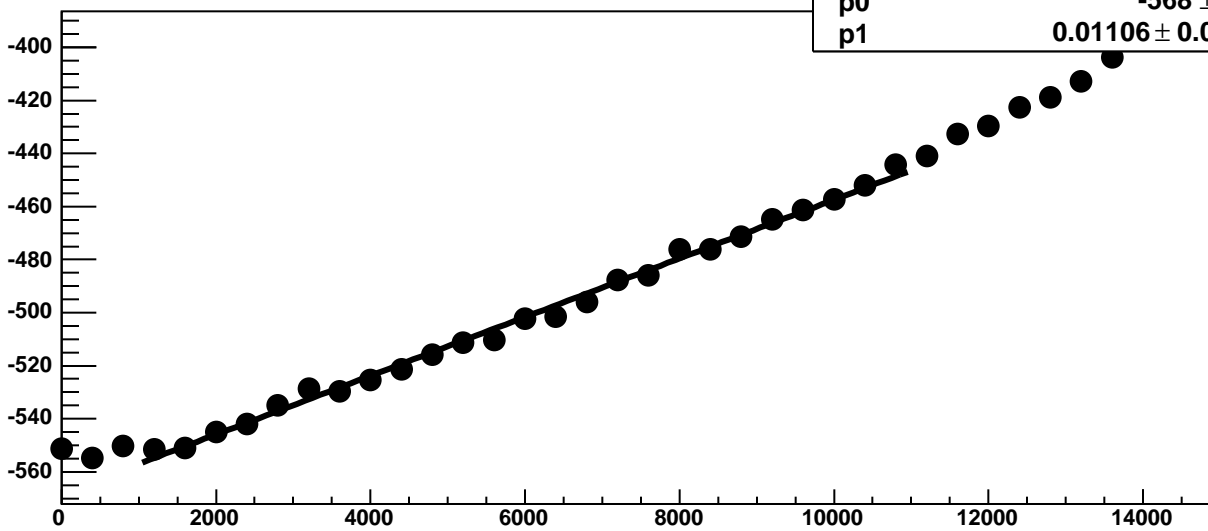
Chip 3, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

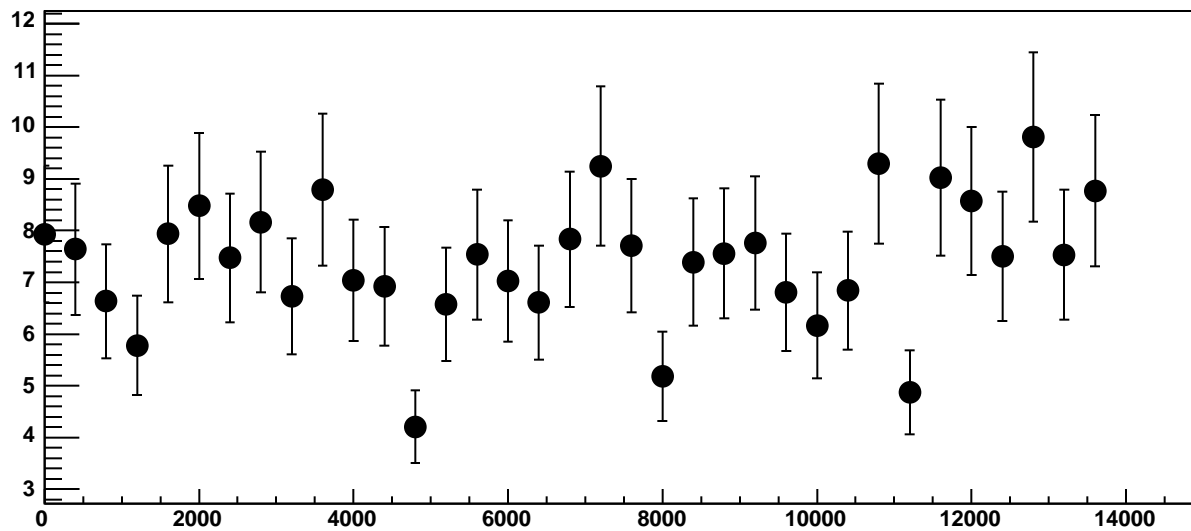


Chip 3, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

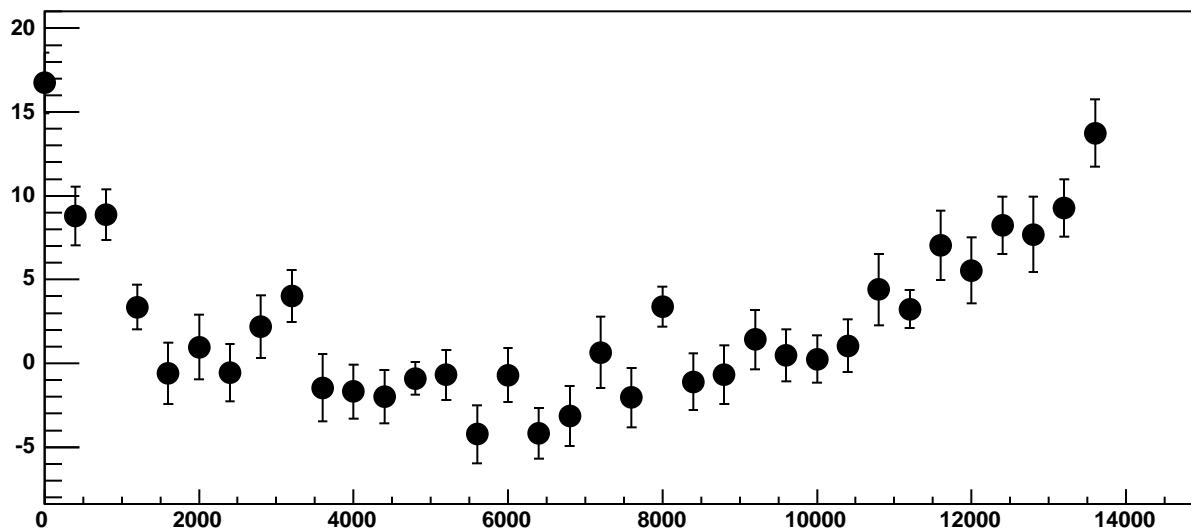


χ^2 / ndf 51.57 / 23
p0 -568 ± 0.7449
p1 0.01106 ± 0.0001133

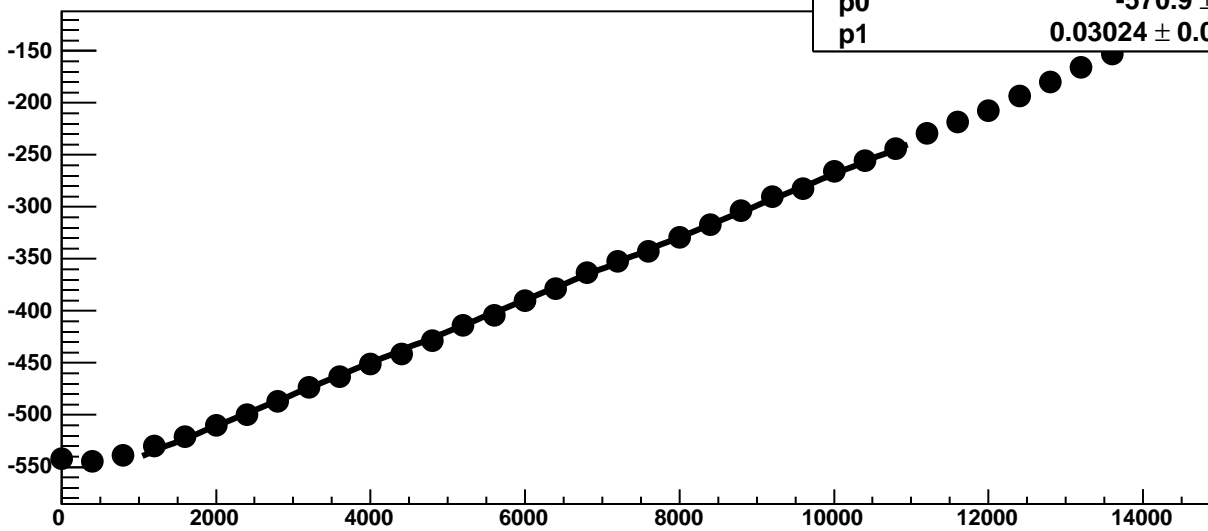
Chip 3, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

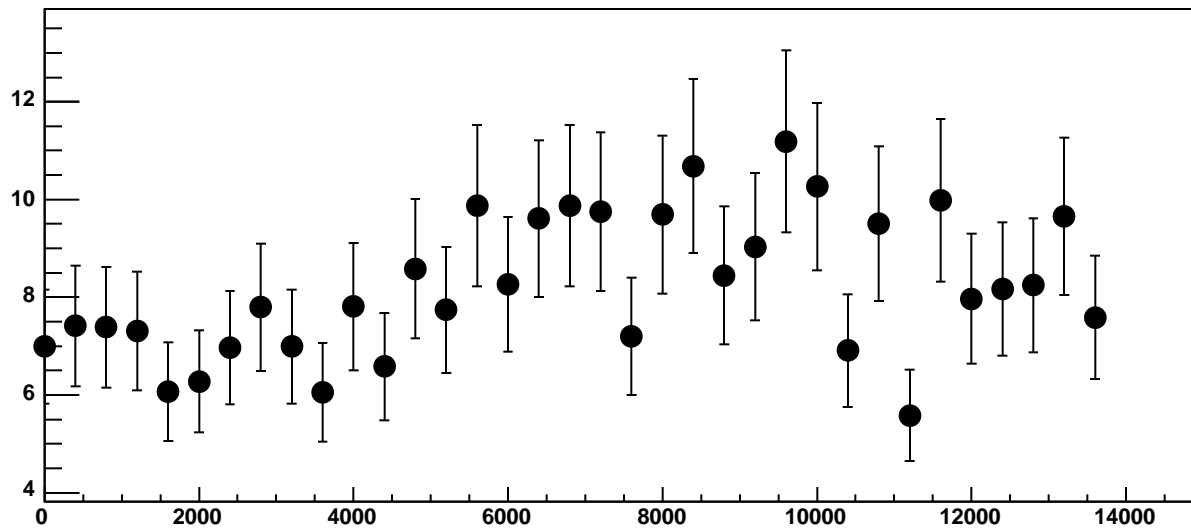


Chip 3, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC

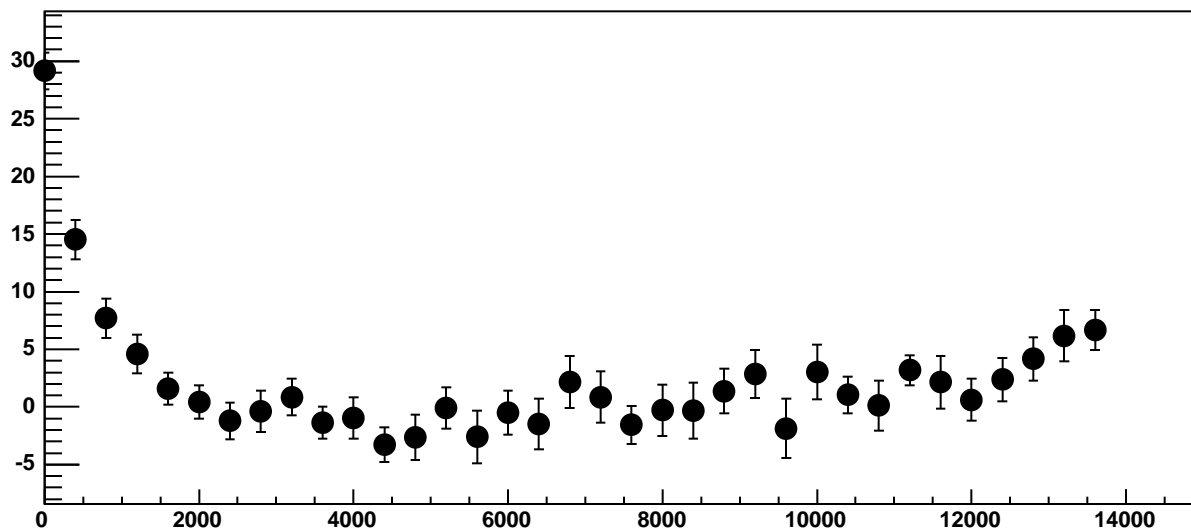


χ^2 / ndf 26.36 / 23
p0 -570.9 ± 0.7544
p1 0.03024 ± 0.0001245

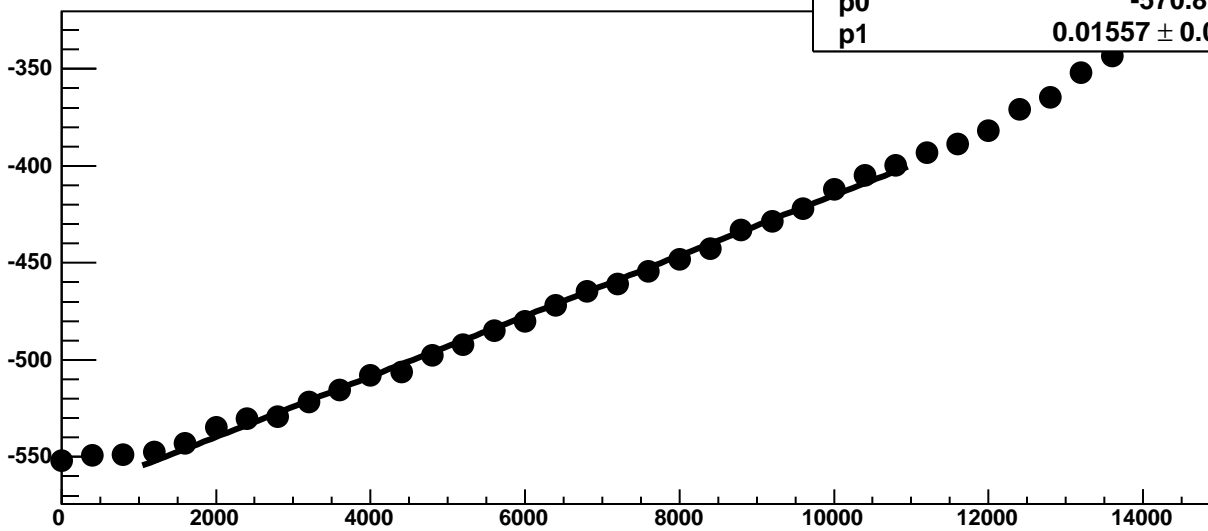
Chip 3, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC

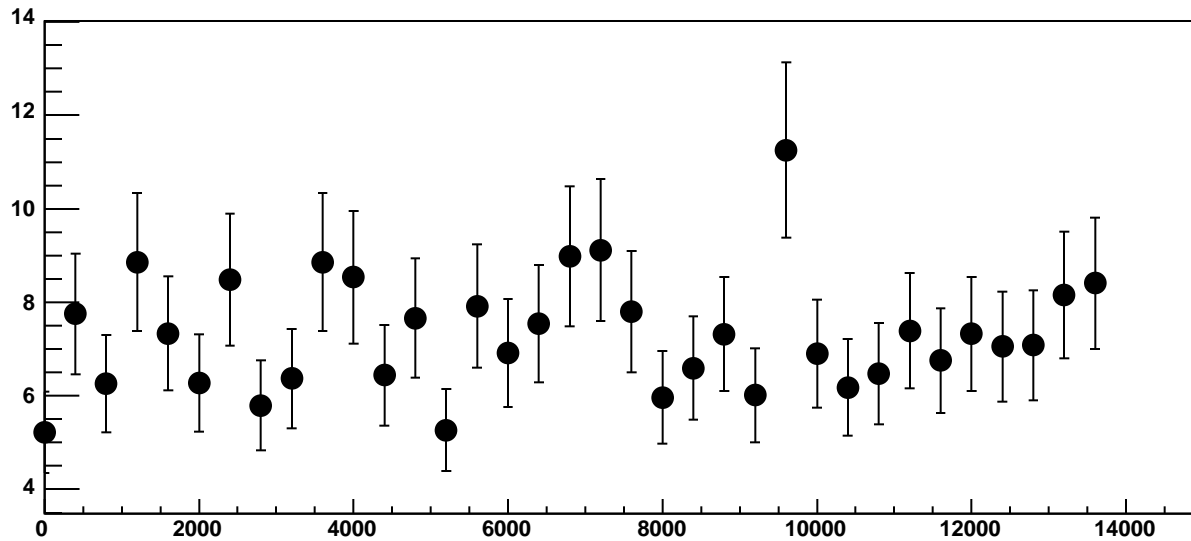


Chip 3, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

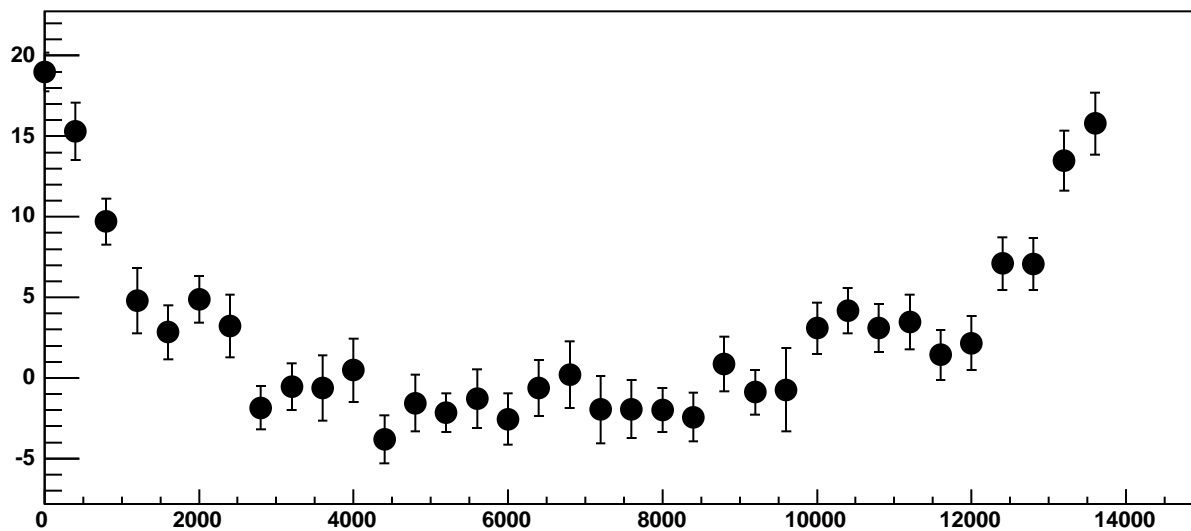


χ^2 / ndf 62.95 / 23
p0 -570.8 ± 0.753
p1 0.01557 ± 0.0001117

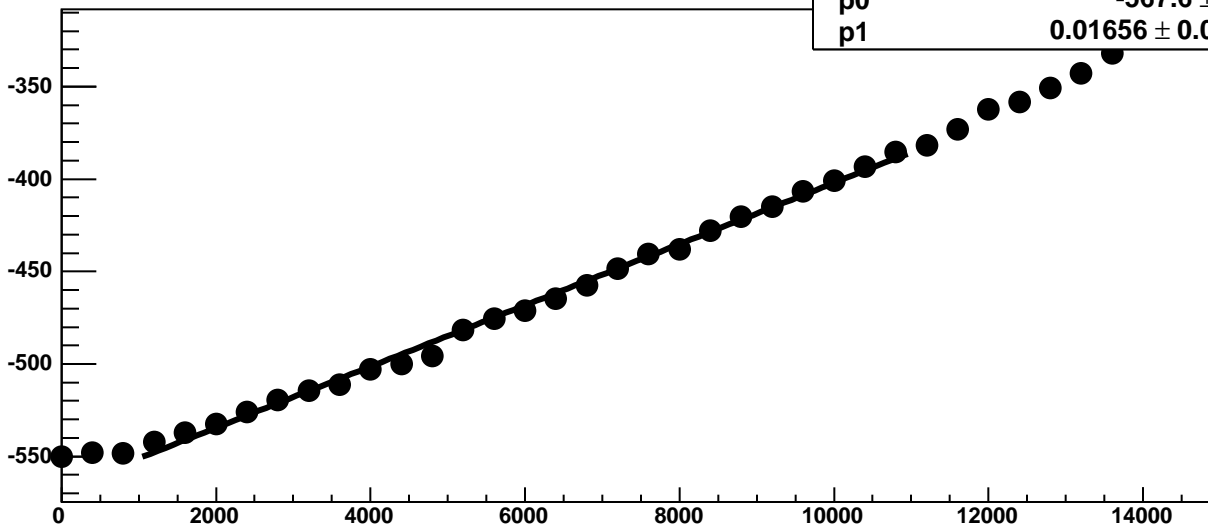
Chip 3, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

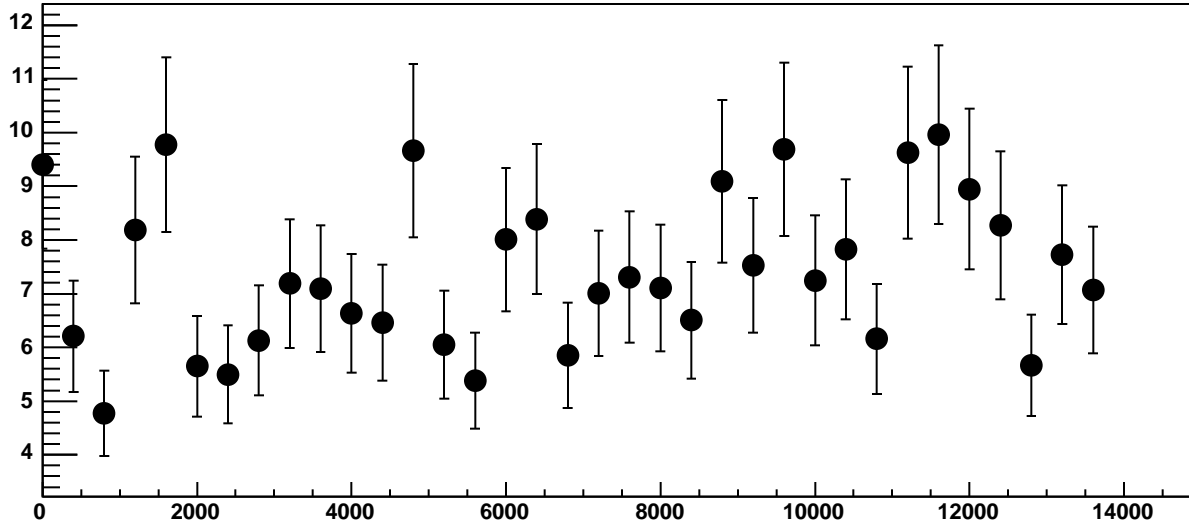


Chip 3, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

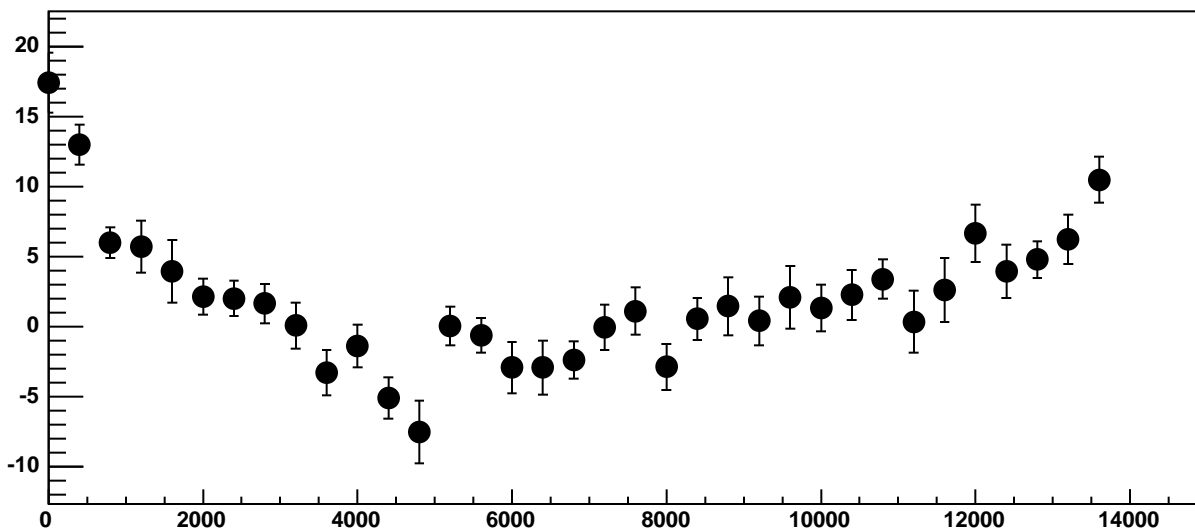


χ^2 / ndf 68.63 / 23
p0 -567.6 ± 0.7283
p1 0.01656 ± 0.0001127

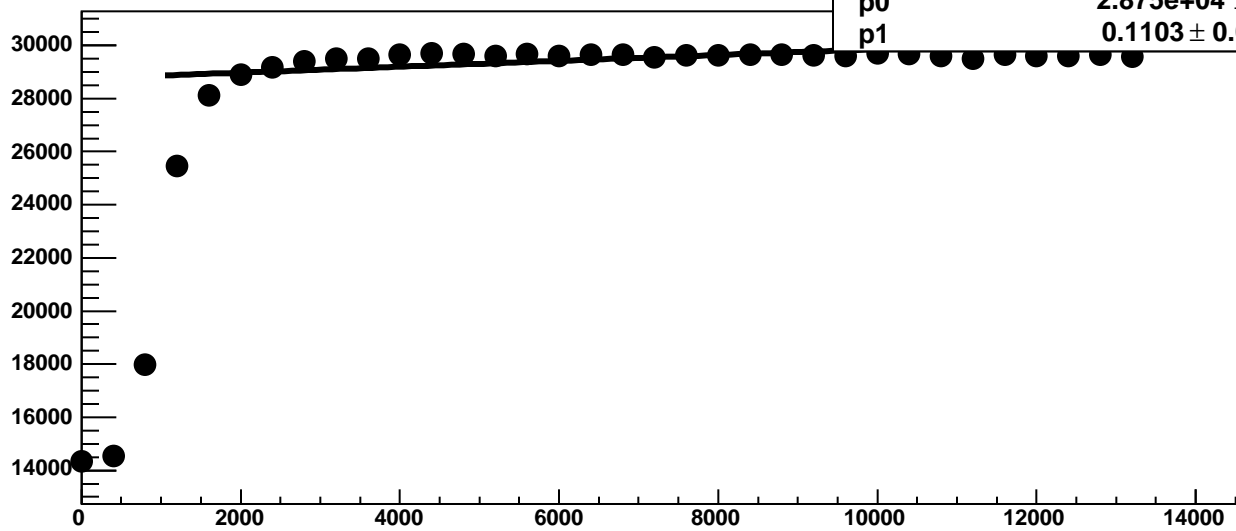
Chip 3, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



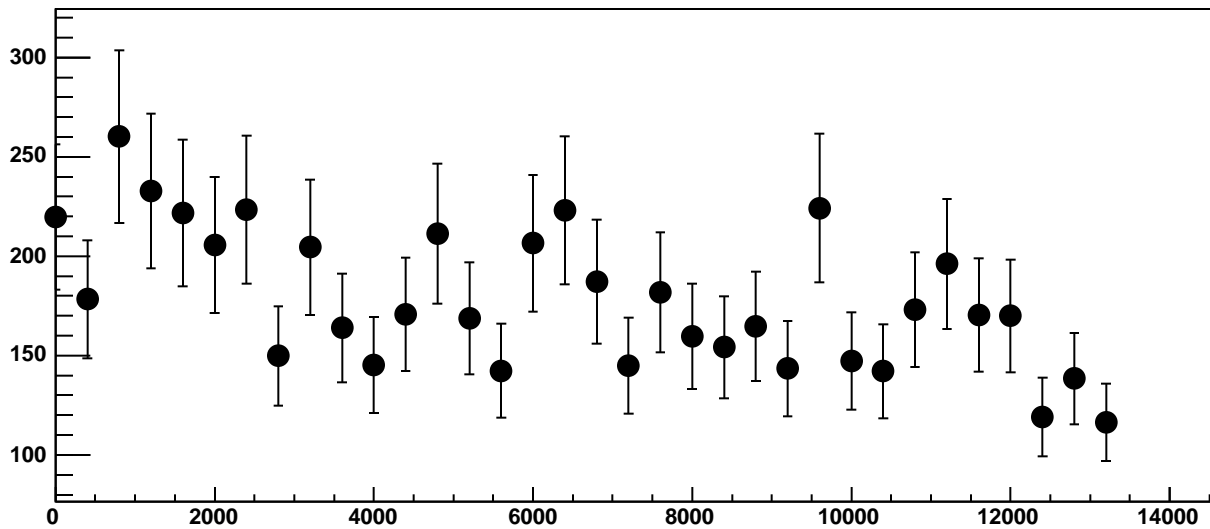
Chip 3, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



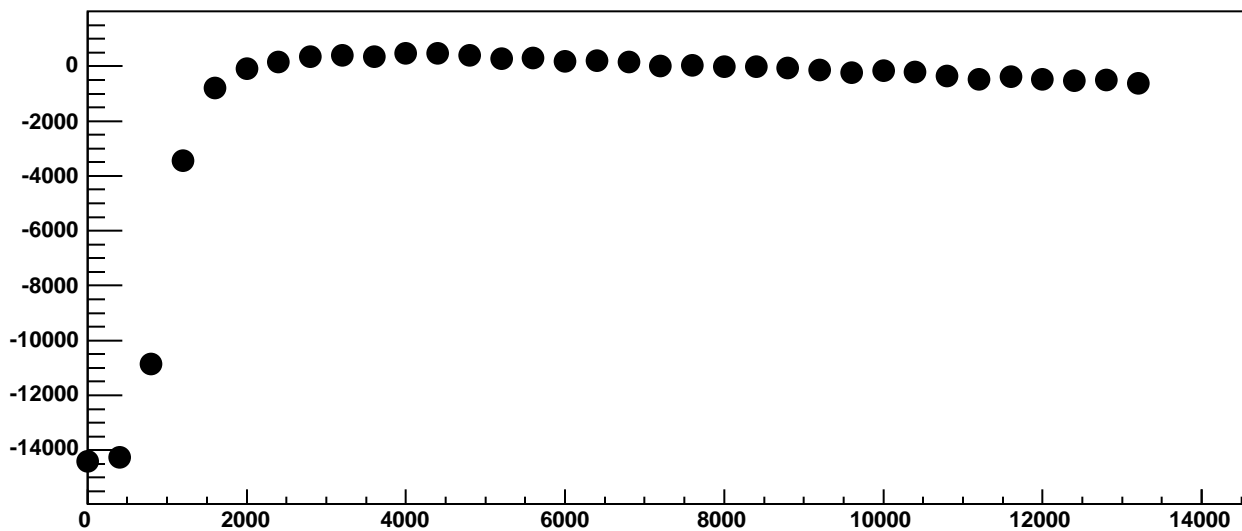
Chip 3, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC



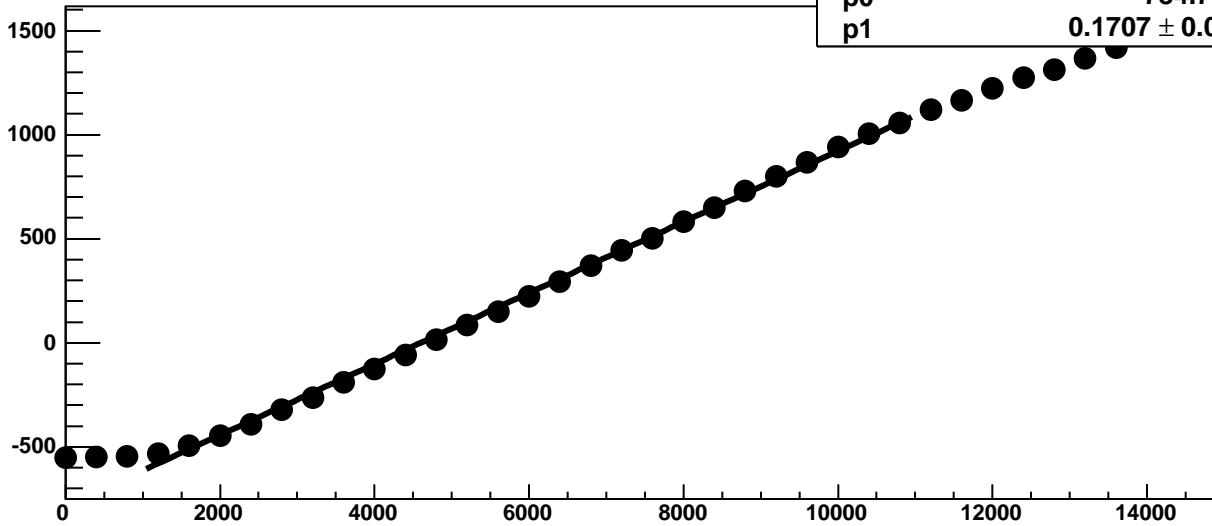
Chip 3, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

937 / 23

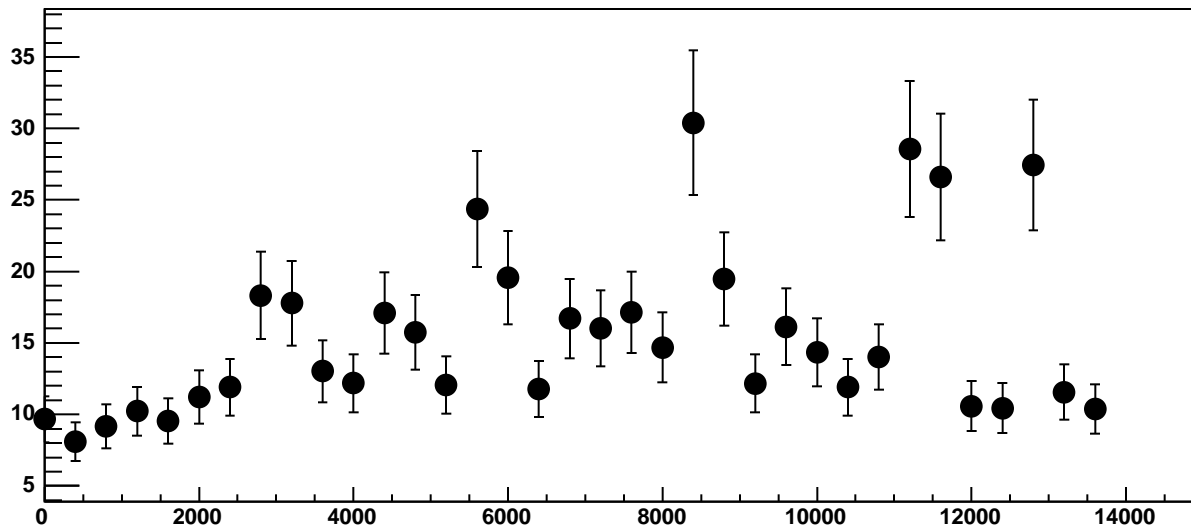
p0

-784.7 ± 1.286

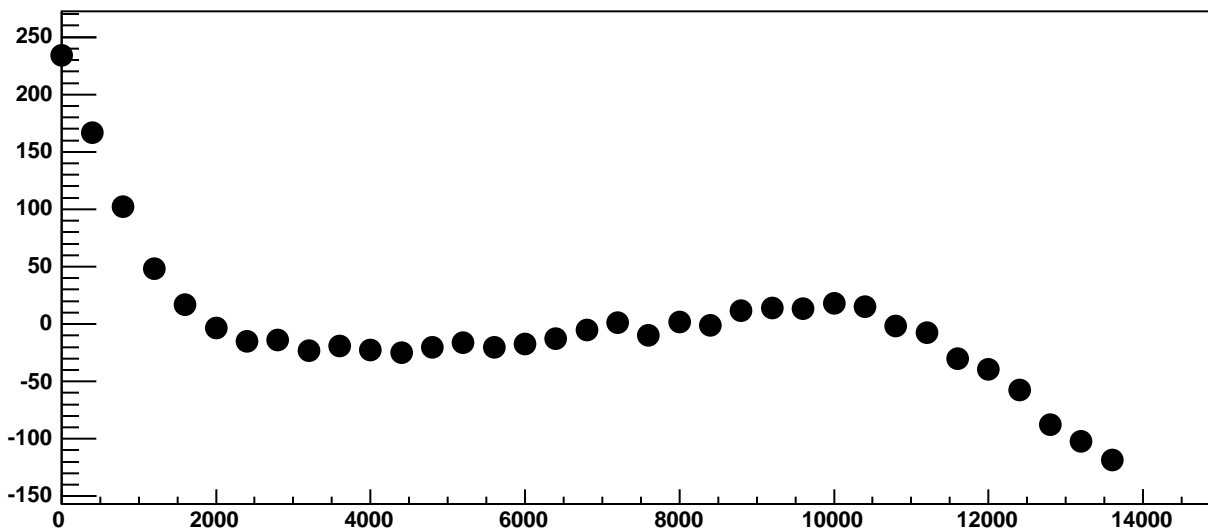
p1

0.1707 ± 0.0002034

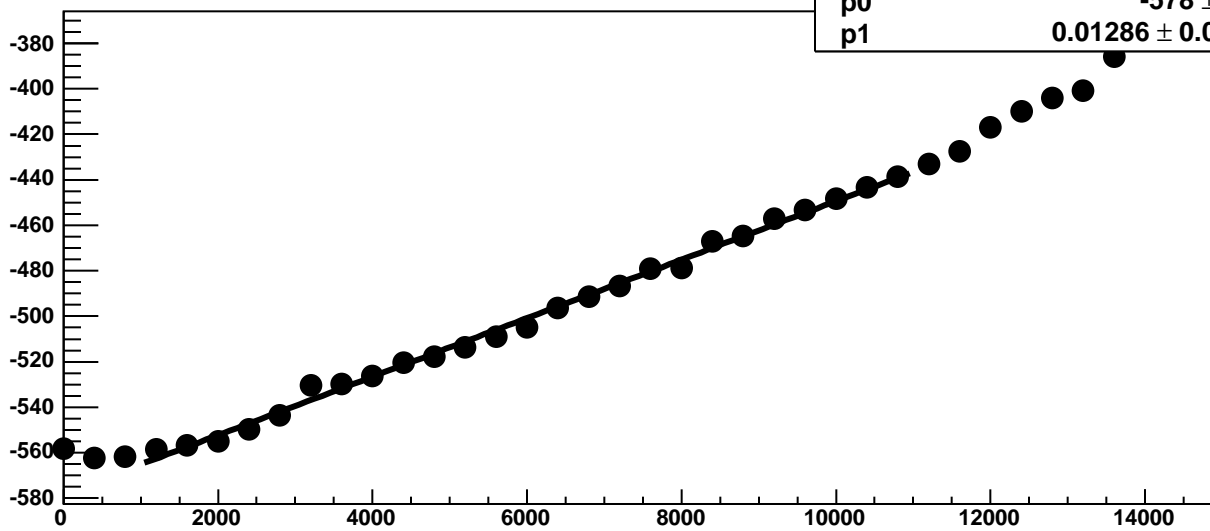
Chip 3, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

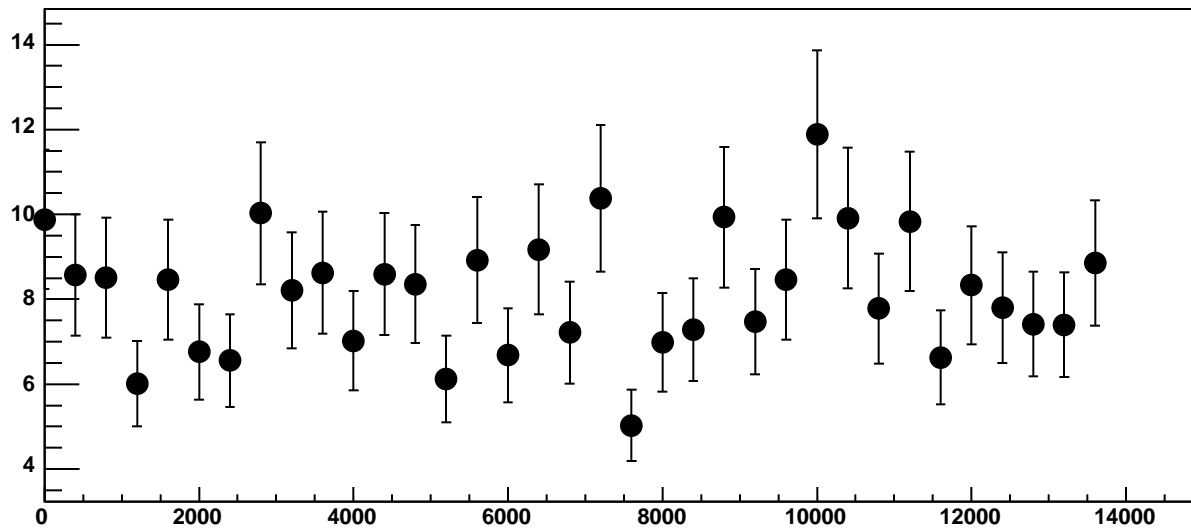


Chip 3, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

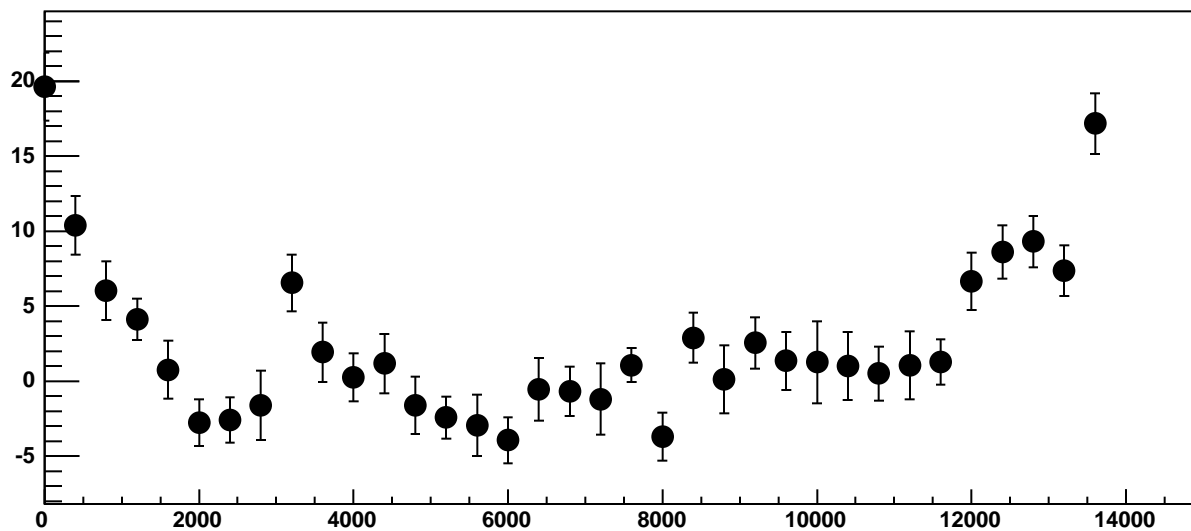


χ^2 / ndf 54.3 / 23
p0 -578 ± 0.7929
p1 0.01286 ± 0.0001237

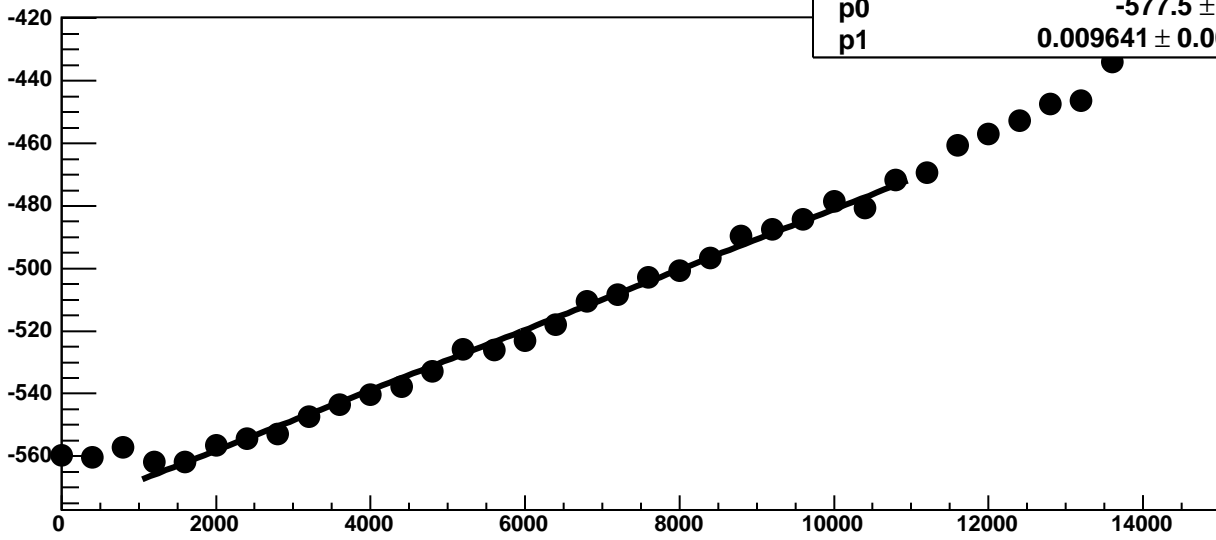
Chip 3, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

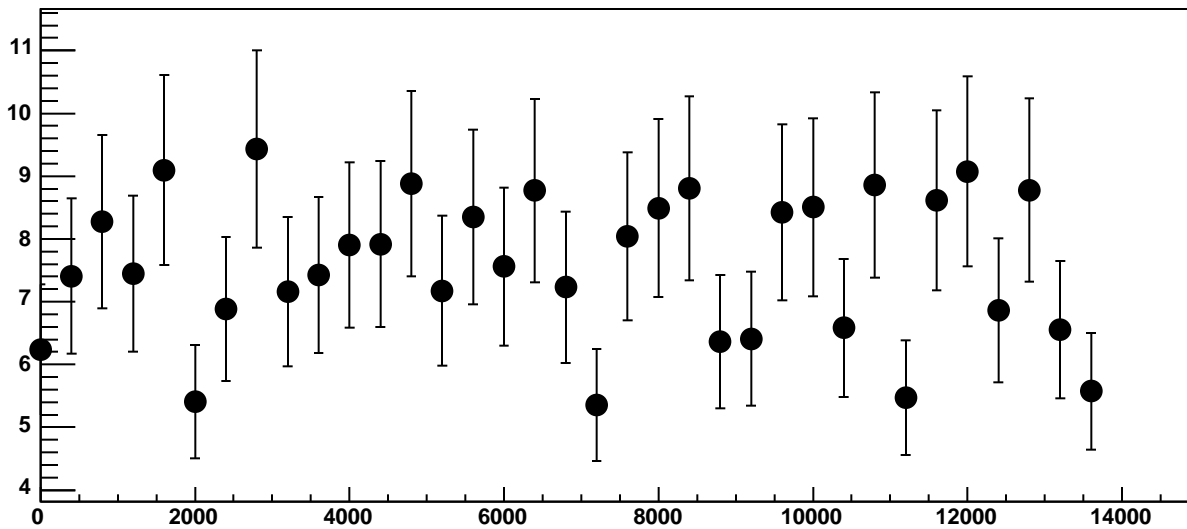


Chip 3, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

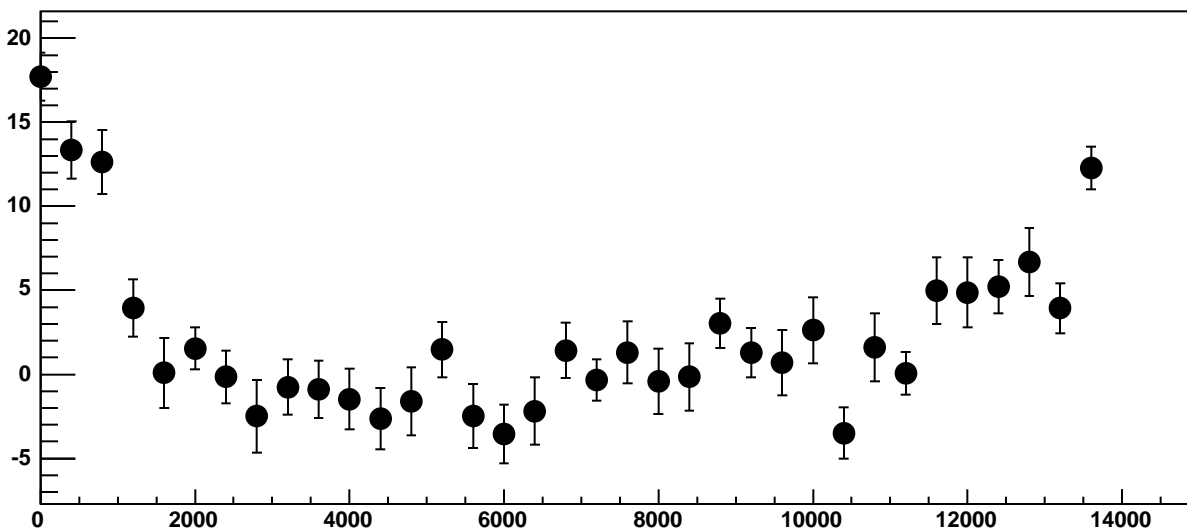


χ^2 / ndf 34.3 / 23
p0 -577.5 ± 0.7797
p1 0.009641 ± 0.0001177

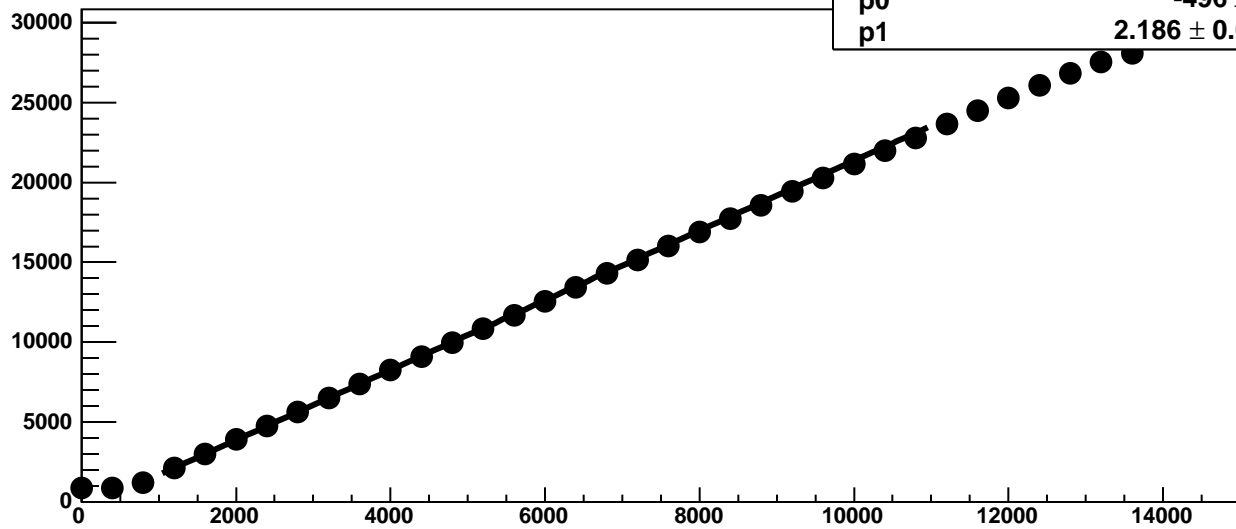
Chip 3, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



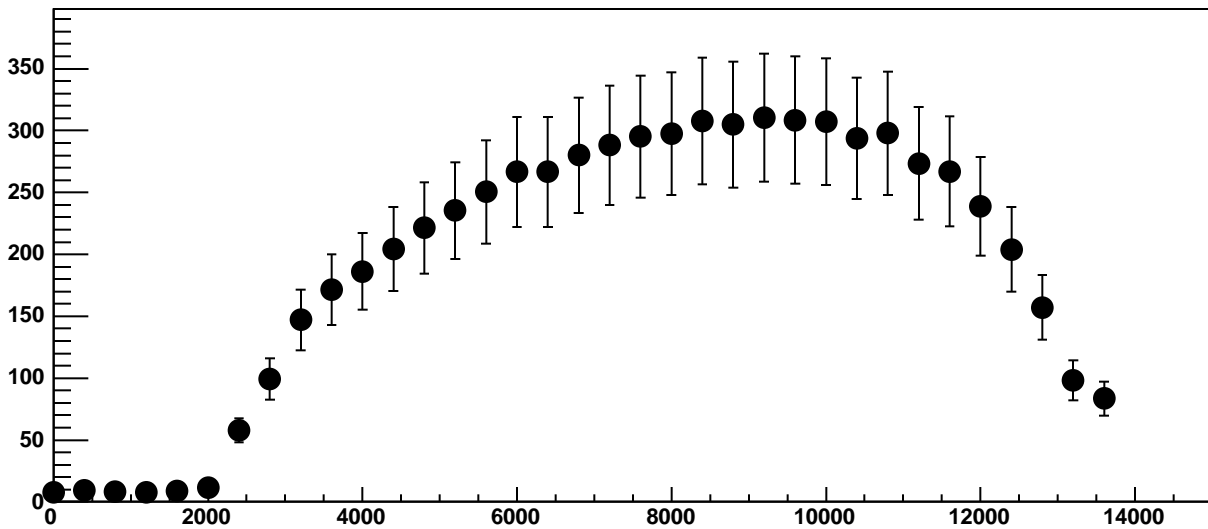
Chip 3, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



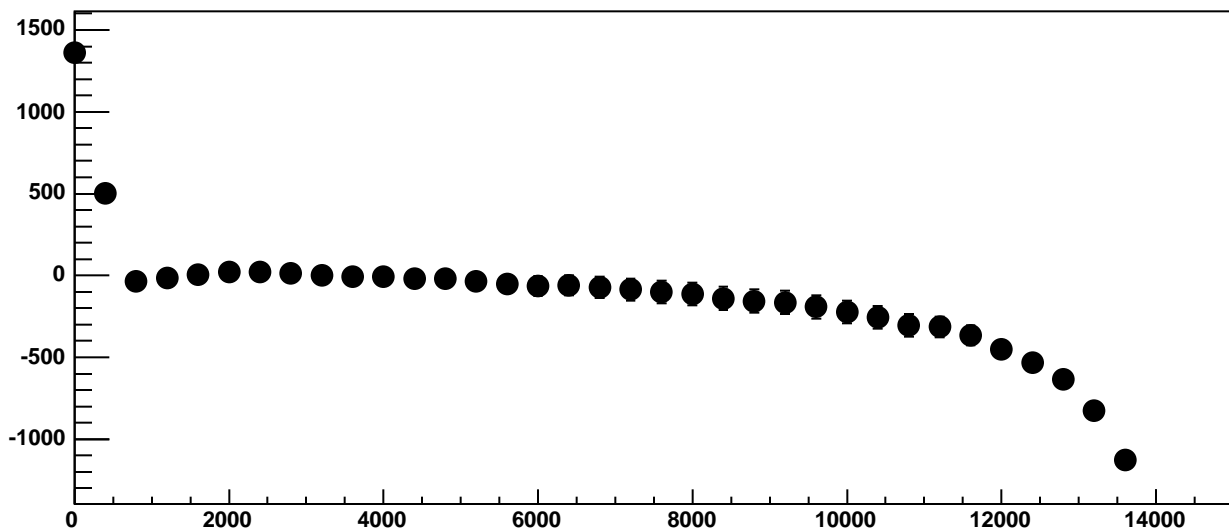
Chip 3, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC



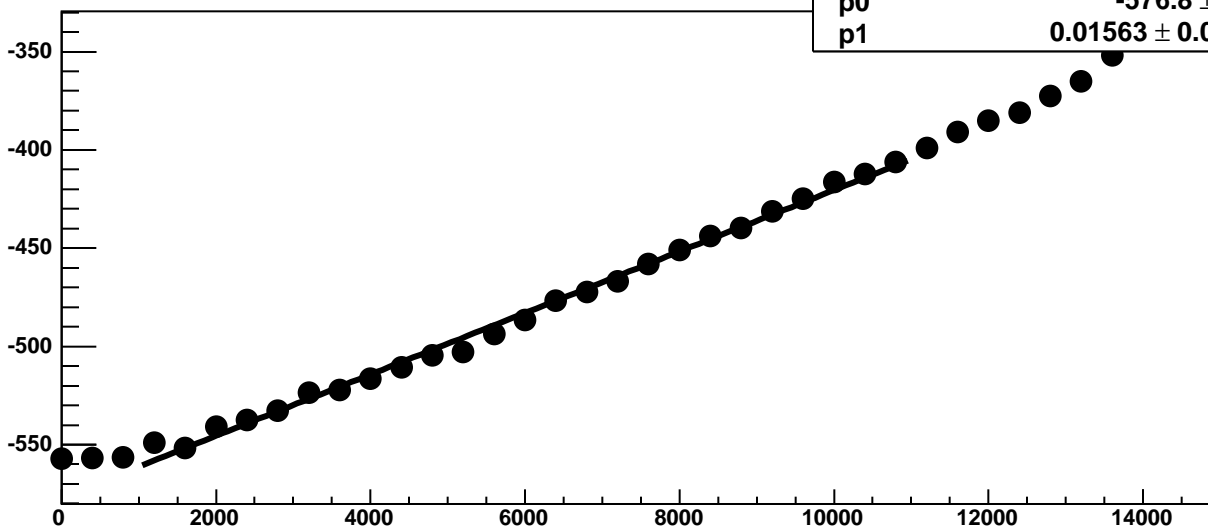
Chip 3, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC

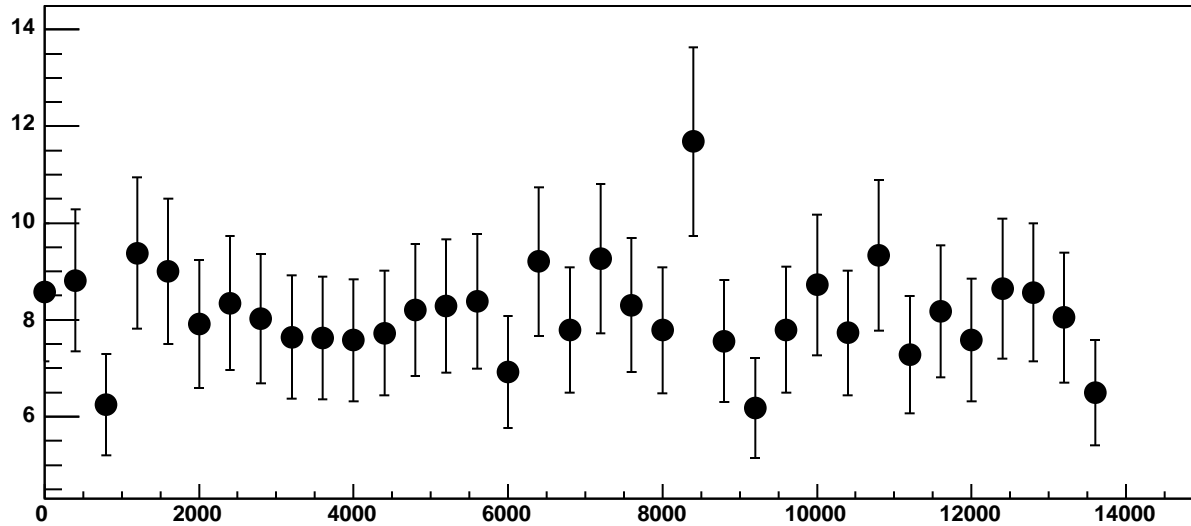


Chip 3, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

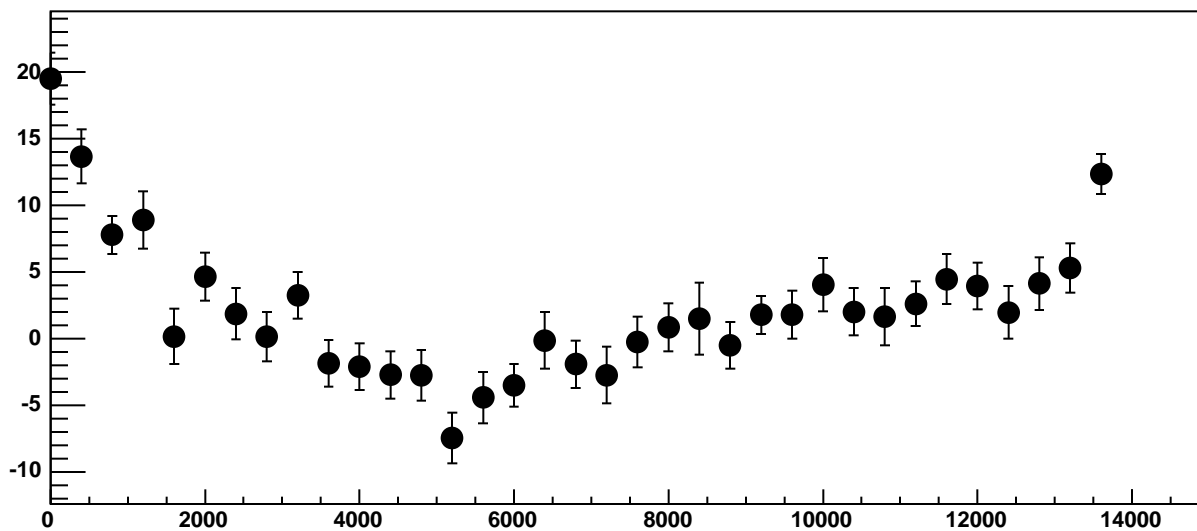


χ^2 / ndf 72.57 / 23
p0 -576.8 ± 0.8746
p1 0.01563 ± 0.0001309

Chip 3, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

χ^2 / ndf

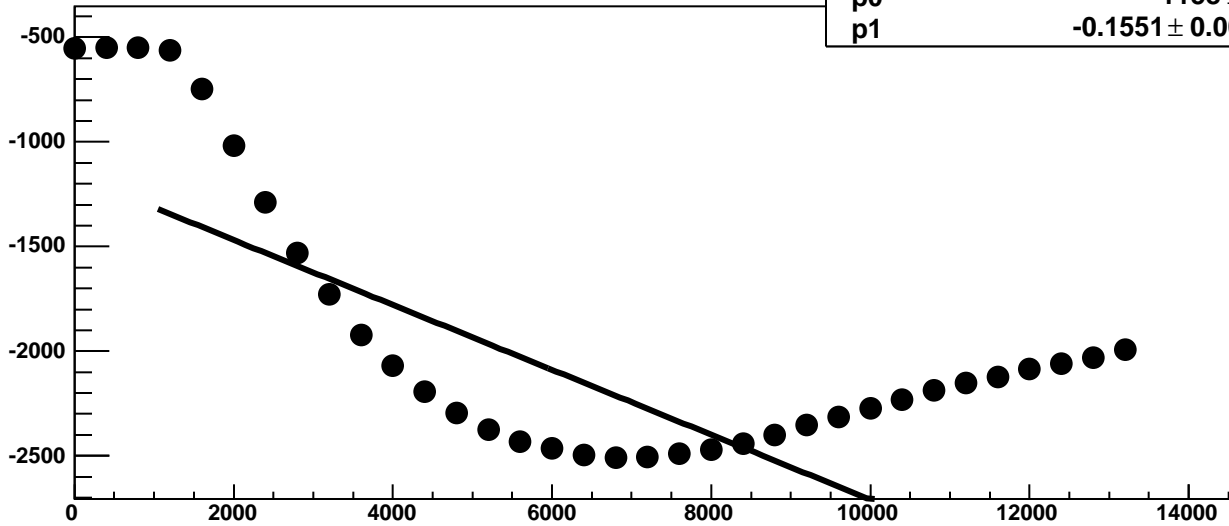
6.405e+05 / 23

p0

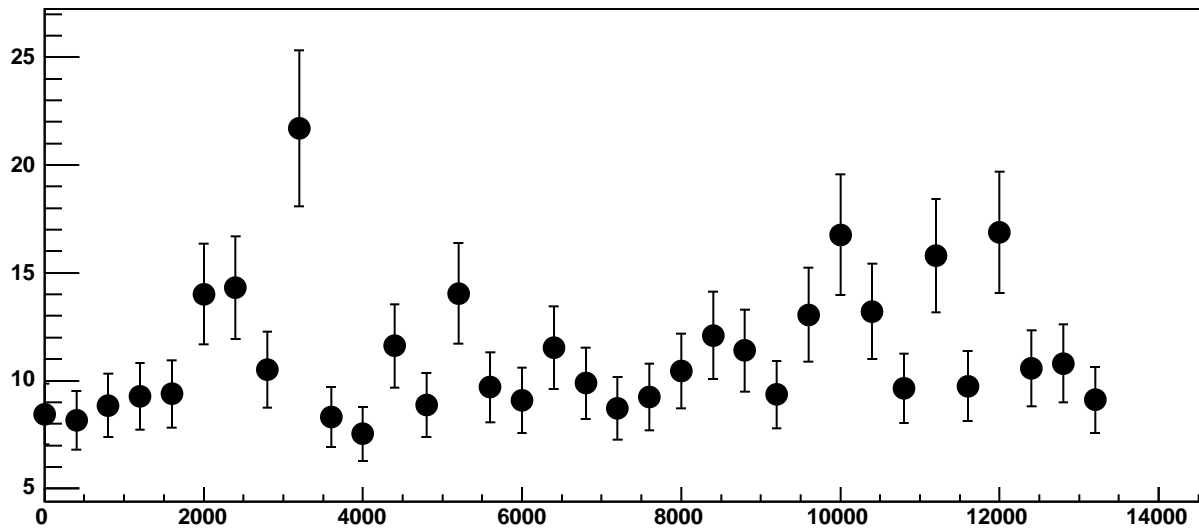
-1158 ± 1.128

p1

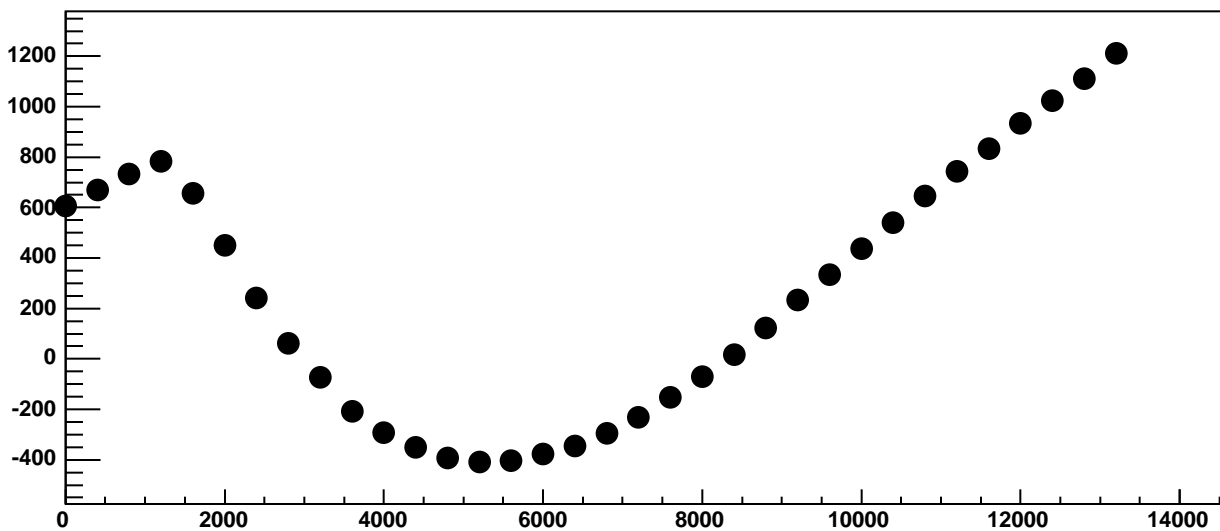
-0.1551 ± 0.0001742



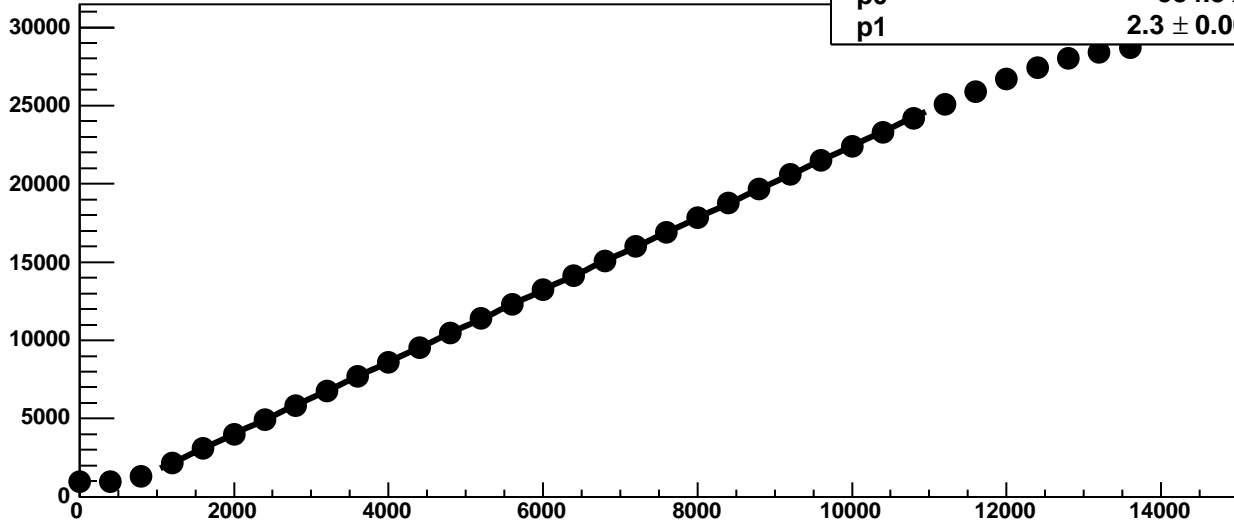
Chip 3, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



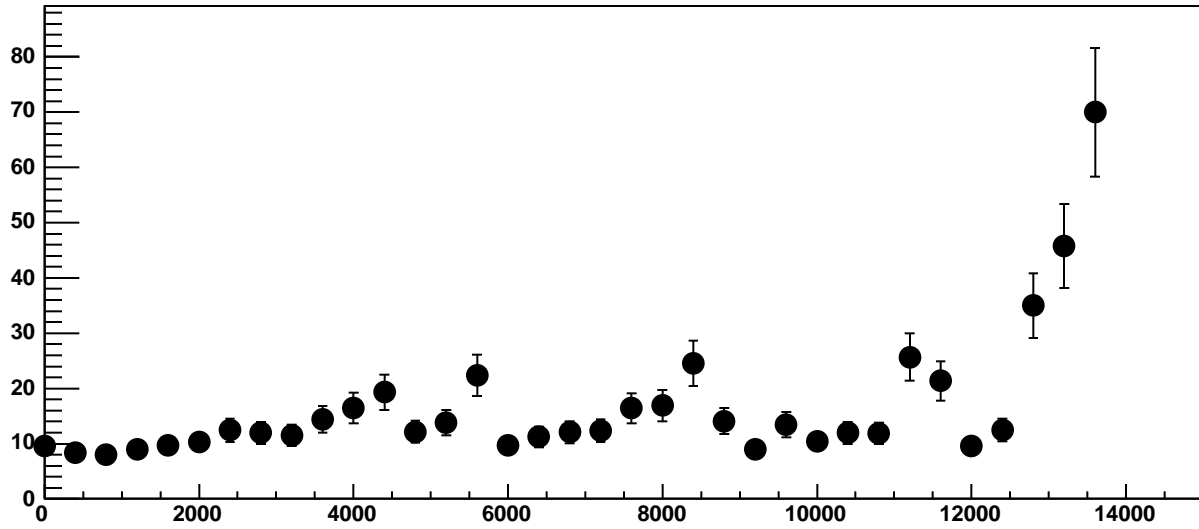
Chip 3, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



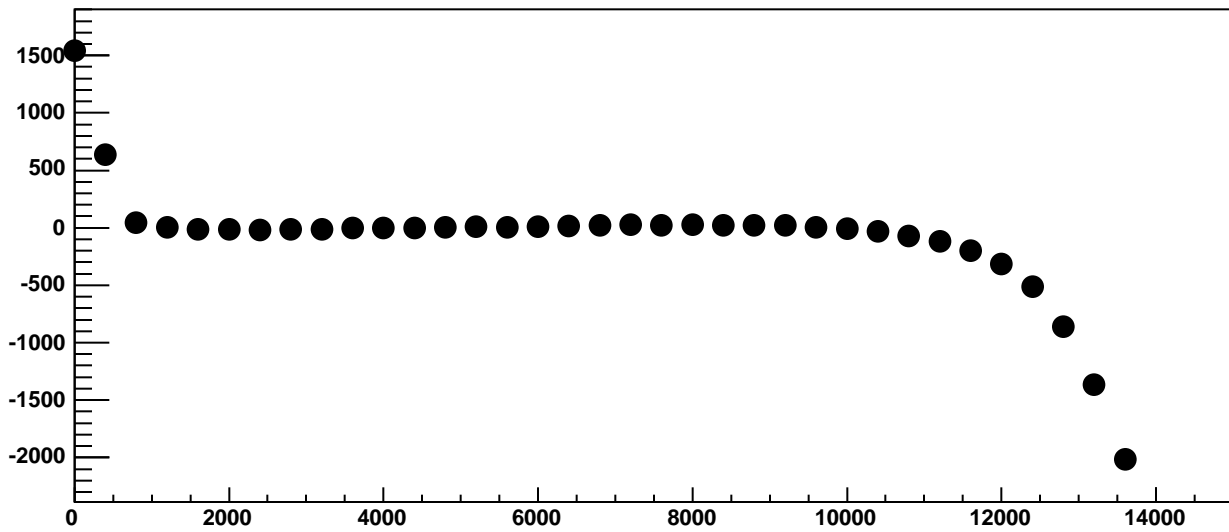
Chip 3, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC



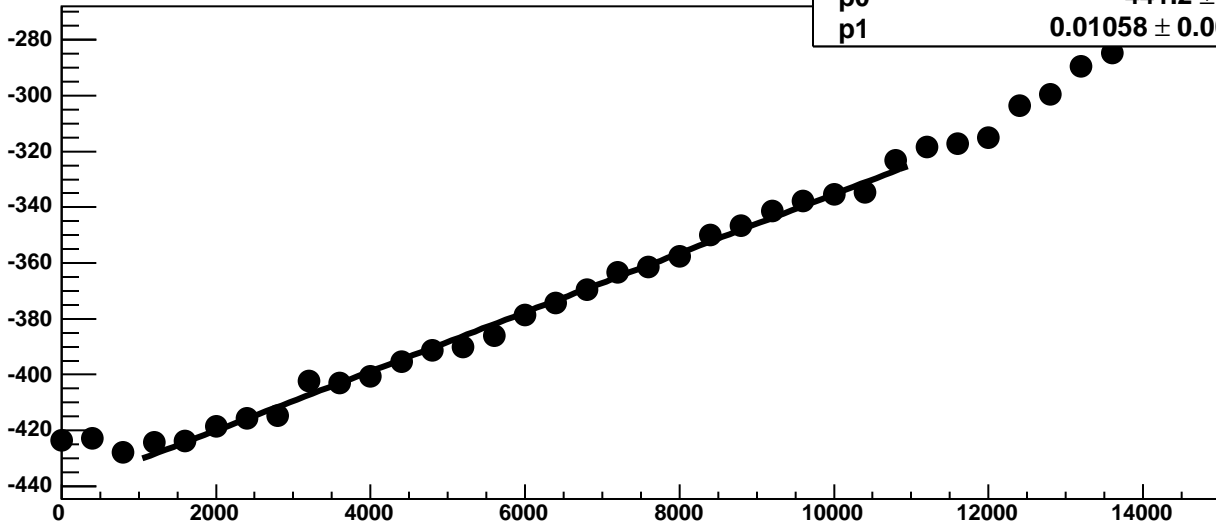
Chip 3, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC

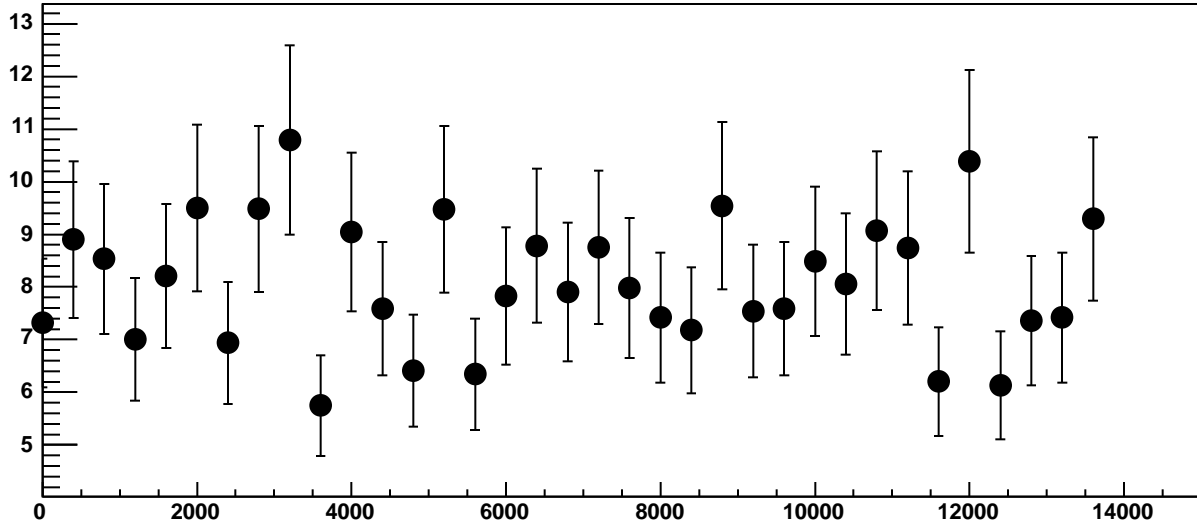


Chip 3, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

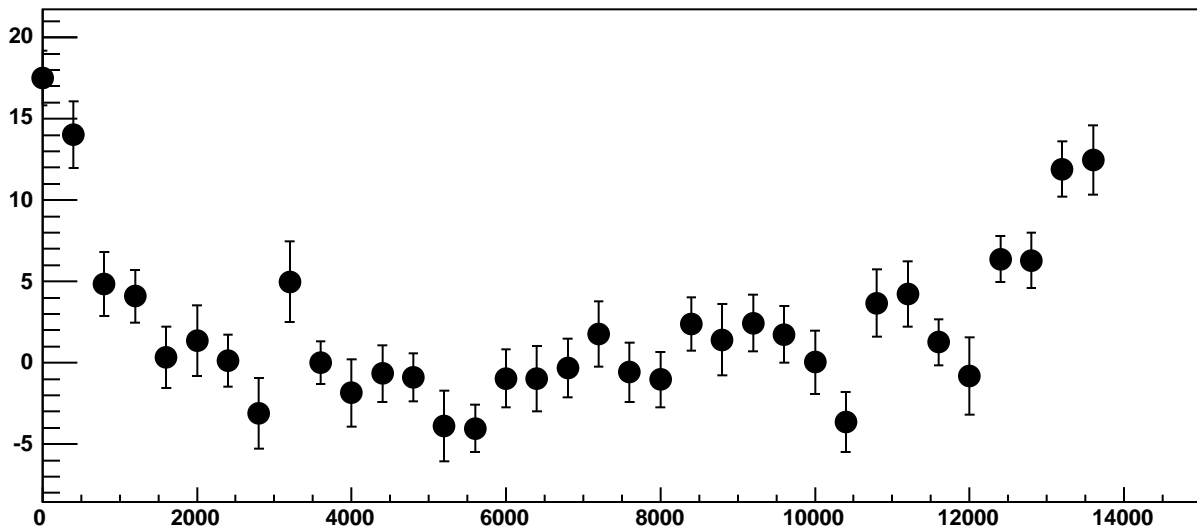


χ^2 / ndf 39.42 / 23
p0 -441.2 ± 0.8332
p1 0.01058 ± 0.0001274

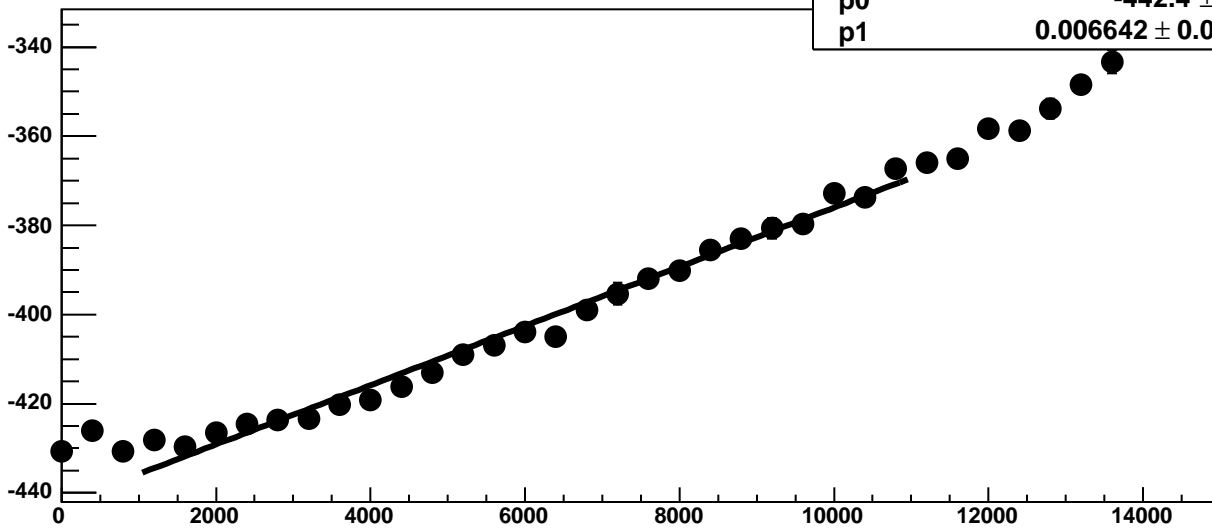
Chip 3, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



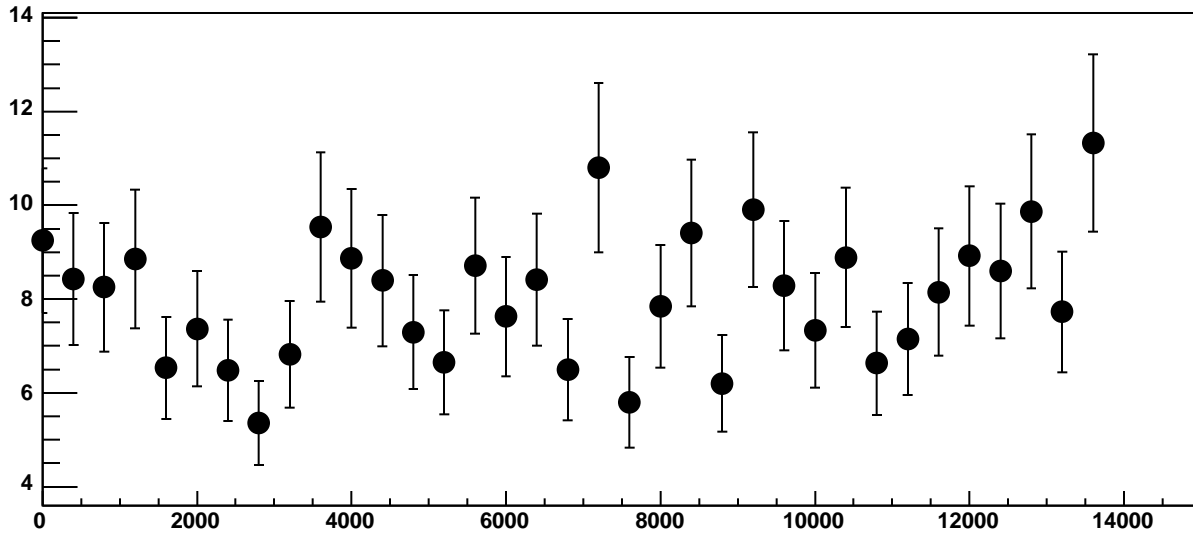
Chip 3, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



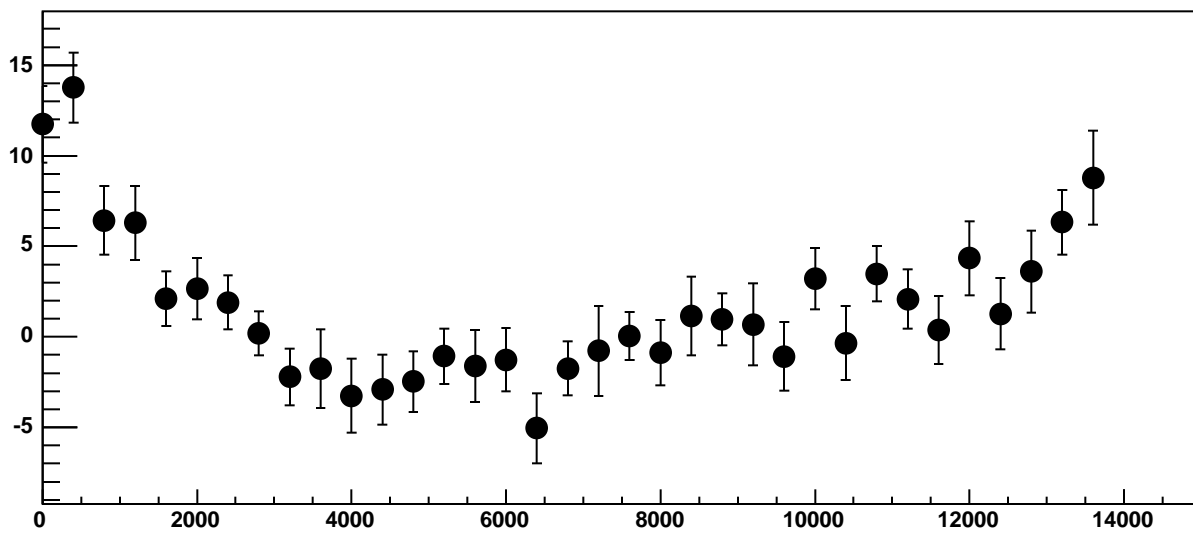
Chip 3, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



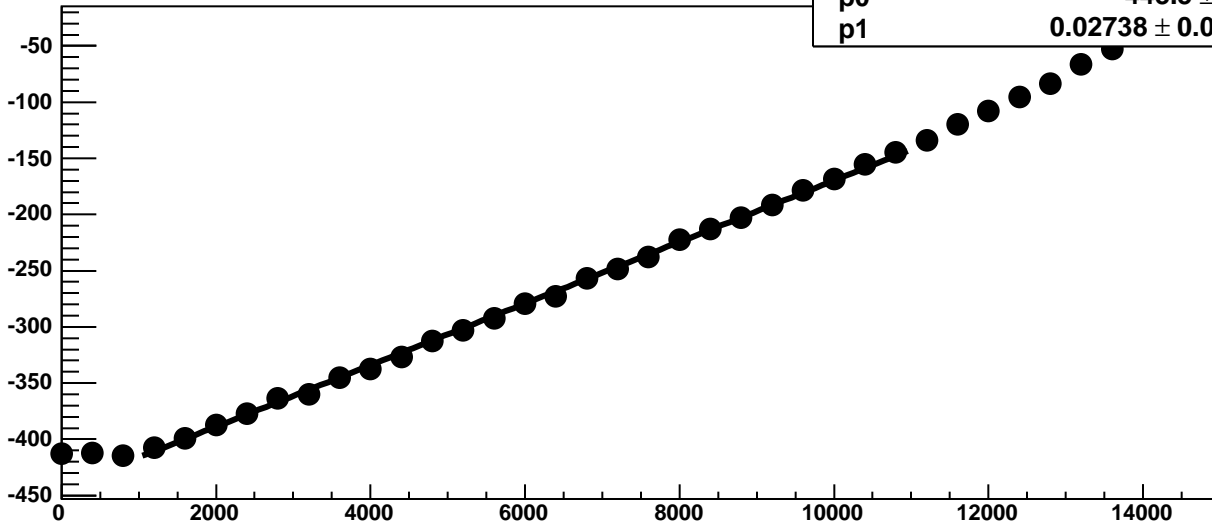
Chip 3, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



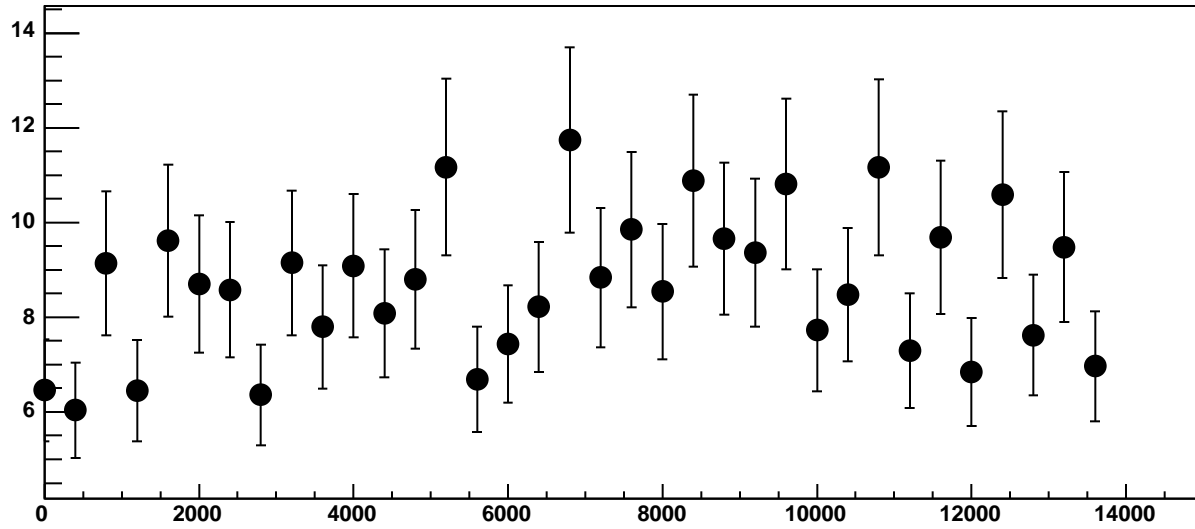
Chip 3, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



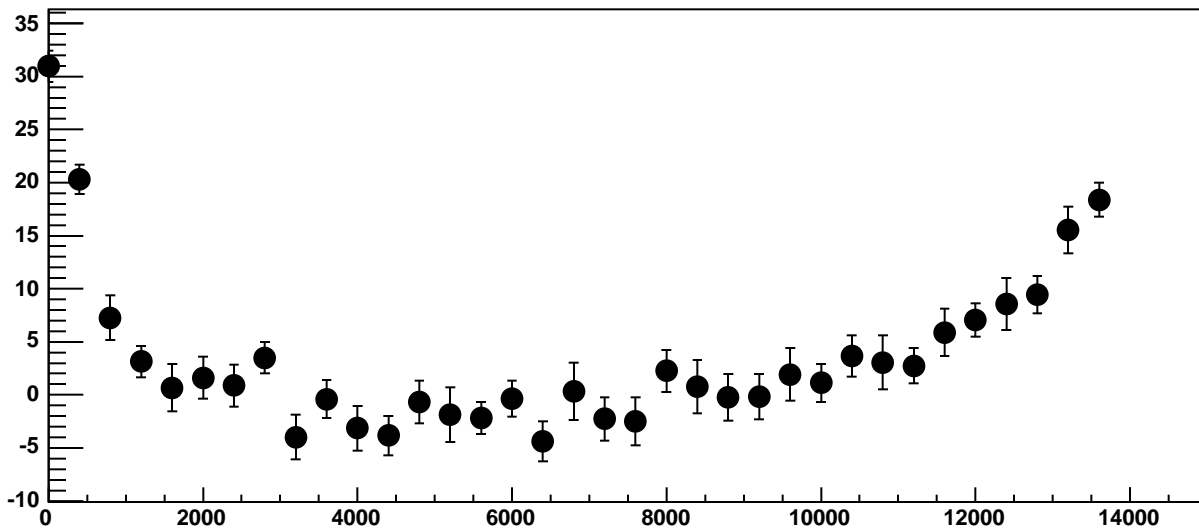
Chip 3, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC



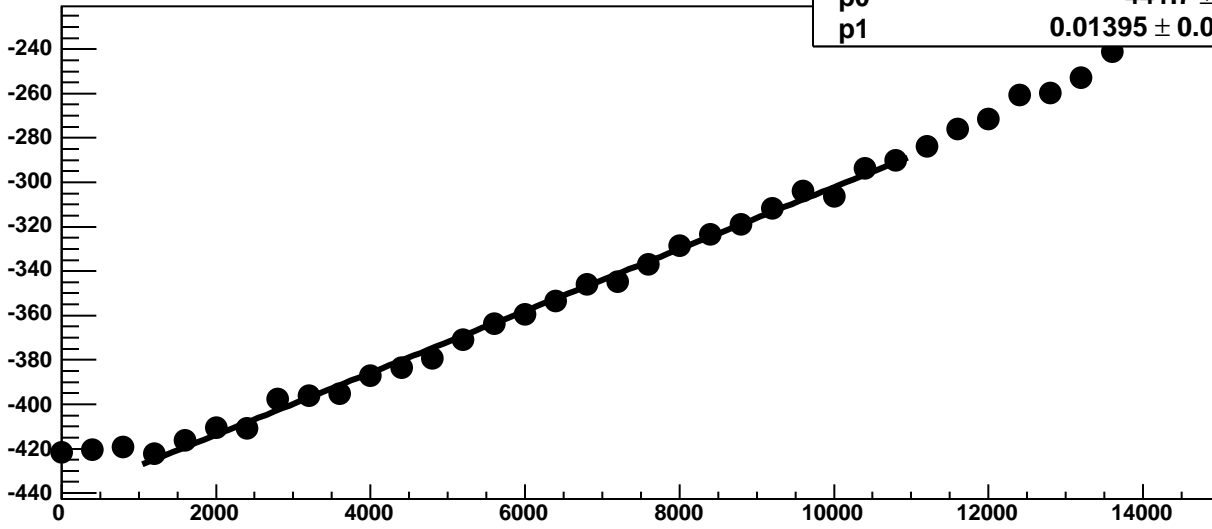
Chip 3, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

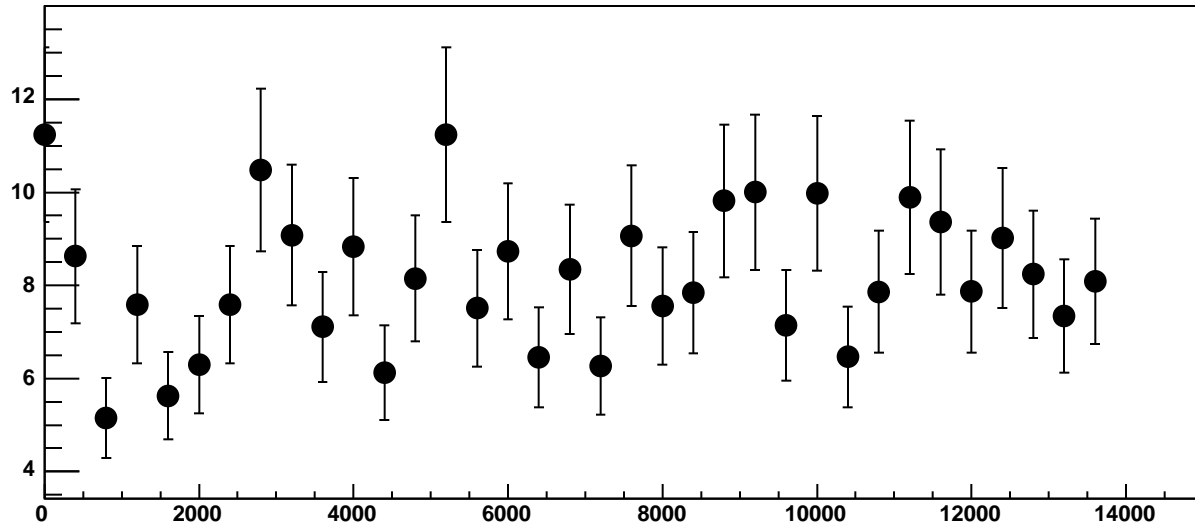


Chip 3, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

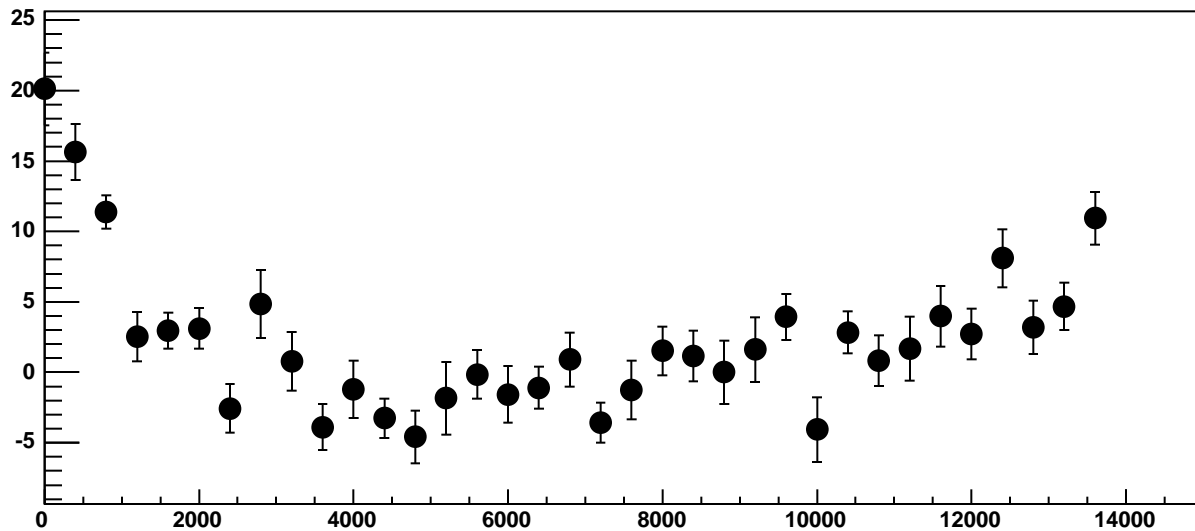


χ^2 / ndf 58.78 / 23
p0 -441.7 ± 0.7693
p1 0.01395 ± 0.0001185

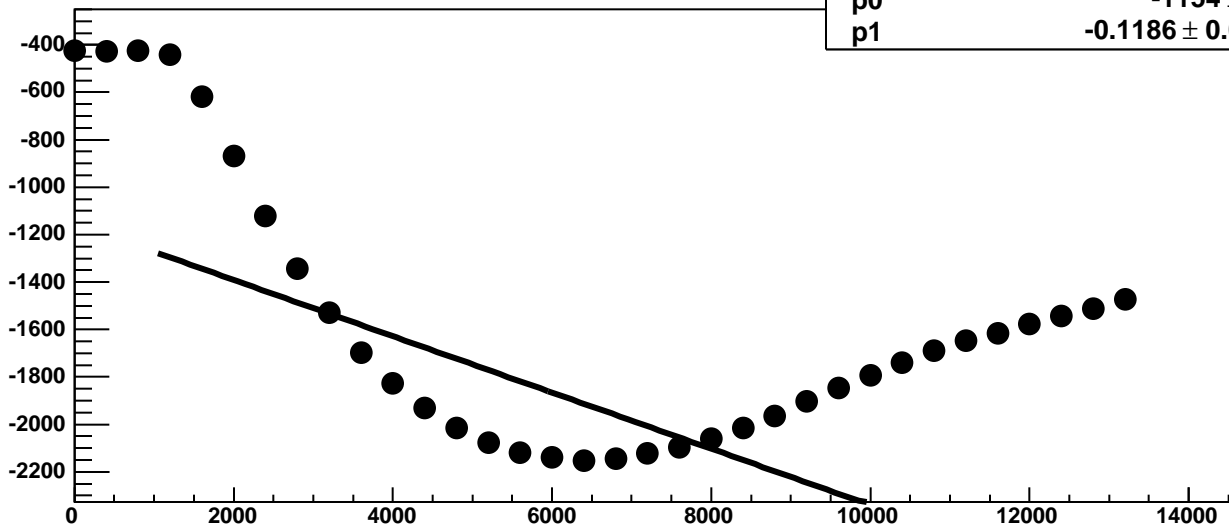
Chip 3, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

6.1e+05 / 23

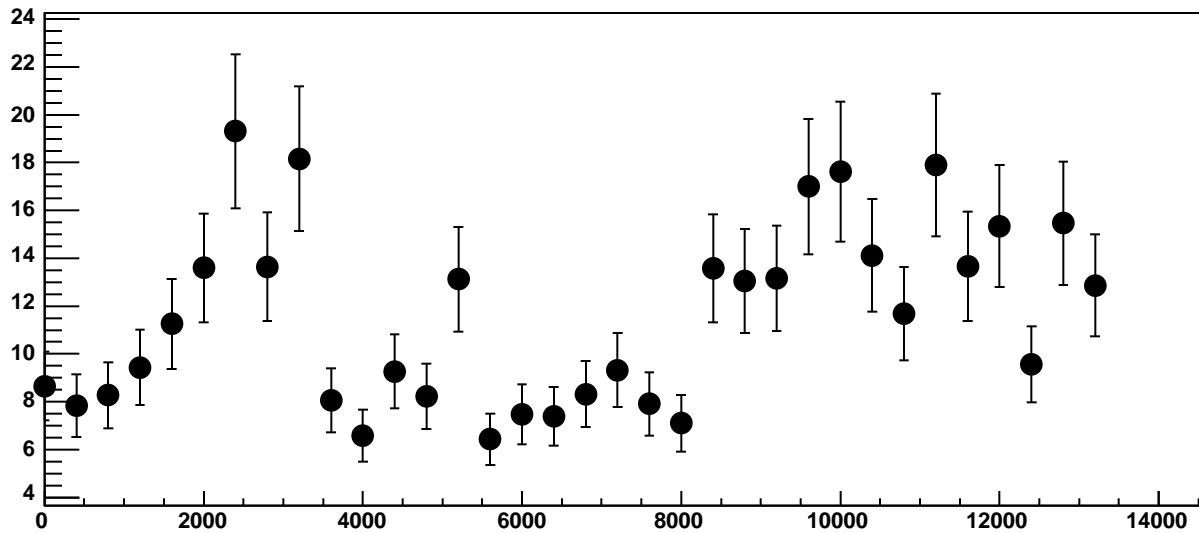
p0

-1154 ± 1.179

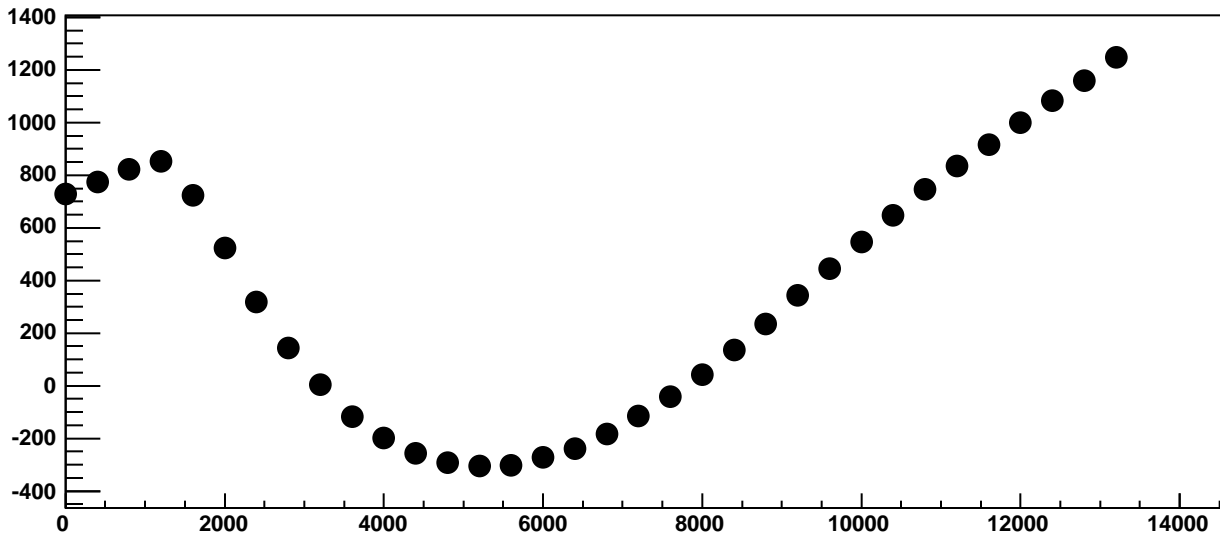
p1

-0.1186 ± 0.000188

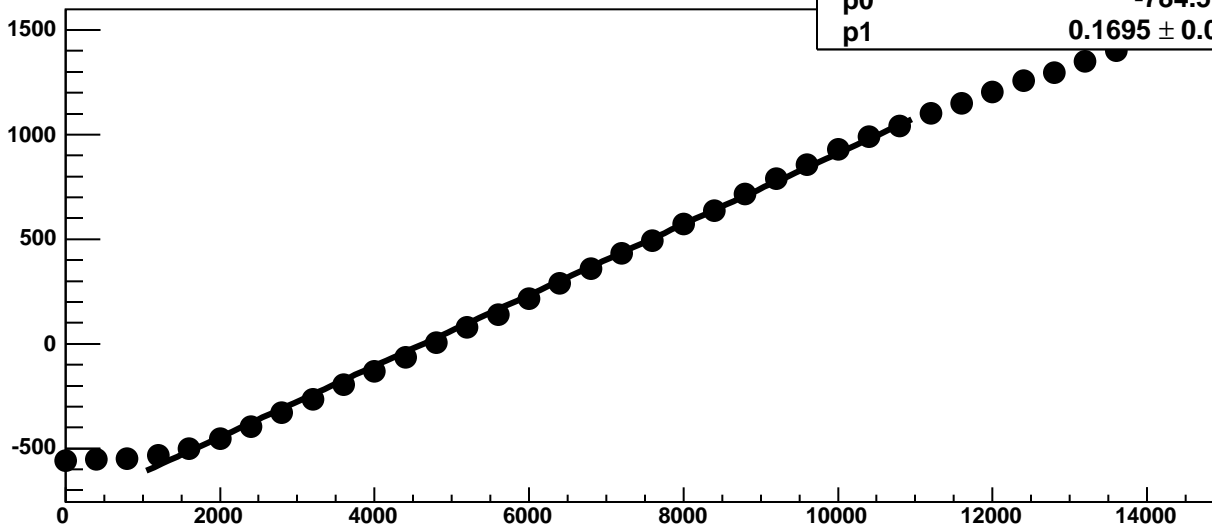
Chip 3, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



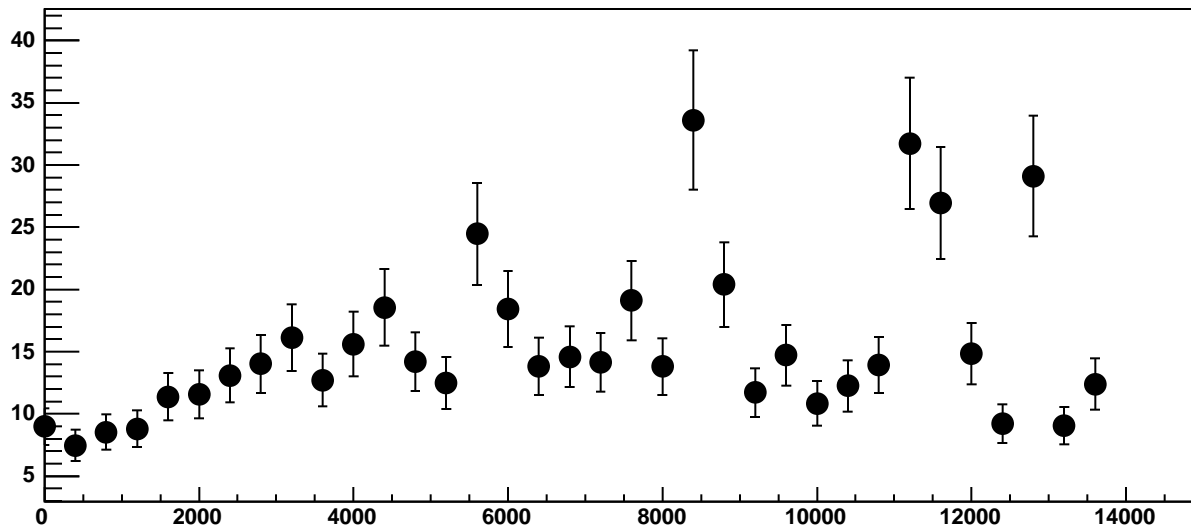
Chip 3, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



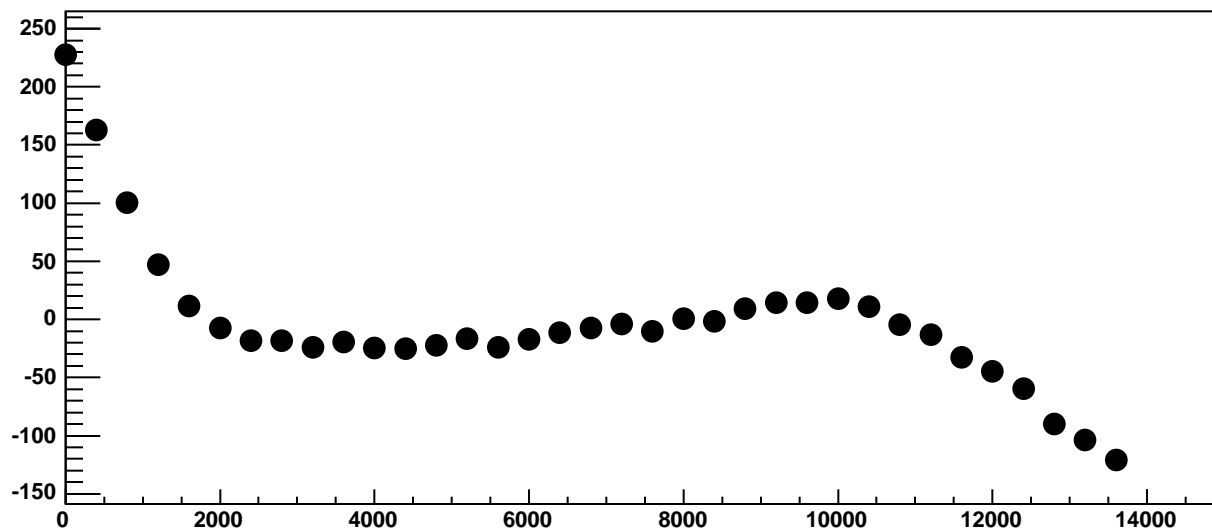
Chip 3, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC



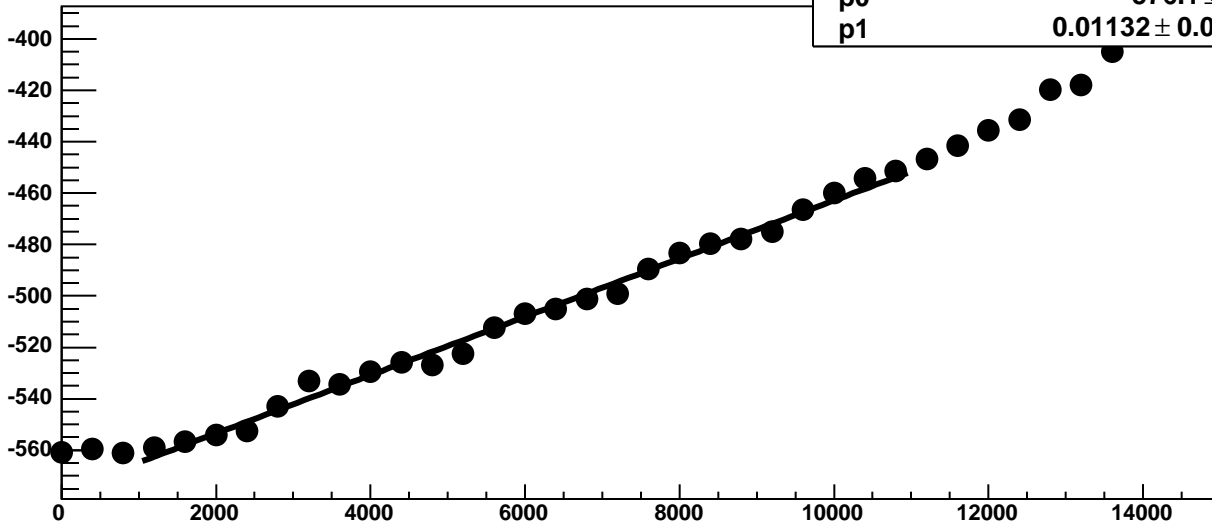
Chip 3, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

73.52 / 23

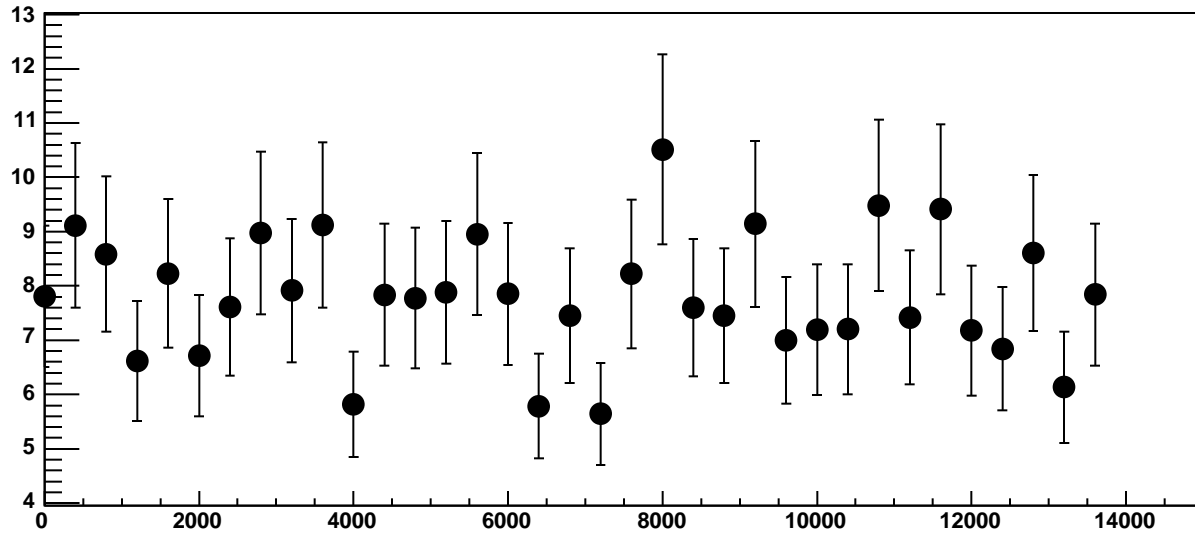
p0

-576.1 ± 0.7931

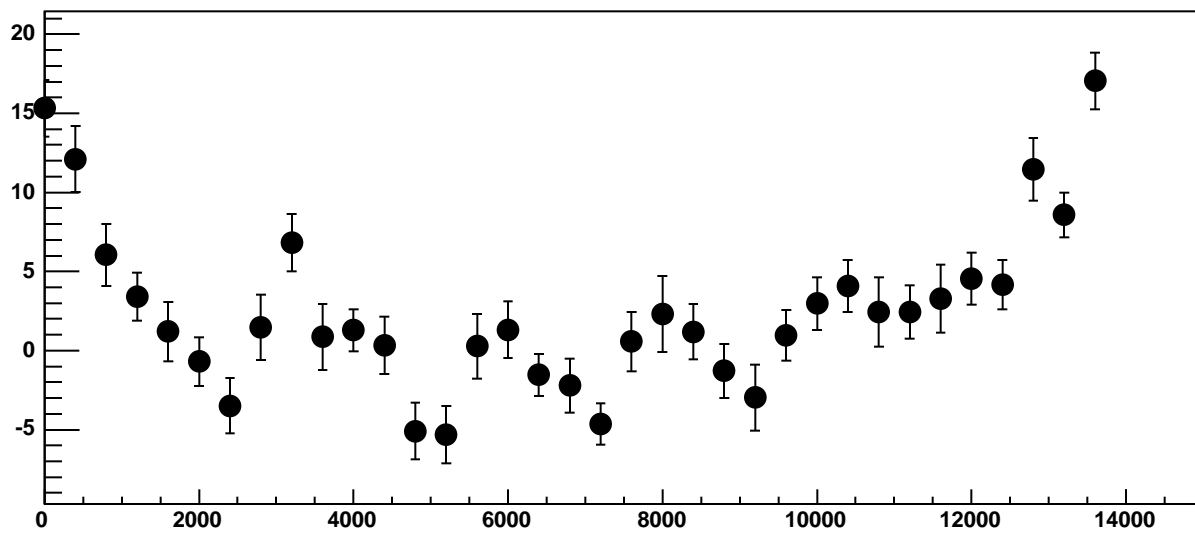
p1

0.01132 ± 0.0001208

Chip 3, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

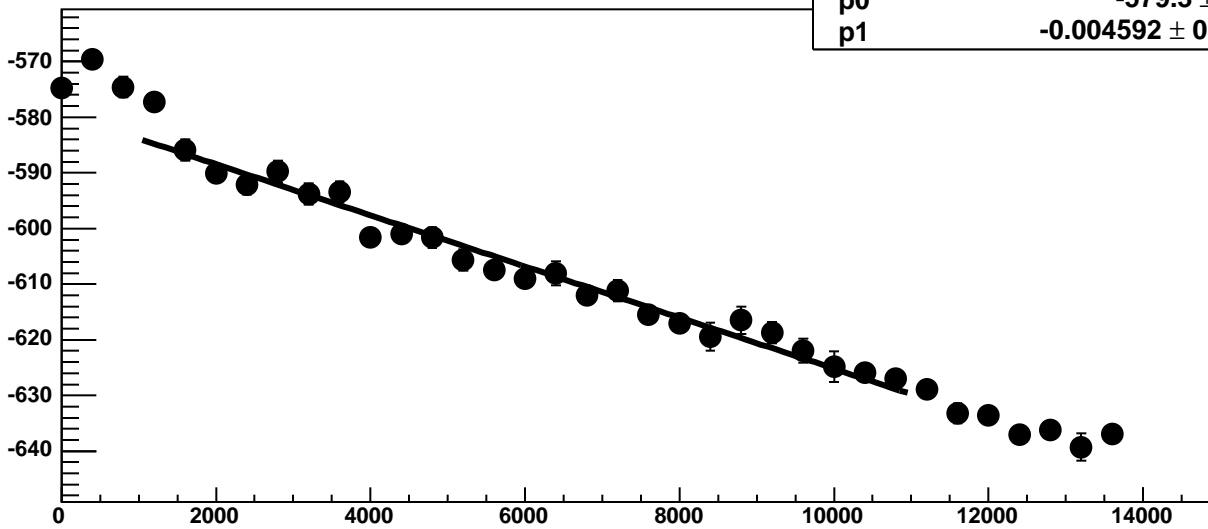


Chip 3, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

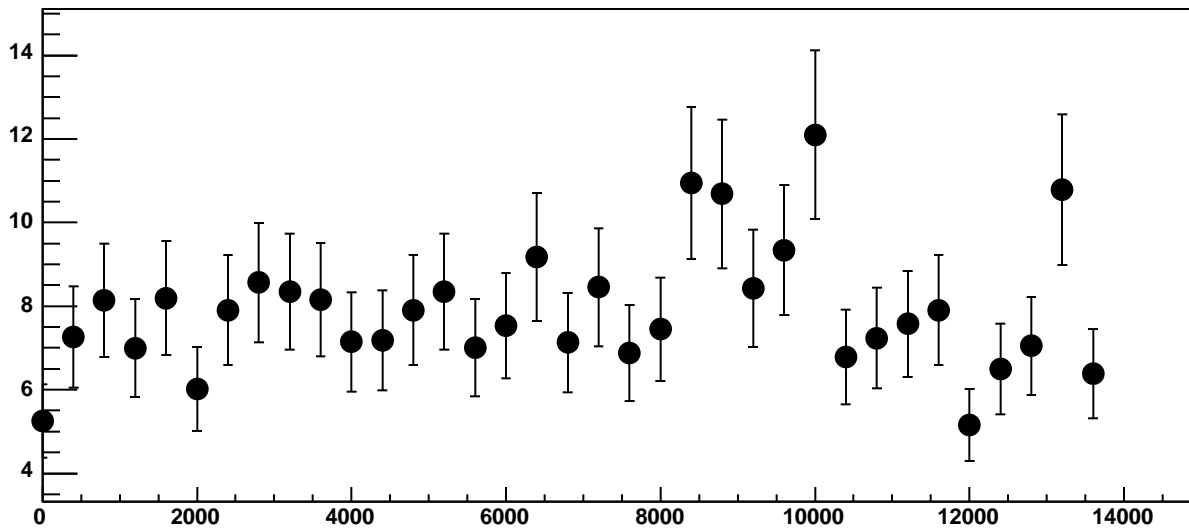


Chip 3, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

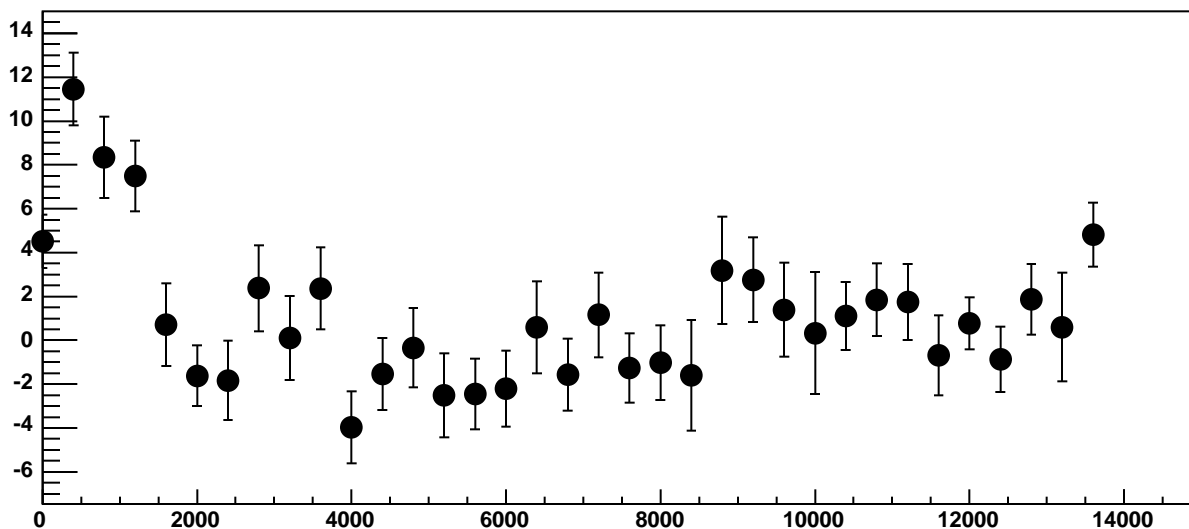
χ^2 / ndf 48.48 / 23
p0 -579.3 ± 0.7972
p1 -0.004592 ± 0.000124



Chip 3, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

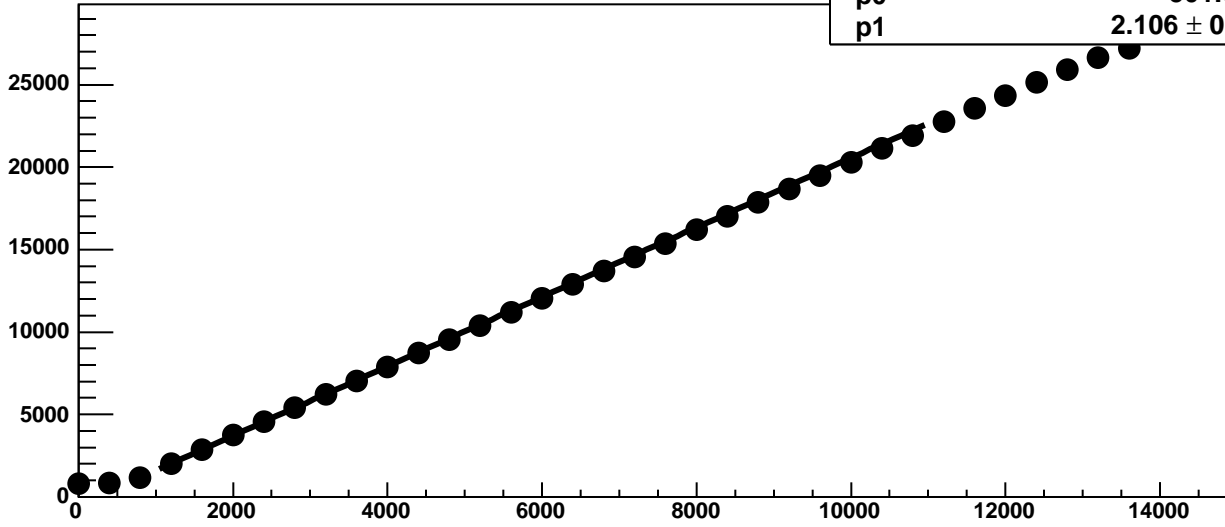


Chip 3, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

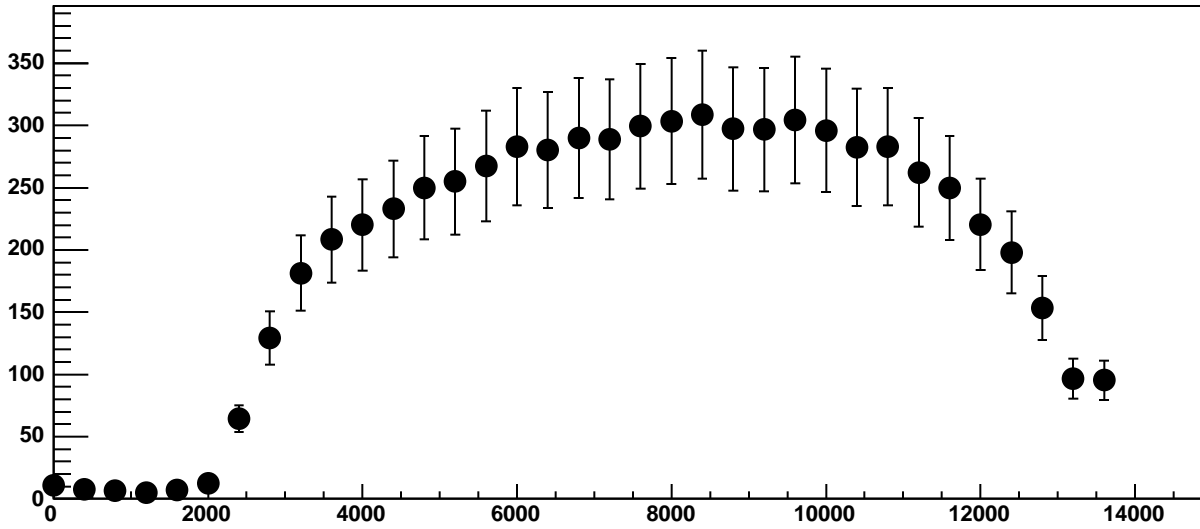


Chip 3, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC

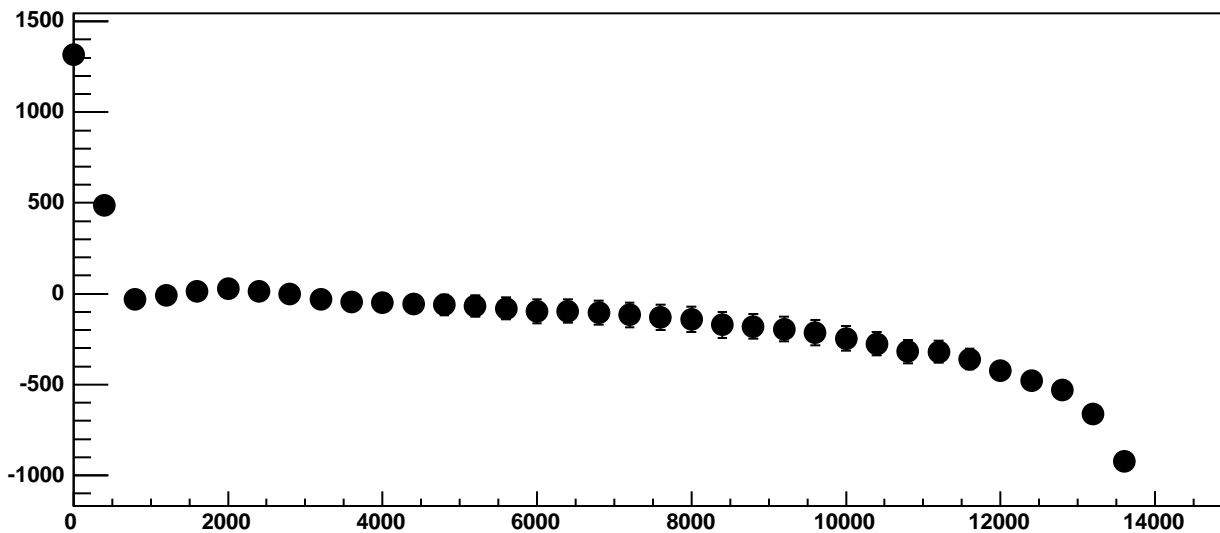
χ^2 / ndf 315.5 / 23
p0 -501.6 ± 2.91
p1 2.106 ± 0.001948



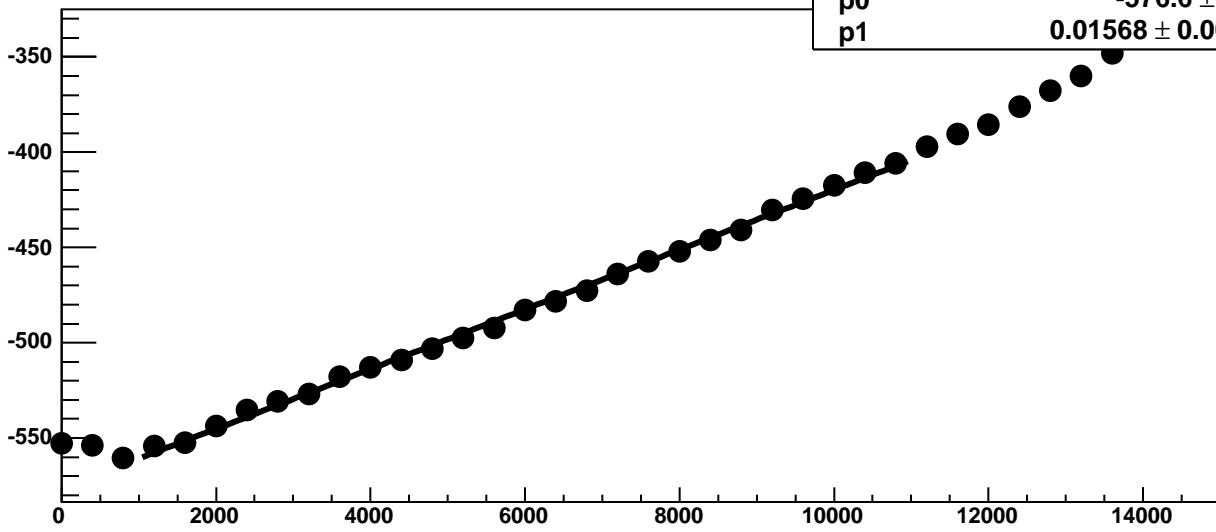
Chip 3, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC

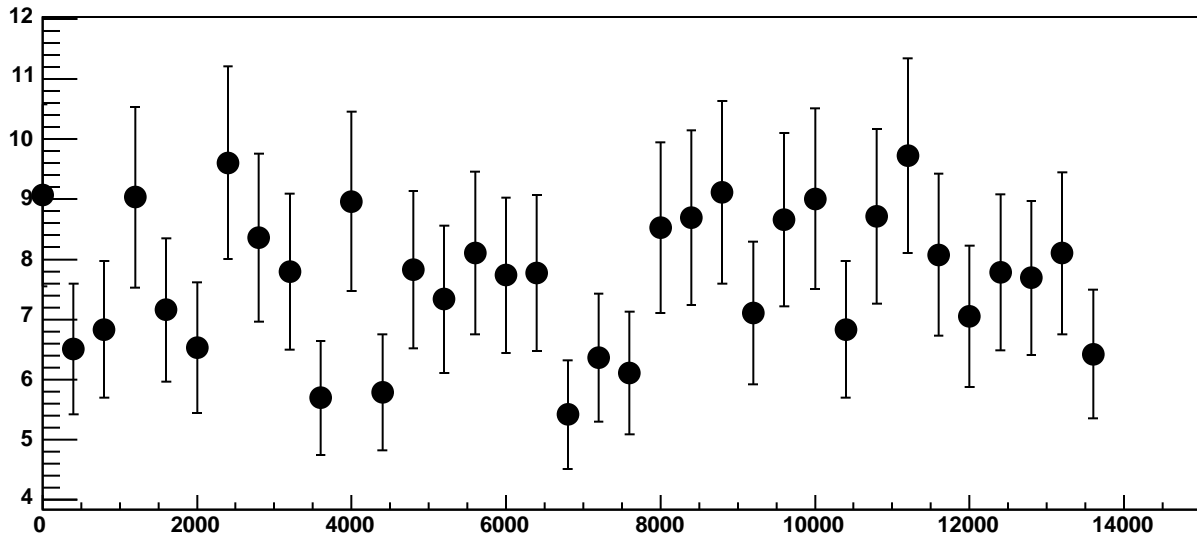


Chip 3, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

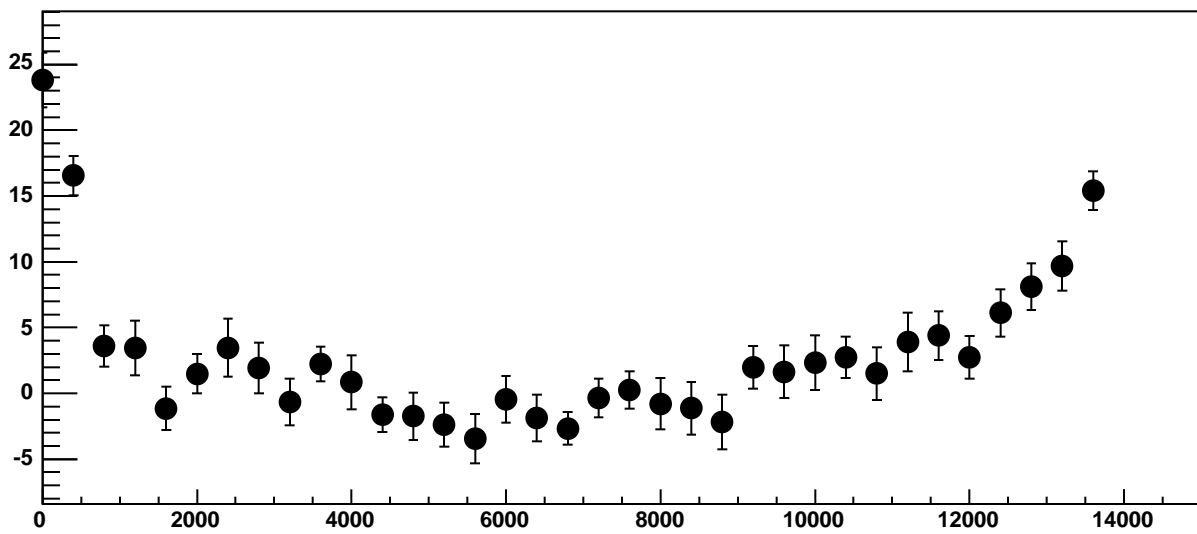


χ^2 / ndf 33.24 / 23
p0 -576.6 ± 0.8068
p1 0.01568 ± 0.0001238

Chip 3, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

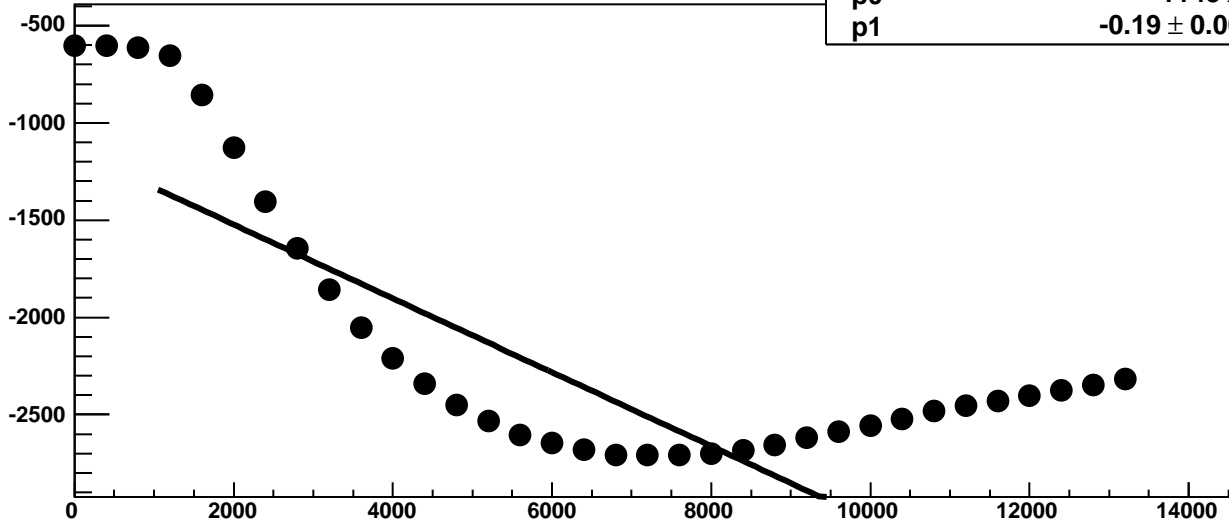


Chip 3, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

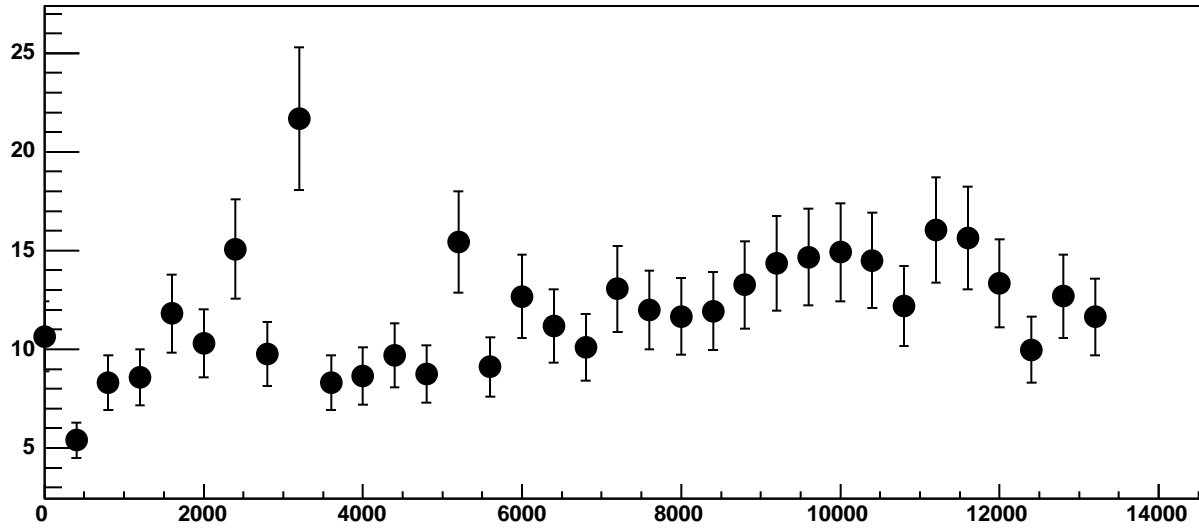


Chip 3, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

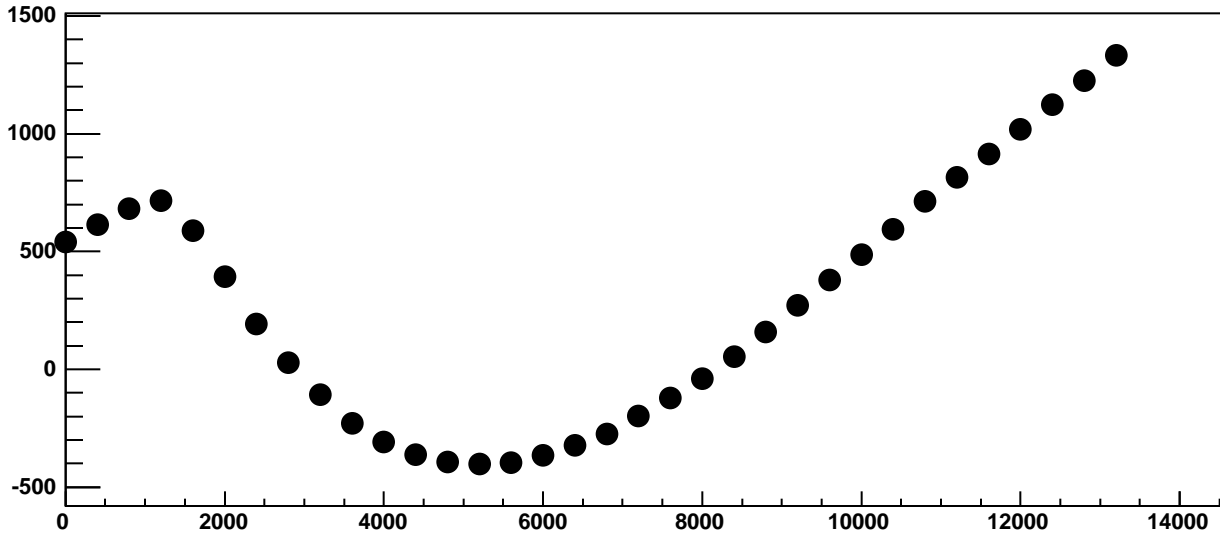
χ^2 / ndf 5.561e+05 / 23
p0 -1143 ± 1.145
p1 -0.19 ± 0.0001882



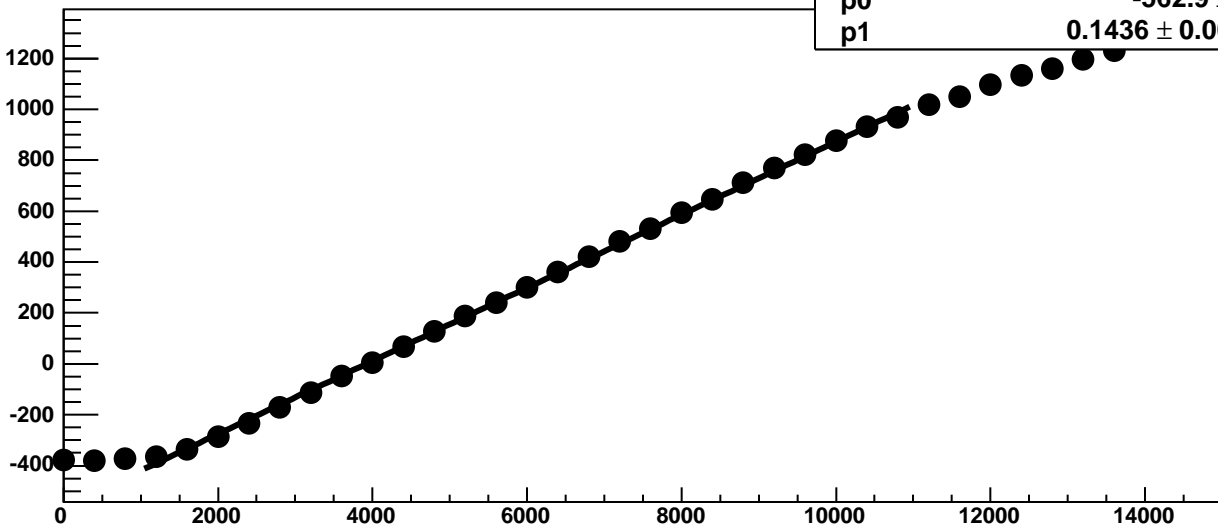
Chip 3, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



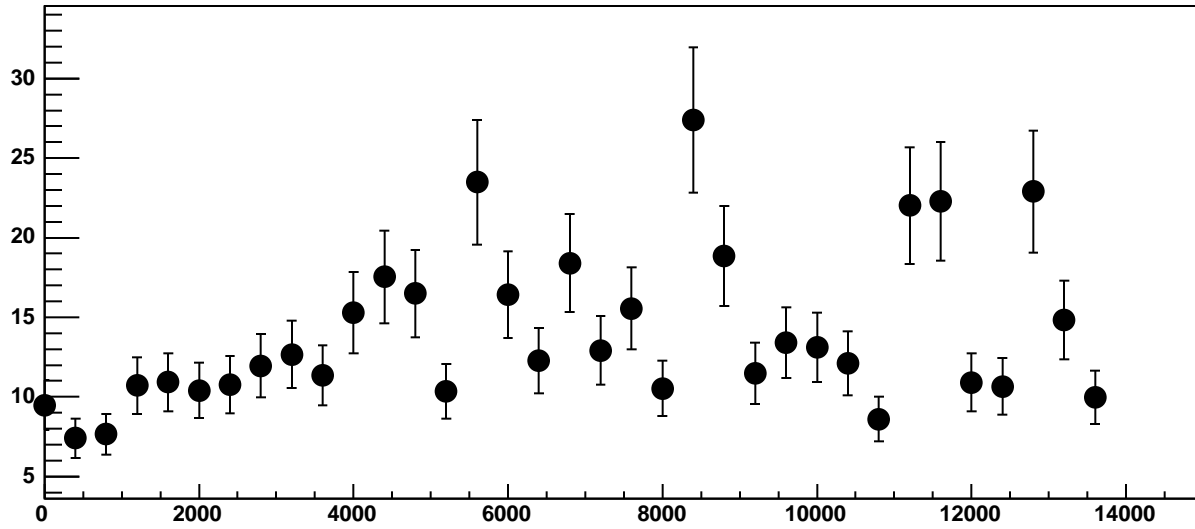
Chip 3, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



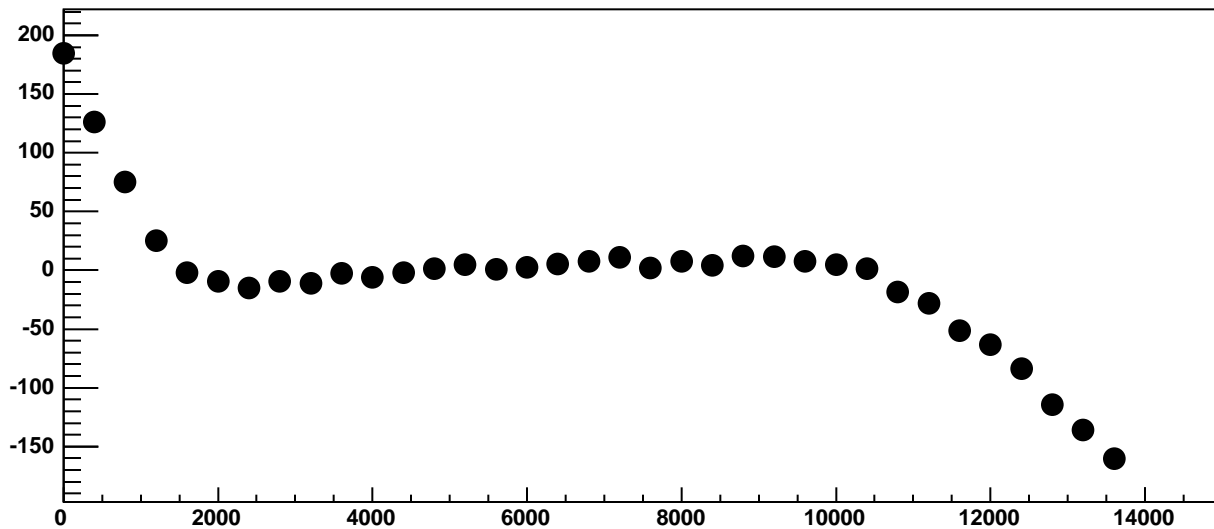
Chip 3, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



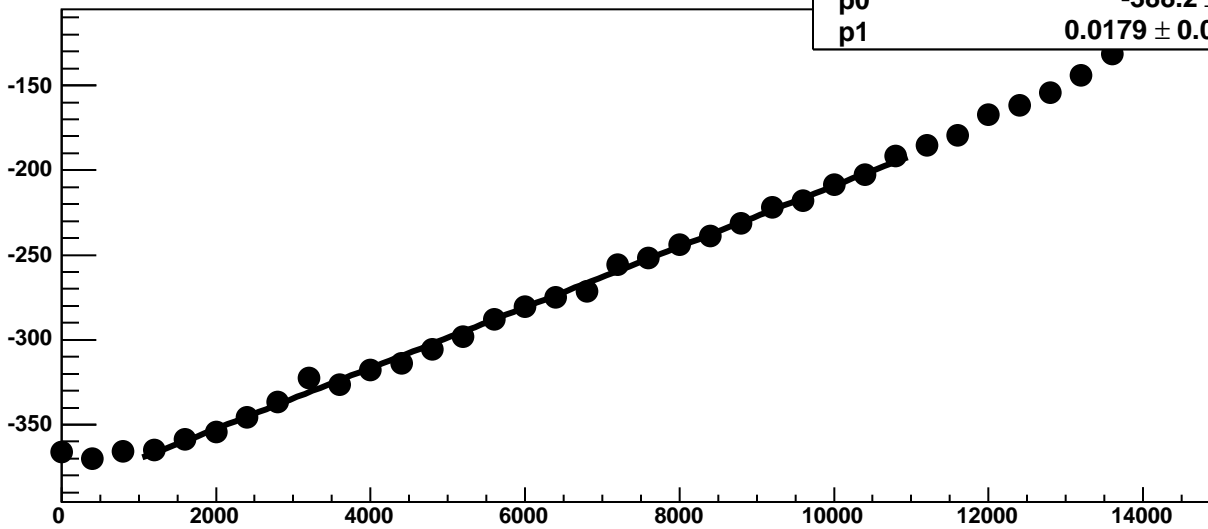
Chip 3, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

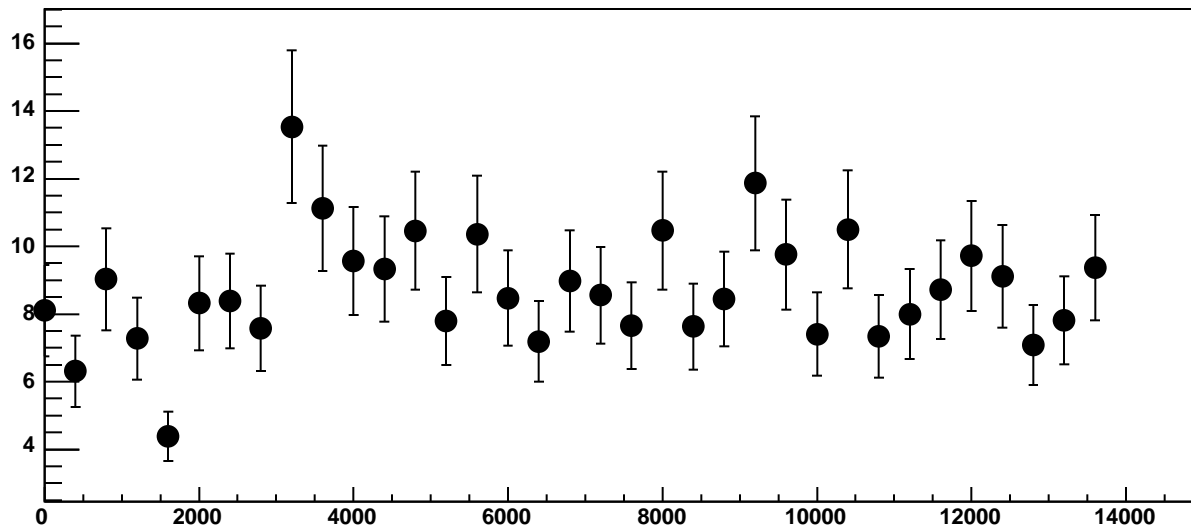


Chip 3, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

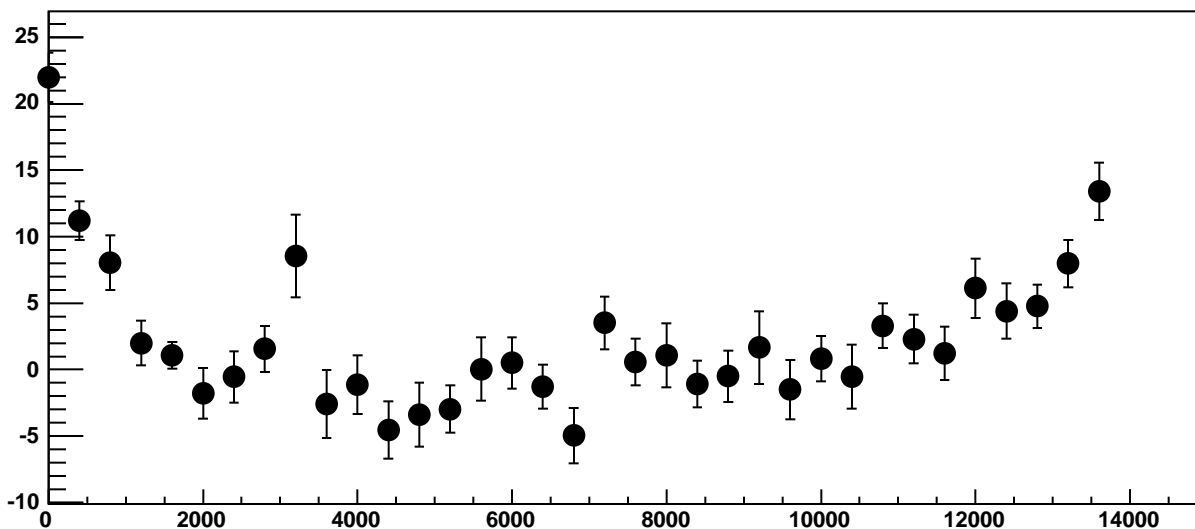


χ^2 / ndf 37.85 / 23
p0 -388.2 ± 0.7731
p1 0.0179 ± 0.0001214

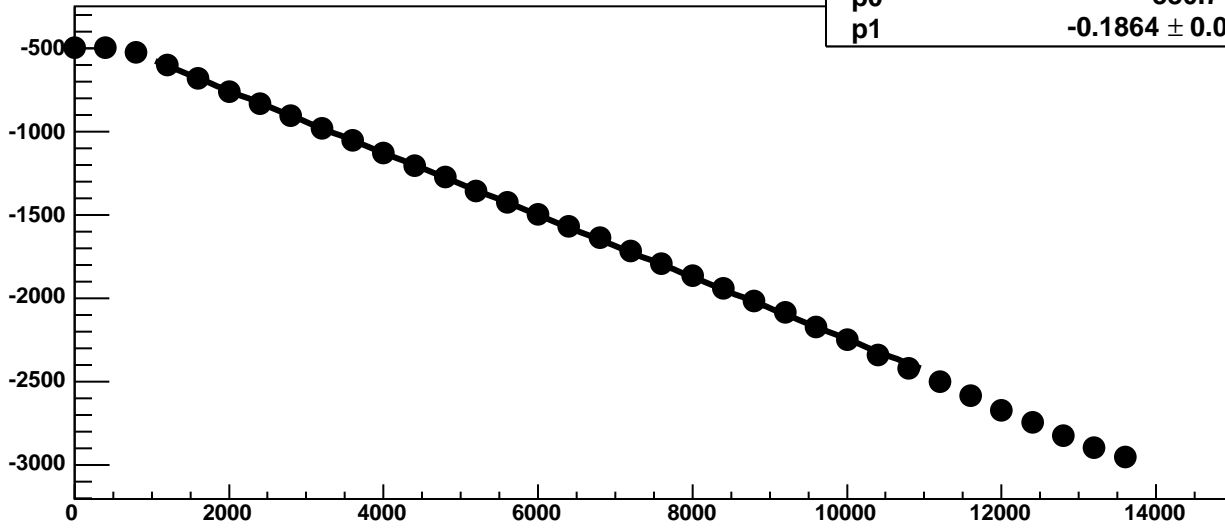
Chip 3, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC

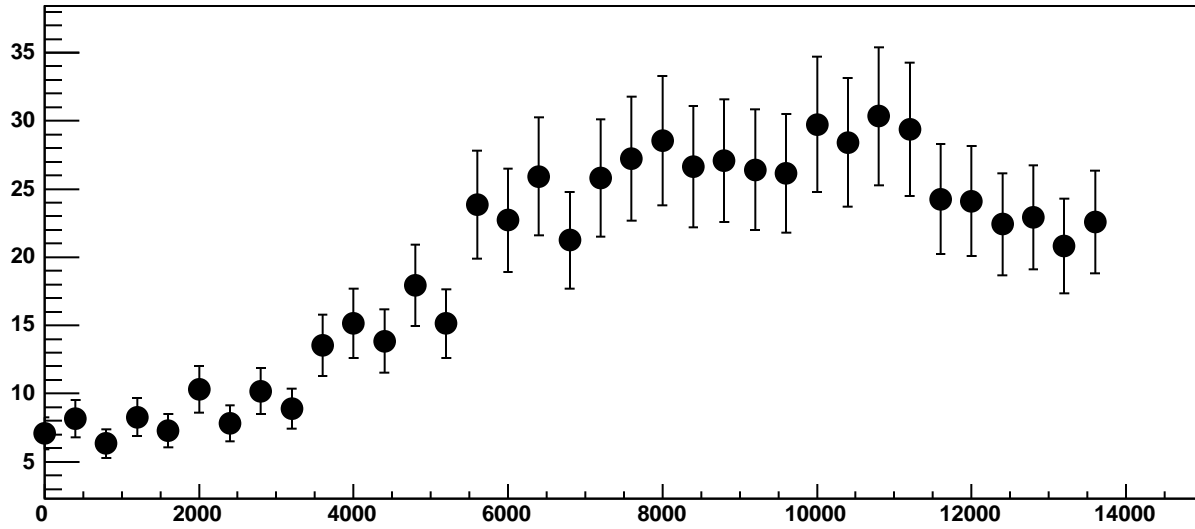


Chip 3, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

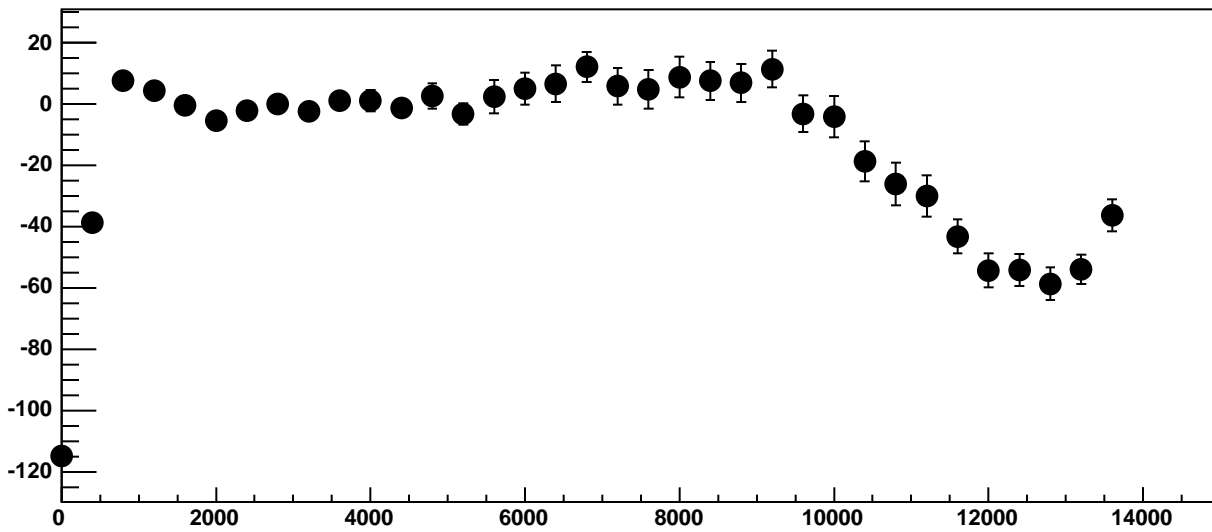


χ^2 / ndf	56.56 / 23
p0	-380.7 ± 1.169
p1	-0.1864 ± 0.0002786

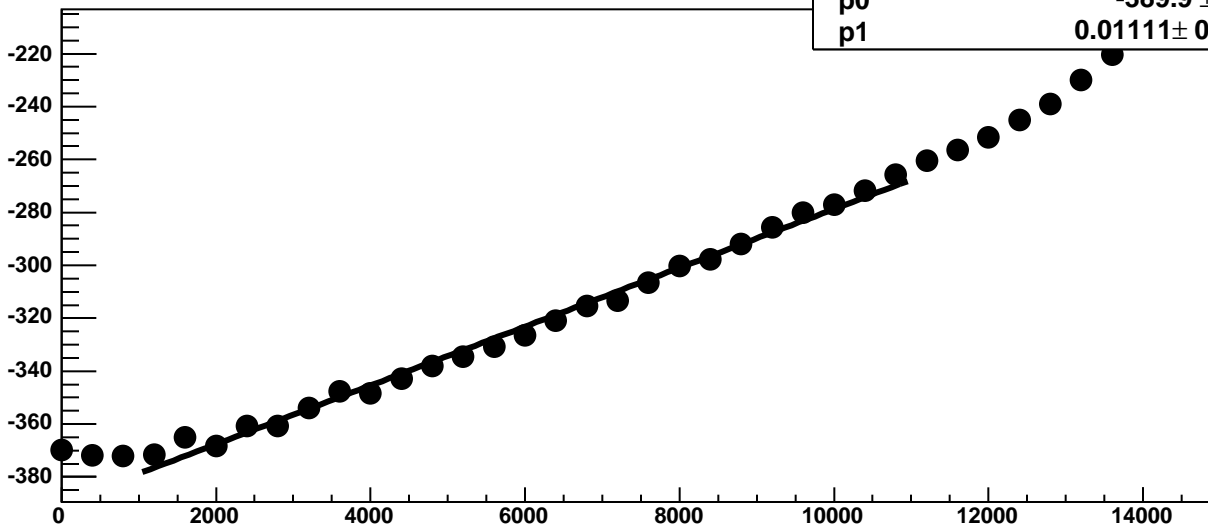
Chip 3, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



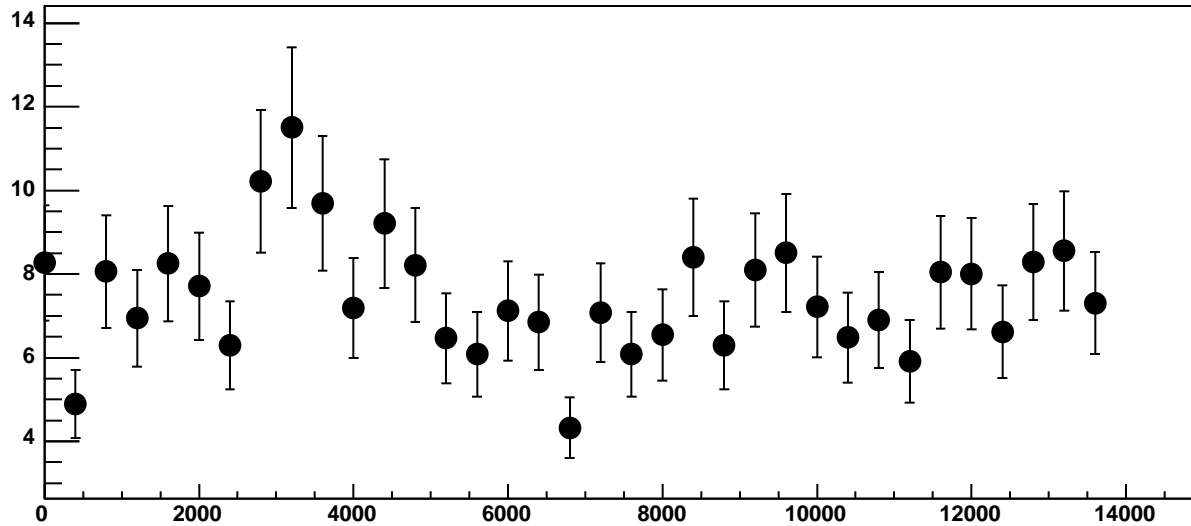
Chip 3, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



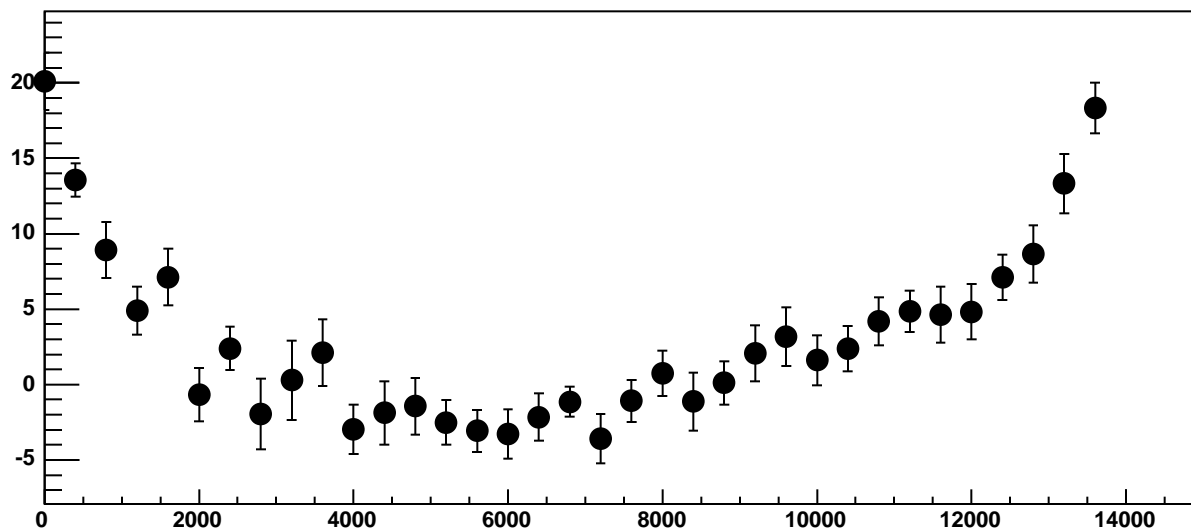
Chip 3, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



Chip 3, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC

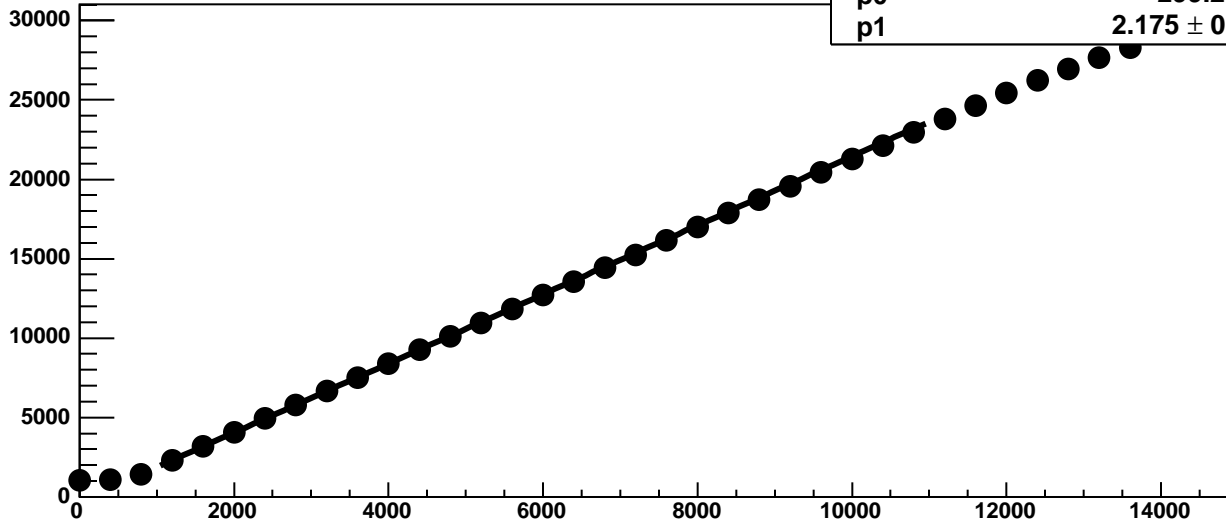


Chip 3, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

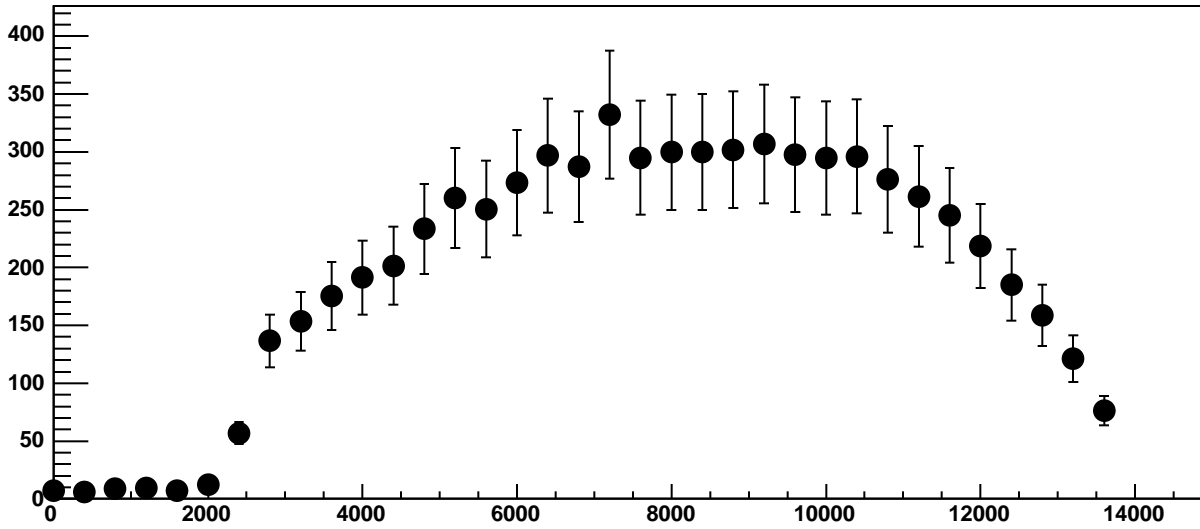


Chip 3, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC

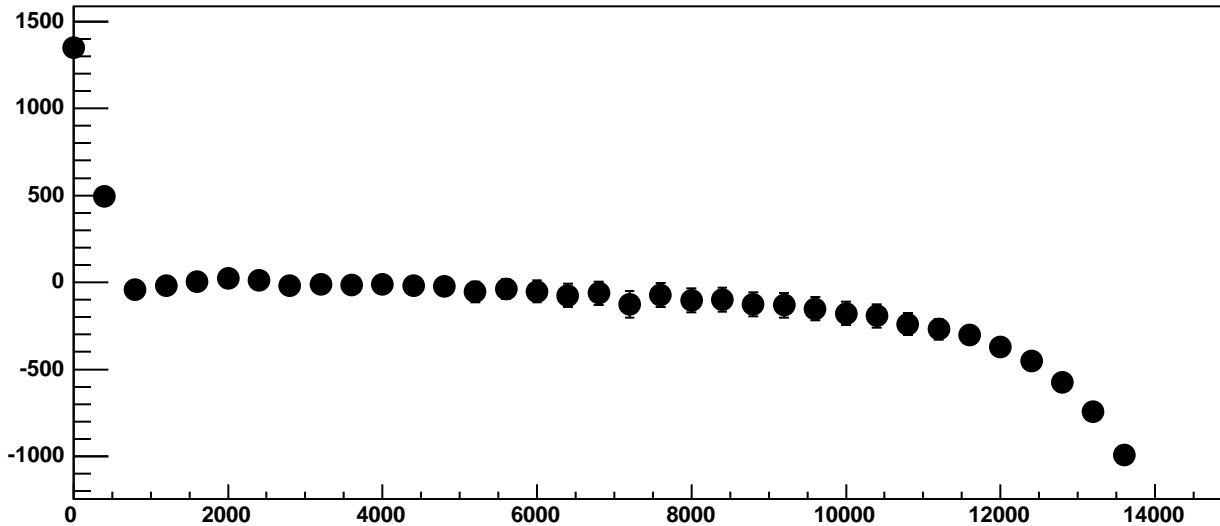
χ^2 / ndf 209.5 / 23
p0 -299.2 ± 3.599
p1 2.175 ± 0.002132



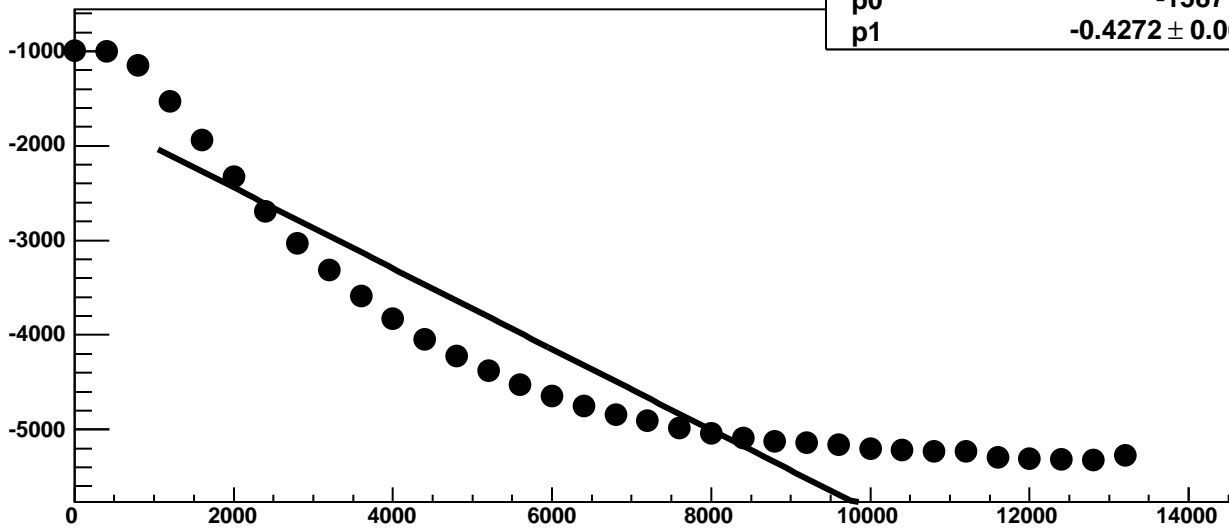
Chip 3, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC



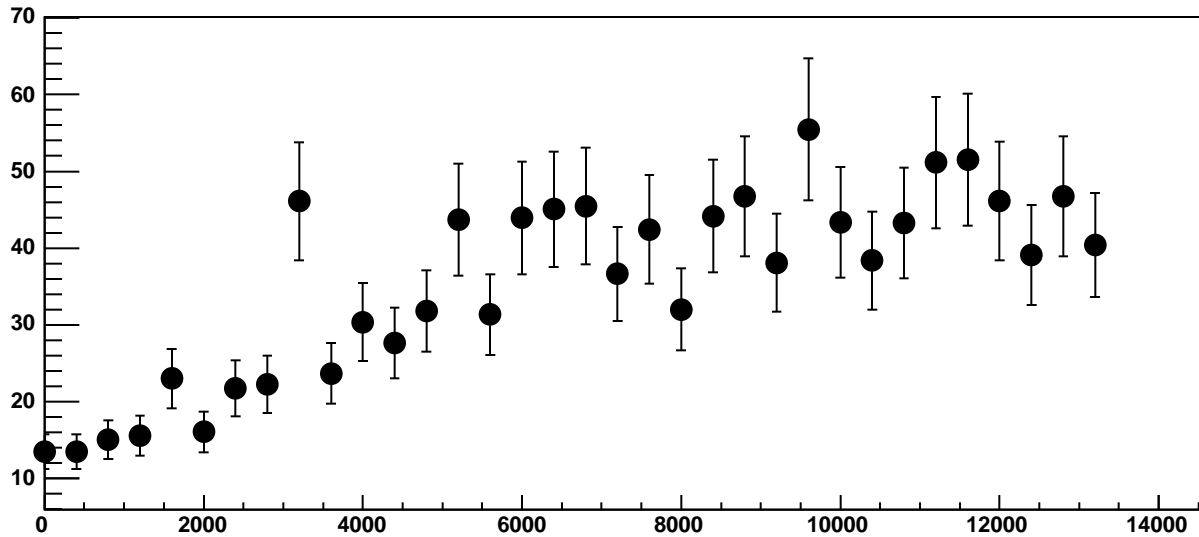
Chip 3, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC



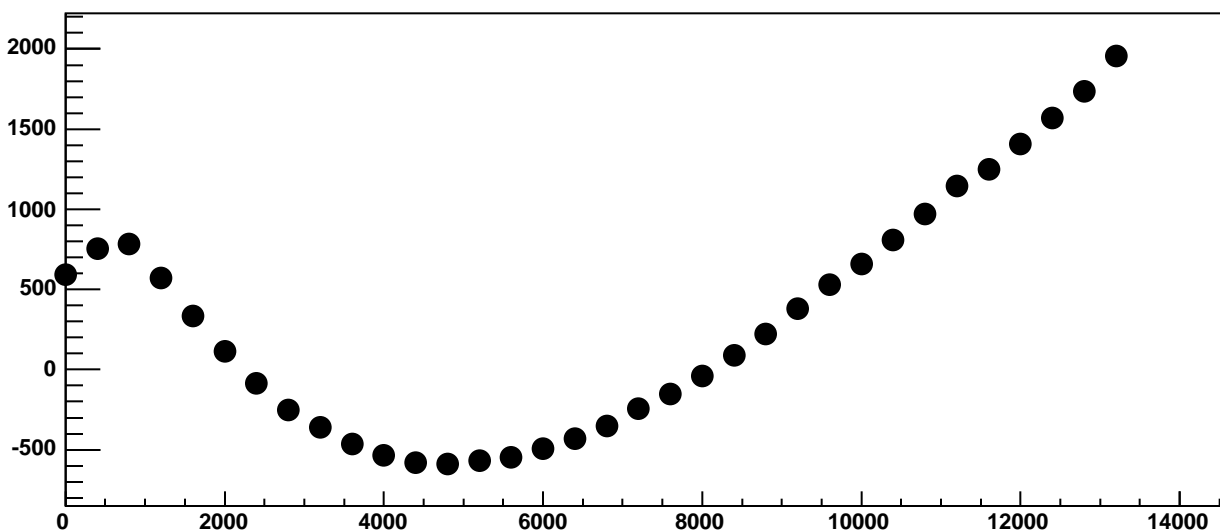
Chip 3, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC



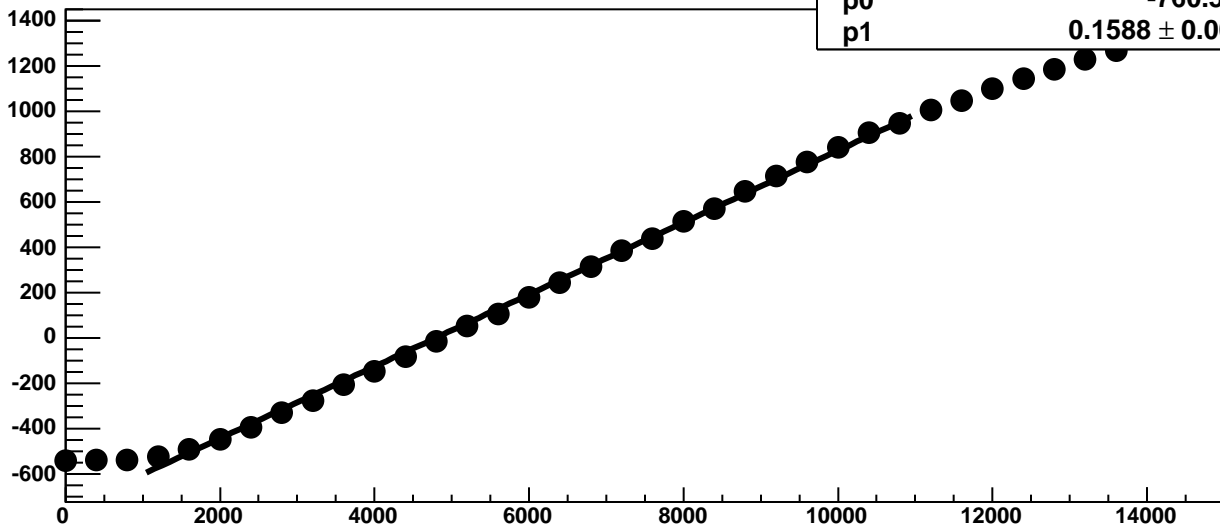
Chip 3, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



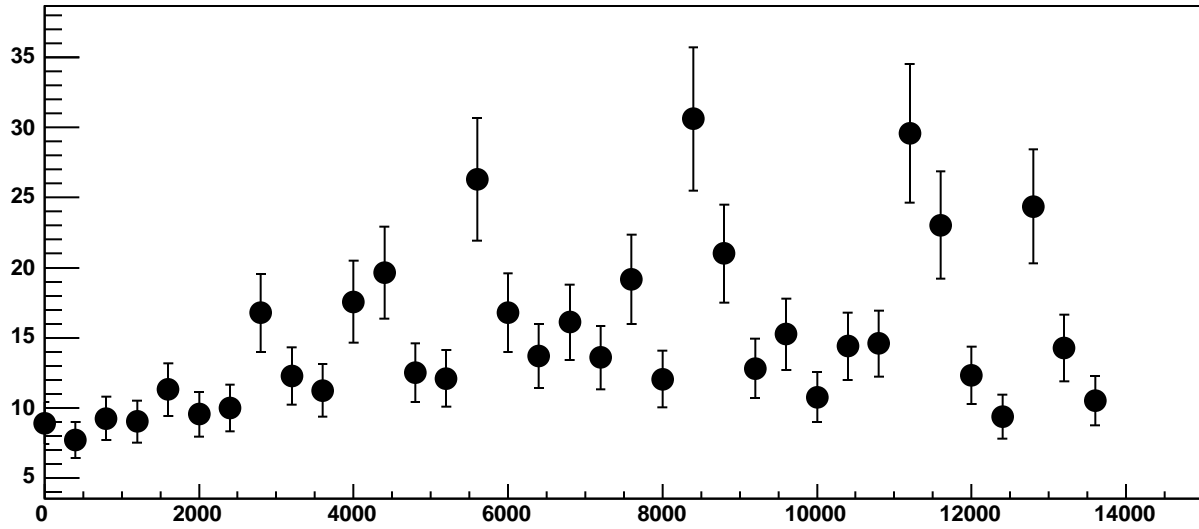
Chip 3, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC



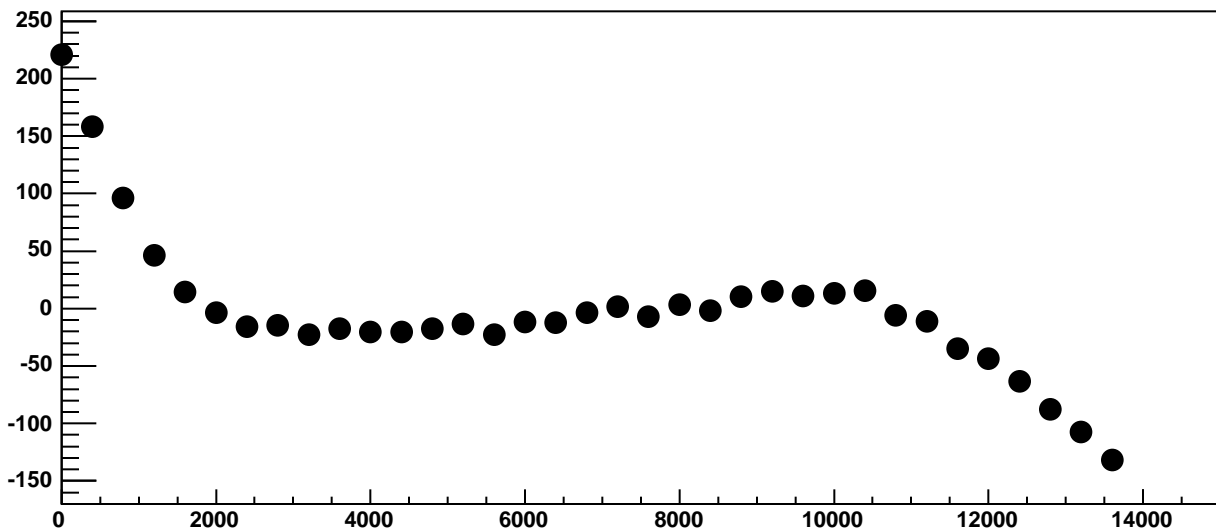
Chip 3, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



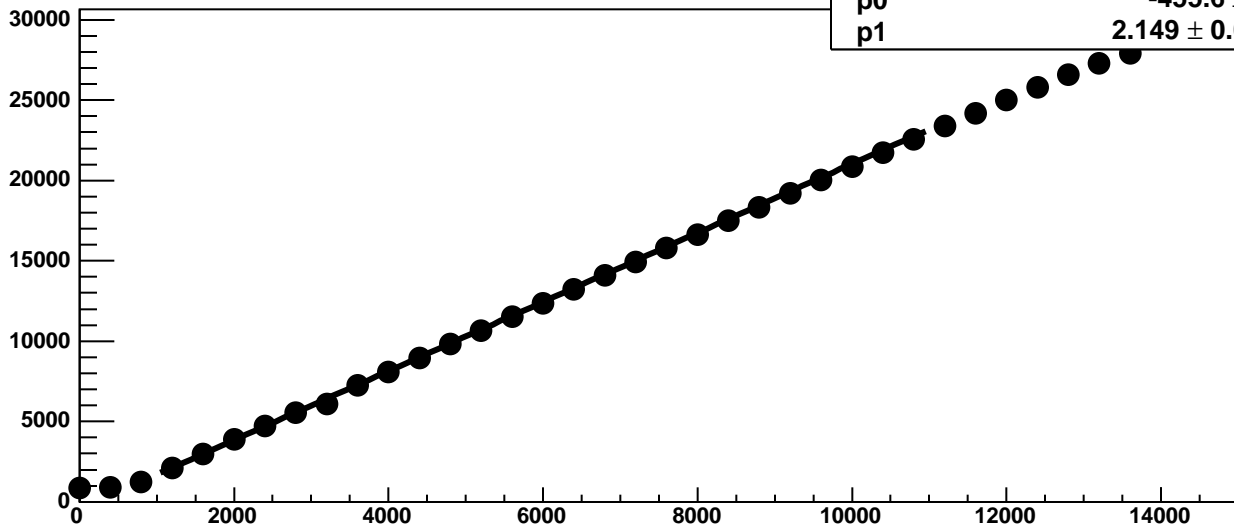
Chip 3, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



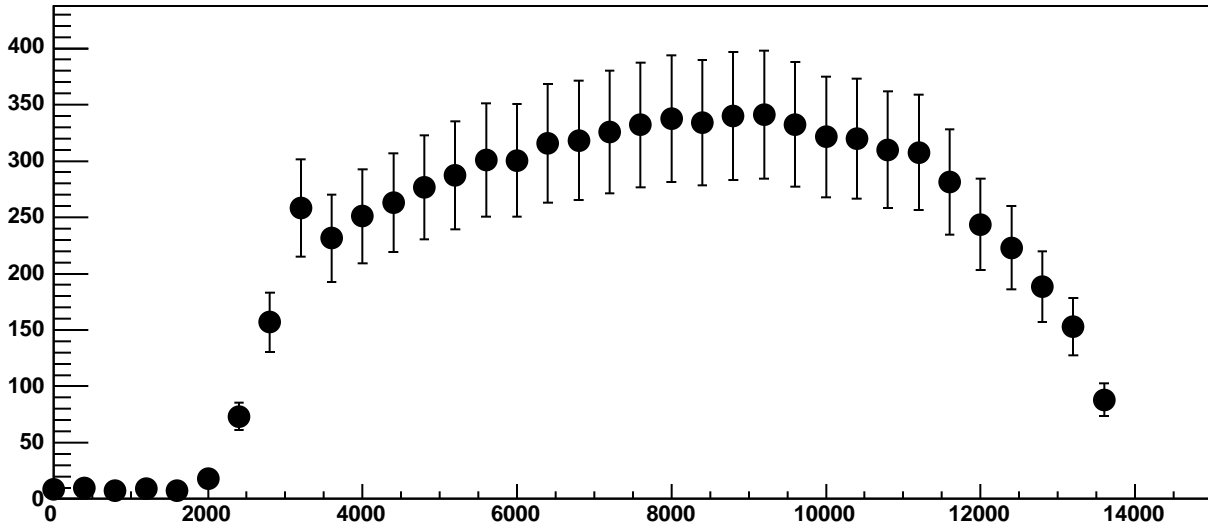
Chip 3, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



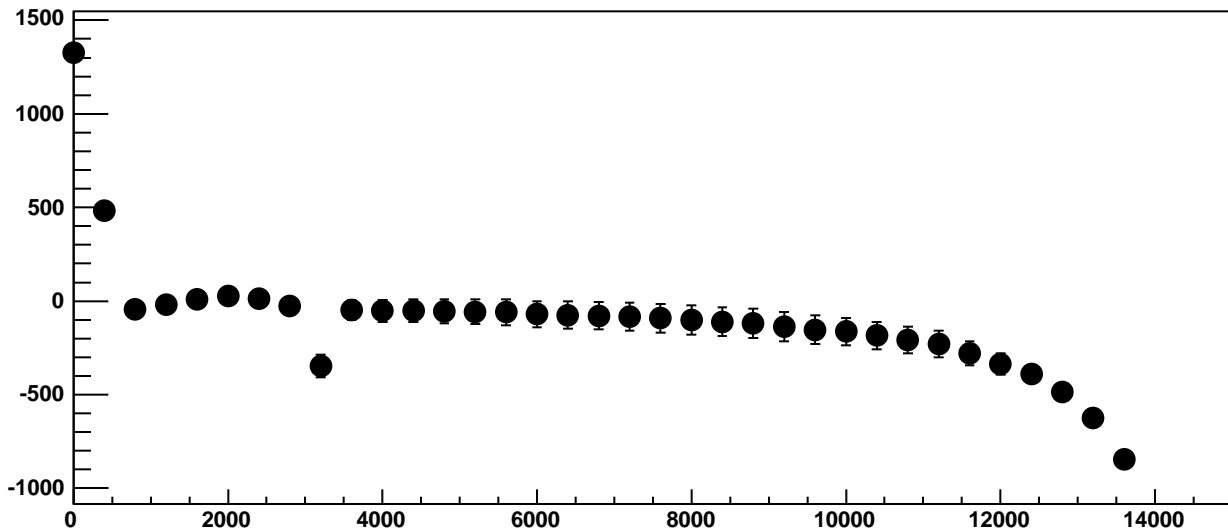
Chip 3, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC



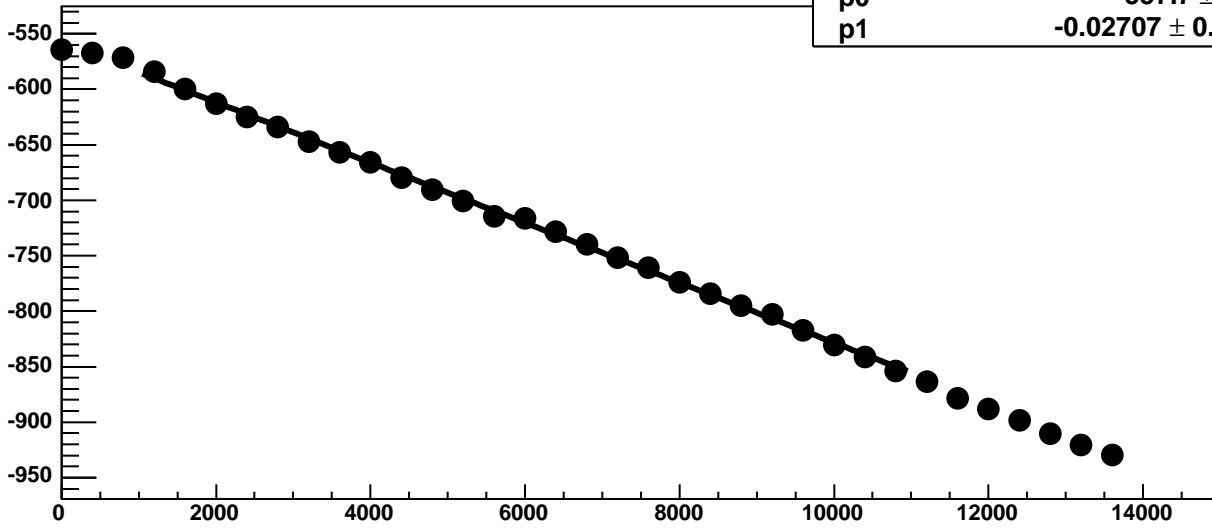
Chip 3, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

43.38 / 23

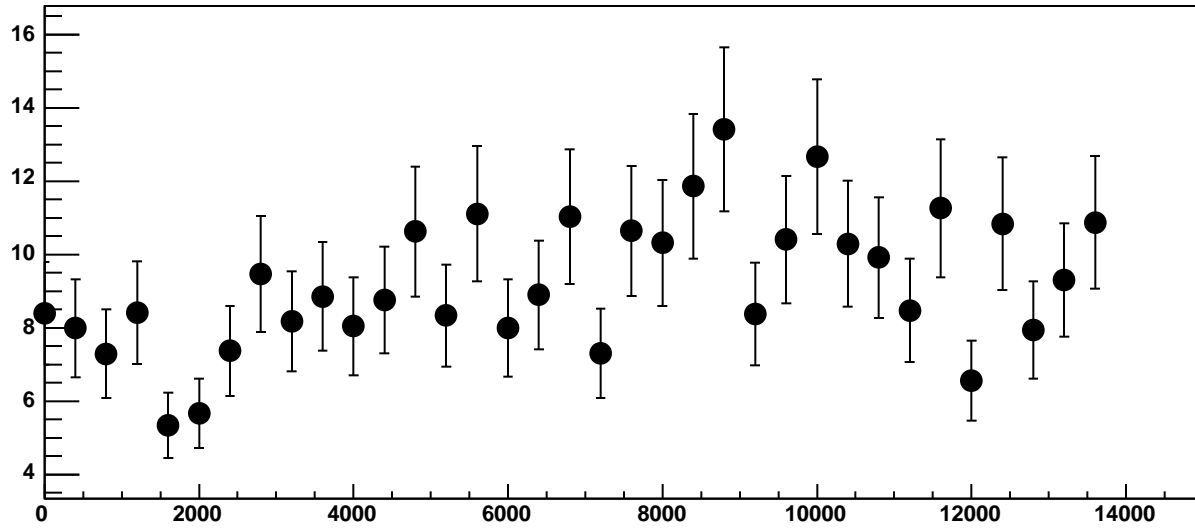
p0

-557.7 ± 0.7976

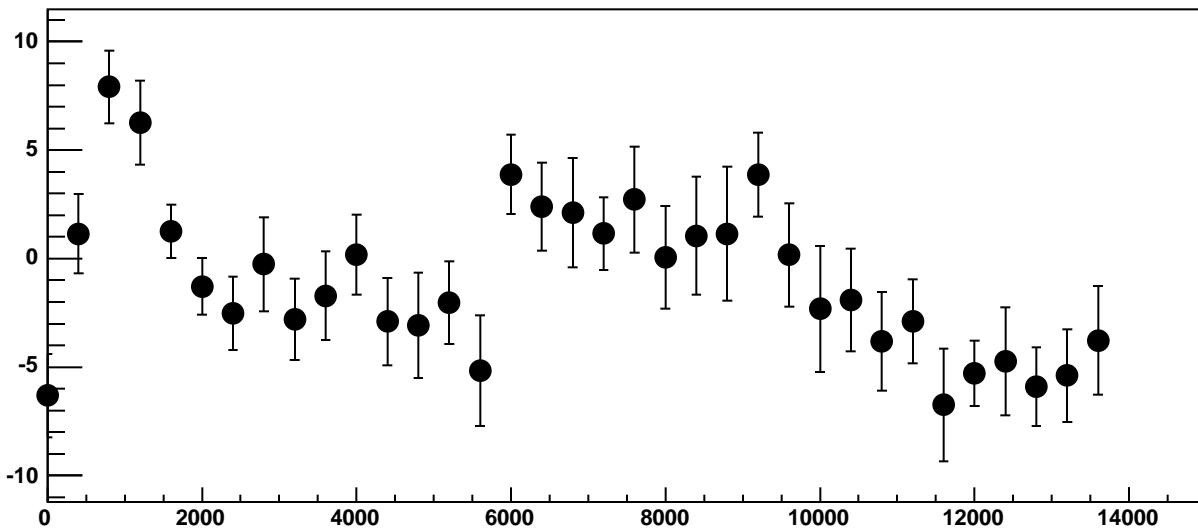
p1

-0.02707 ± 0.000136

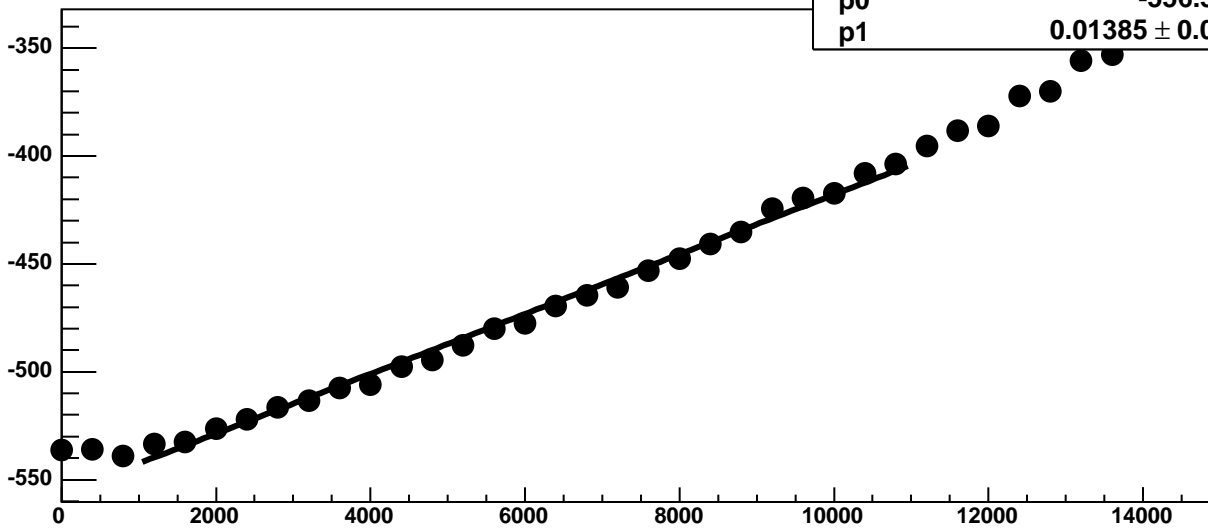
Chip 3, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



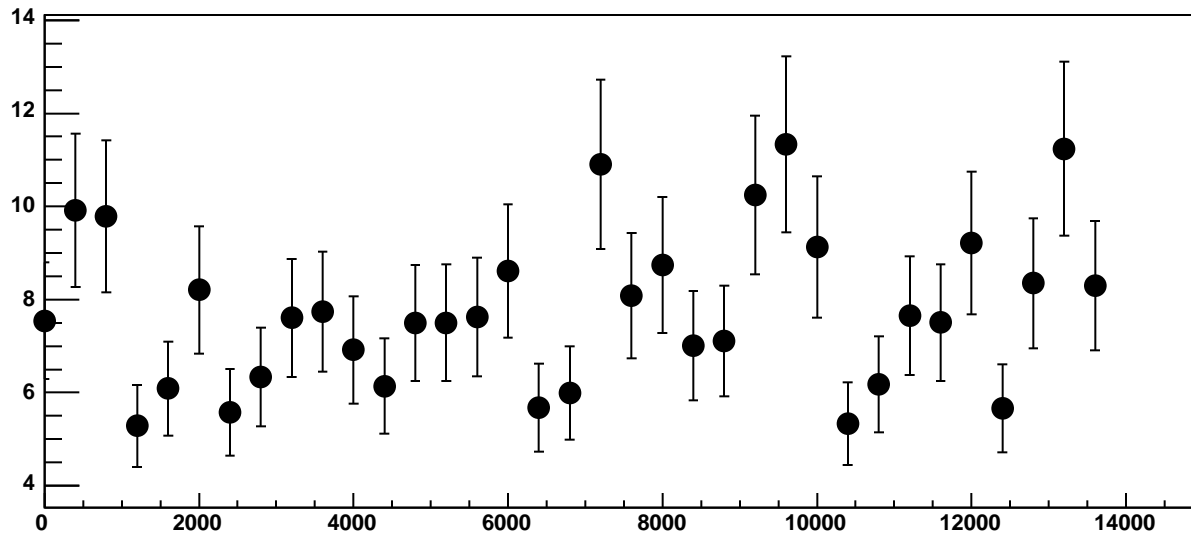
Chip 3, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



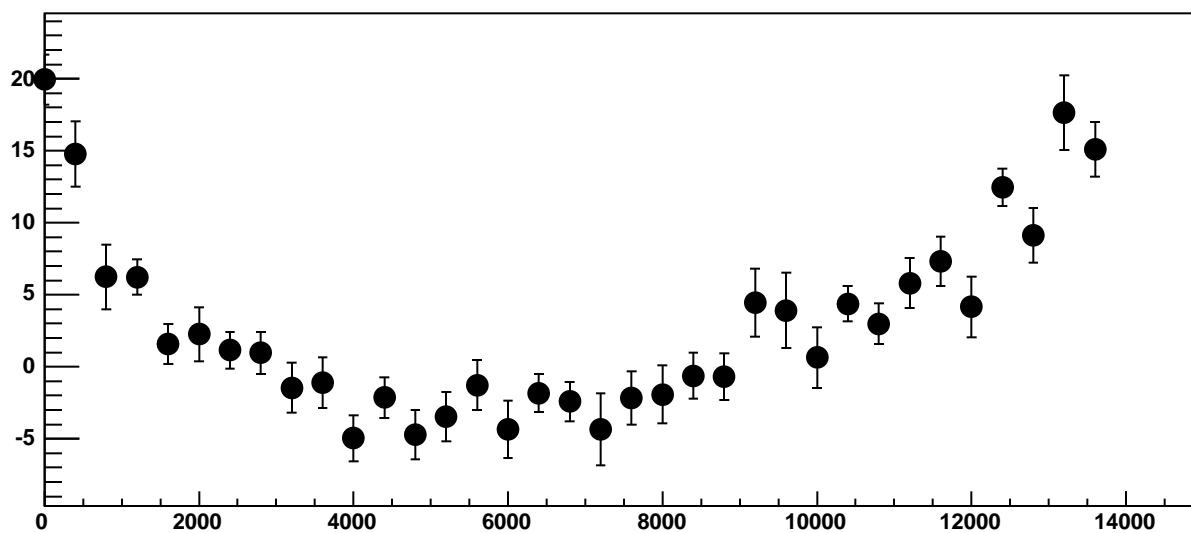
Chip 3, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC



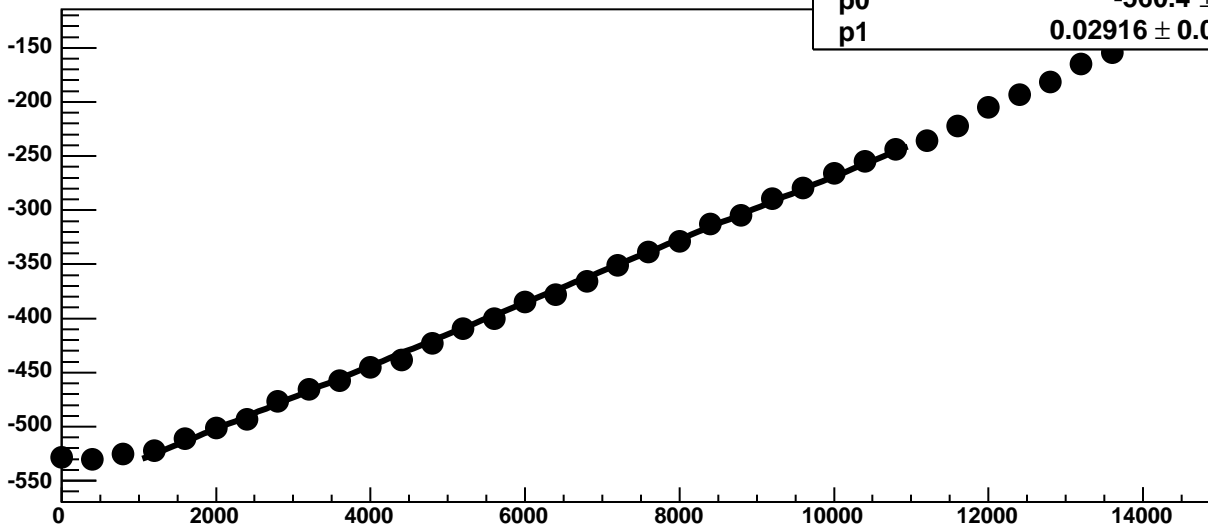
Chip 3, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



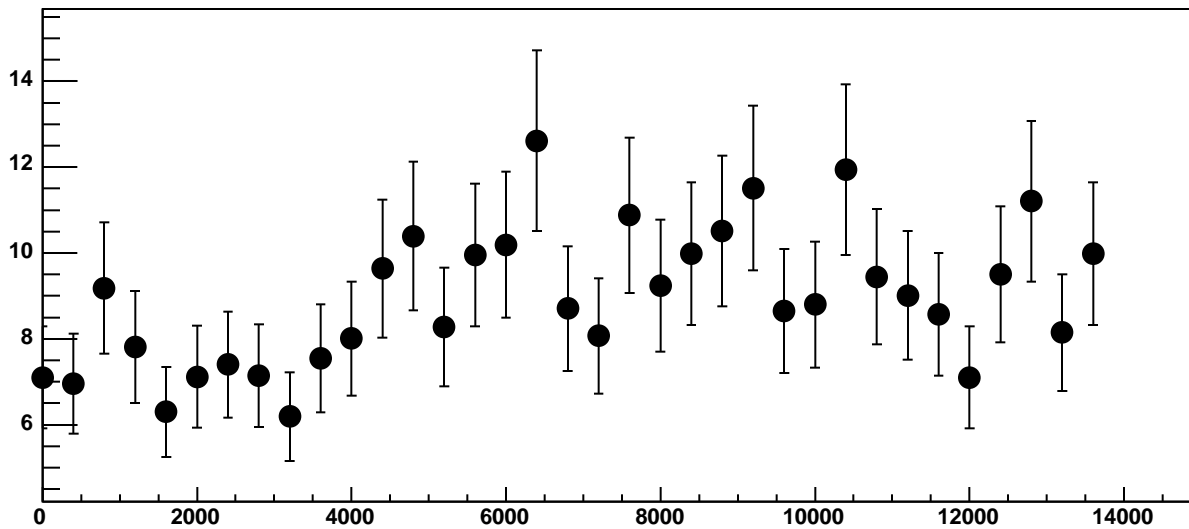
Chip 3, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



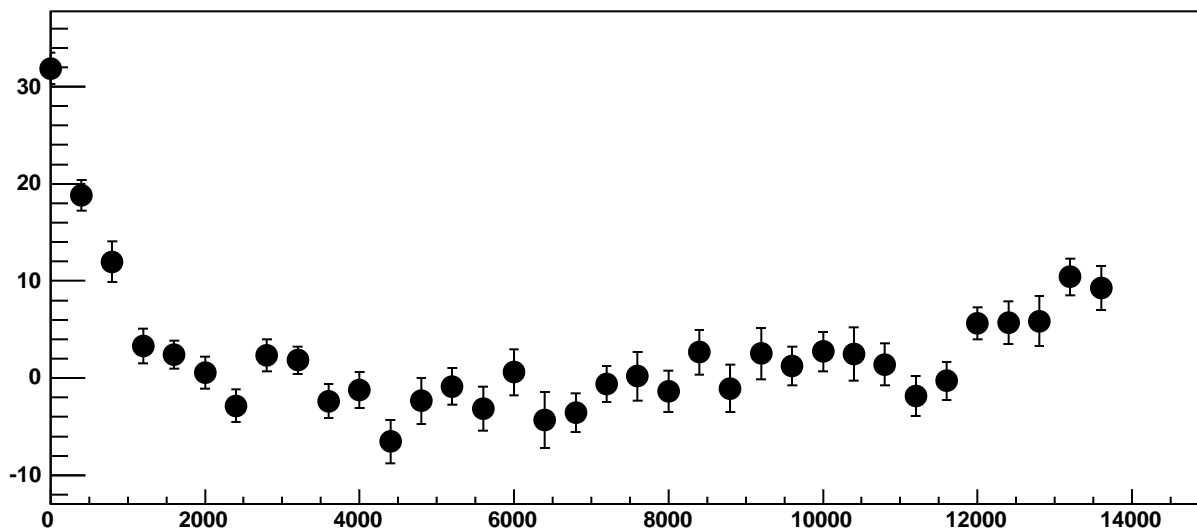
Chip 3, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC



Chip 3, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC

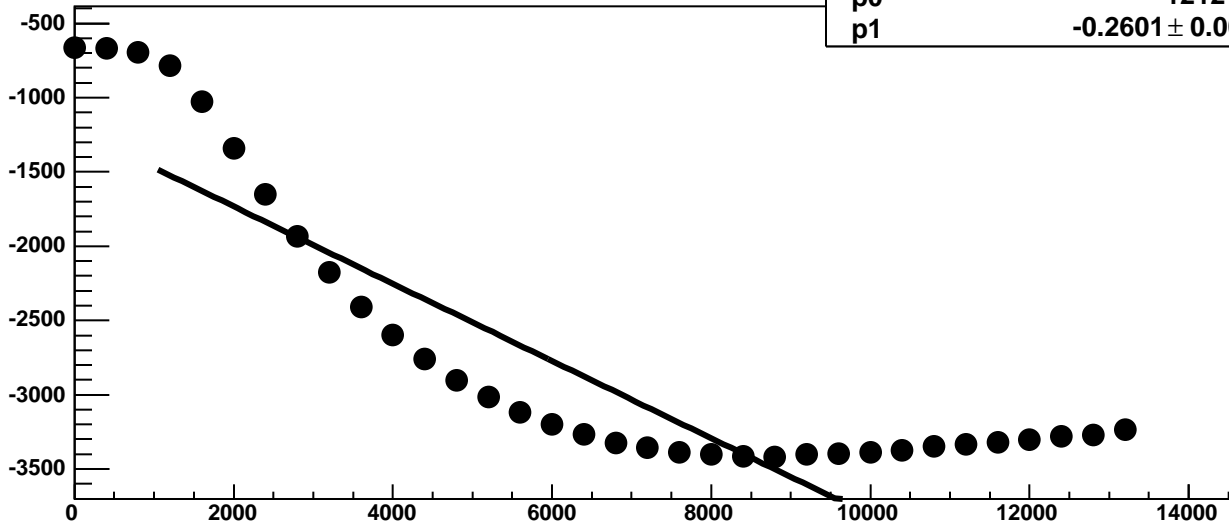


Chip 3, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC

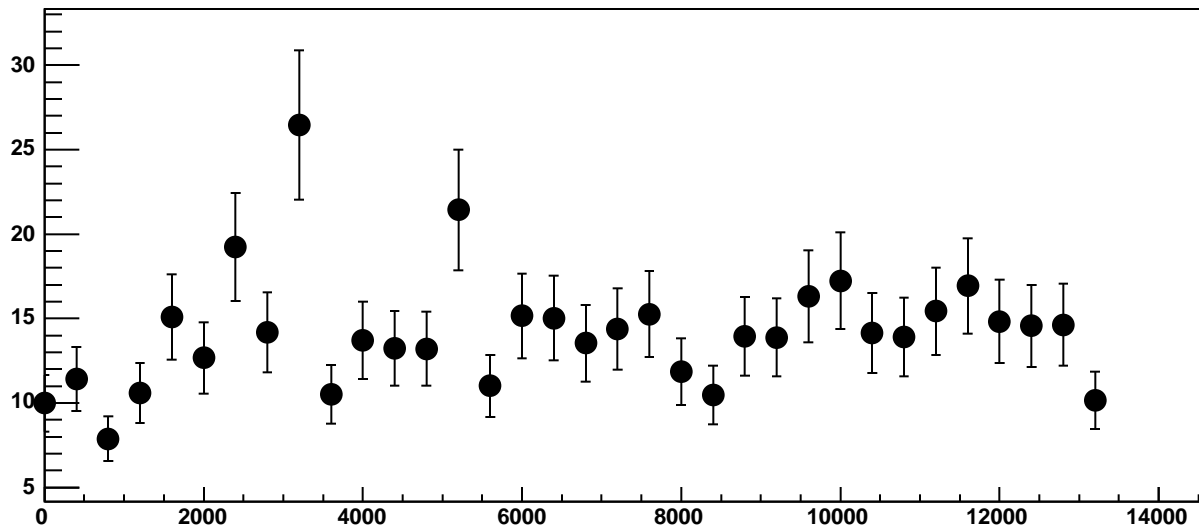


Chip 3, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

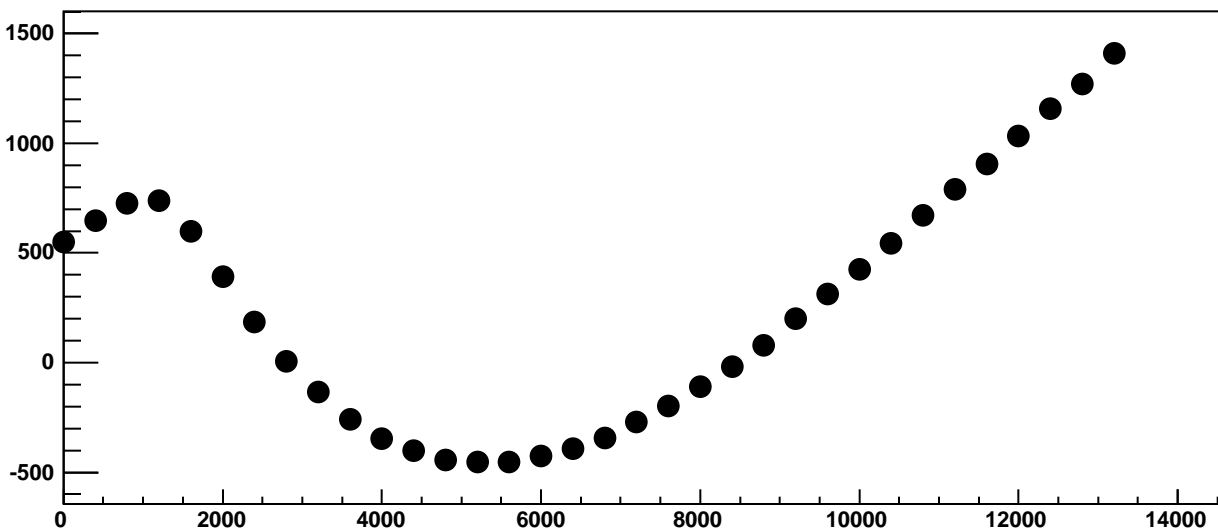
χ^2 / ndf 3.931e+05 / 23
p0 -1212 ± 1.441
p1 -0.2601 ± 0.0002187



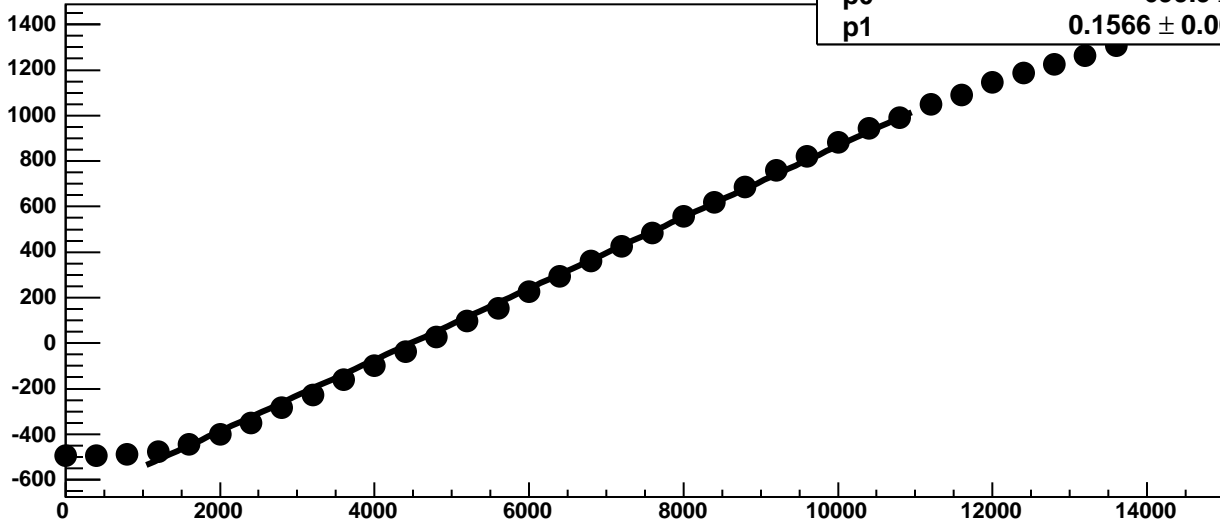
Chip 3, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



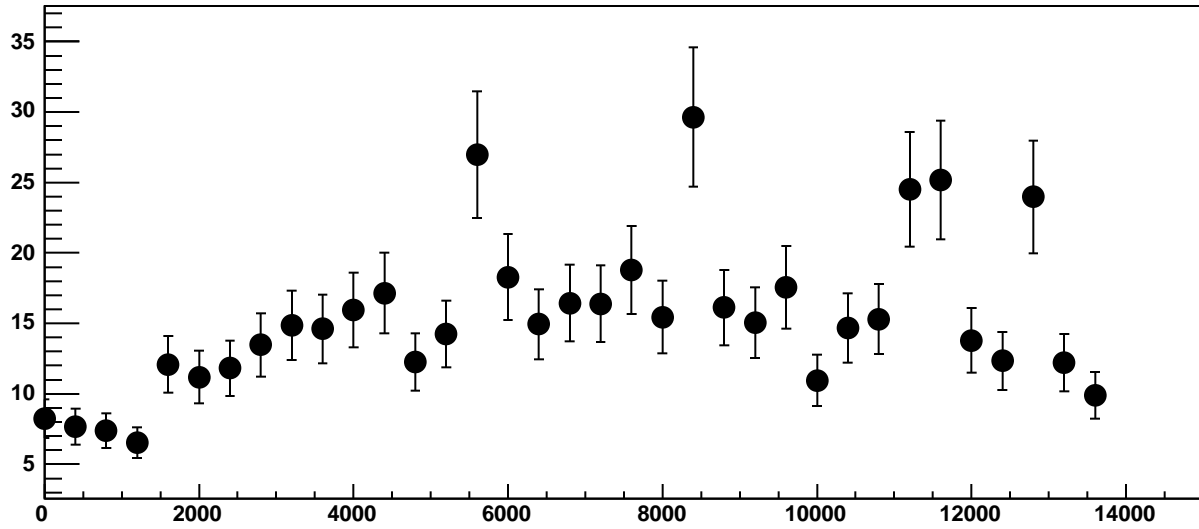
Chip 3, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



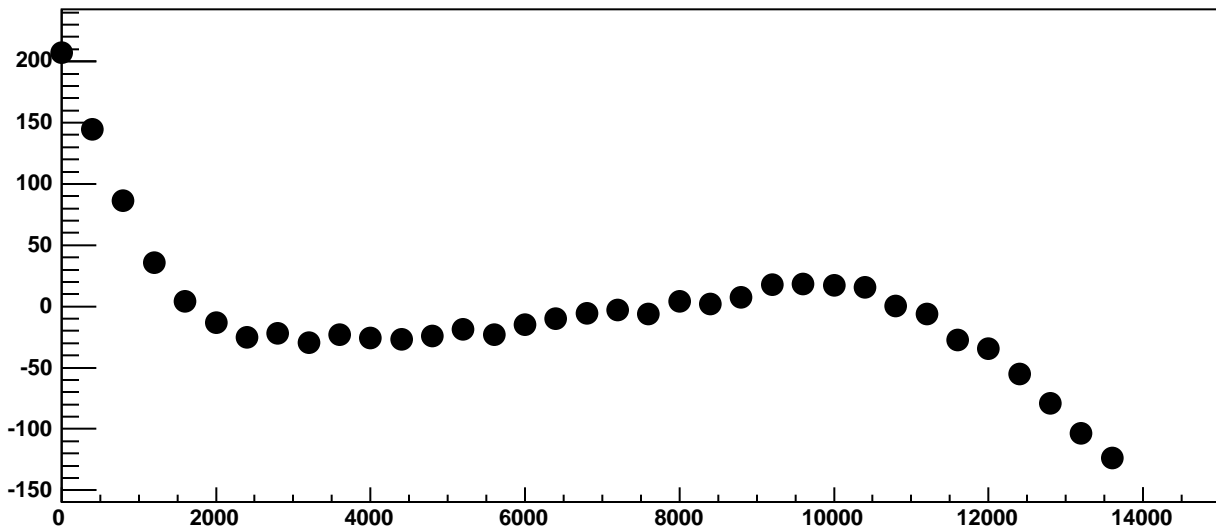
Chip 3, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



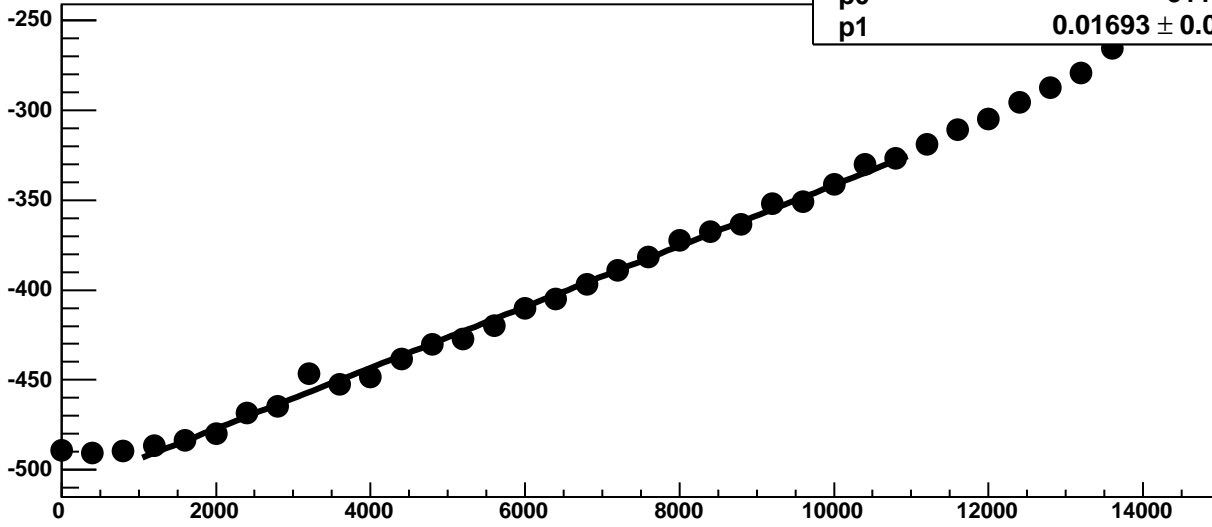
Chip 3, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

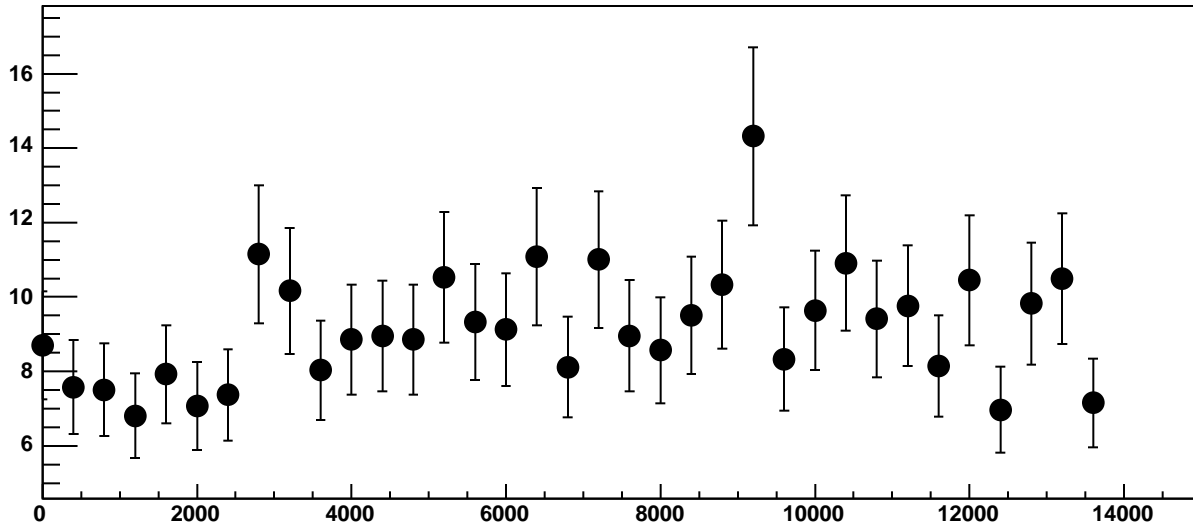


Chip 3, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC

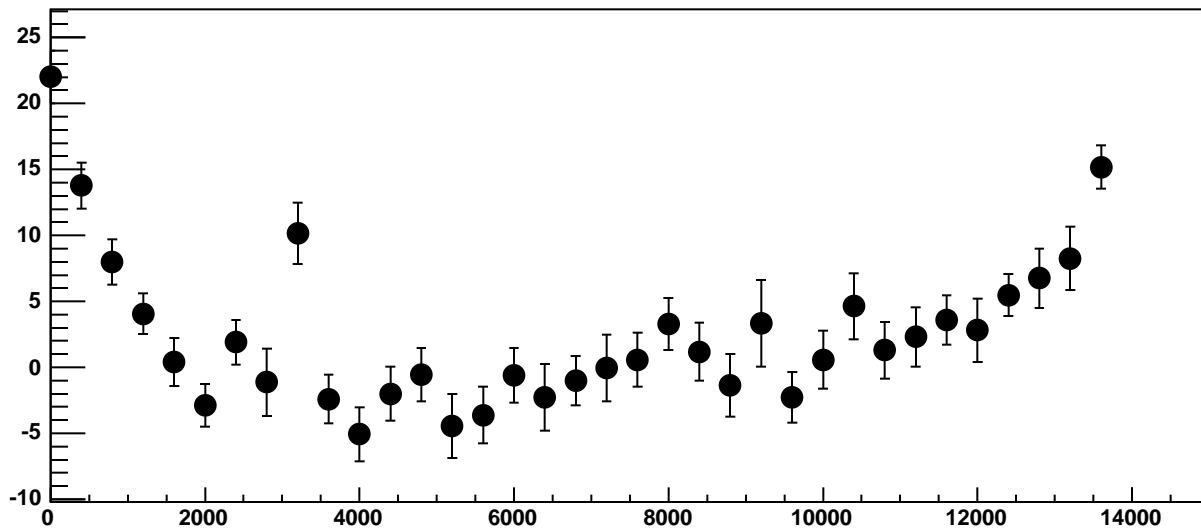


χ^2 / ndf 56.64 / 23
p0 -511 ± 0.869
p1 0.01693 ± 0.0001391

Chip 3, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC

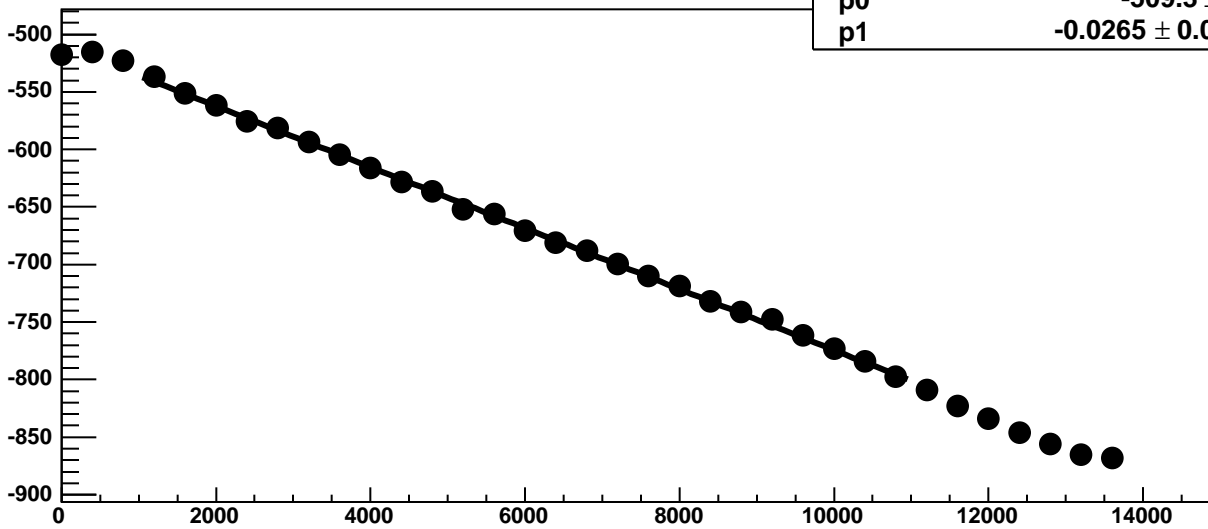


Chip 3, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC

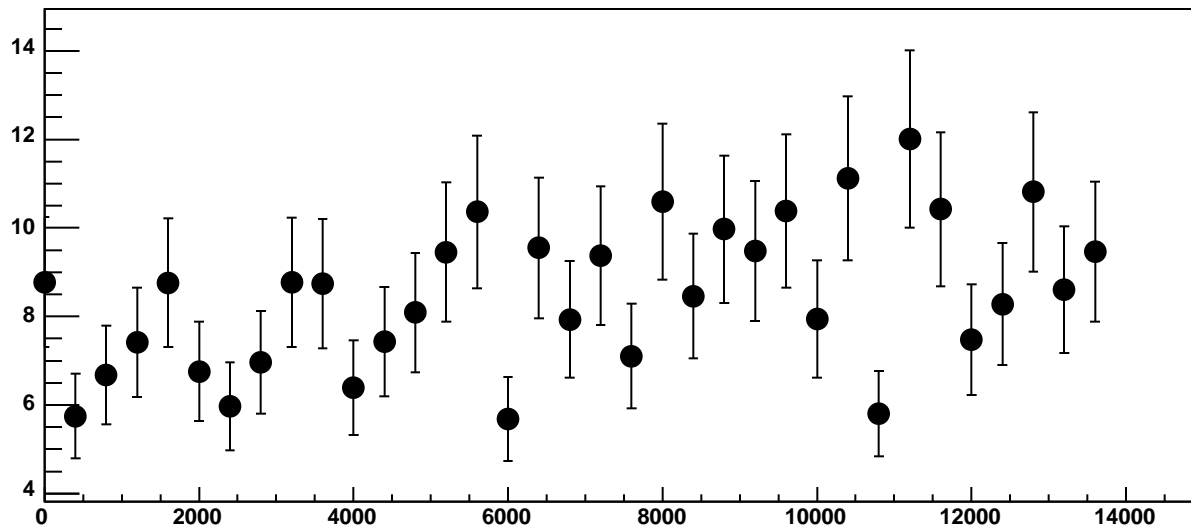


Chip 3, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

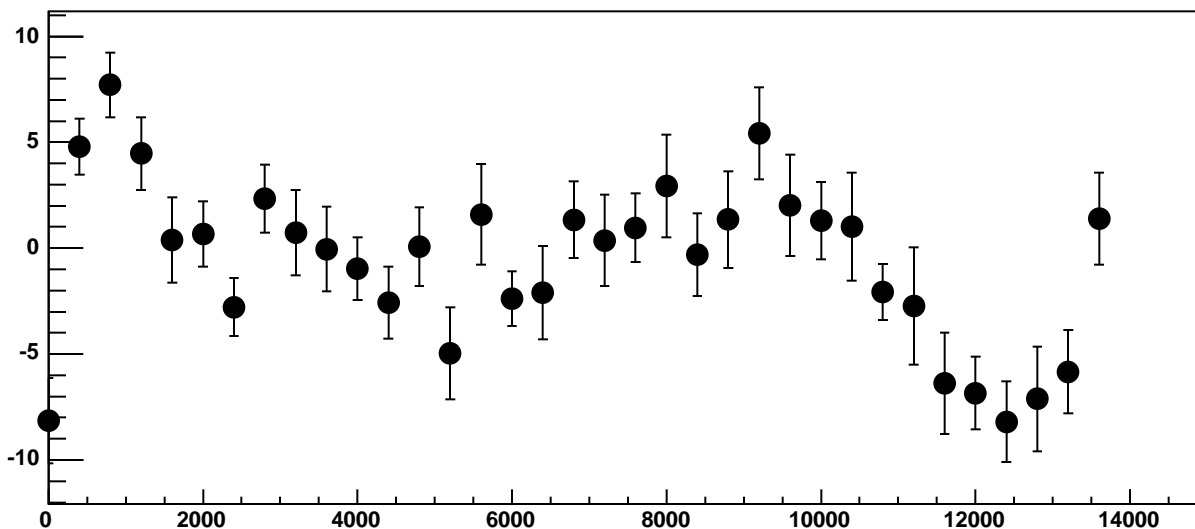
χ^2 / ndf 38.94 / 23
p0 -509.3 ± 0.7851
p1 -0.0265 ± 0.0001224



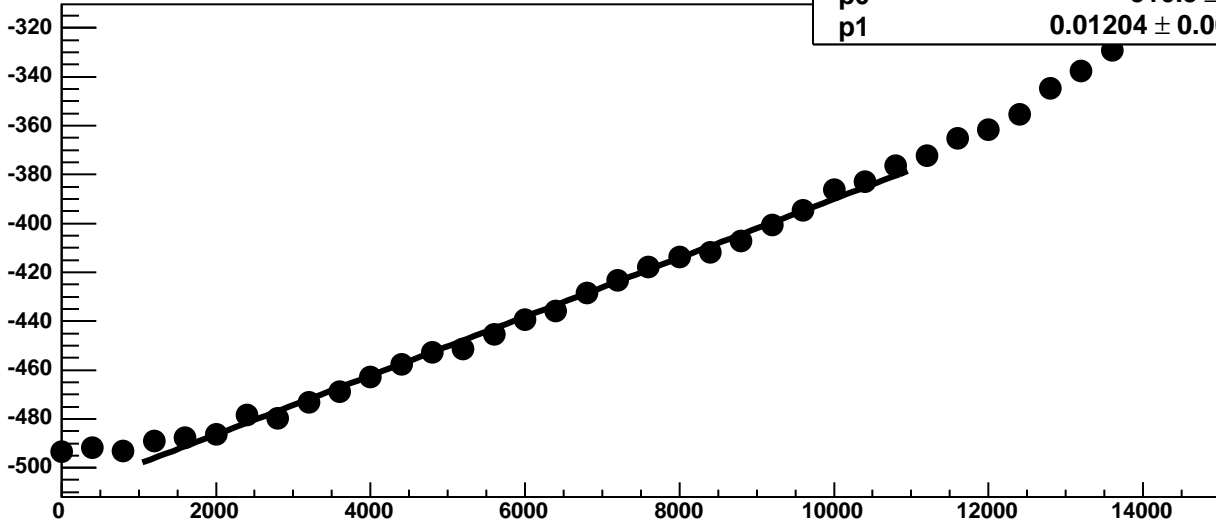
Chip 3, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

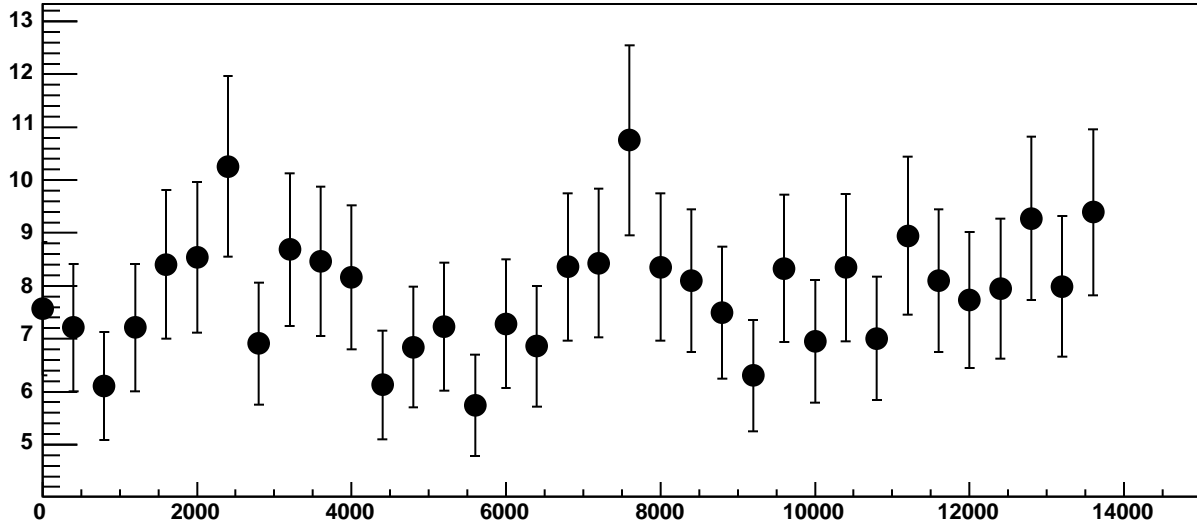


Chip 3, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

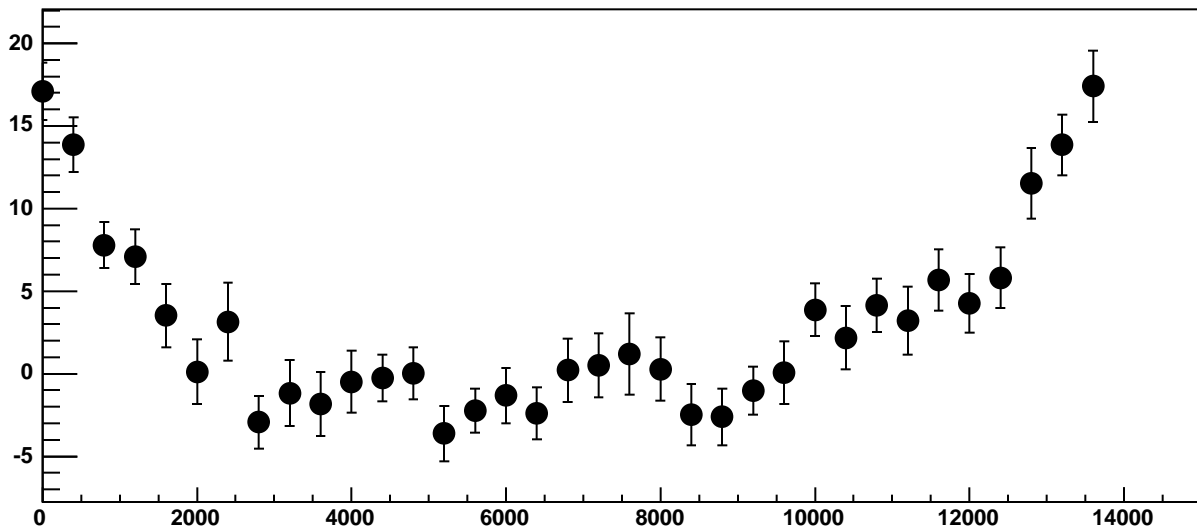


χ^2 / ndf 57.49 / 23
p0 -510.5 ± 0.8231
p1 0.01204 ± 0.0001228

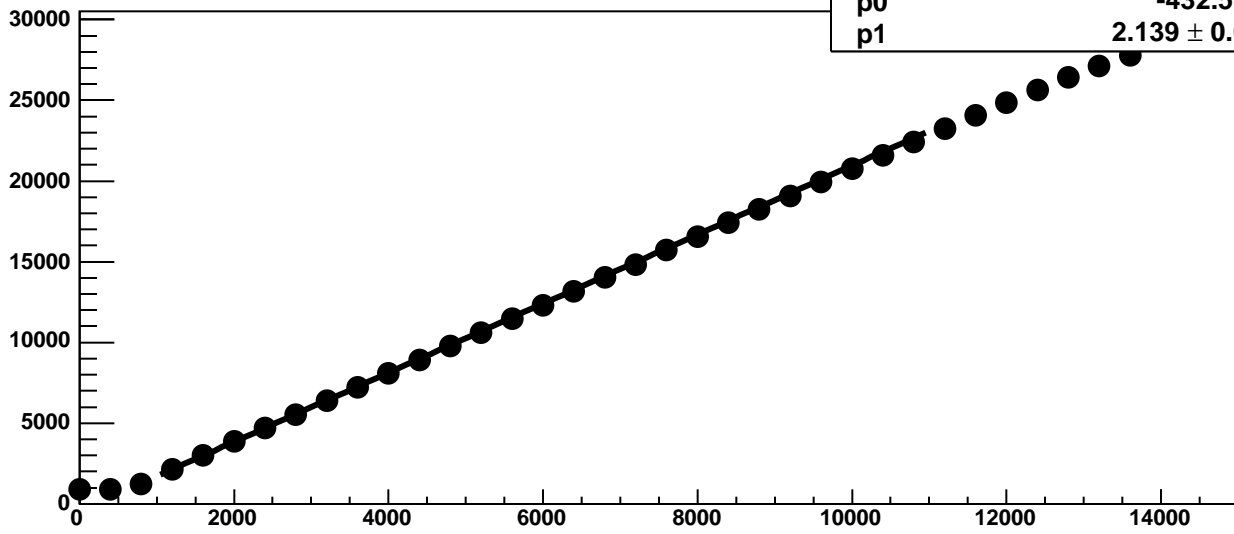
Chip 3, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



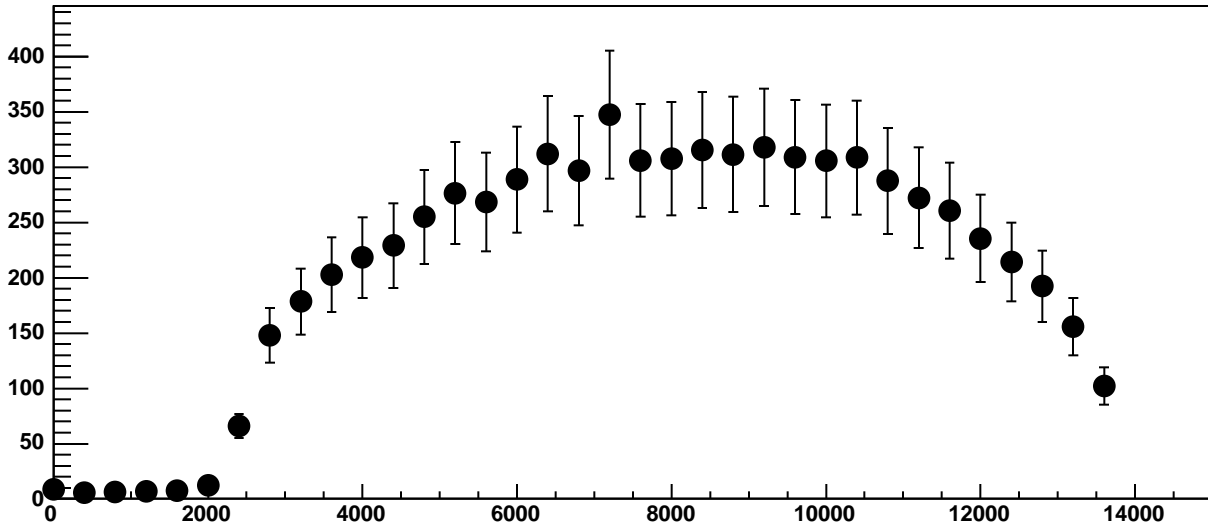
Chip 3, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



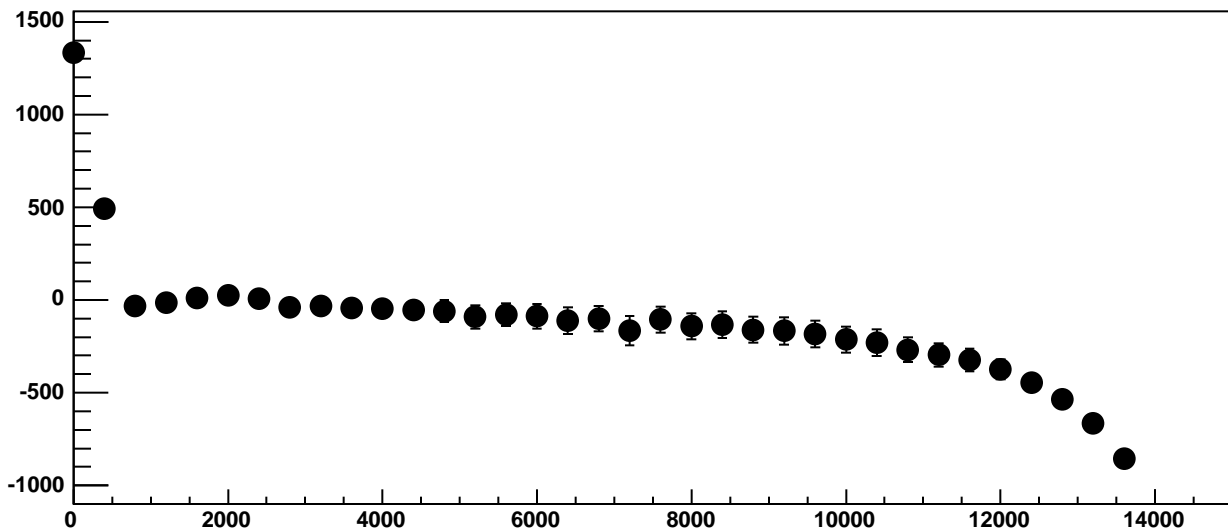
Chip 3, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC



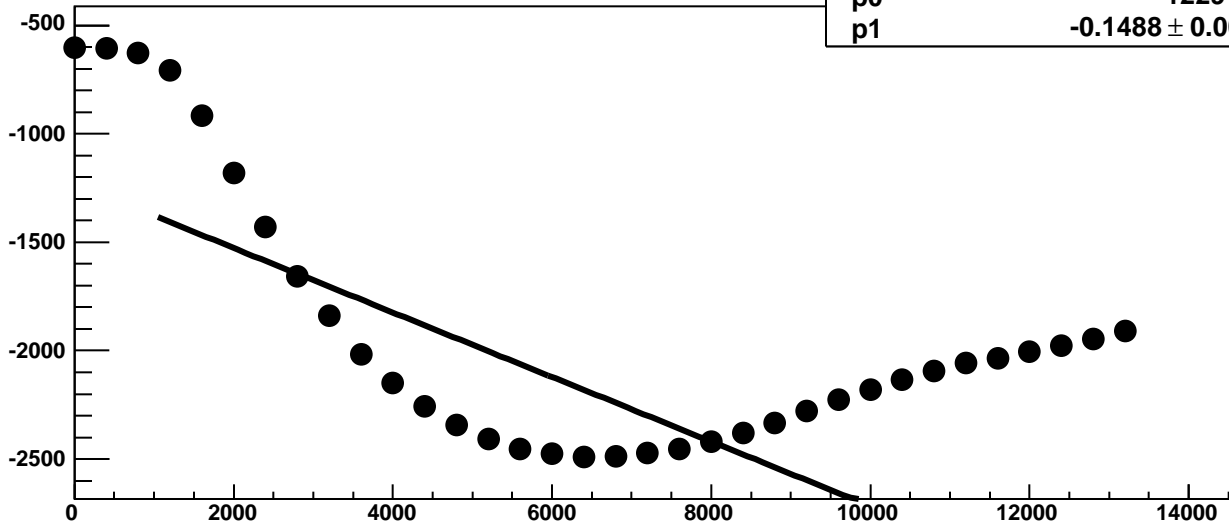
Chip 3, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC

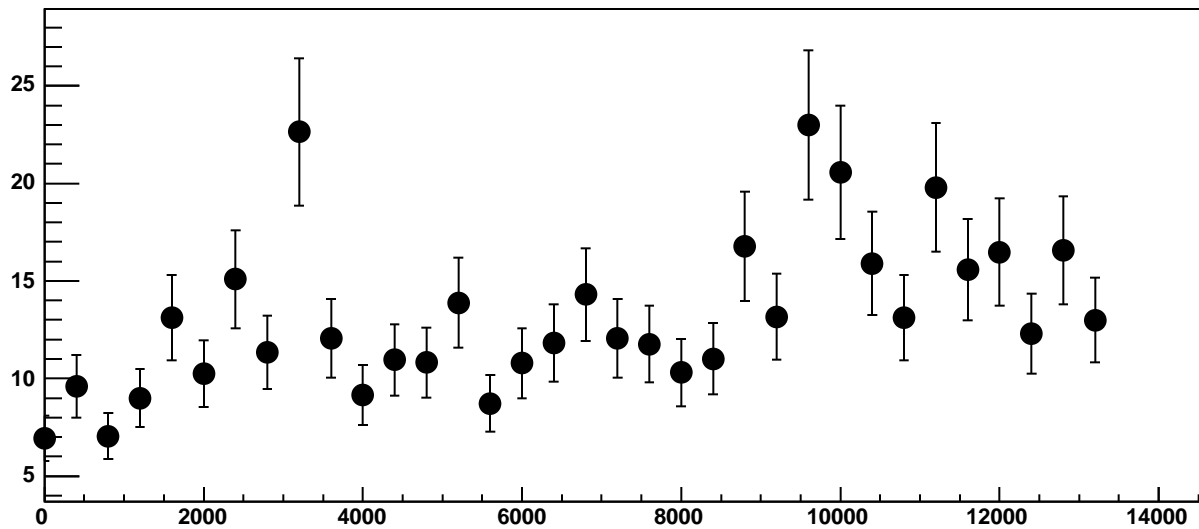


Chip 3, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

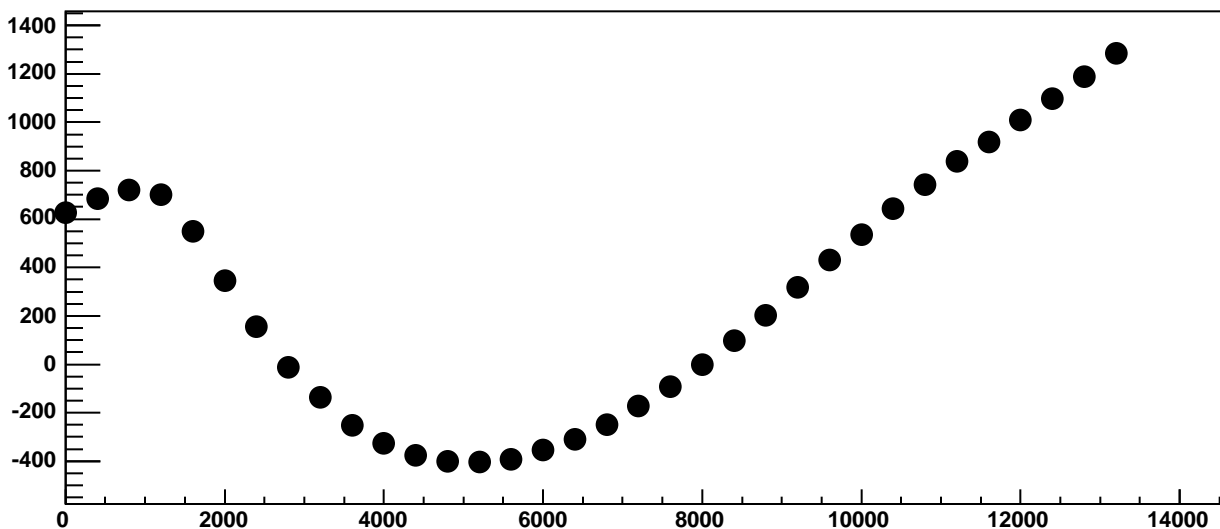


χ^2 / ndf	4.796e+05 / 23
p0	-1229 ± 1.251
p1	-0.1488 ± 0.0002043

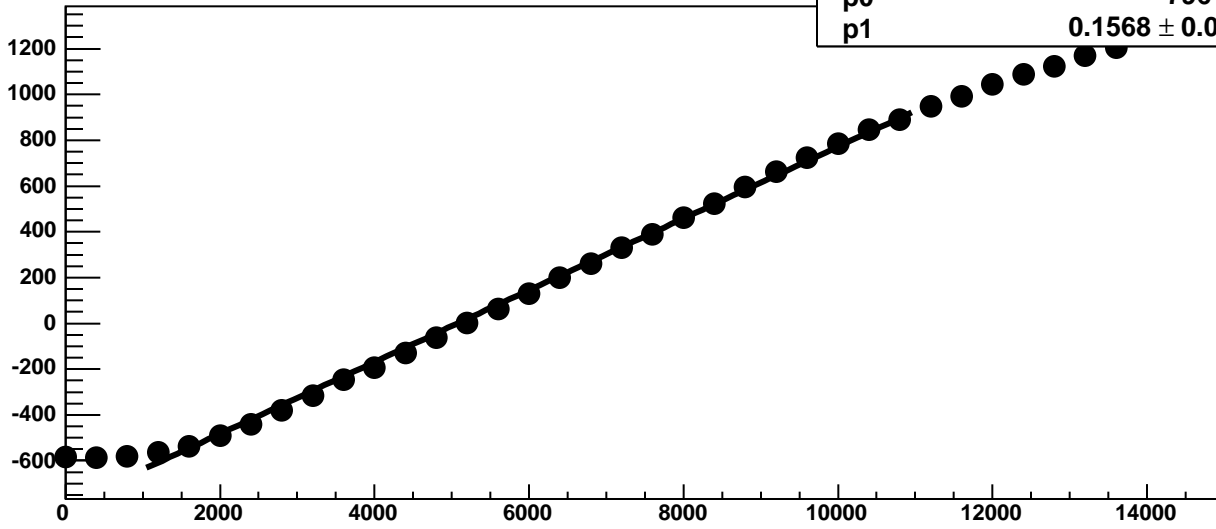
Chip 3, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

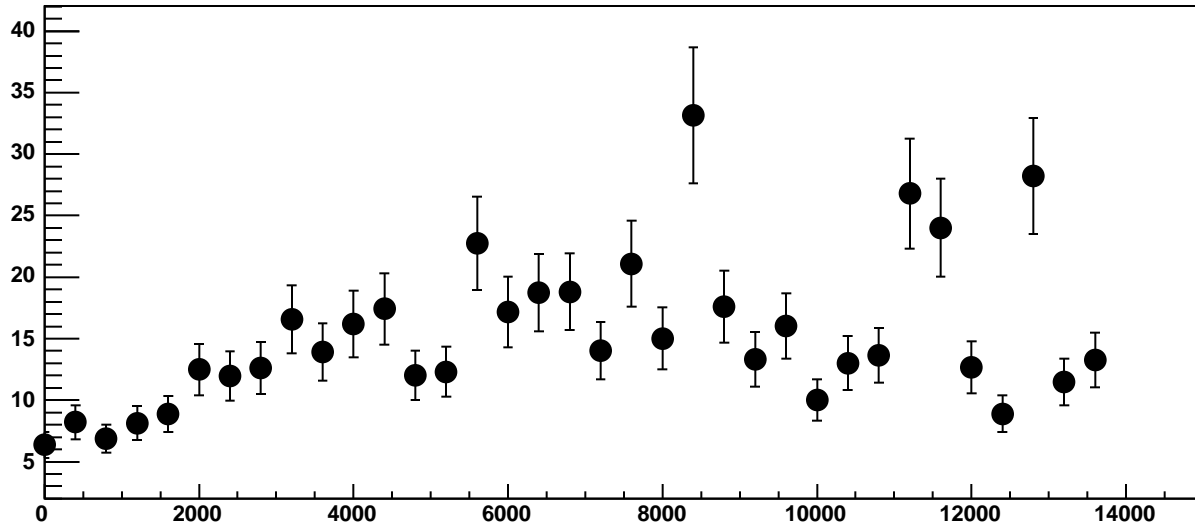


Chip 3, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

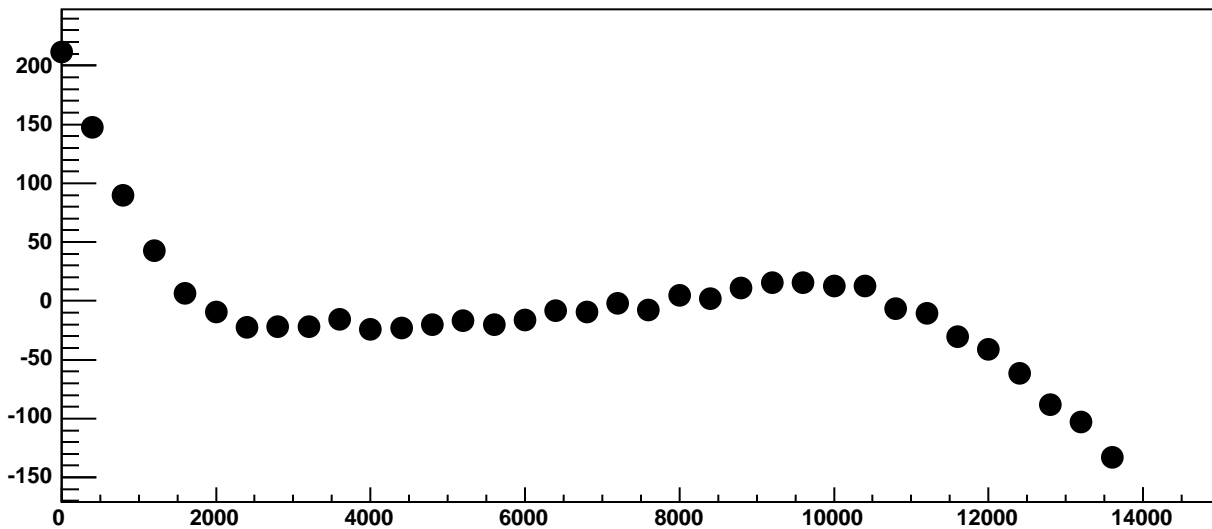


χ^2 / ndf 1045 / 23
p0 -796 ± 1.189
p1 0.1568 ± 0.0001898

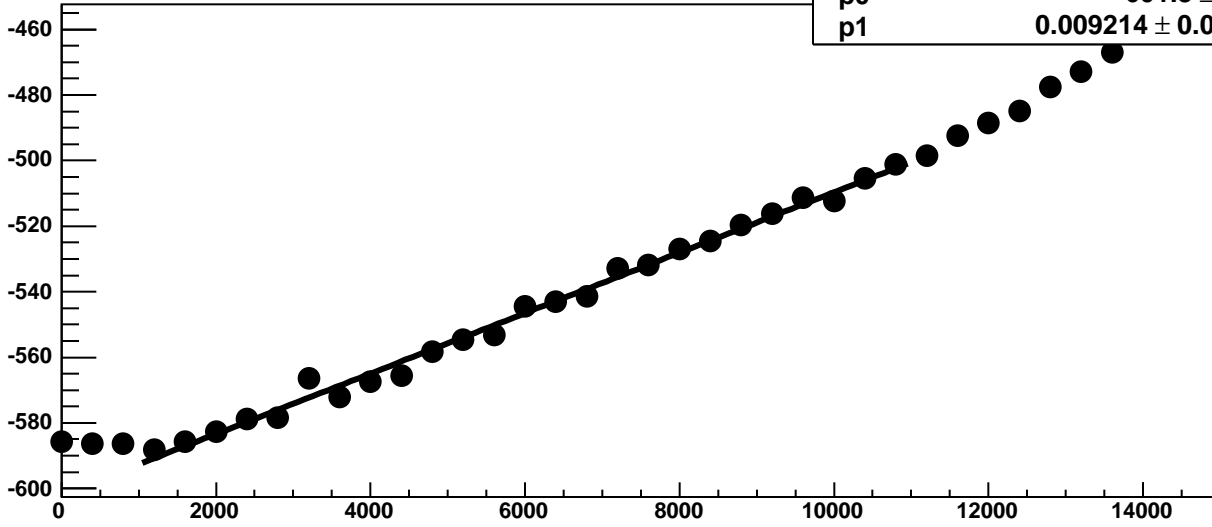
Chip 3, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

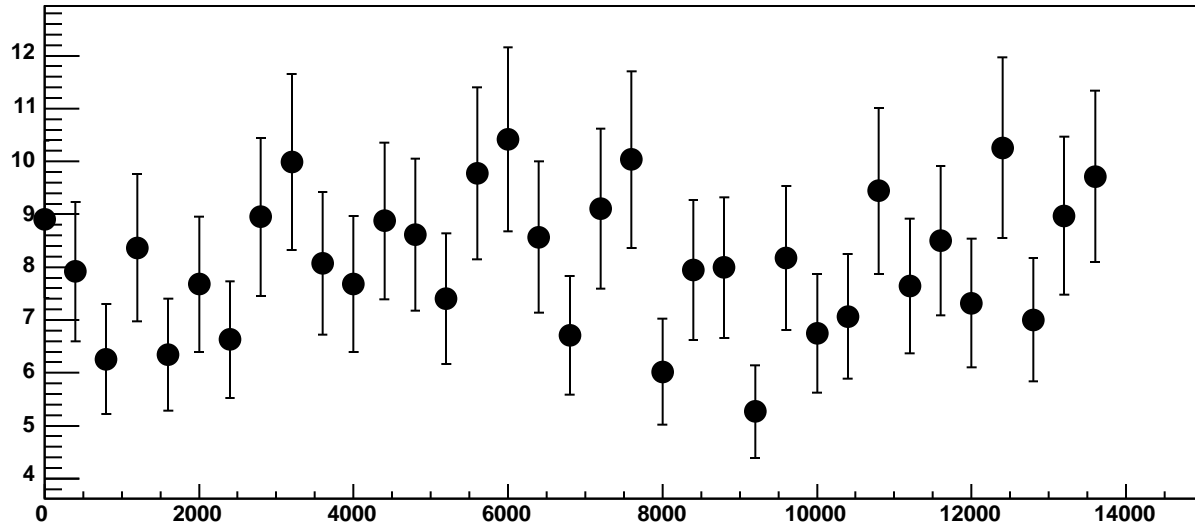


Chip 3, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

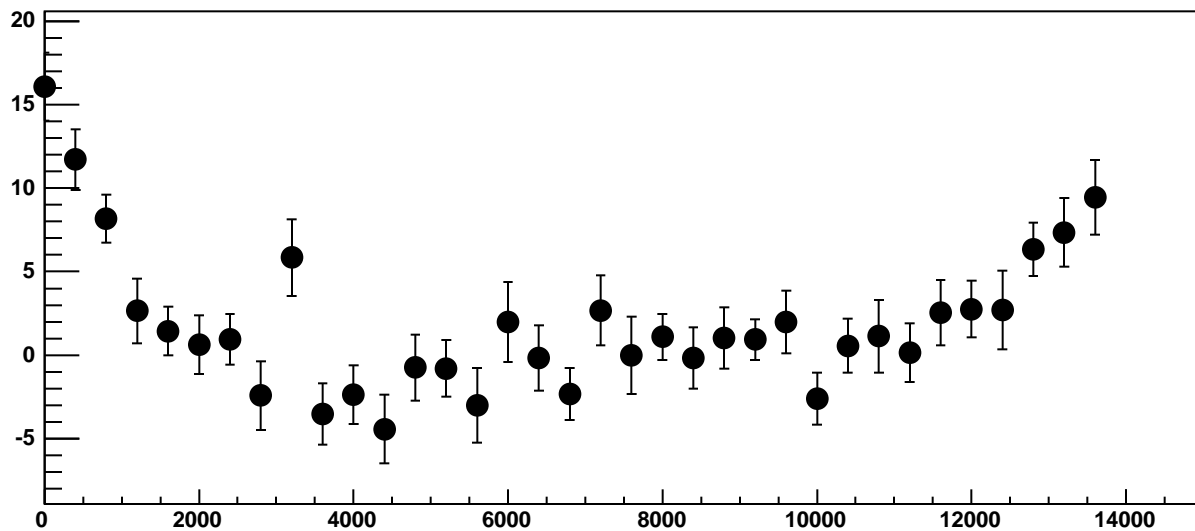


χ^2 / ndf 34.07 / 23
p0 -601.8 ± 0.8127
p1 0.009214 ± 0.0001187

Chip 3, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

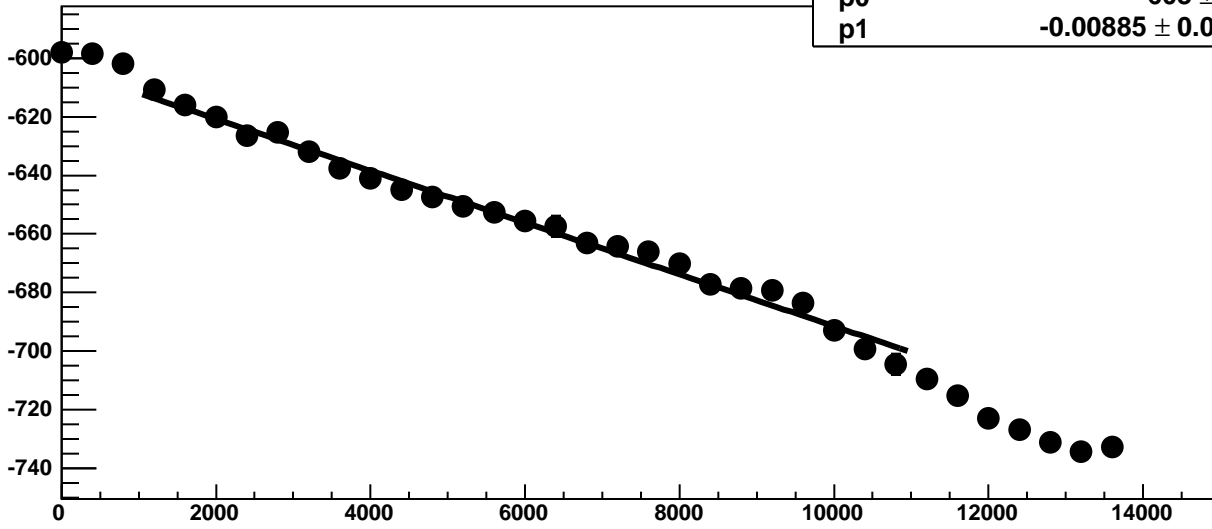


Chip 3, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

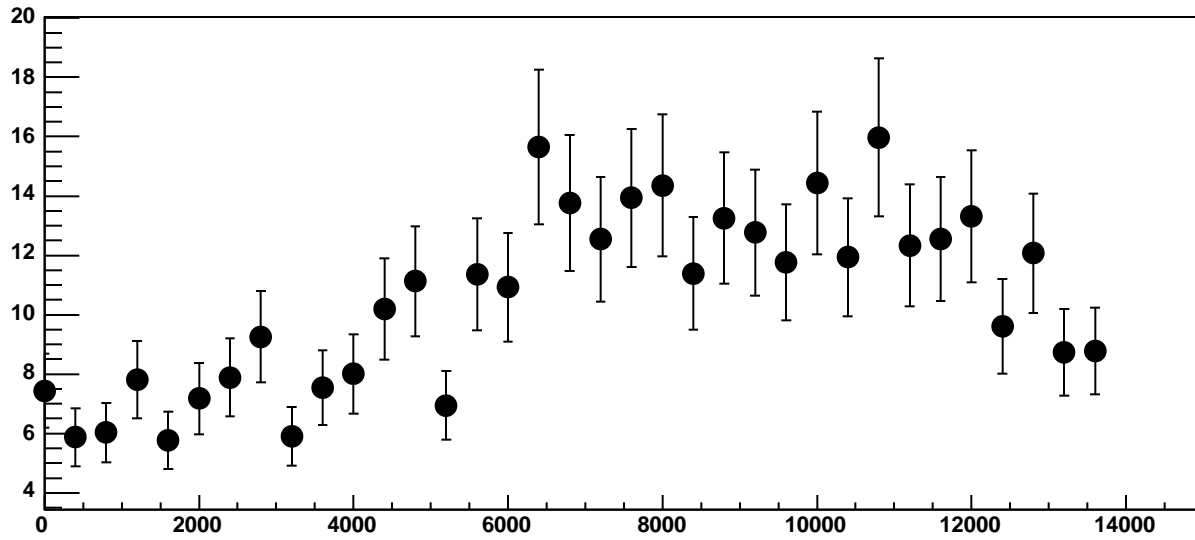


Chip 3, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

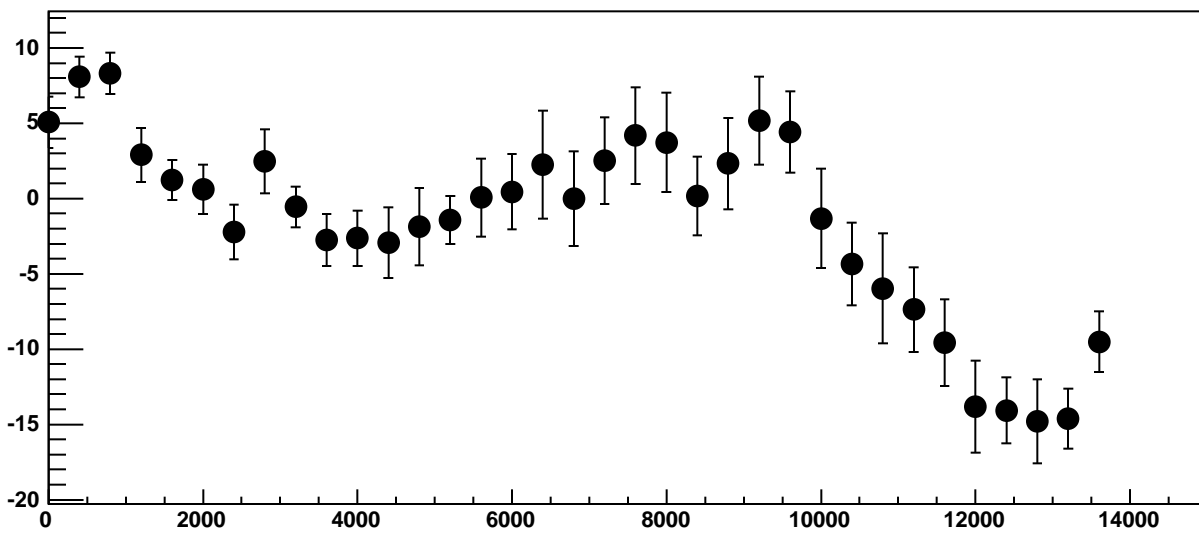
χ^2 / ndf 30.07 / 23
p0 -603 ± 0.8495
p1 -0.00885 ± 0.0001602



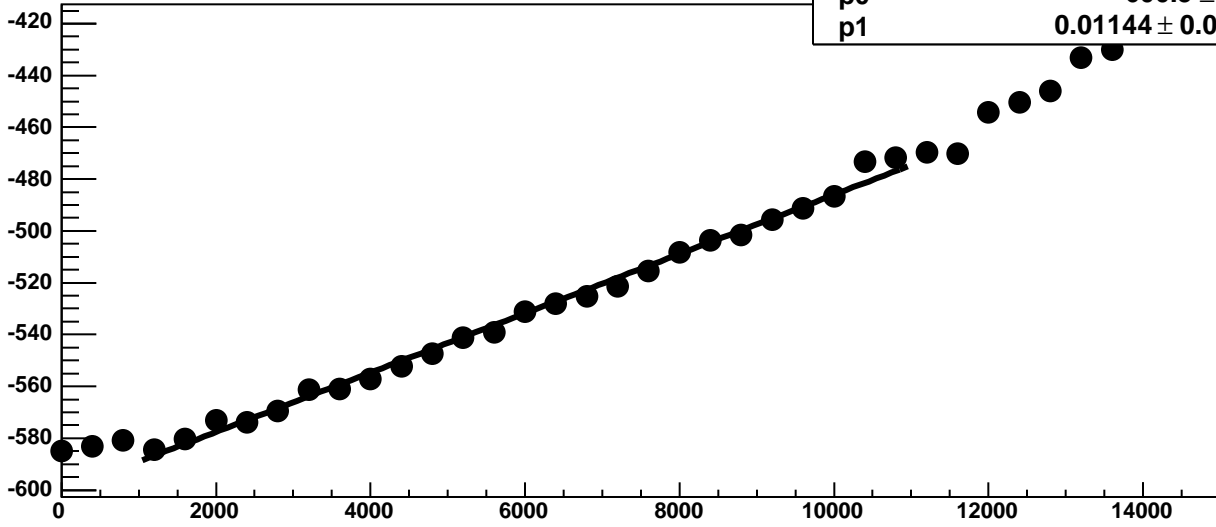
Chip 3, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



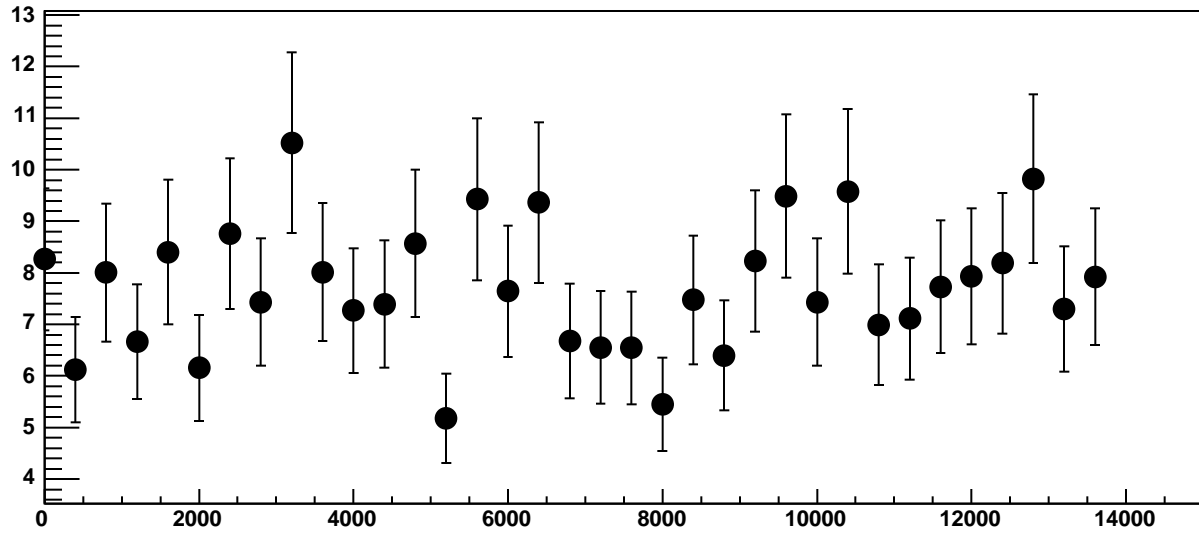
Chip 3, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC



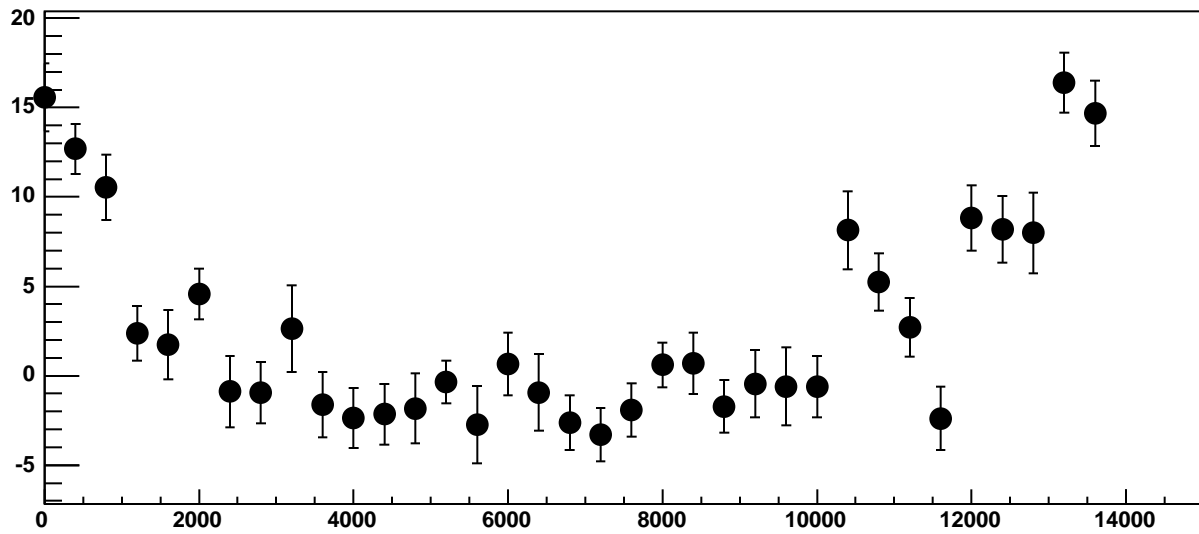
Chip 3, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



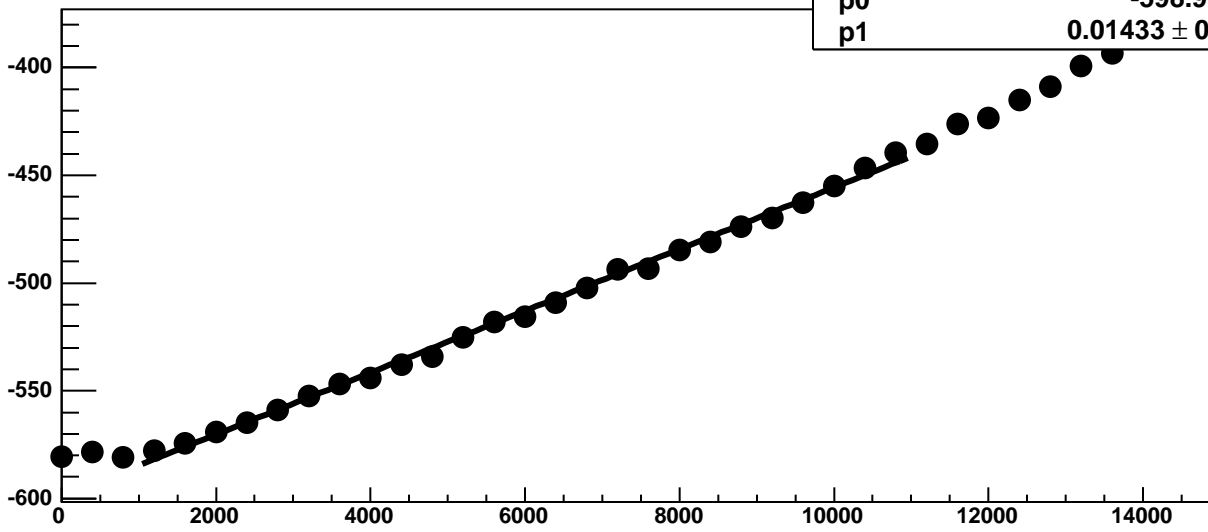
Chip 3, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

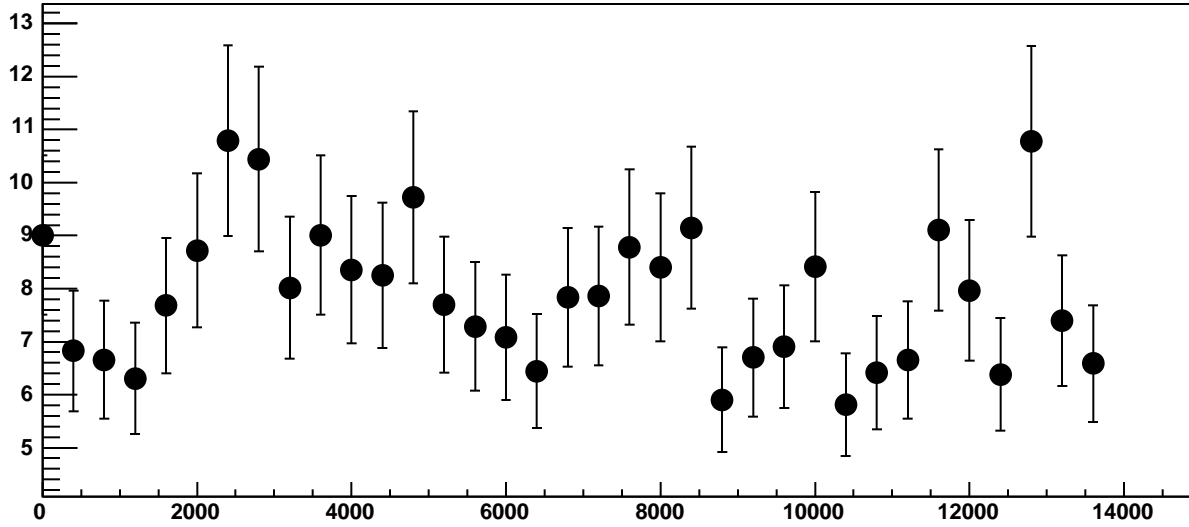


Chip 3, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

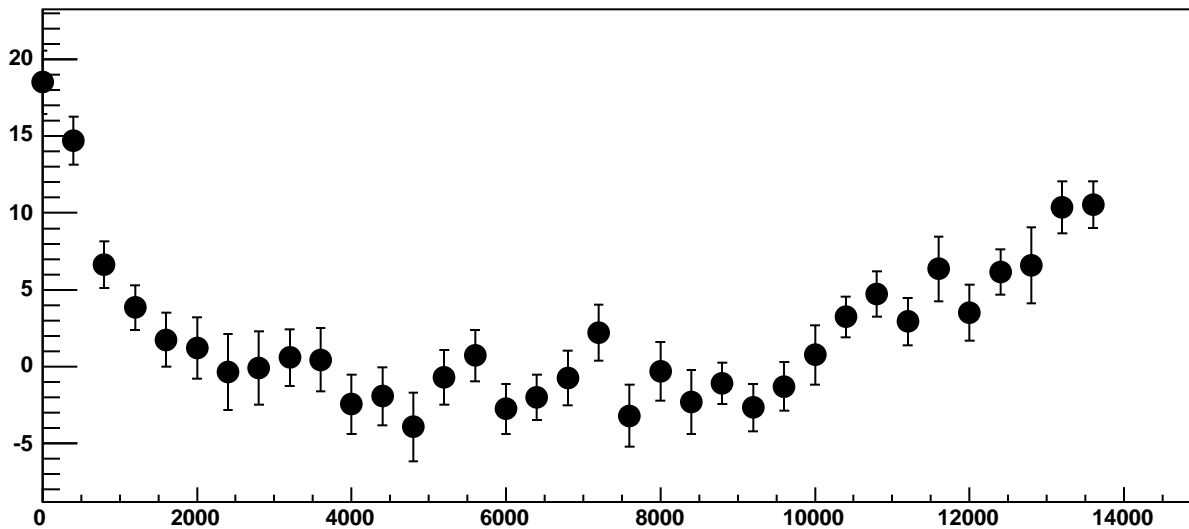


χ^2 / ndf 45.45 / 23
p0 -598.9 ± 0.829
p1 0.01433 ± 0.000117

Chip 3, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

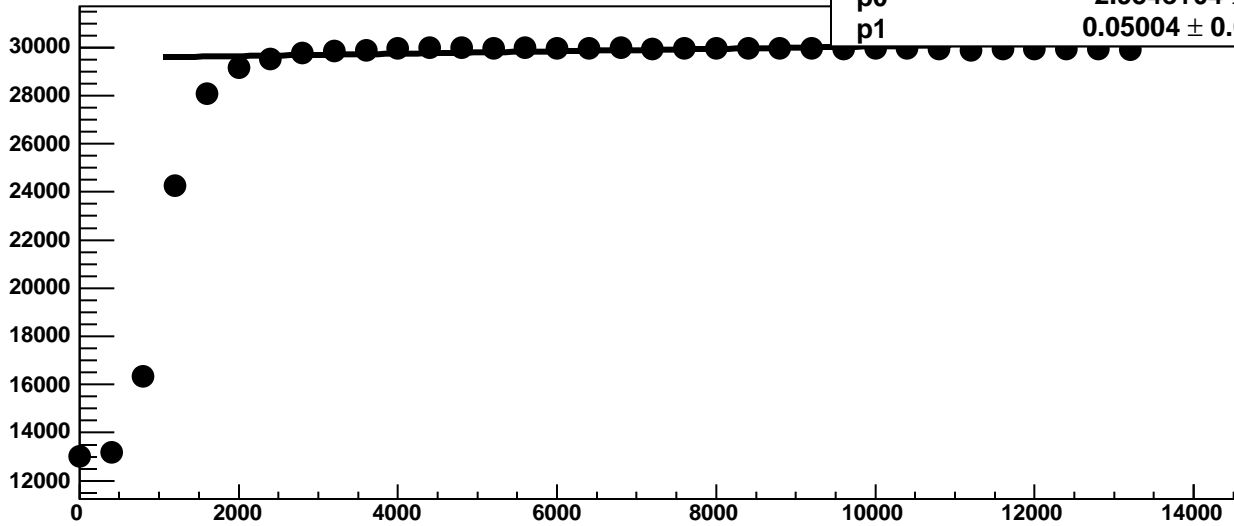
1.299e+04 / 23

p0

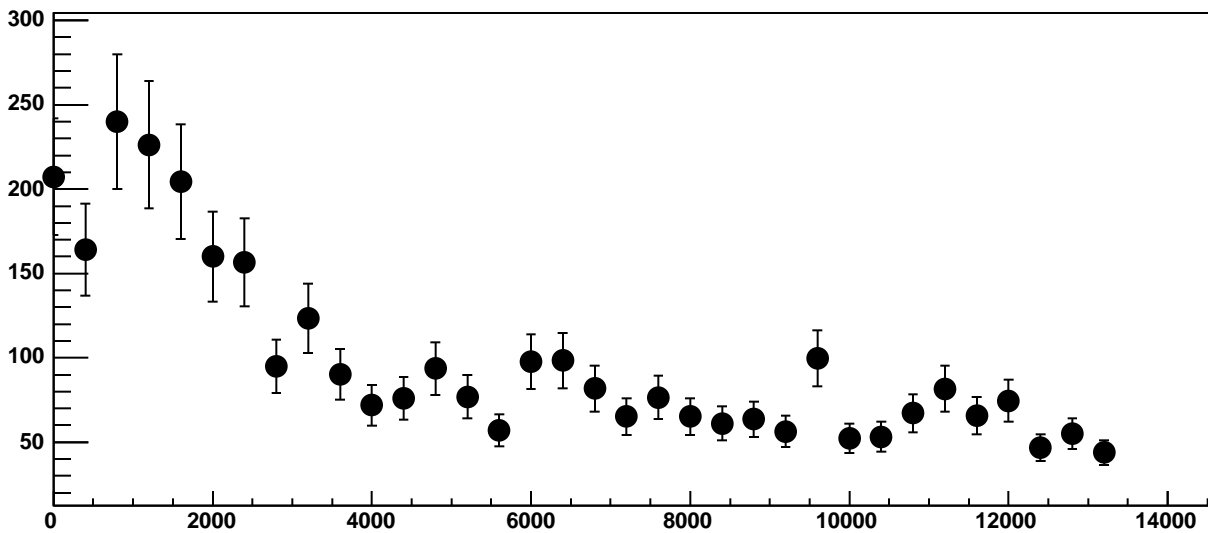
2.954e+04 ± 10.92

p1

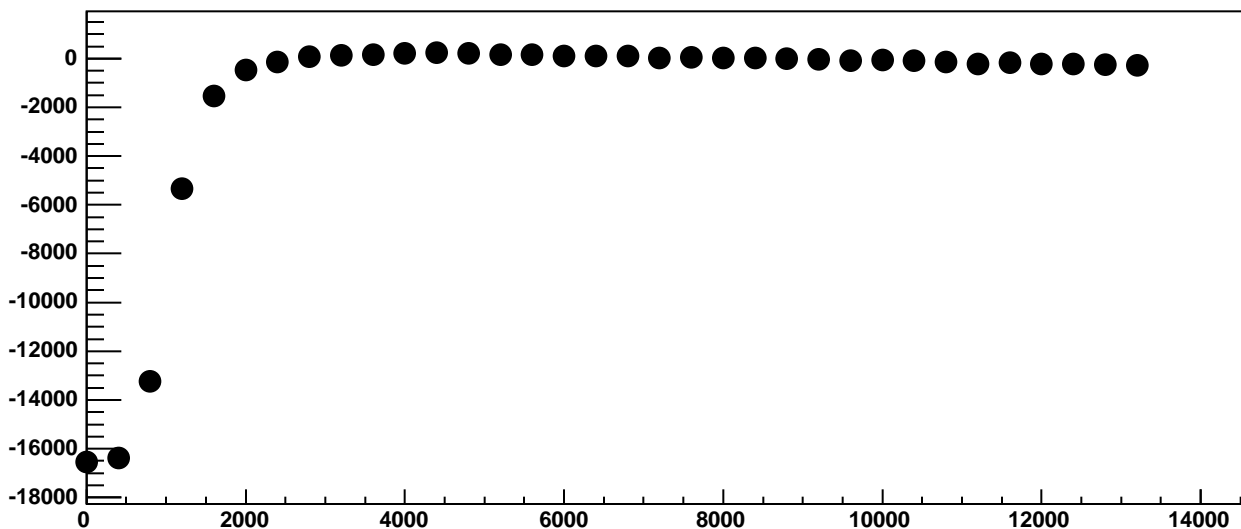
0.05004 ± 0.001414



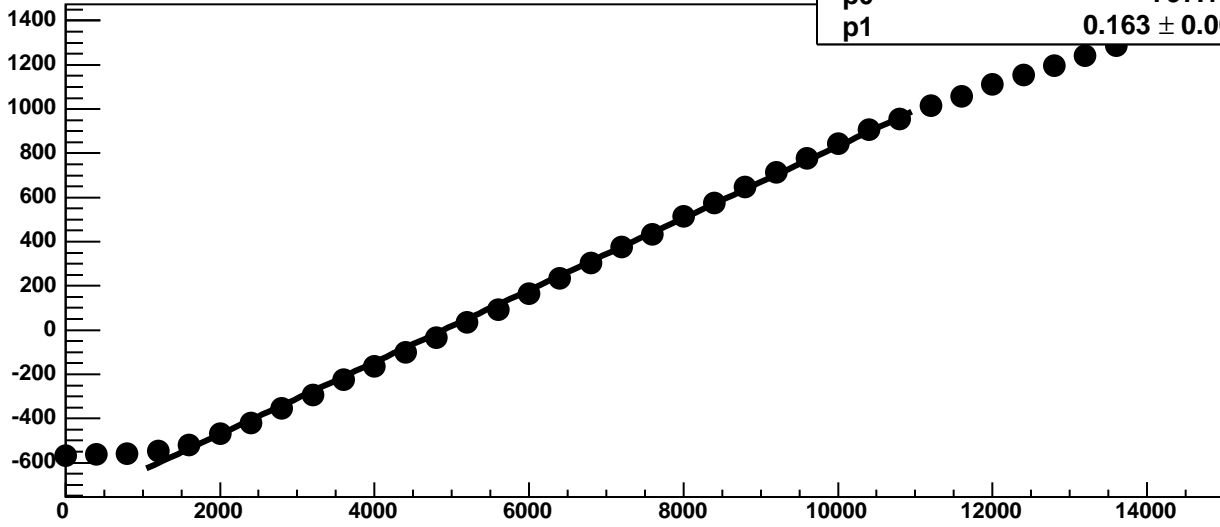
Chip 3, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC



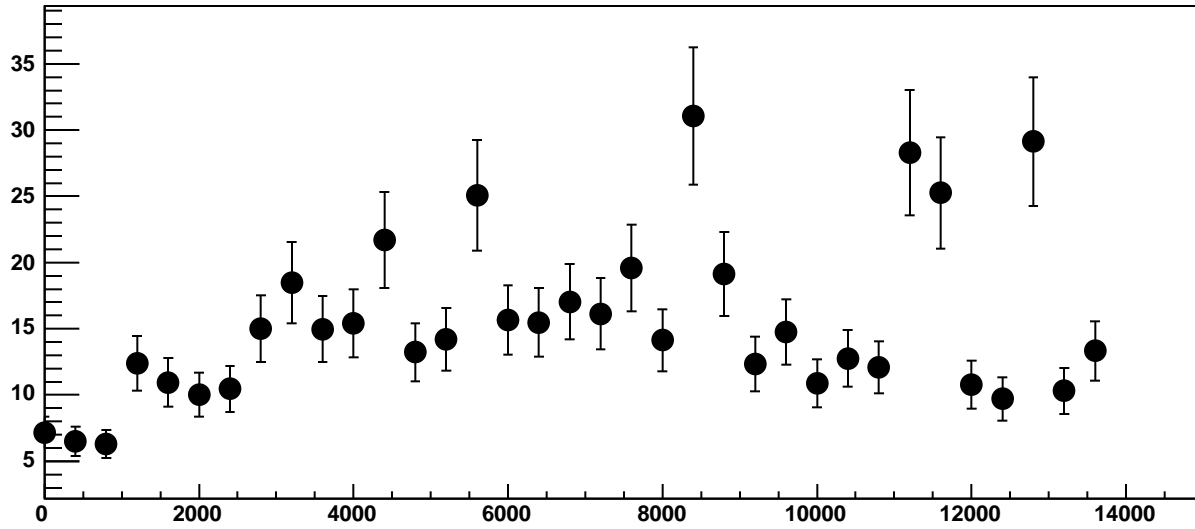
Chip 3, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC



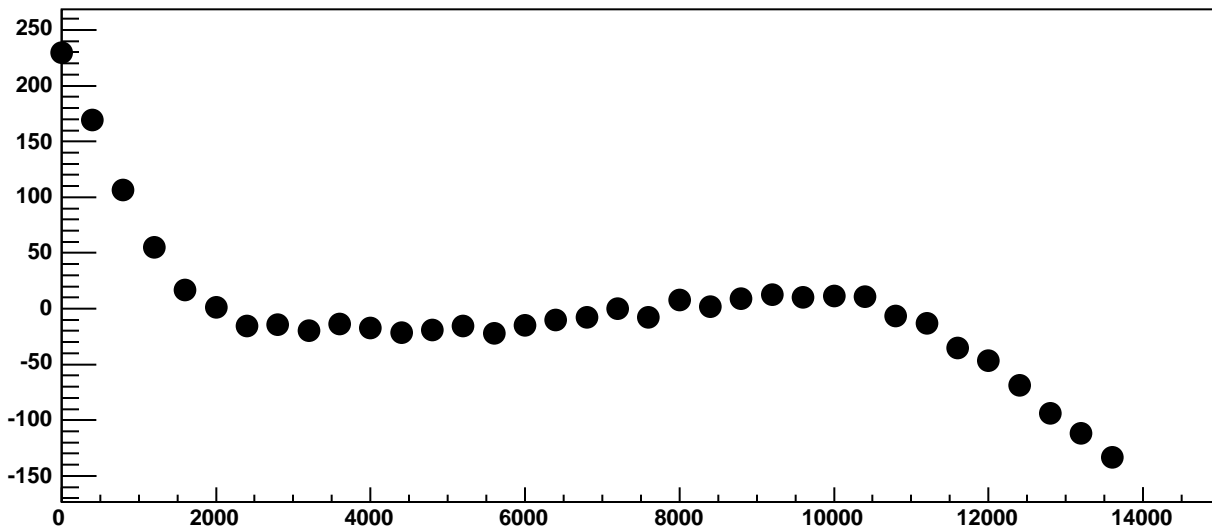
Chip 3, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



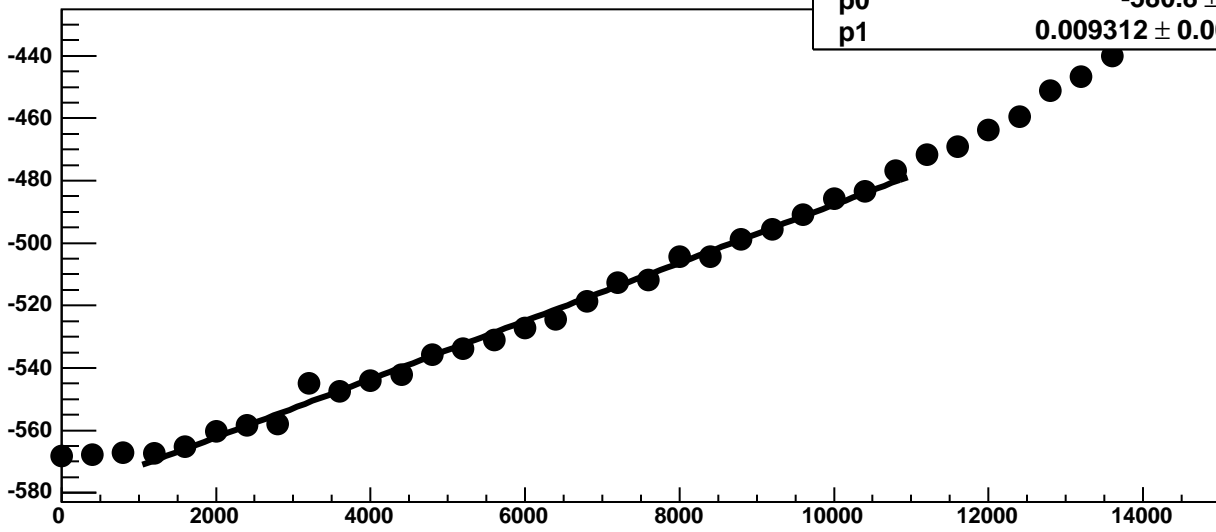
Chip 3, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

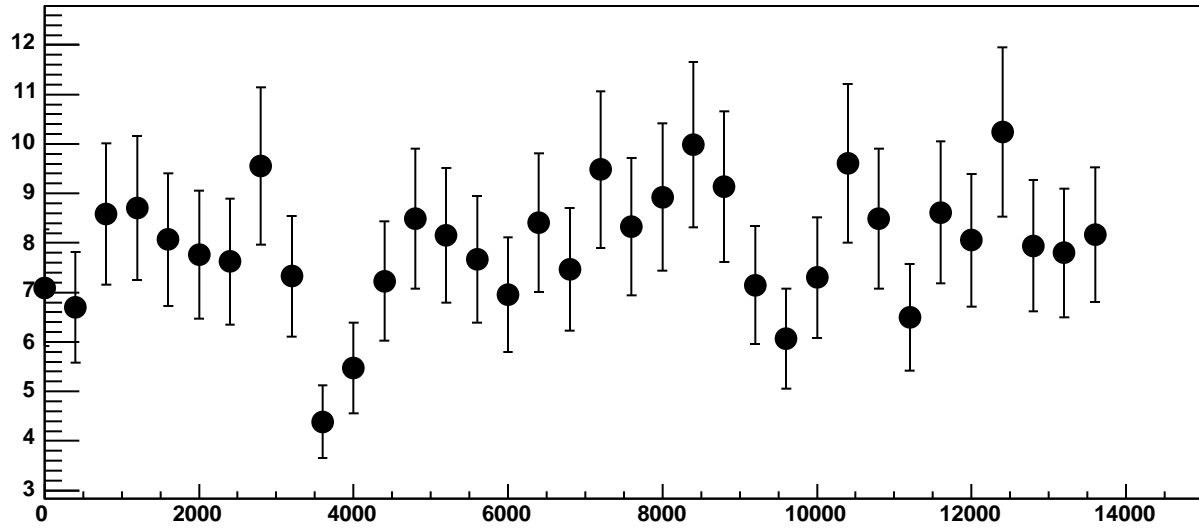


Chip 3, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

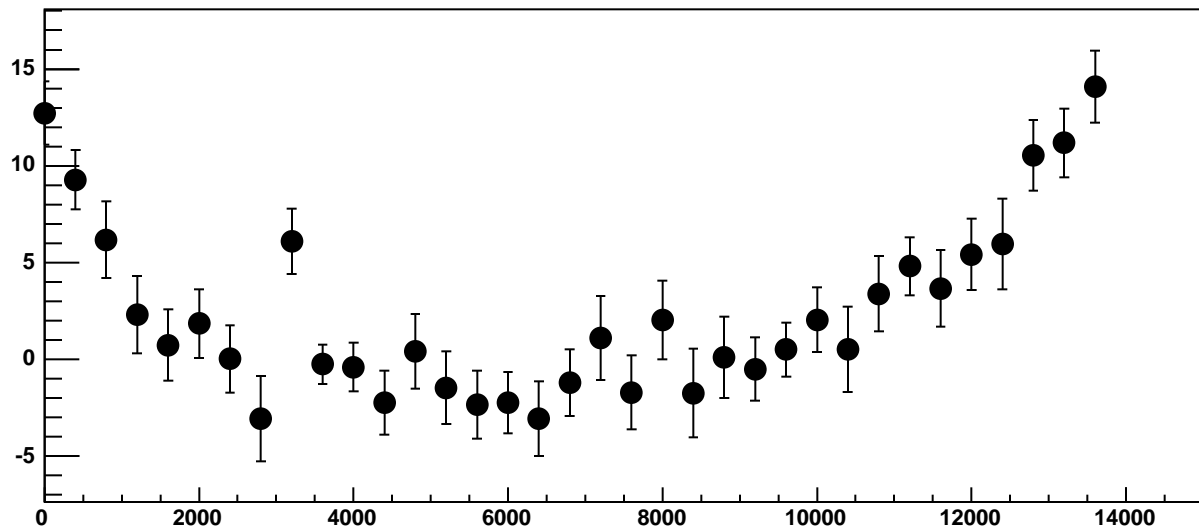


χ^2 / ndf 34.54 / 23
p0 -580.8 ± 0.7851
p1 0.009312 ± 0.0001229

Chip 3, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC

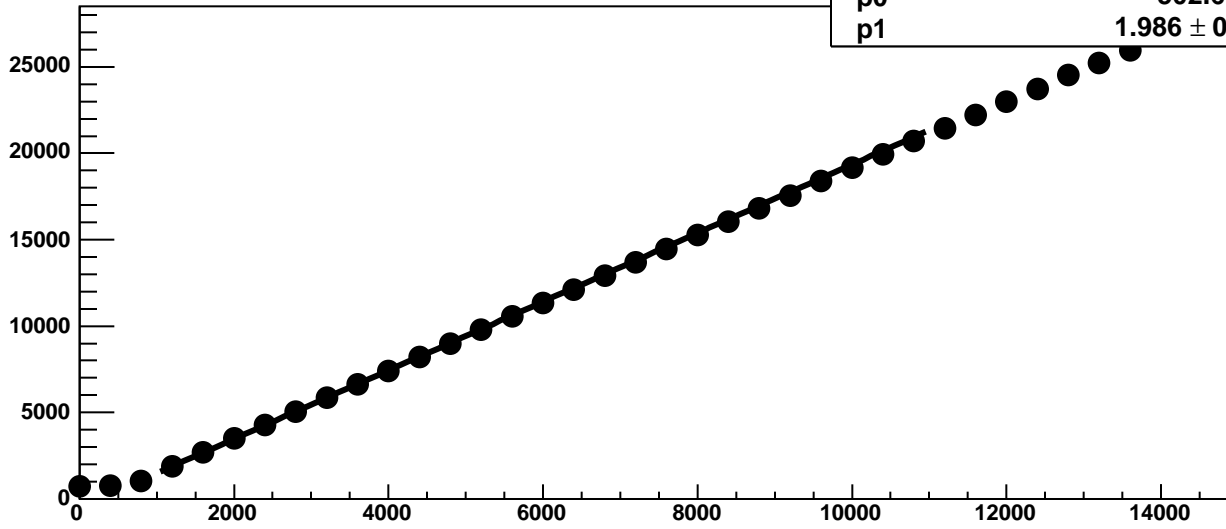


Chip 3, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

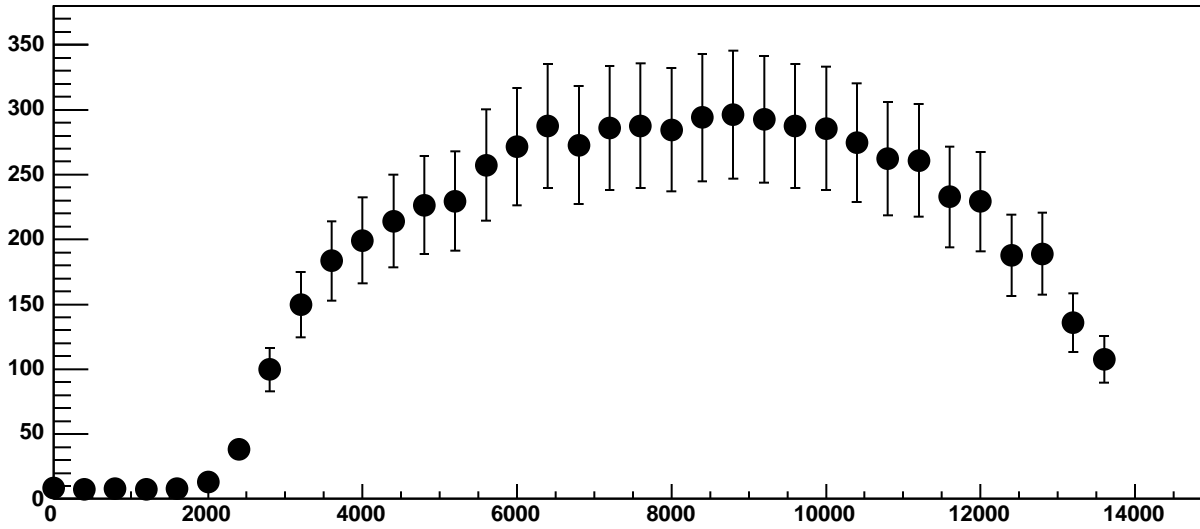


Chip 3, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC

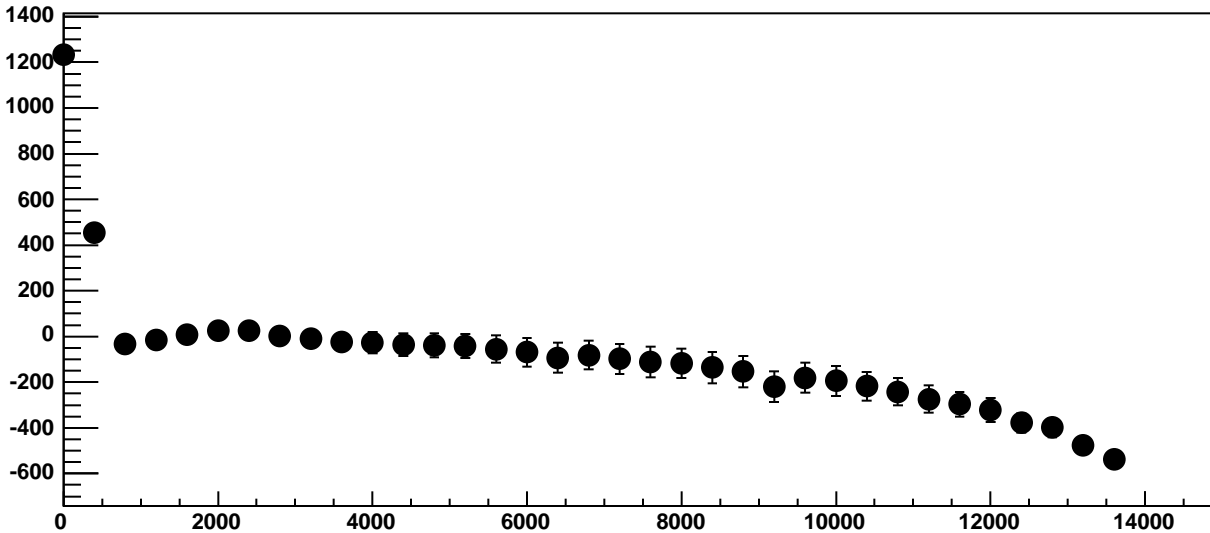
χ^2 / ndf 272.9 / 23
p0 -502.6 ± 3.214
p1 1.986 ± 0.001971



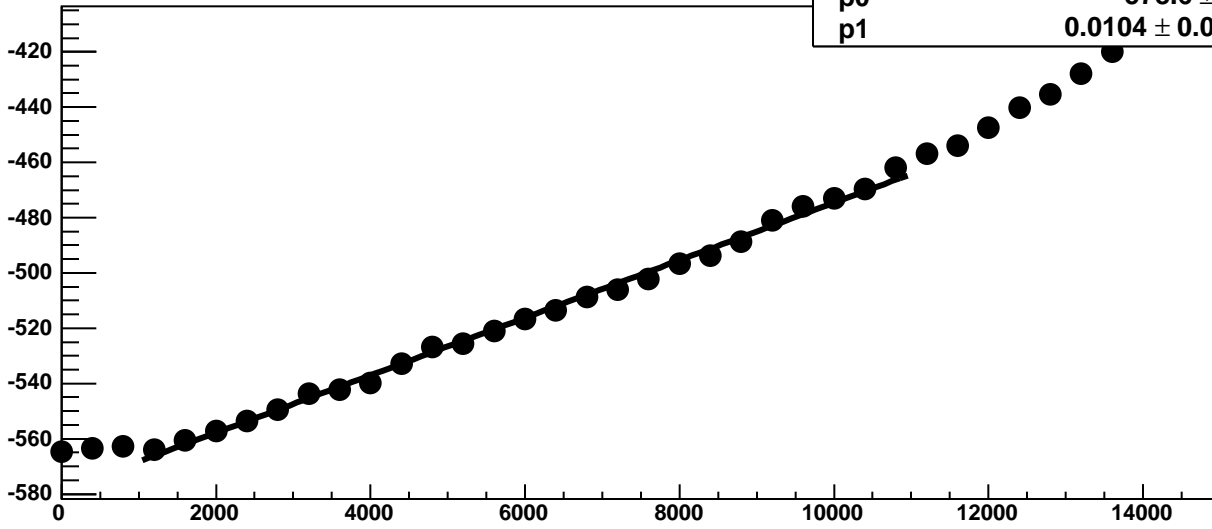
Chip 3, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC

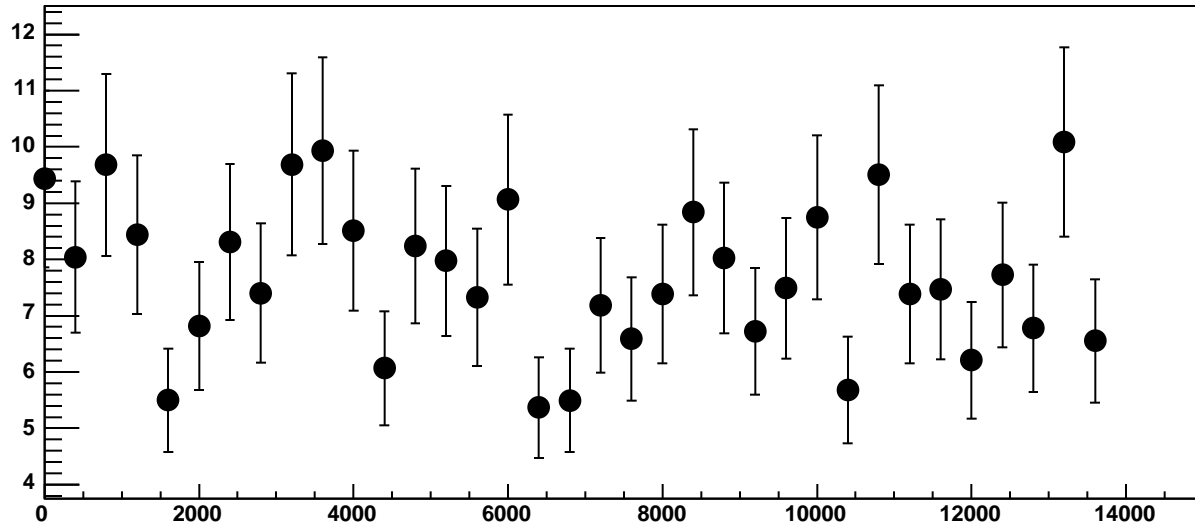


Chip 3, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

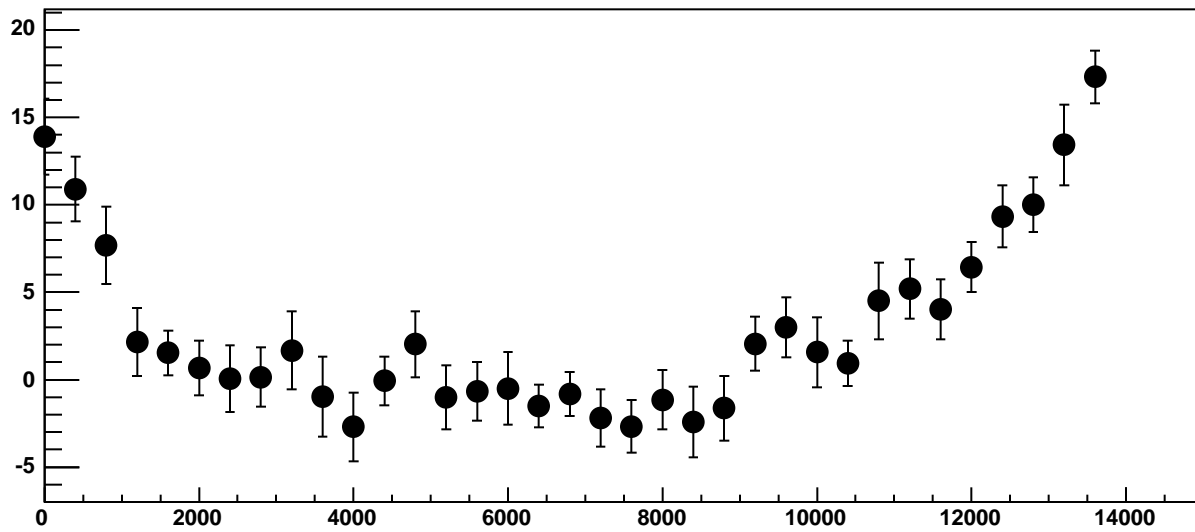


χ^2 / ndf 26.97 / 23
p0 -578.6 ± 0.7768
p1 0.0104 ± 0.0001164

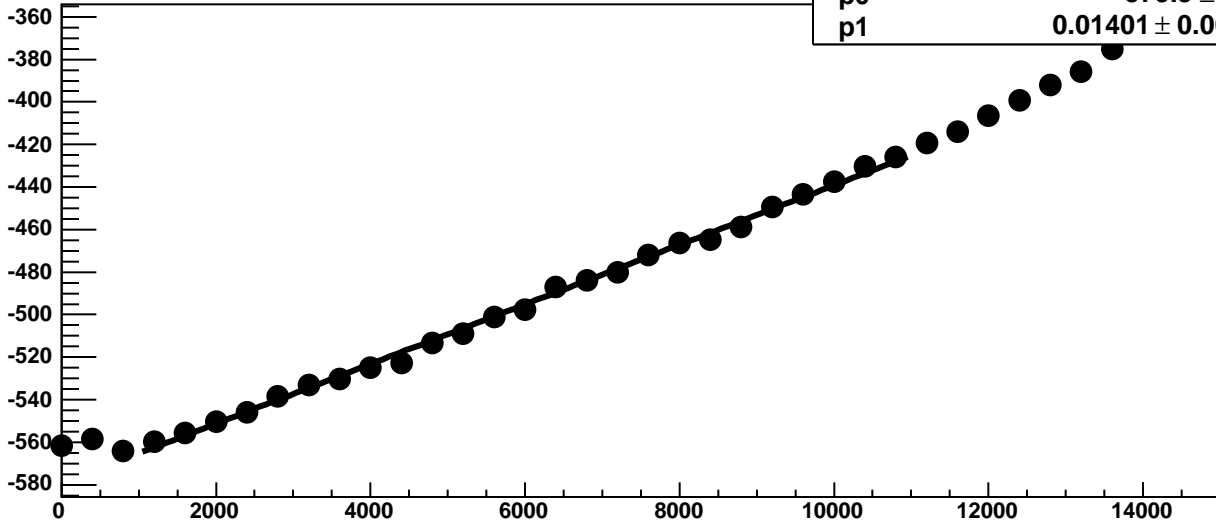
Chip 3, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



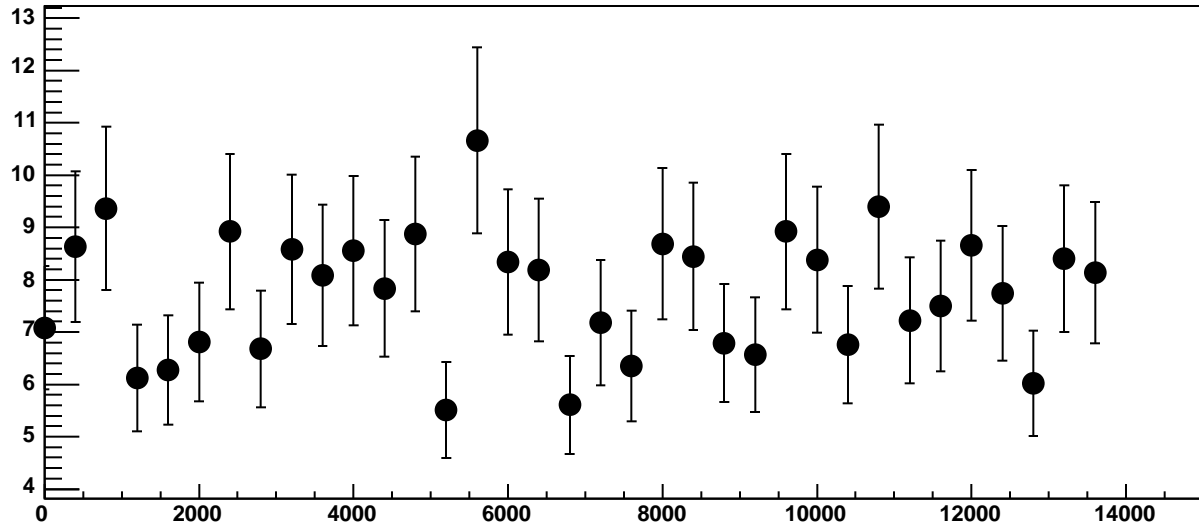
Chip 3, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



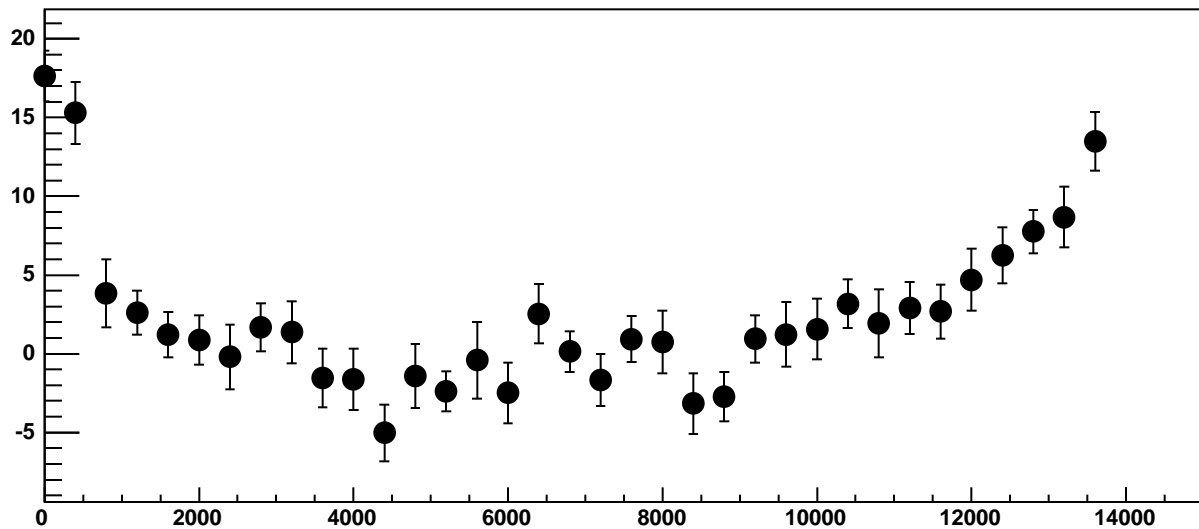
Chip 3, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



Chip 3, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC

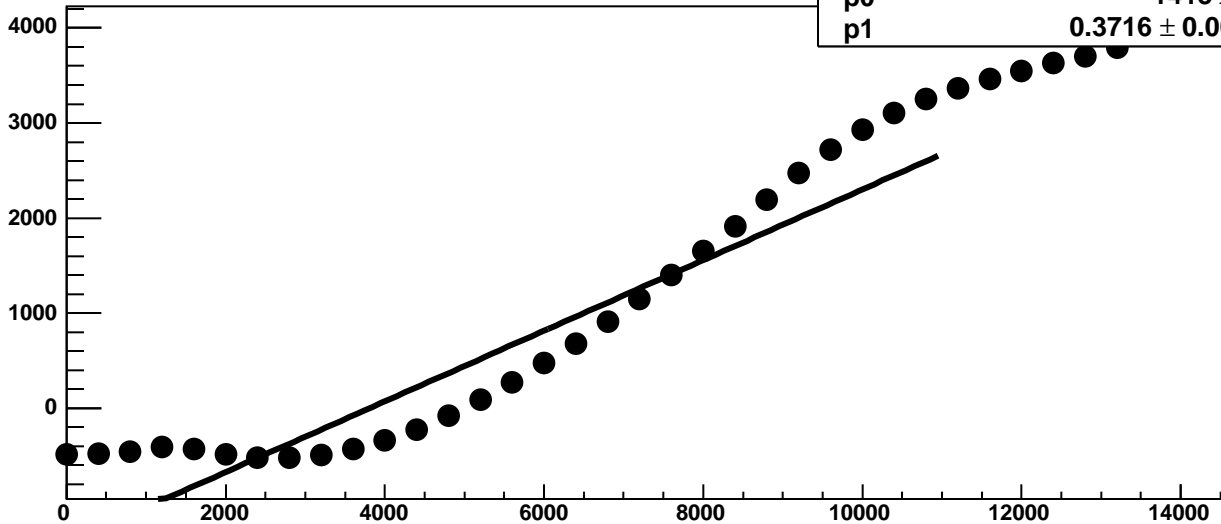


Chip 3, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

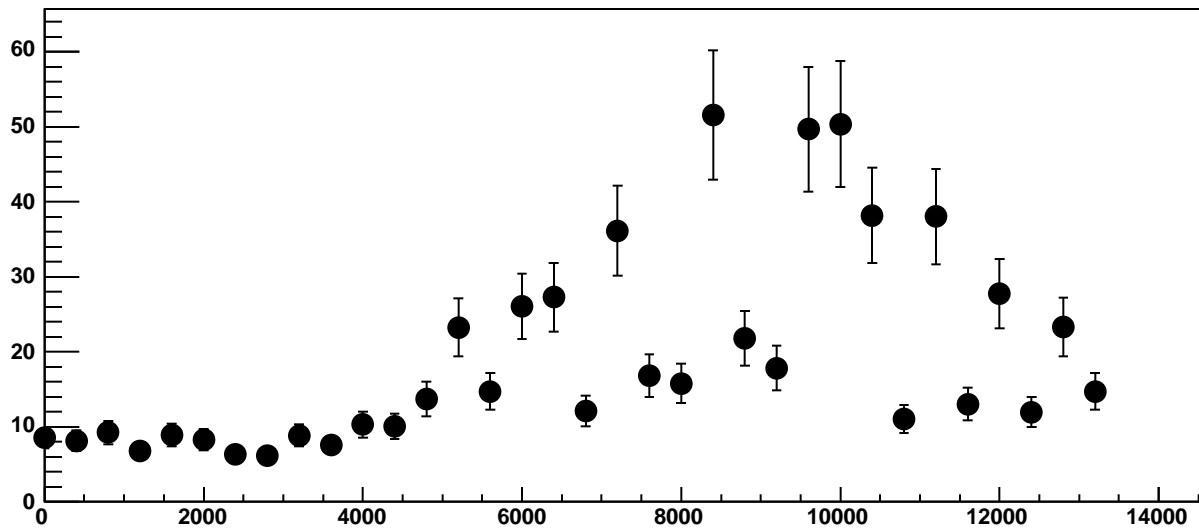


Chip 3, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC

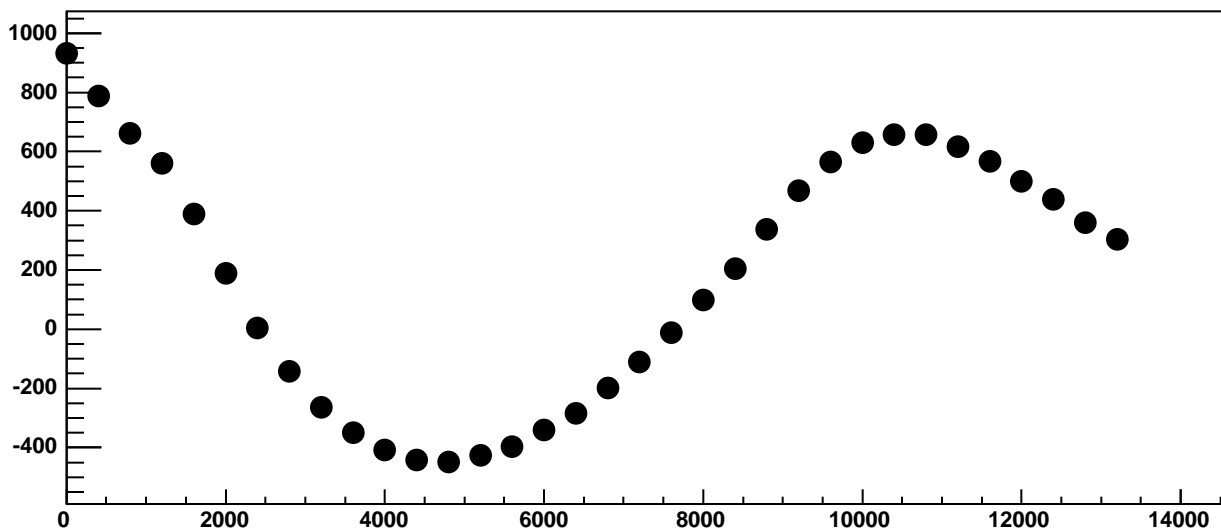
χ^2 / ndf 4.545e+05 / 23
p0 -1415 ± 0.956
p1 0.3716 ± 0.0002113



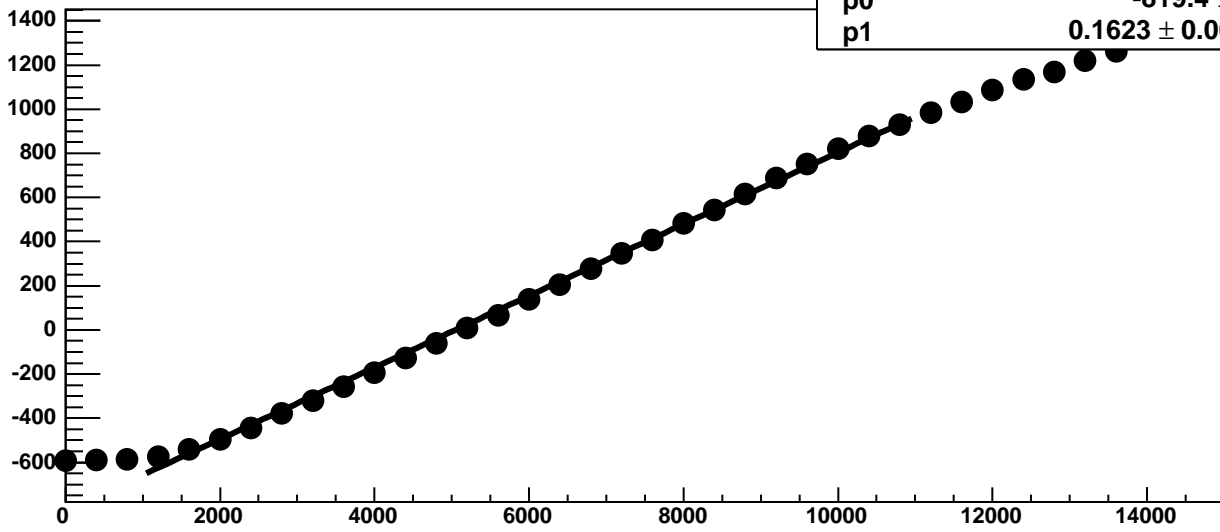
Chip 3, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



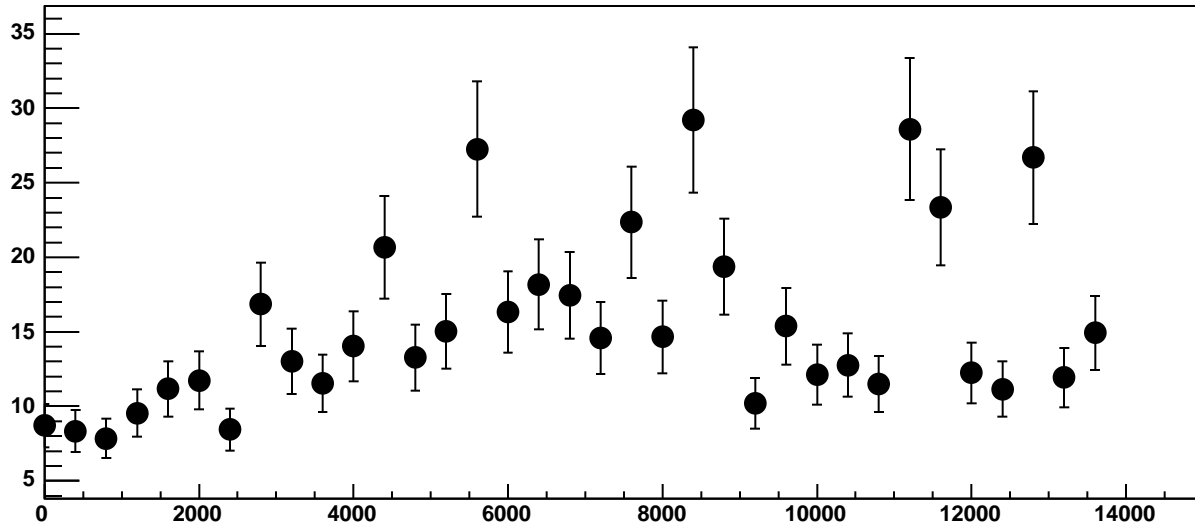
Chip 3, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC



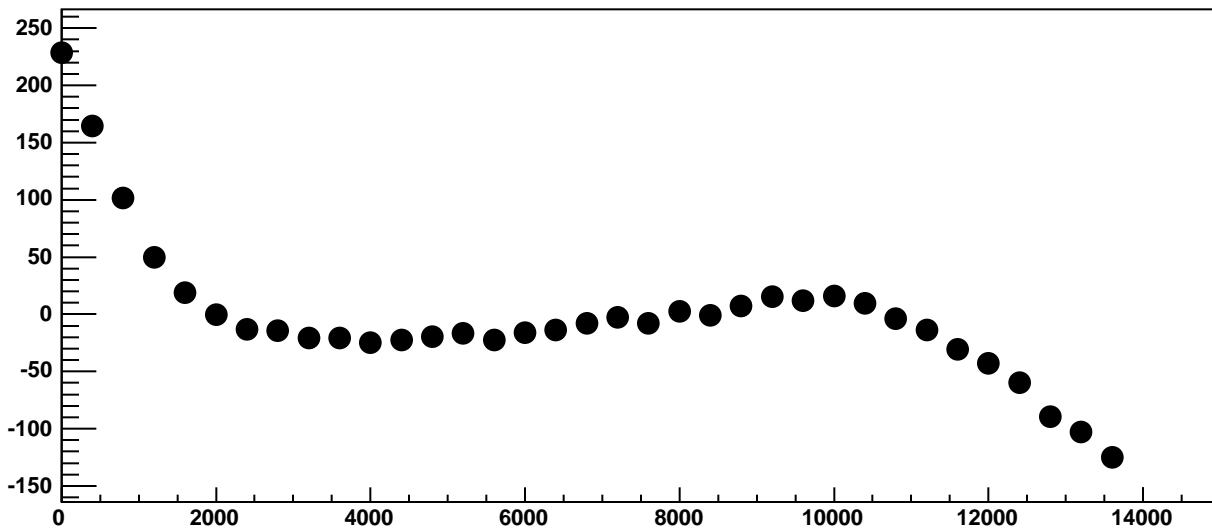
Chip 3, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



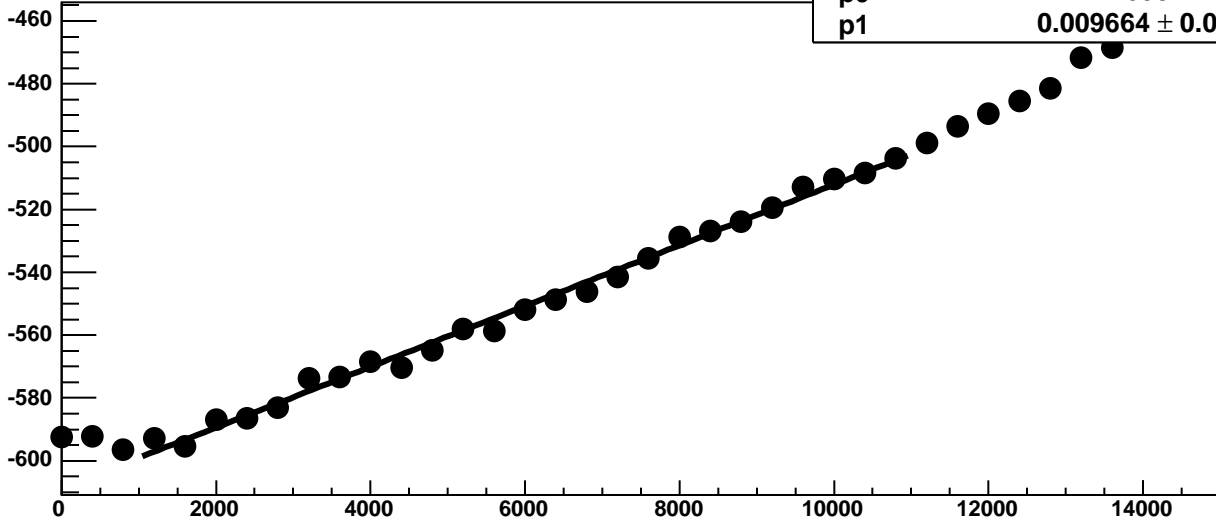
Chip 3, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

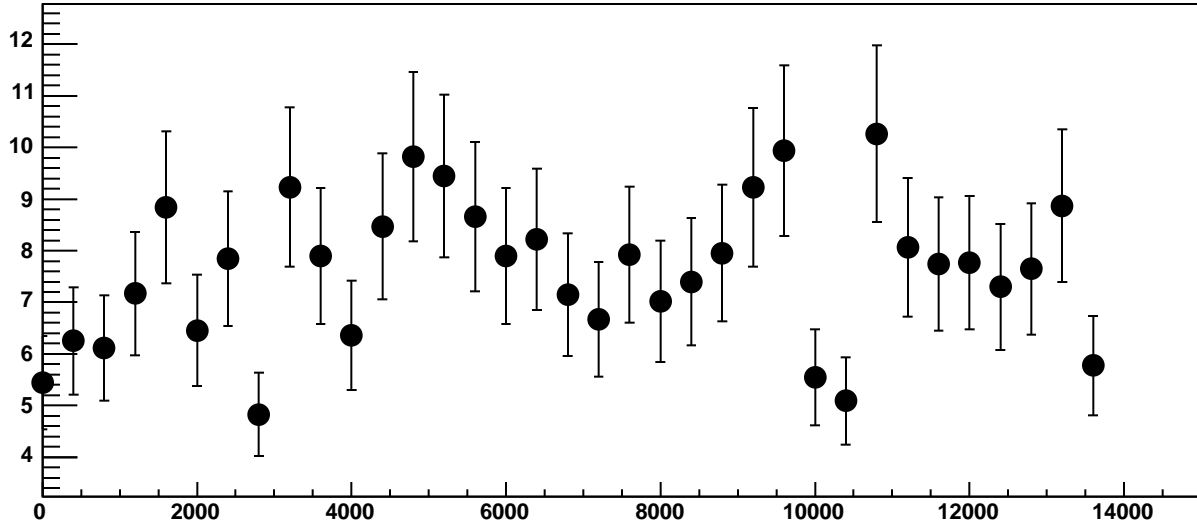


Chip 3, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

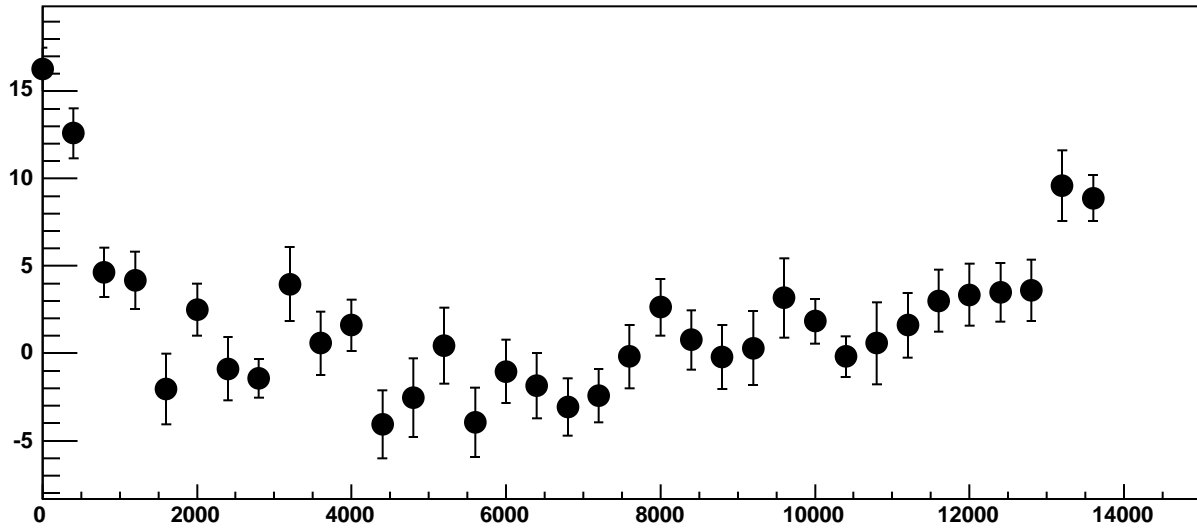


χ^2 / ndf 41.11 / 23
p0 -608.7 ± 0.7534
p1 0.009664 ± 0.0001118

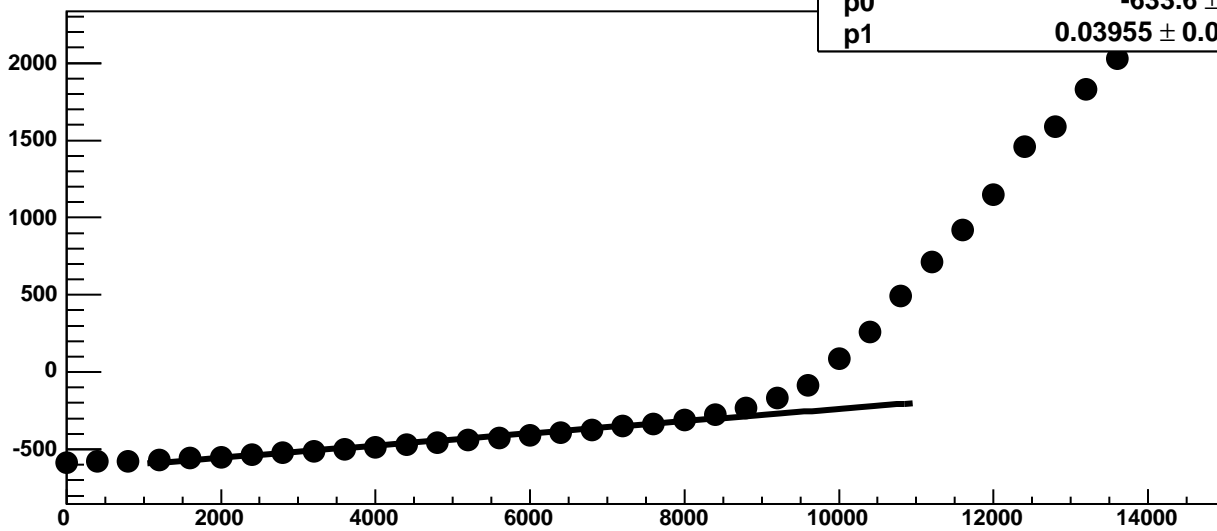
Chip 3, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



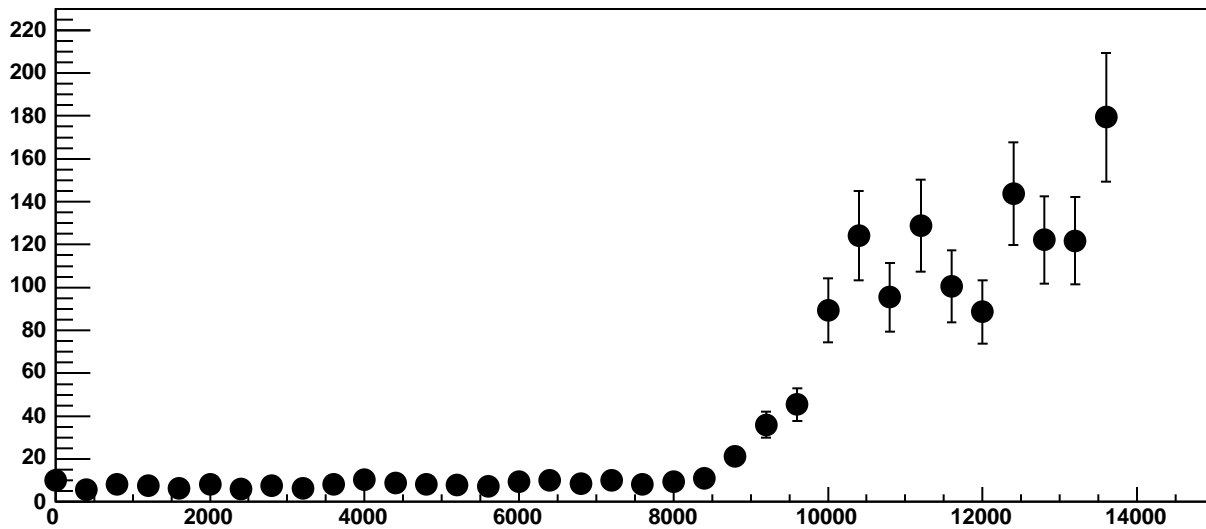
Chip 3, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



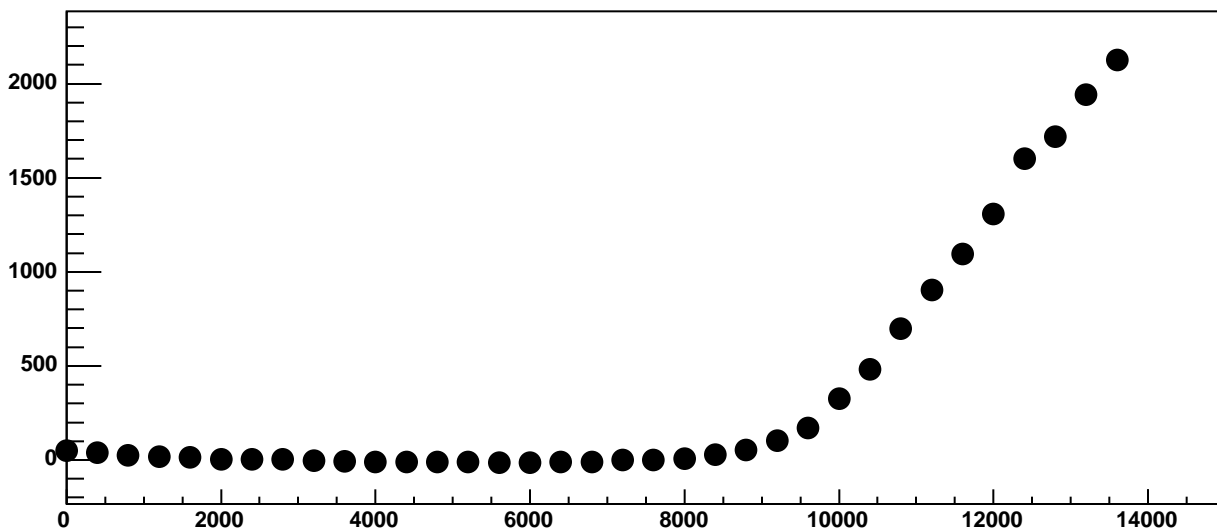
Chip 3, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC



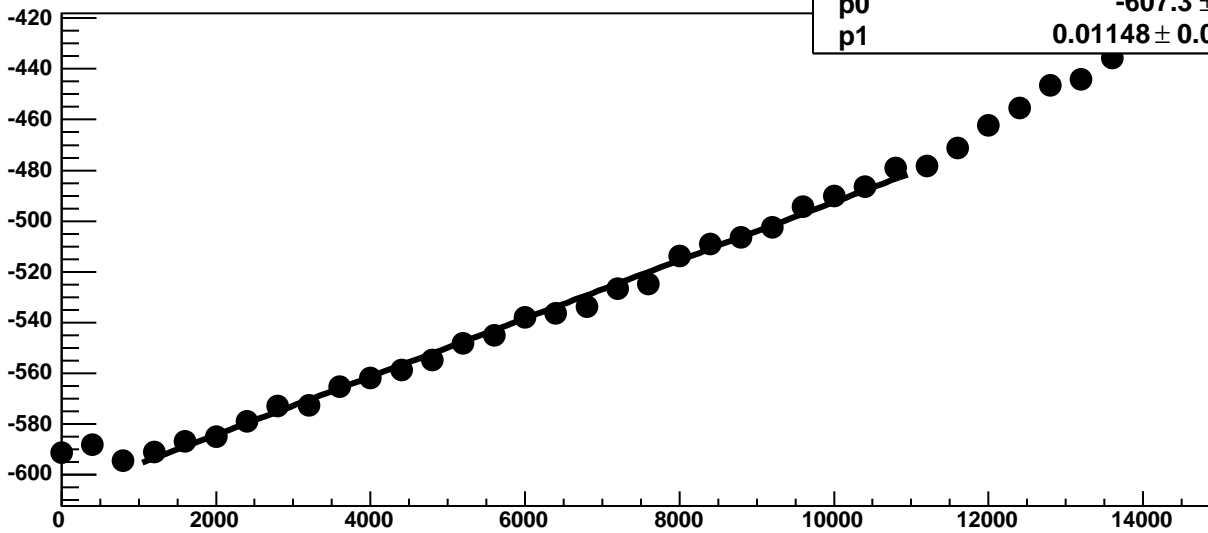
Chip 3, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

47.7 / 23

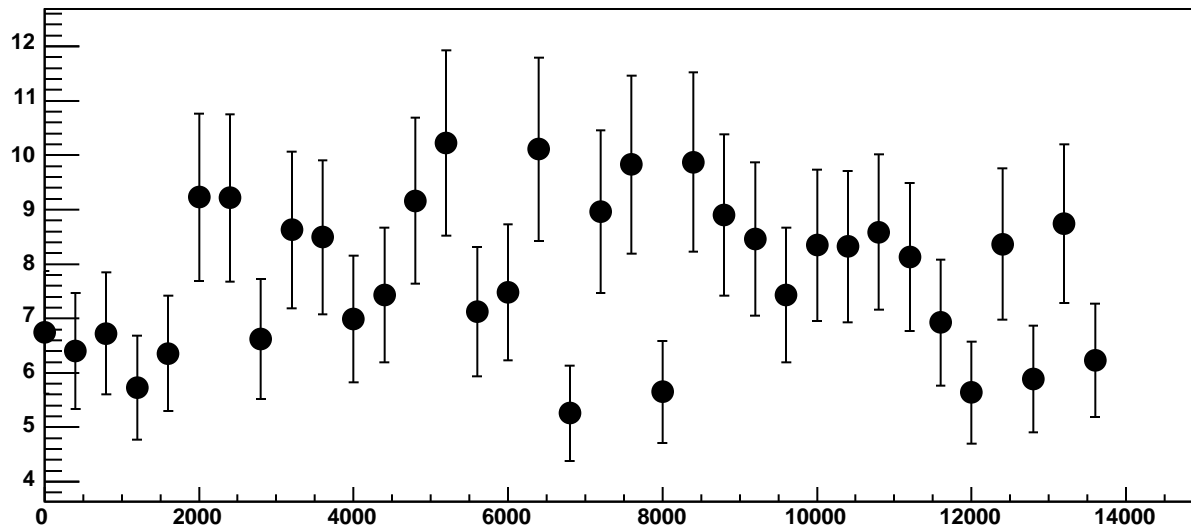
p0

-607.3 ± 0.7779

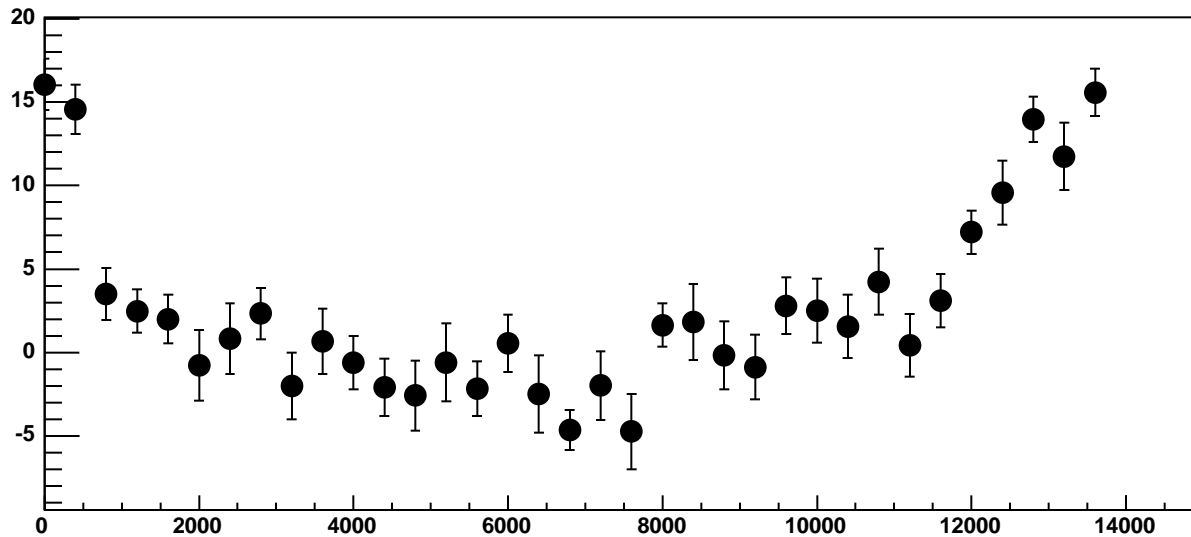
p1

0.01148 ± 0.0001206

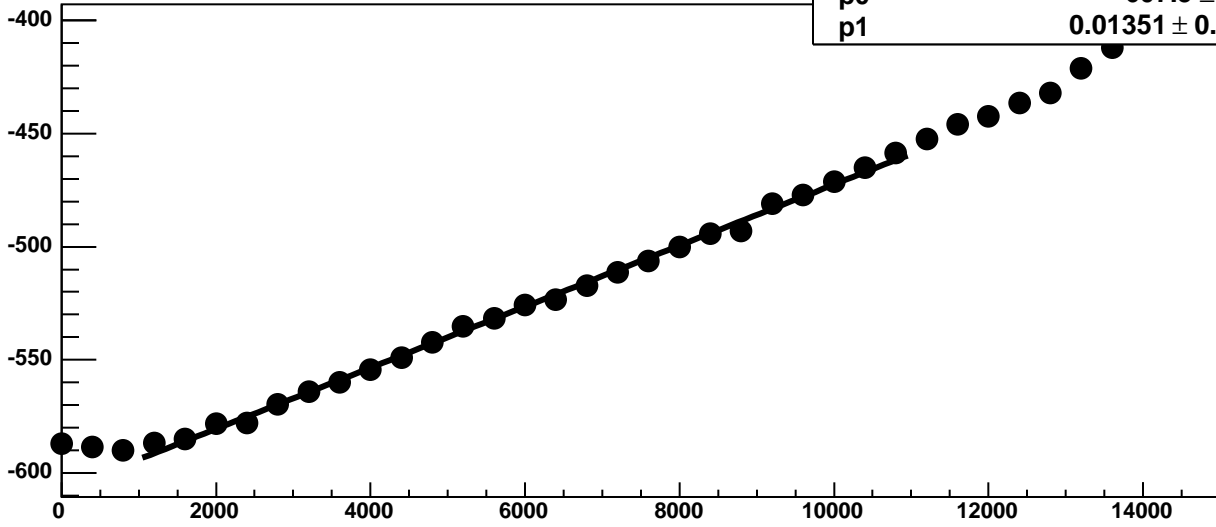
Chip 3, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



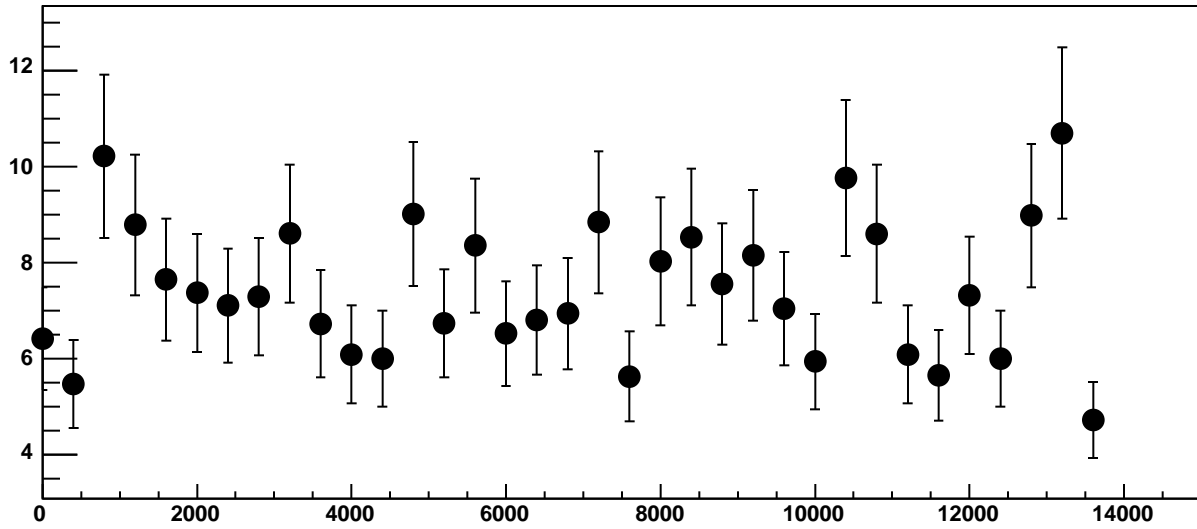
Chip 3, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



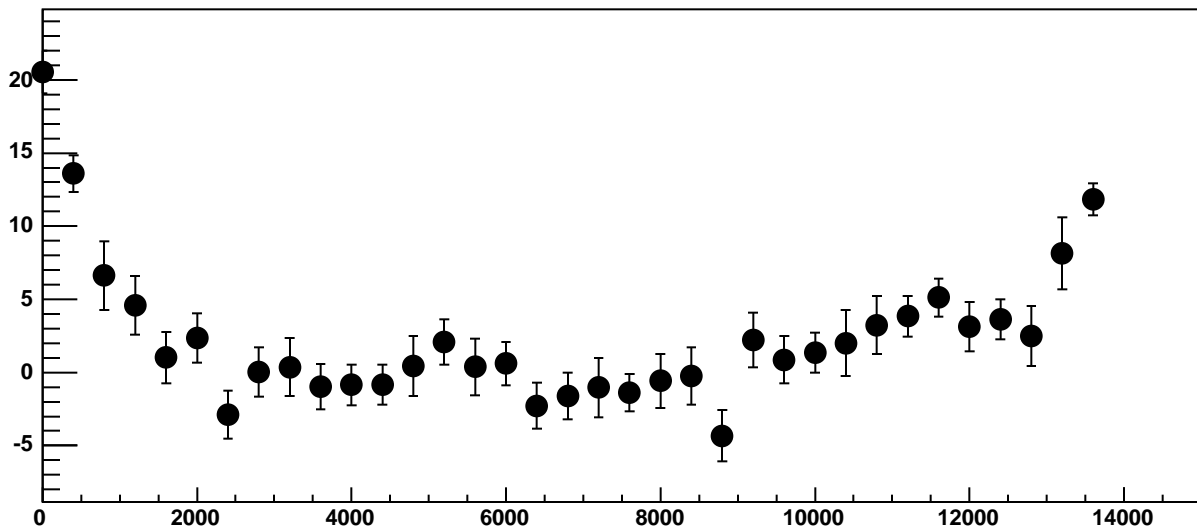
Chip 3, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



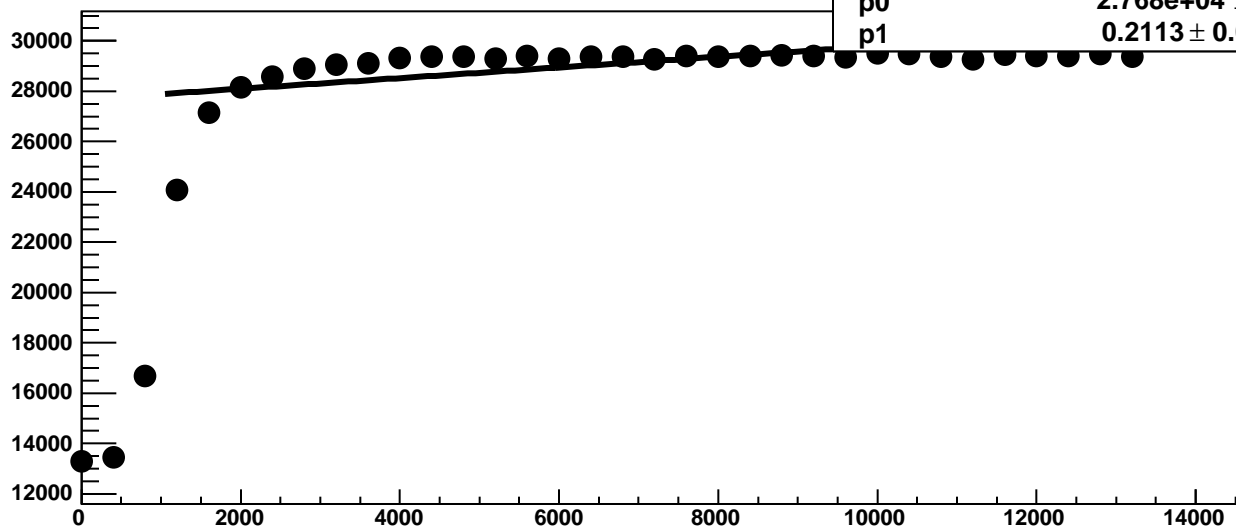
Chip 3, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



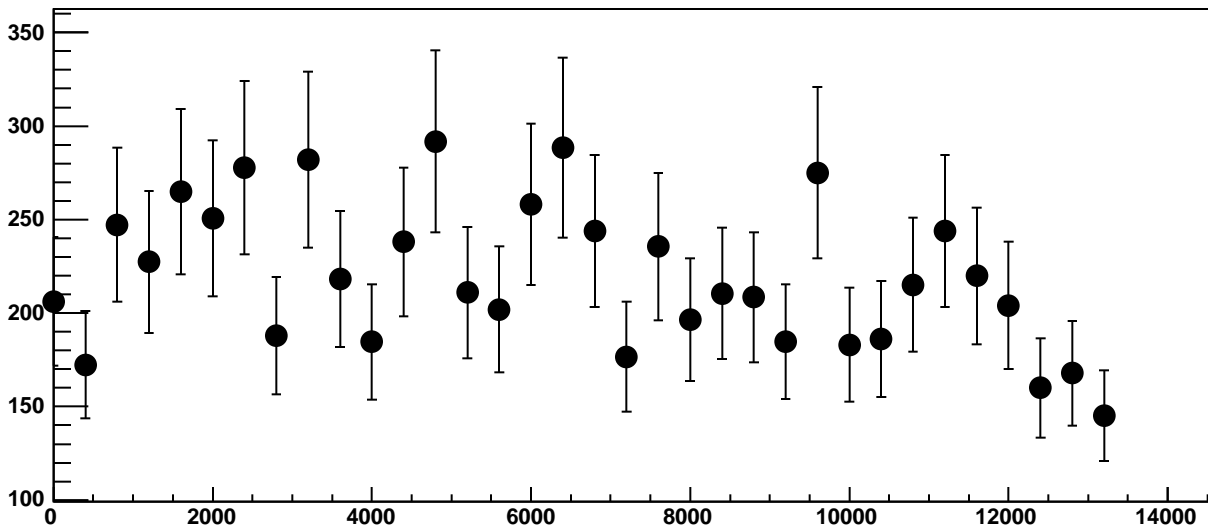
Chip 3, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



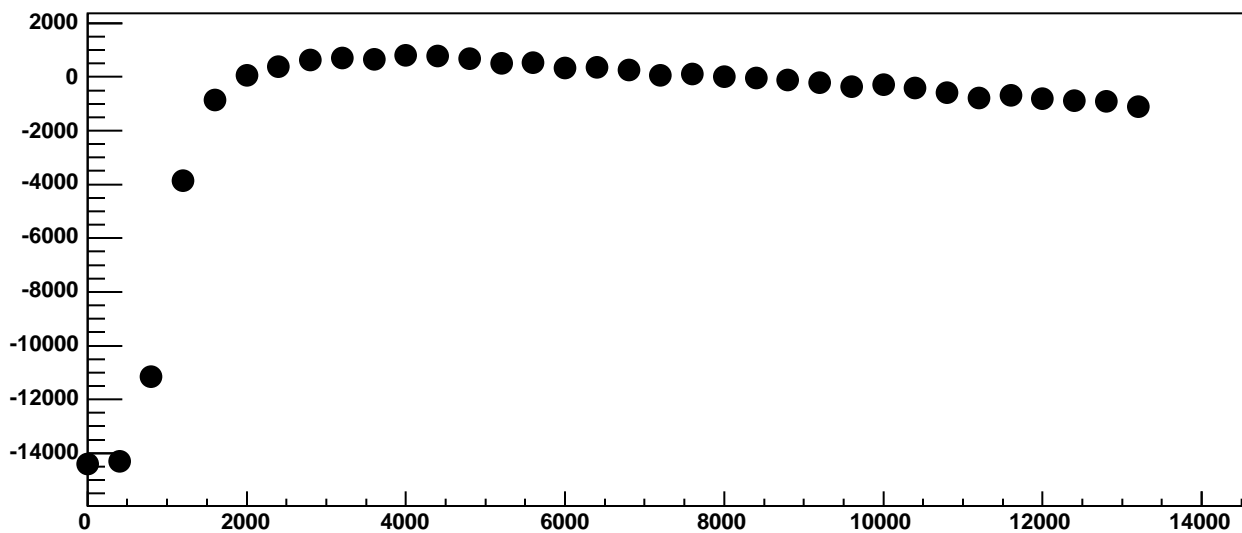
Chip 3, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC



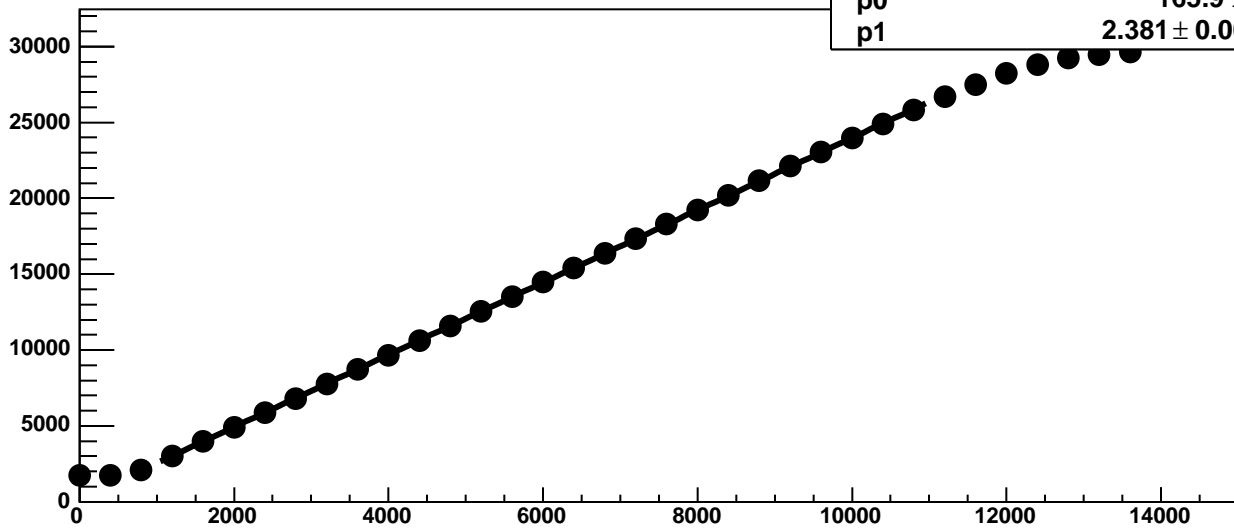
Chip 3, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC



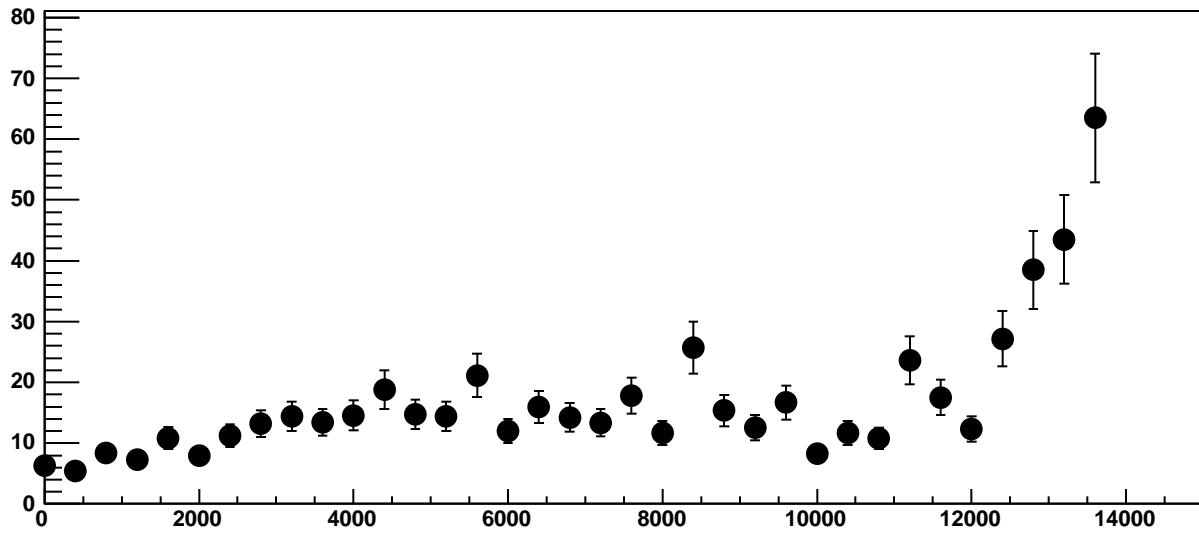
Chip 3, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC



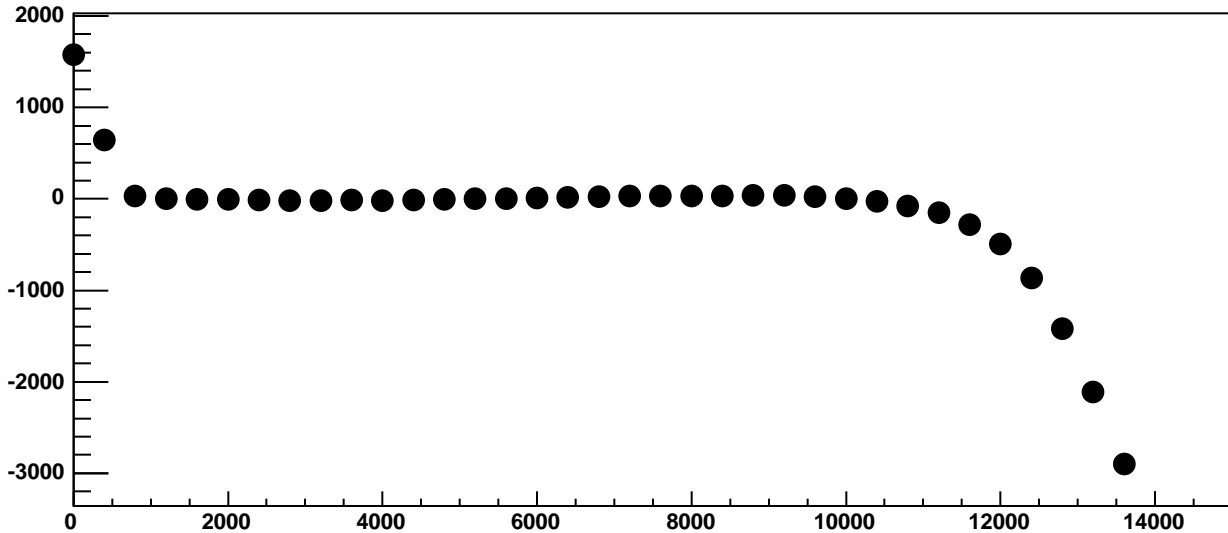
Chip 4, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



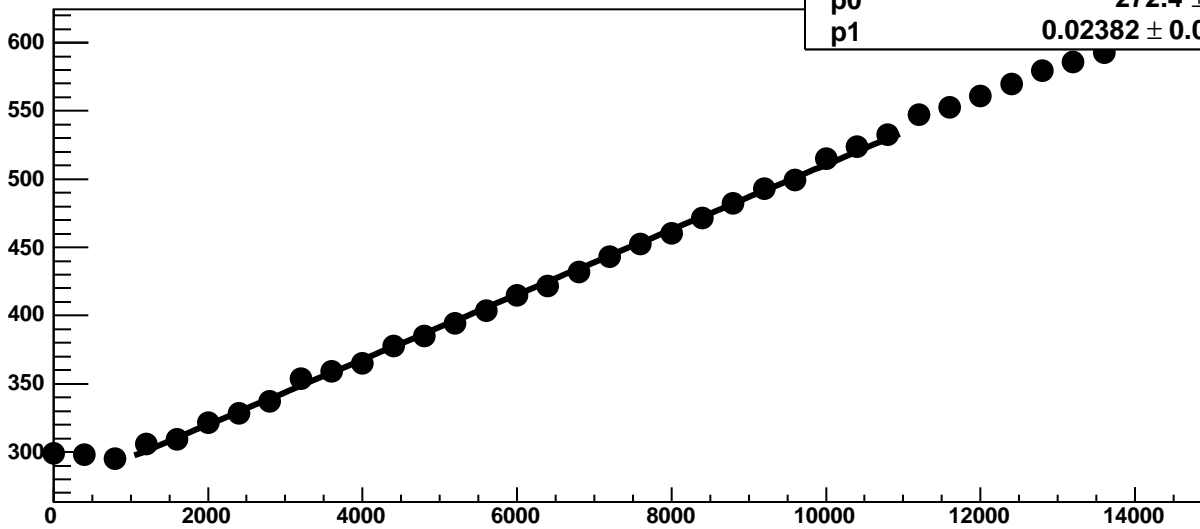
Chip 4, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC

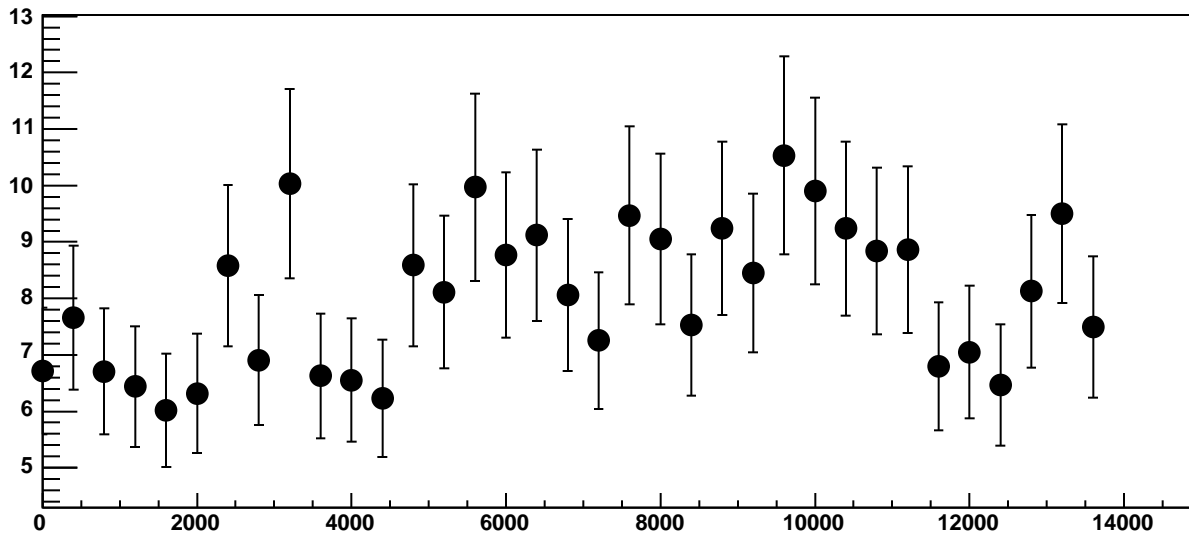


Chip 4, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

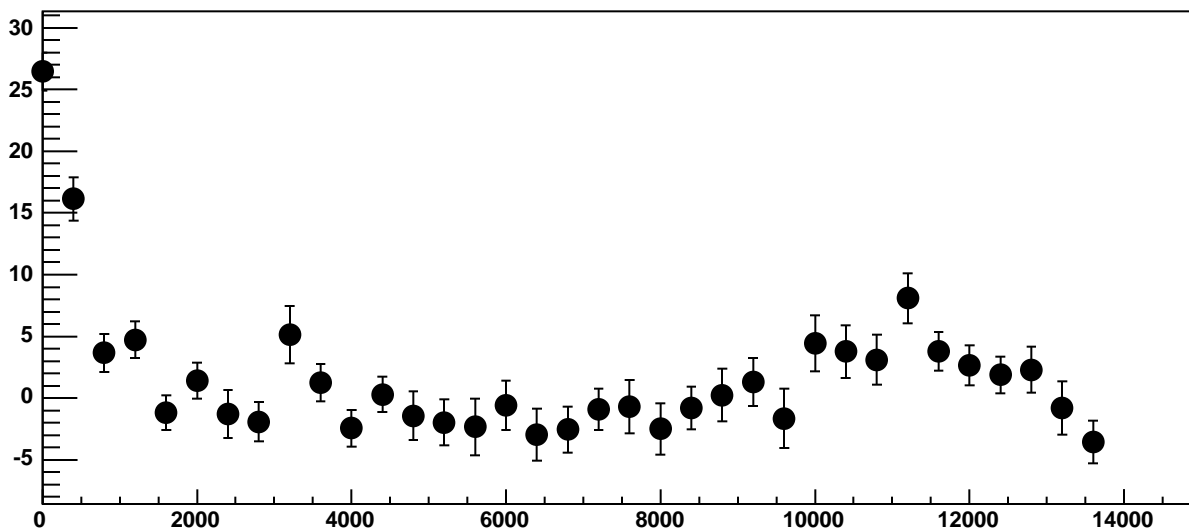


χ^2 / ndf 41.09 / 23
p0 272.4 ± 0.7595
p1 0.02382 ± 0.0001252

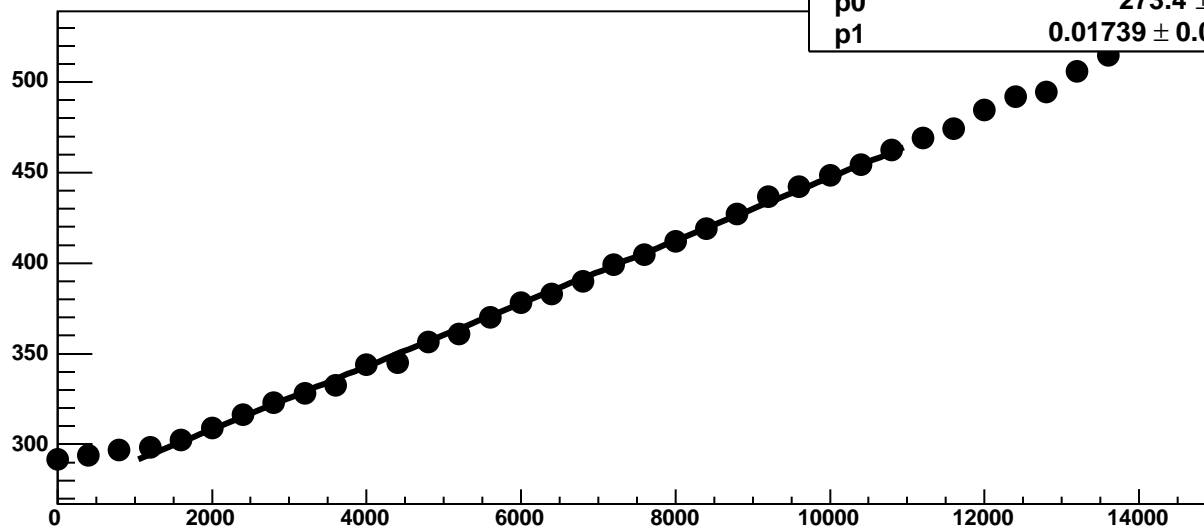
Chip 4, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

26.14 / 23

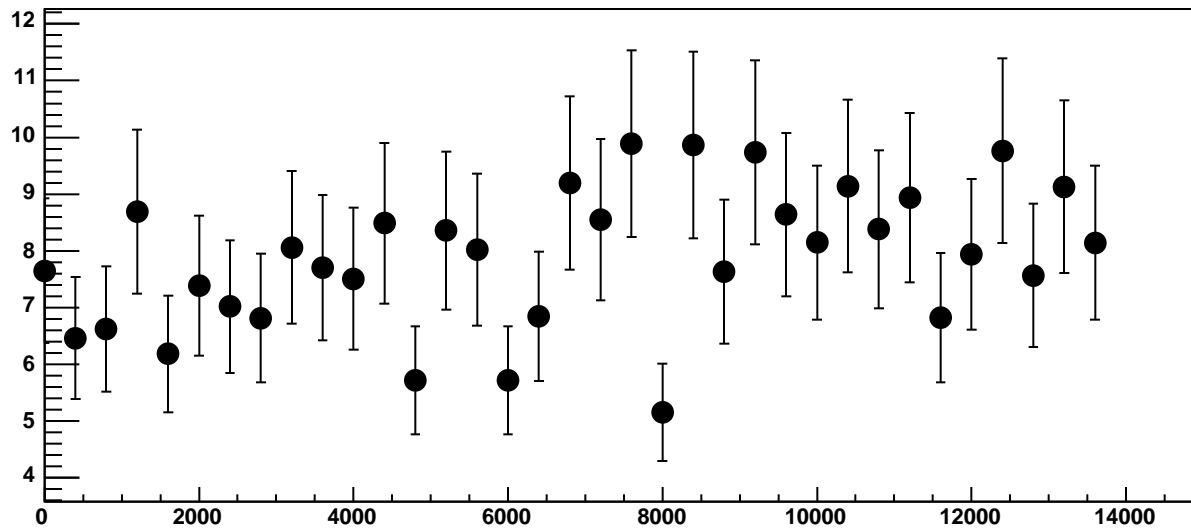
p0

273.4 ± 0.7886

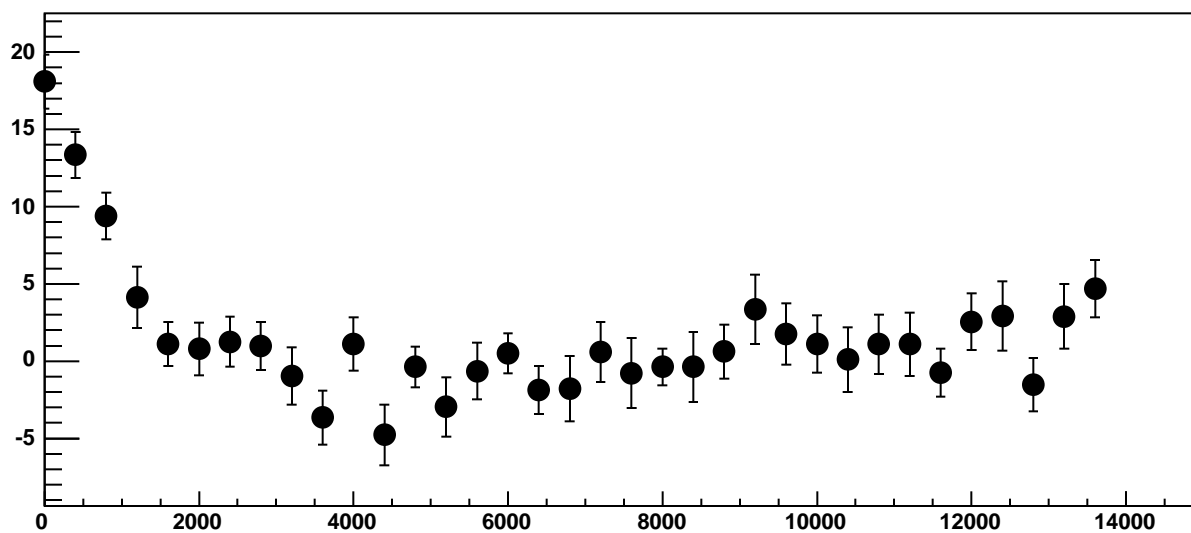
p1

0.01739 ± 0.0001241

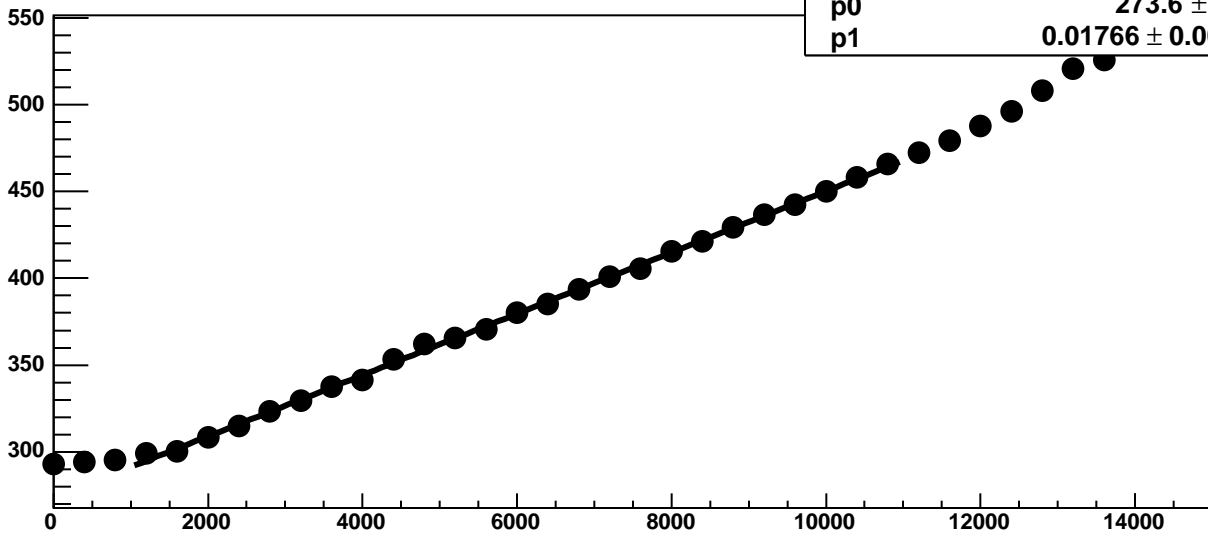
Chip 4, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

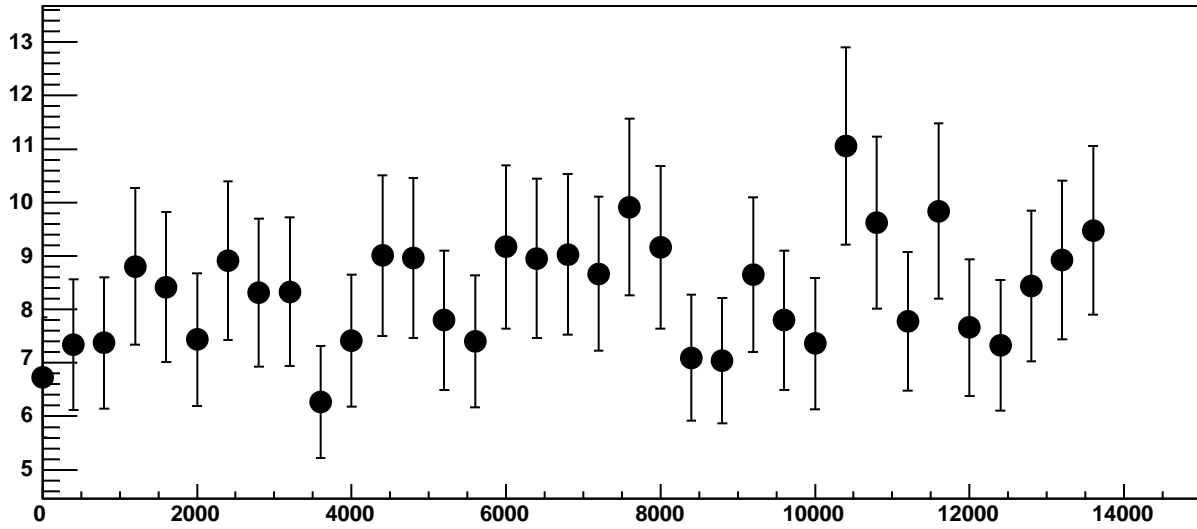


Chip 4, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

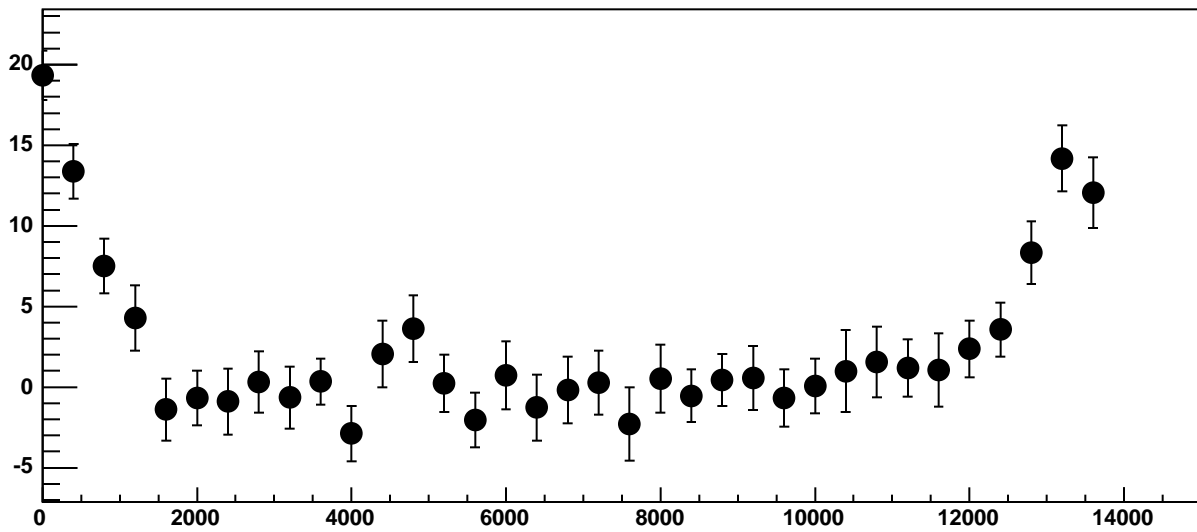


χ^2 / ndf 16.69 / 23
p0 273.6 ± 0.8674
p1 0.01766 ± 0.0001327

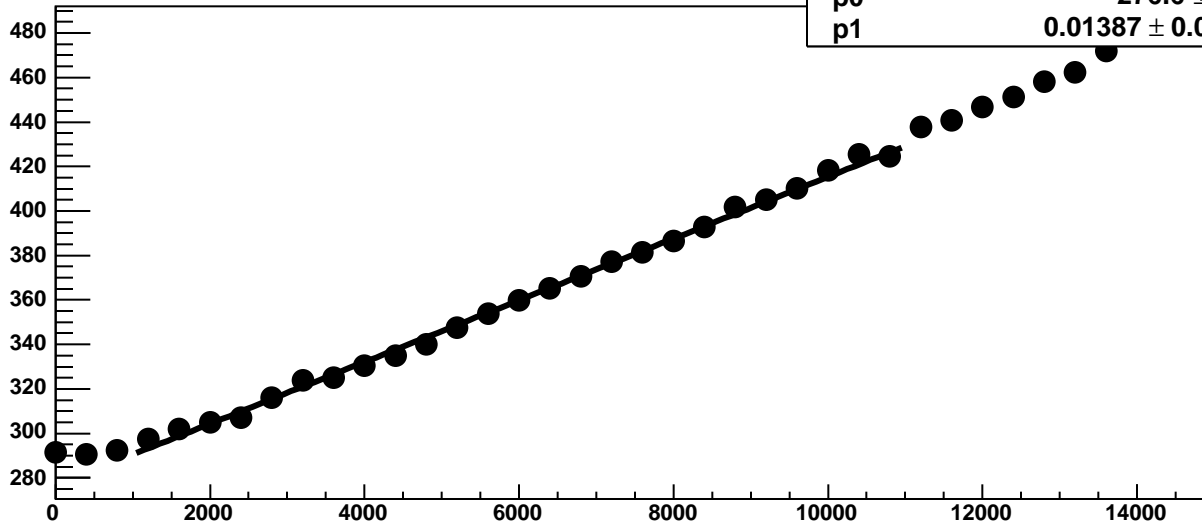
Chip 4, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

46.24 / 23

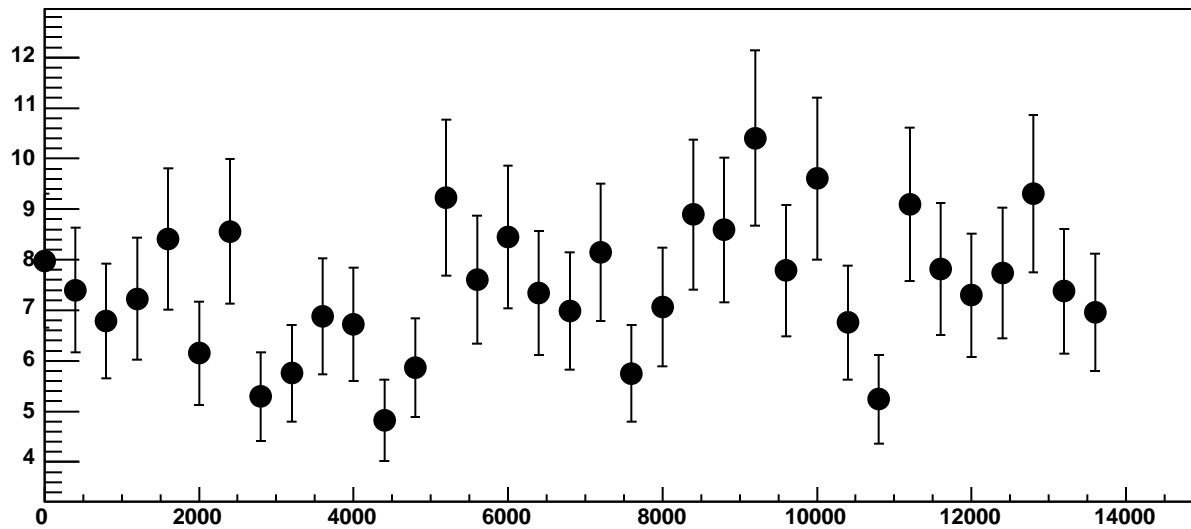
p0

276.6 ± 0.7075

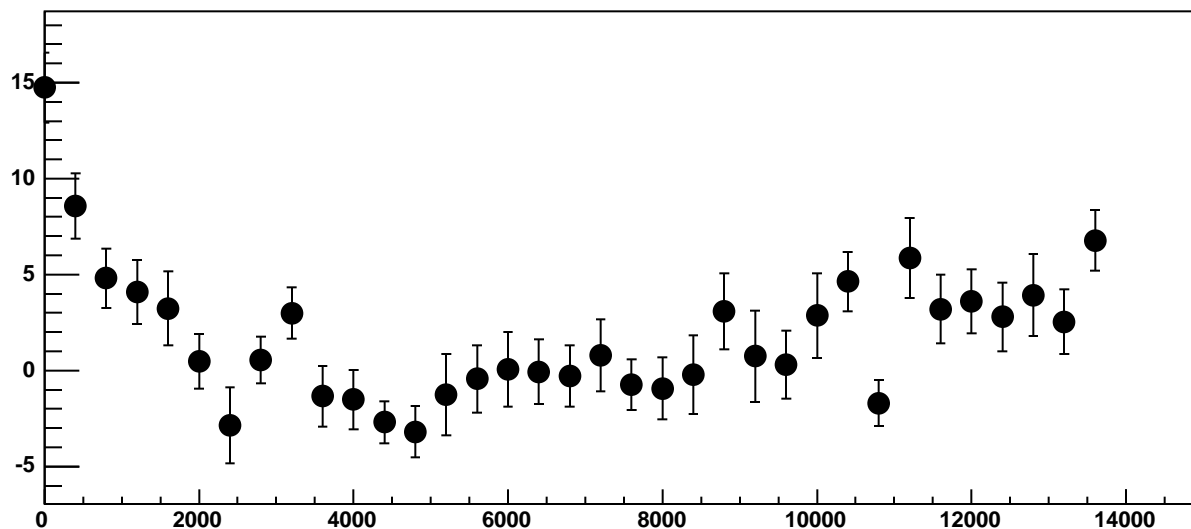
p1

0.01387 ± 0.0001096

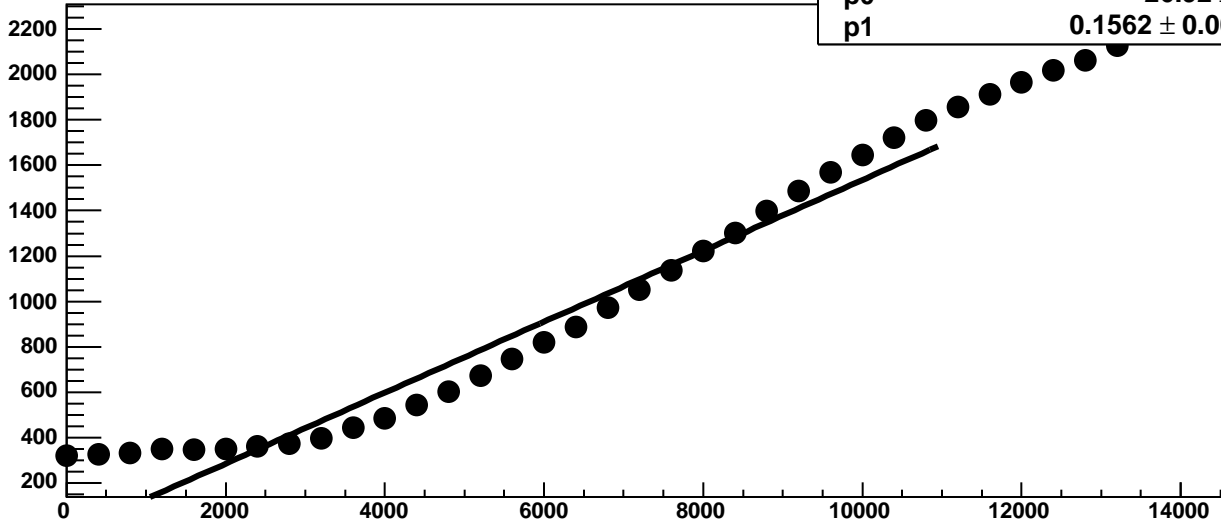
Chip 4, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



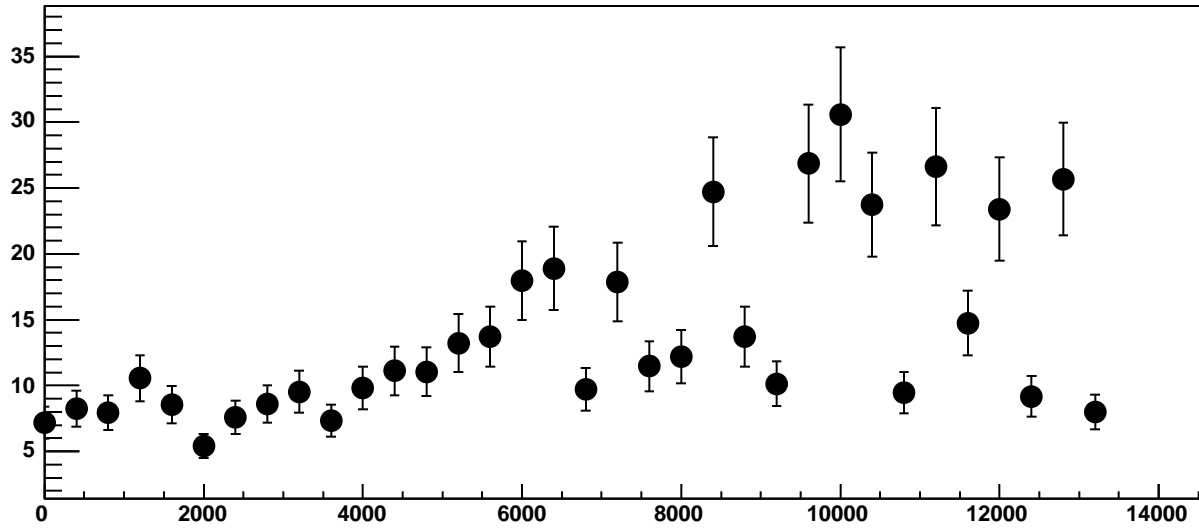
Chip 4, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



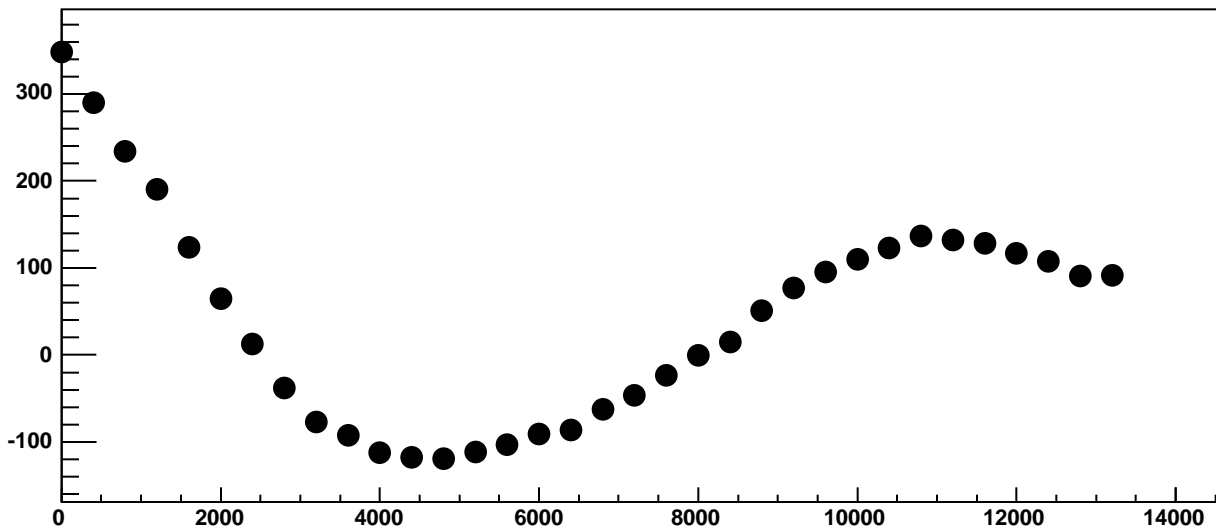
Chip 4, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



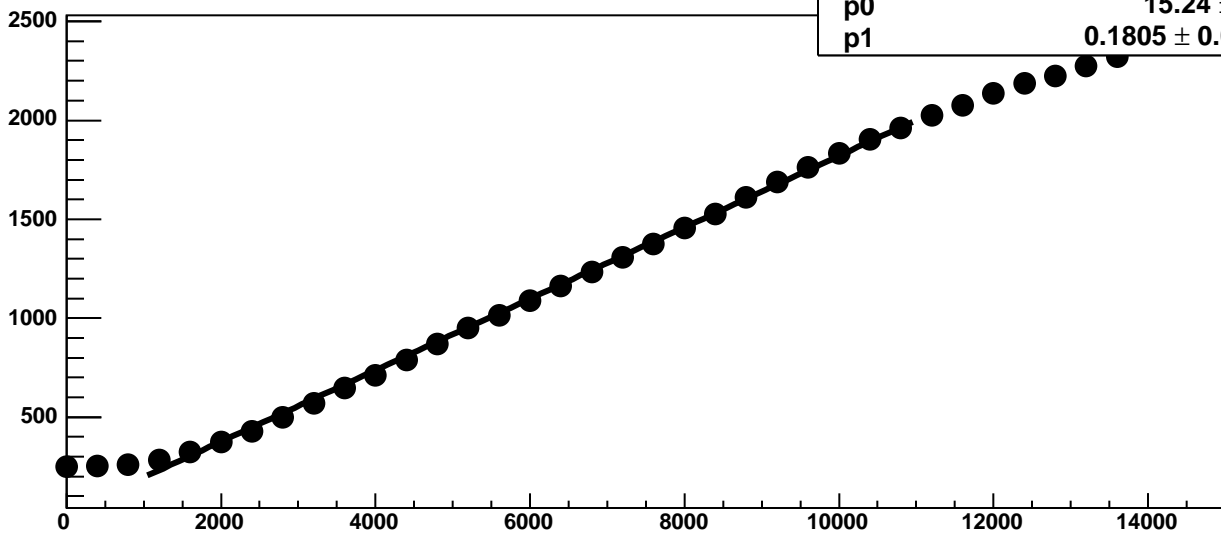
Chip 4, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



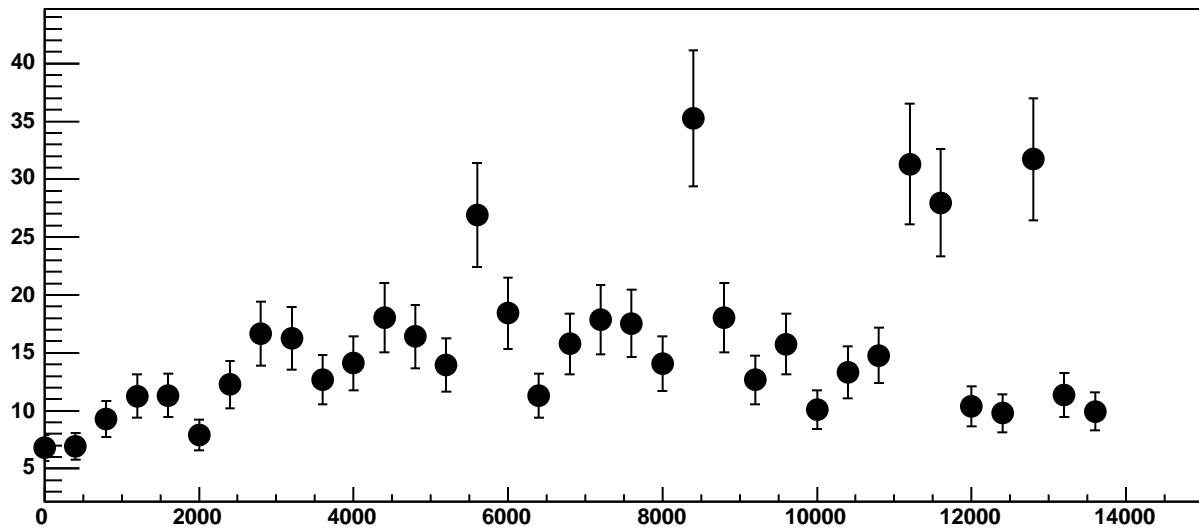
Chip 4, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



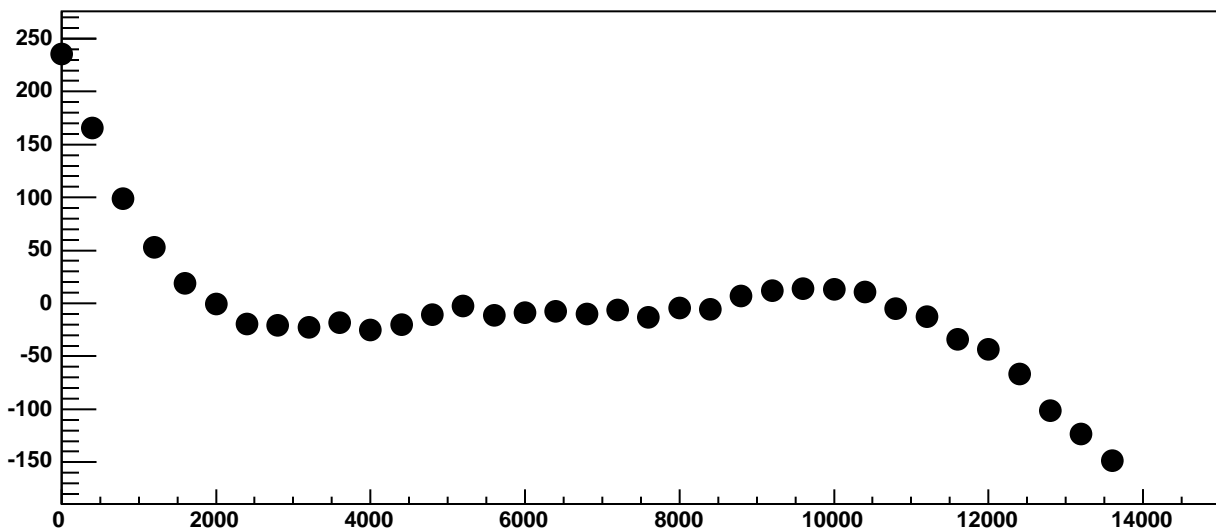
Chip 4, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC



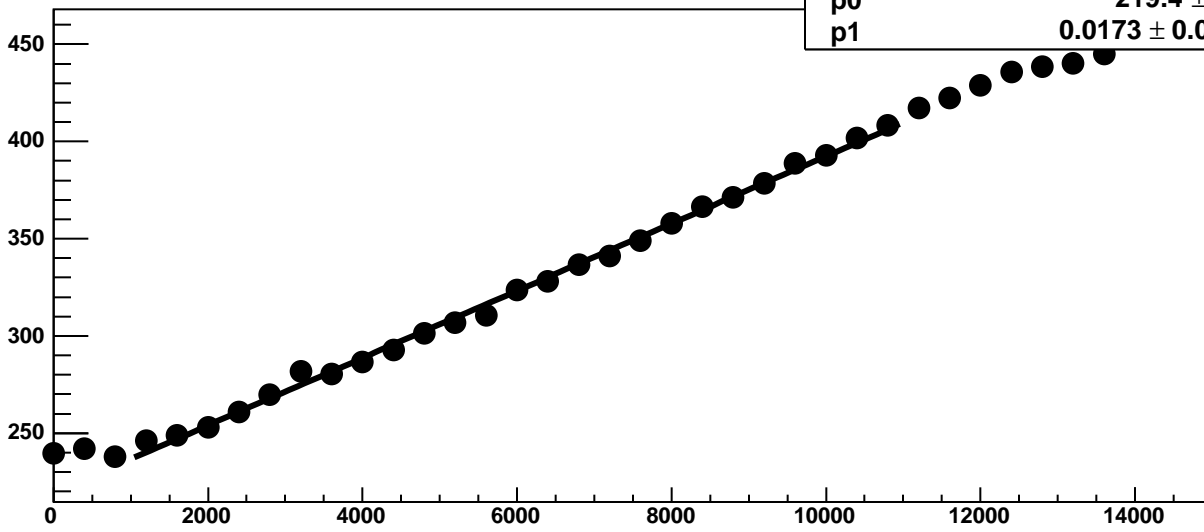
Chip 4, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC

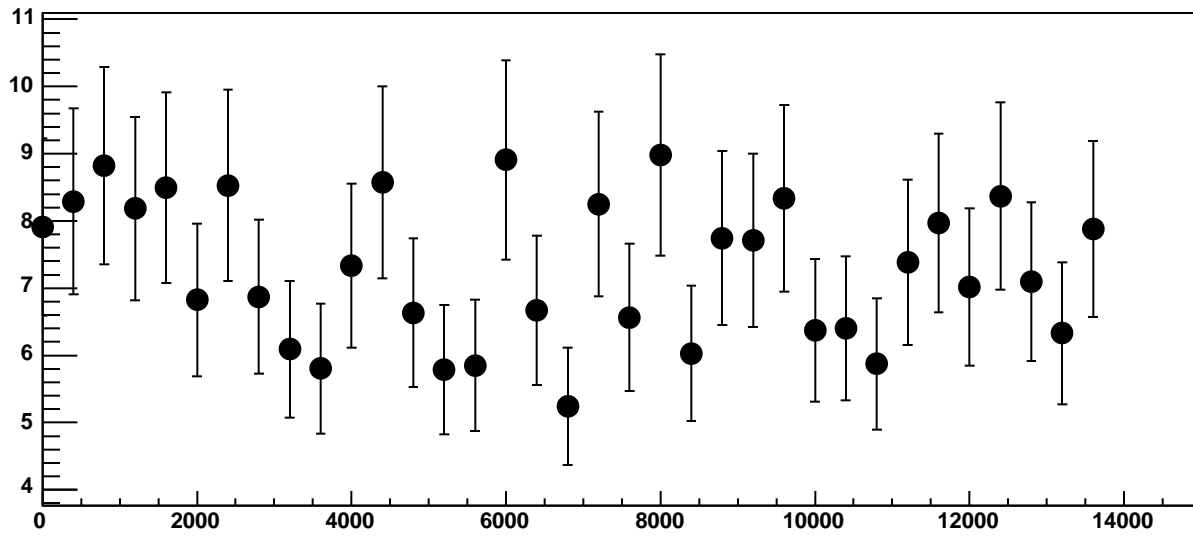


Chip 4, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

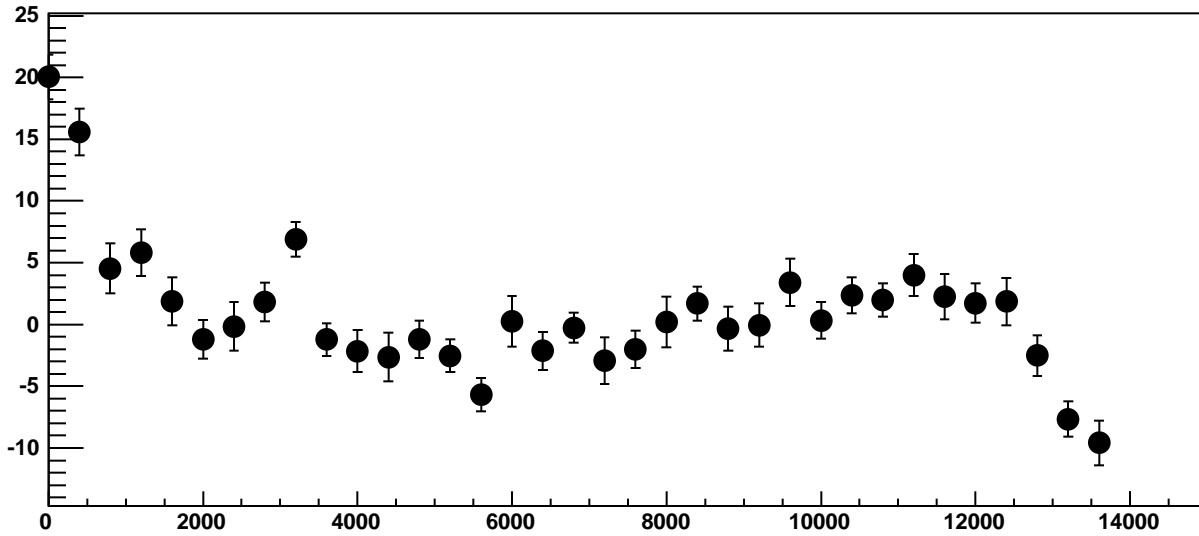


χ^2 / ndf 78.8 / 23
p0 219.4 ± 0.7546
p1 0.0173 ± 0.0001116

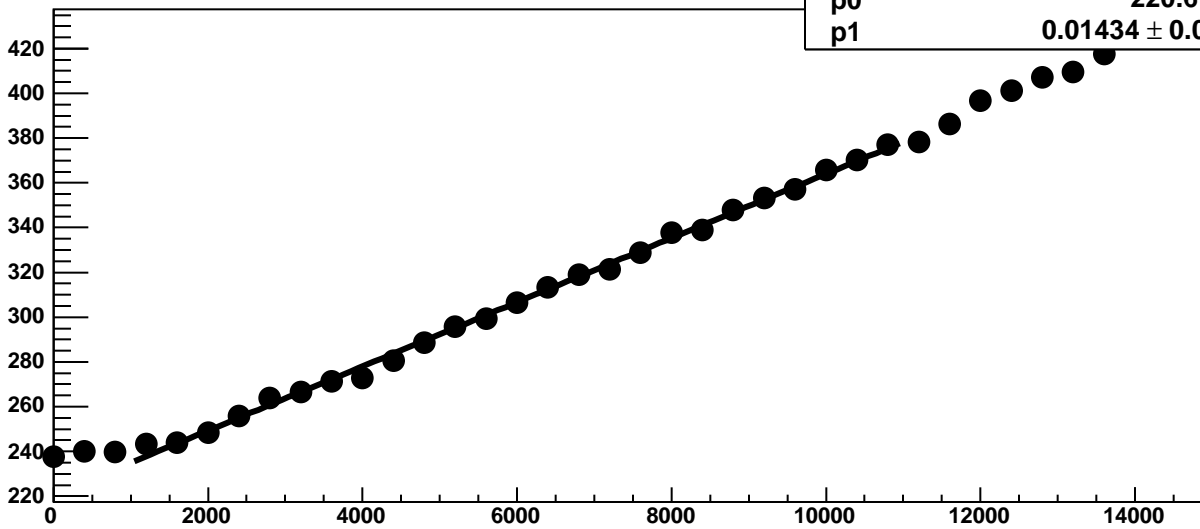
Chip 4, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

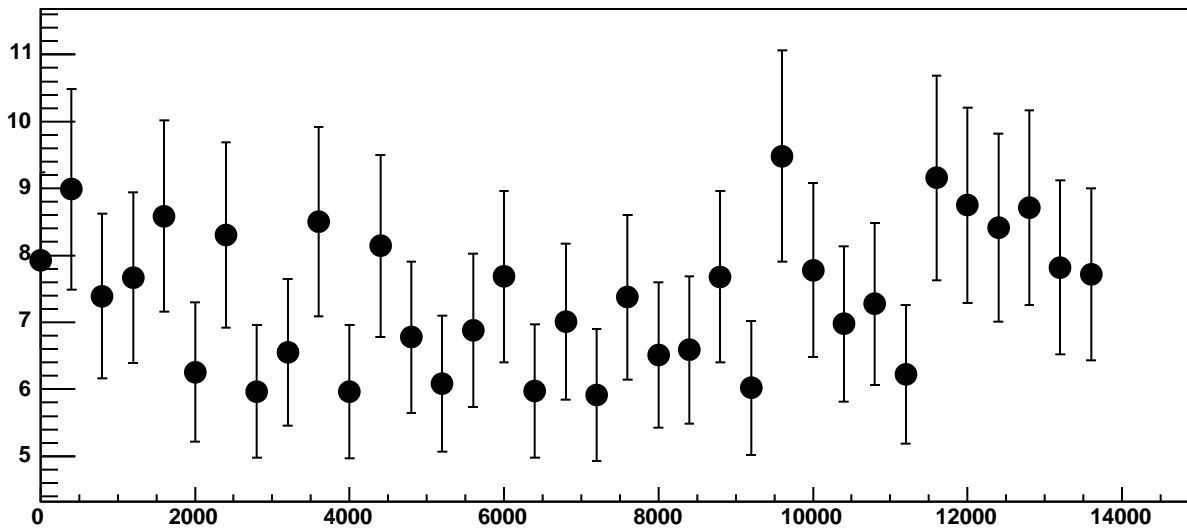


Chip 4, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

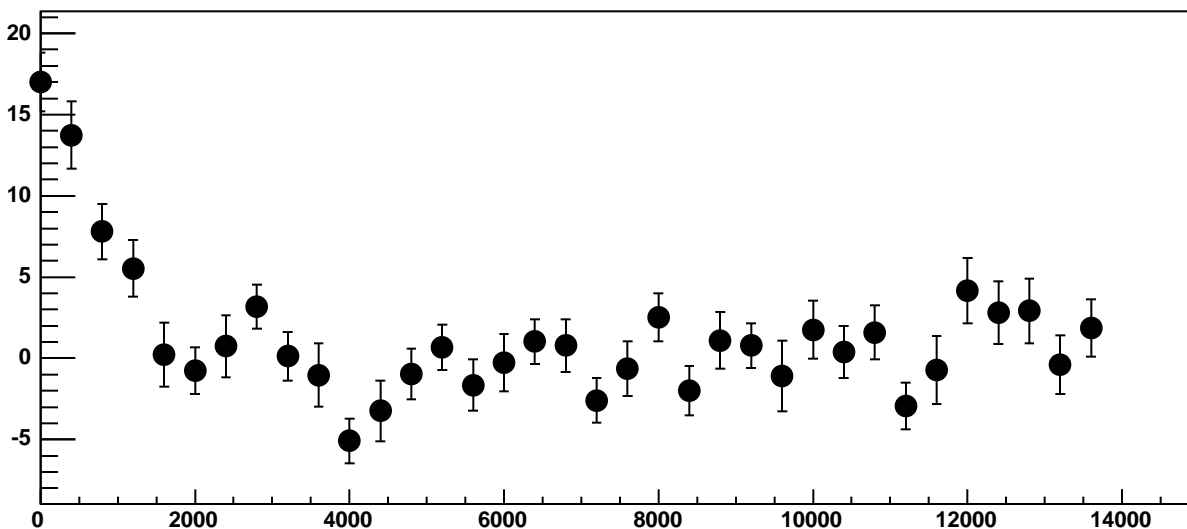


χ^2 / ndf 46.65 / 23
p0 220.6 ± 0.758
p1 0.01434 ± 0.0001148

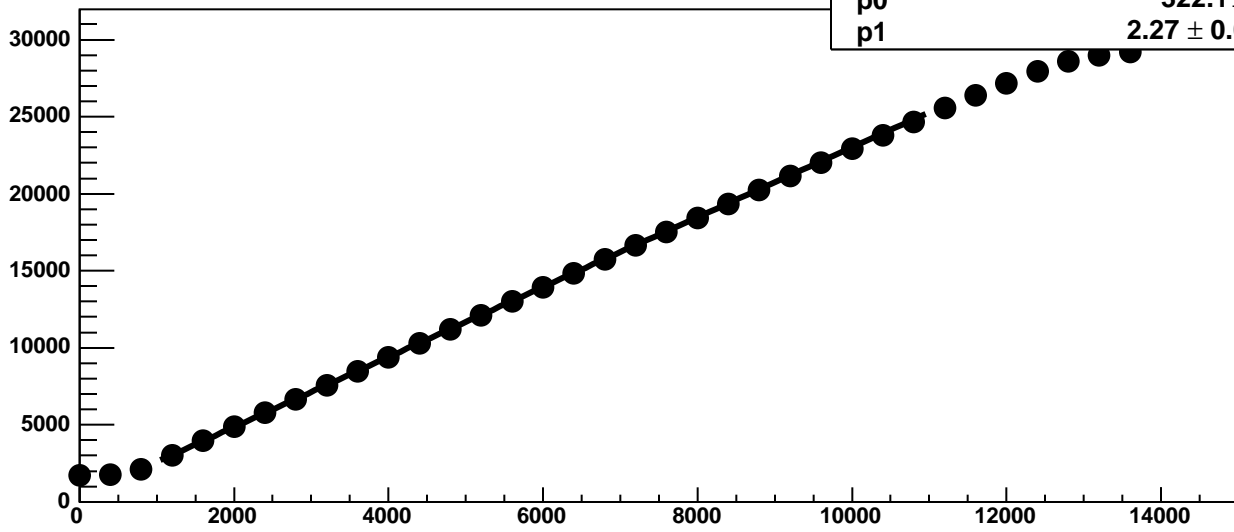
Chip 4, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



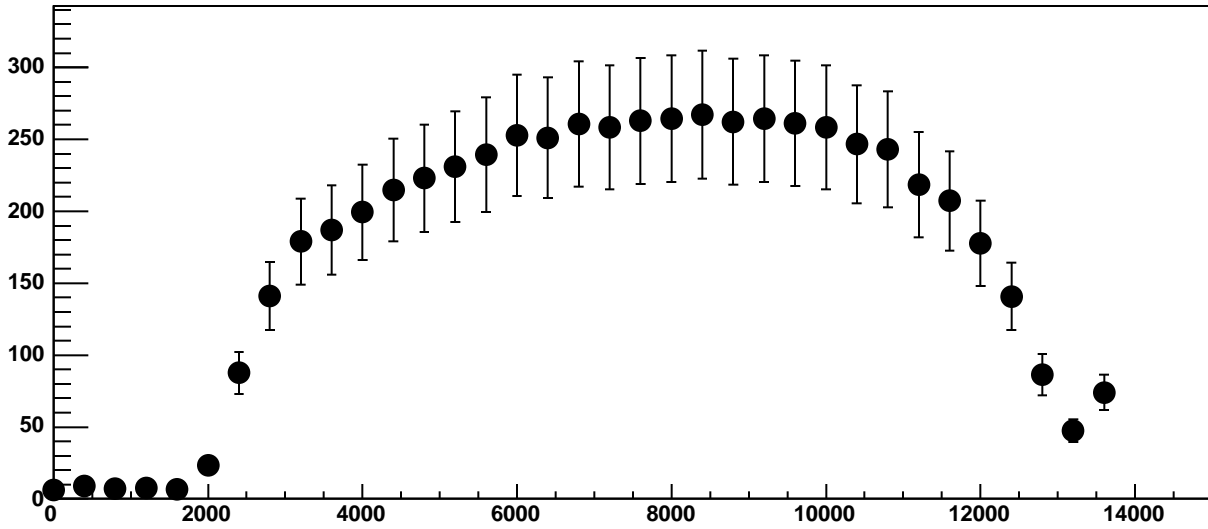
Chip 4, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



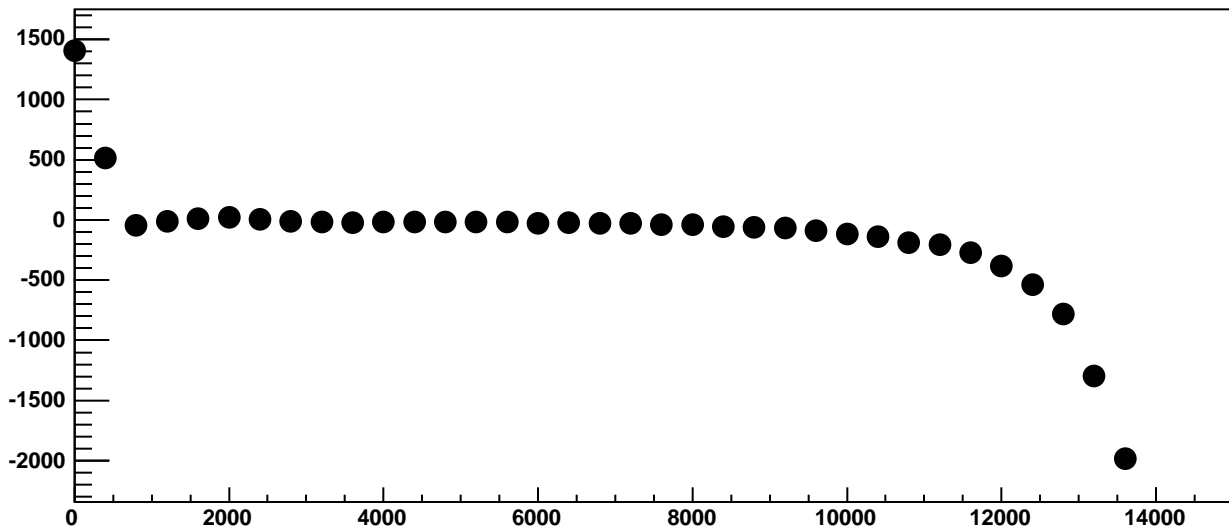
Chip 4, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC



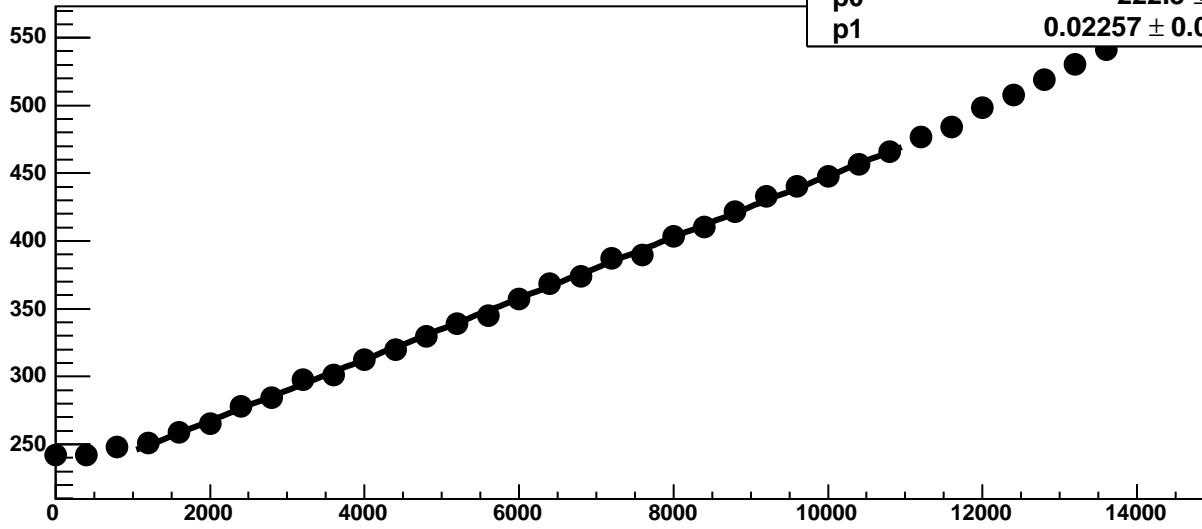
Chip 4, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC

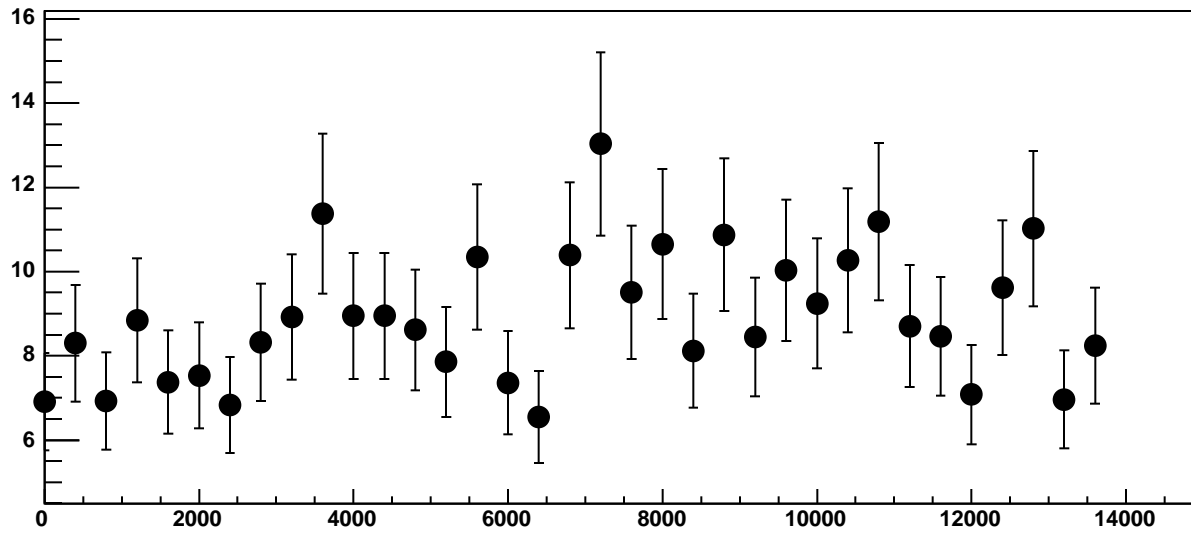


Chip 4, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

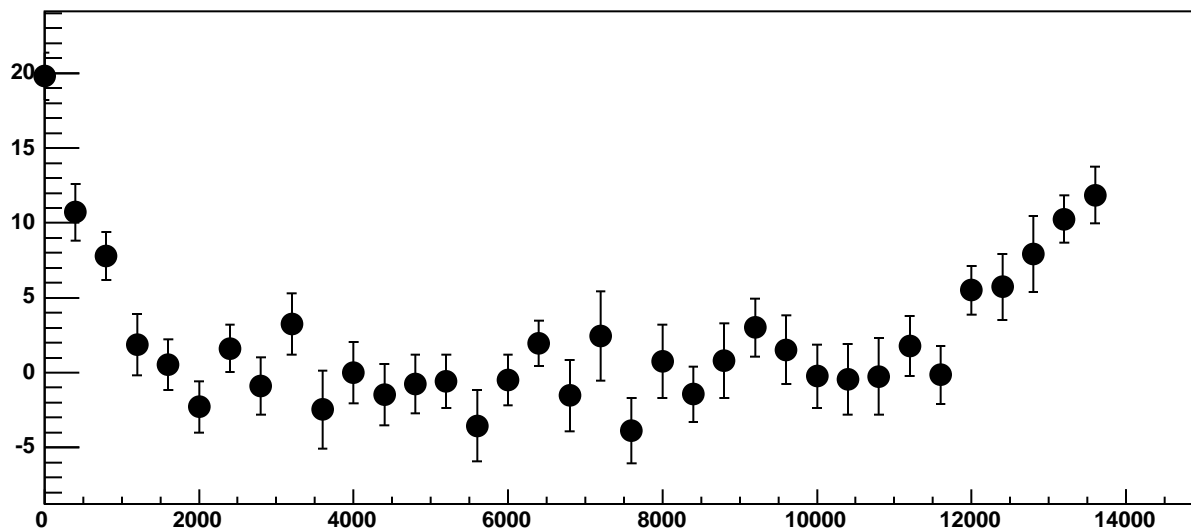


χ^2 / ndf 20.14 / 23
p0 222.3 ± 0.8865
p1 0.02257 ± 0.0001418

Chip 4, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

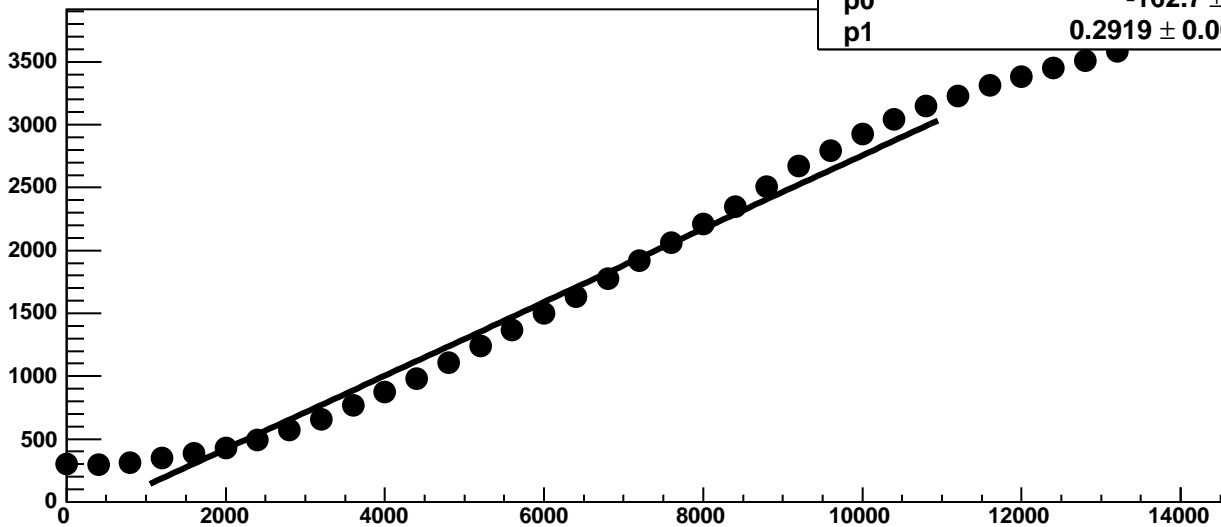


Chip 4, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

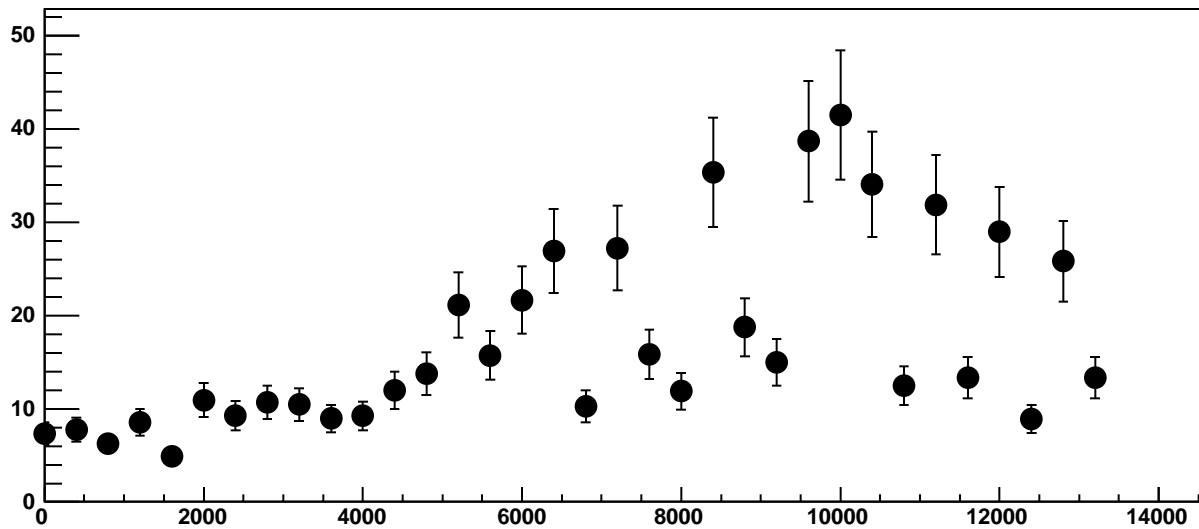


Chip 4, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

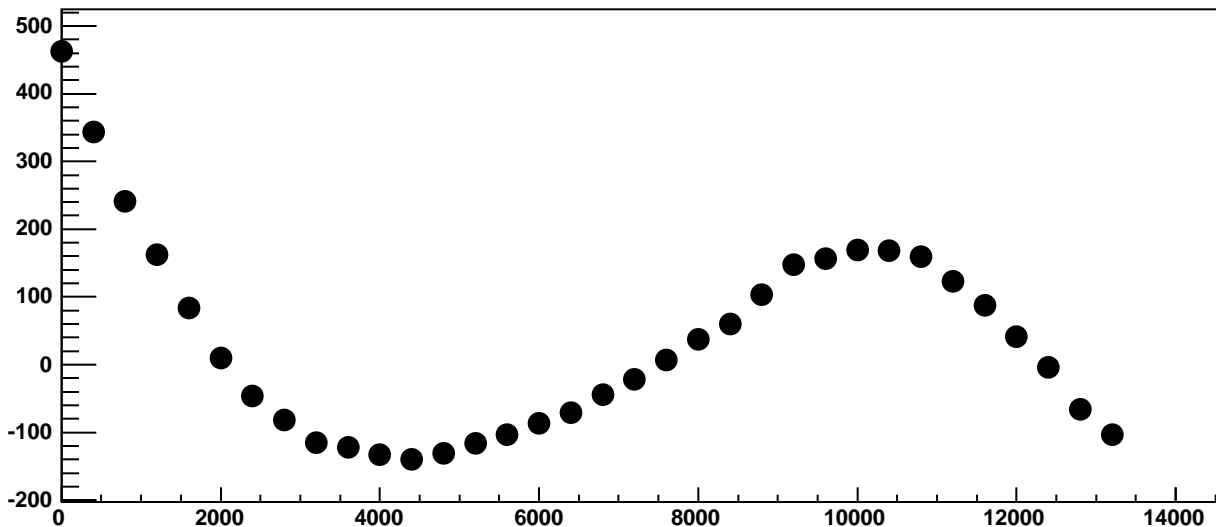
χ^2 / ndf 3.705e+04 / 23
p0 -162.7 ± 0.9711
p1 0.2919 ± 0.0002016



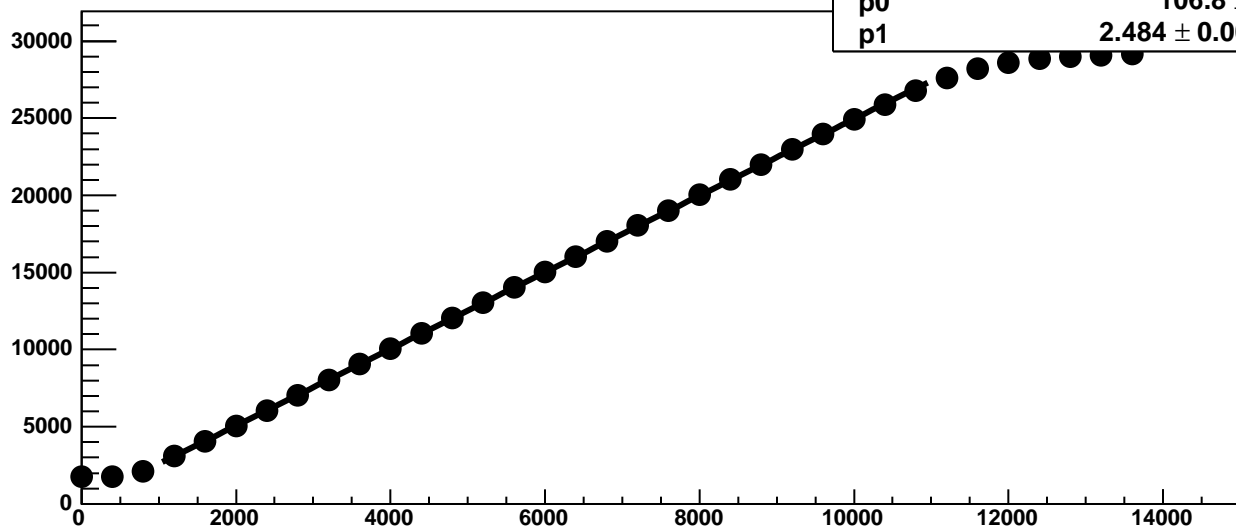
Chip 4, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



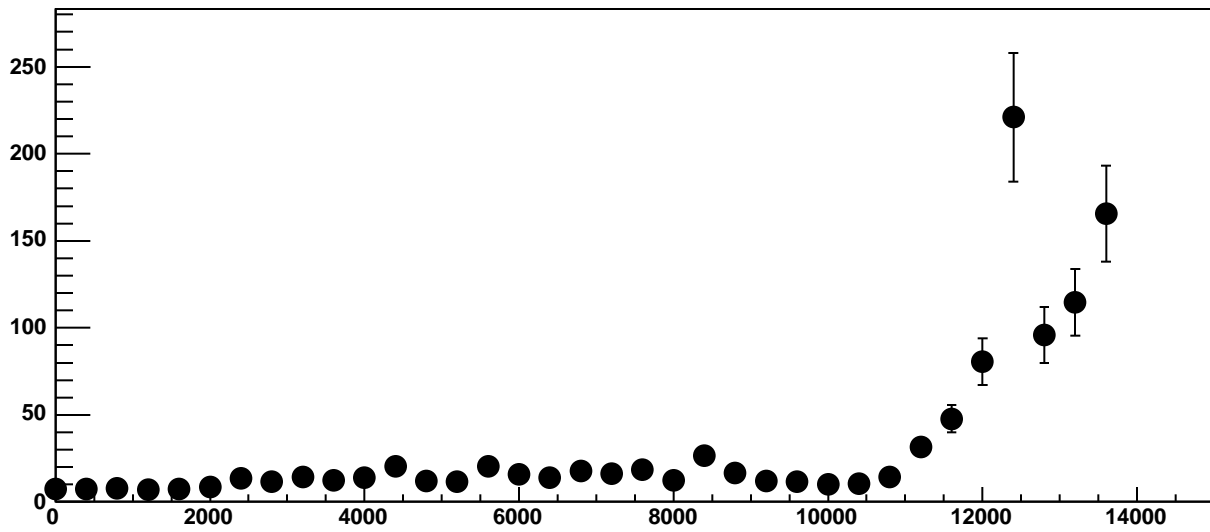
Chip 4, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



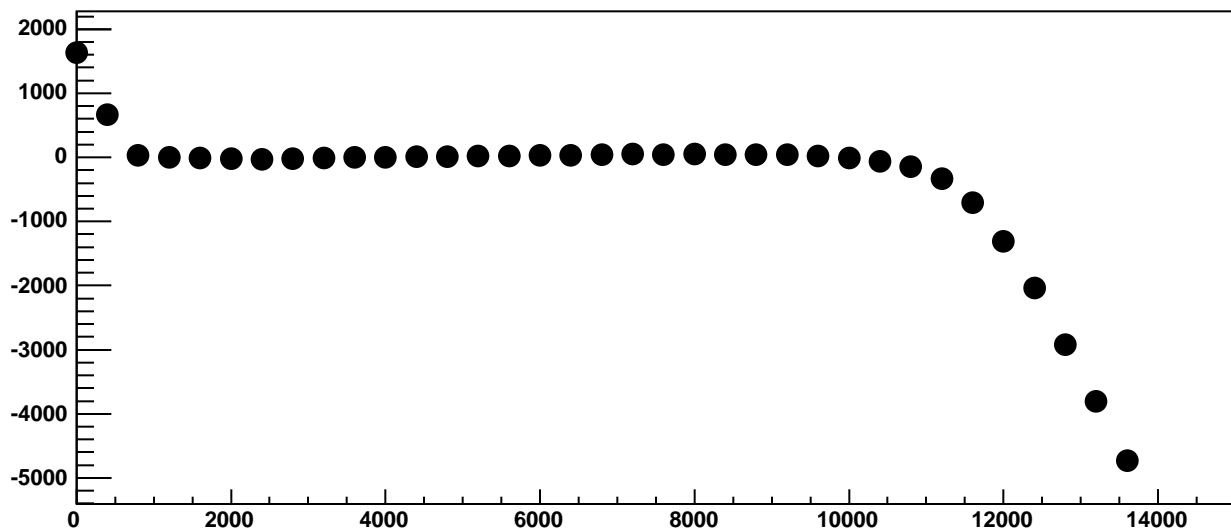
Chip 4, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC



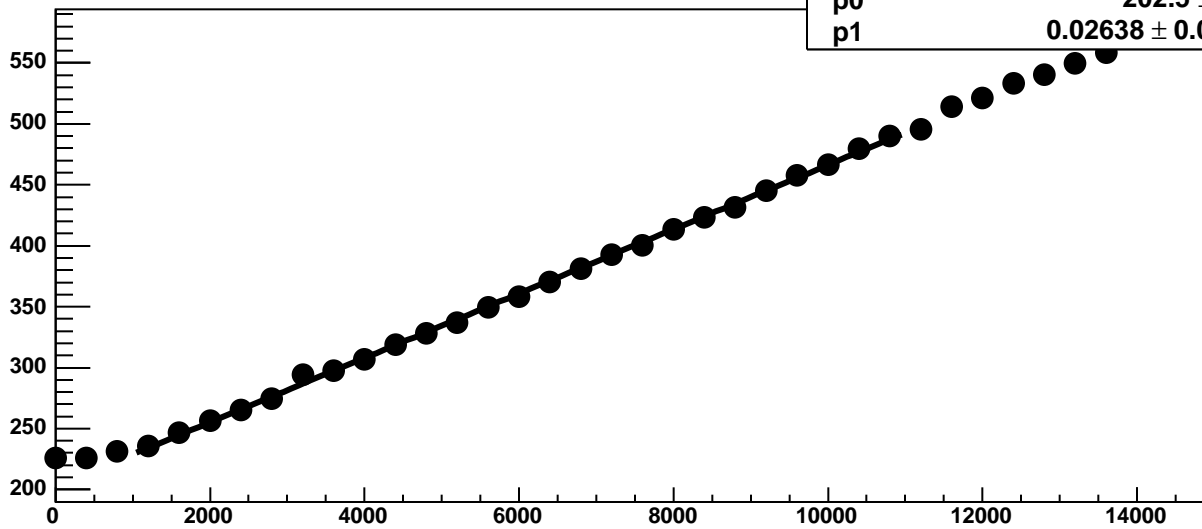
Chip 4, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC

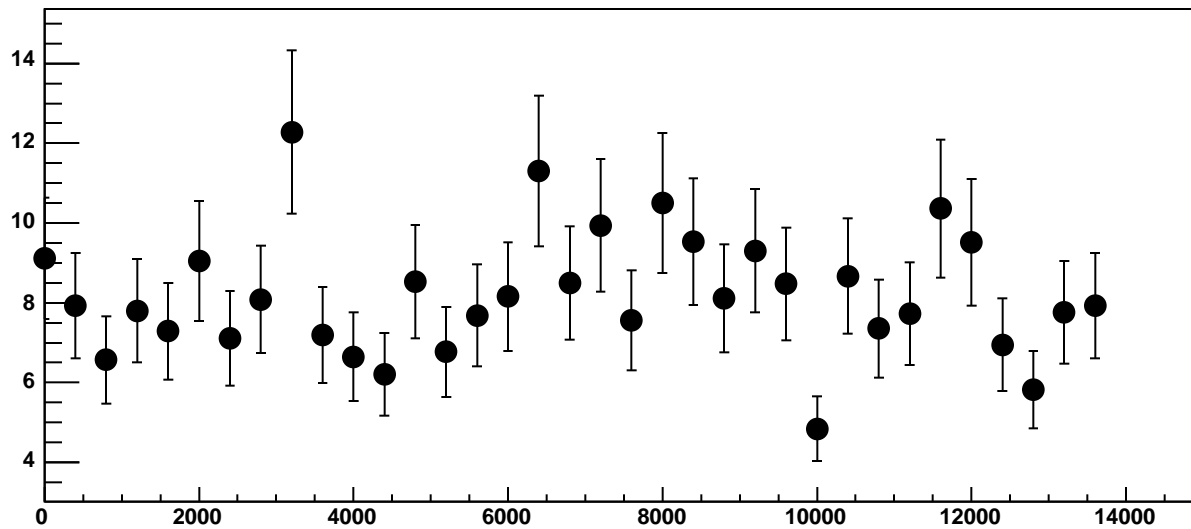


Chip 4, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

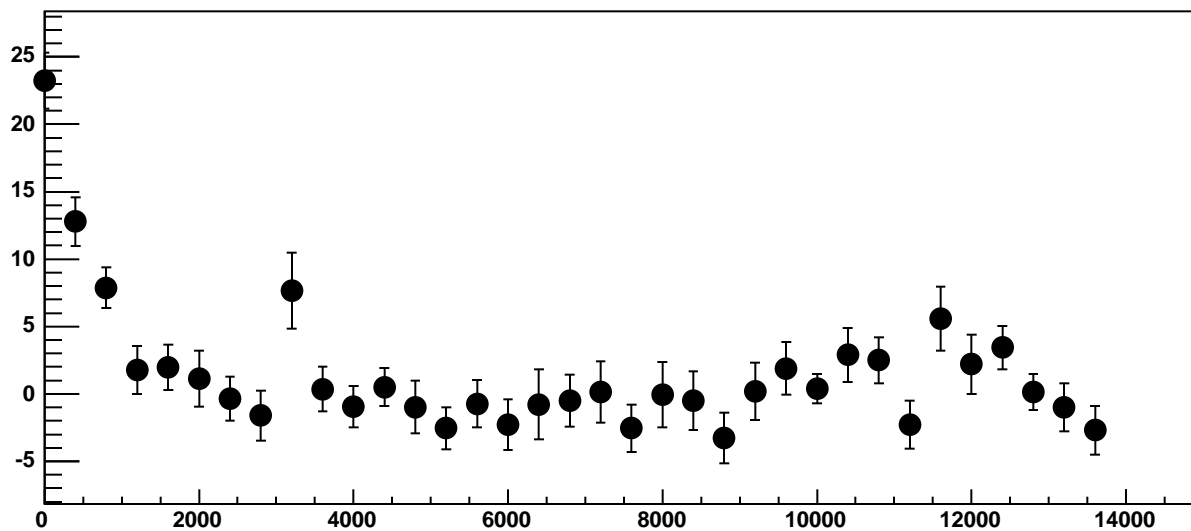


χ^2 / ndf 26.8 / 23
p0 202.5 ± 0.8101
p1 0.02638 ± 0.0001199

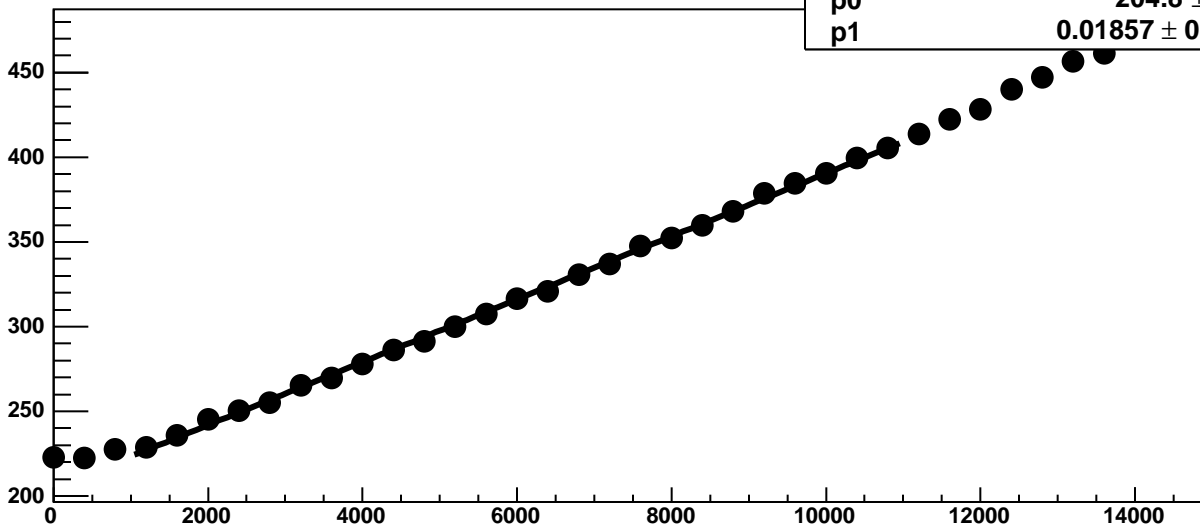
Chip 4, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

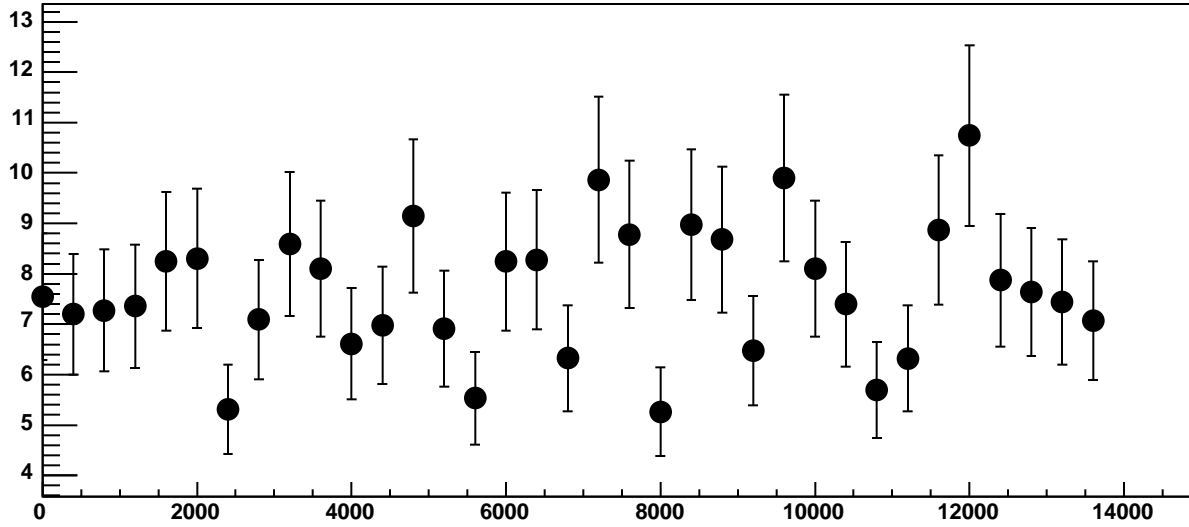


Chip 4, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

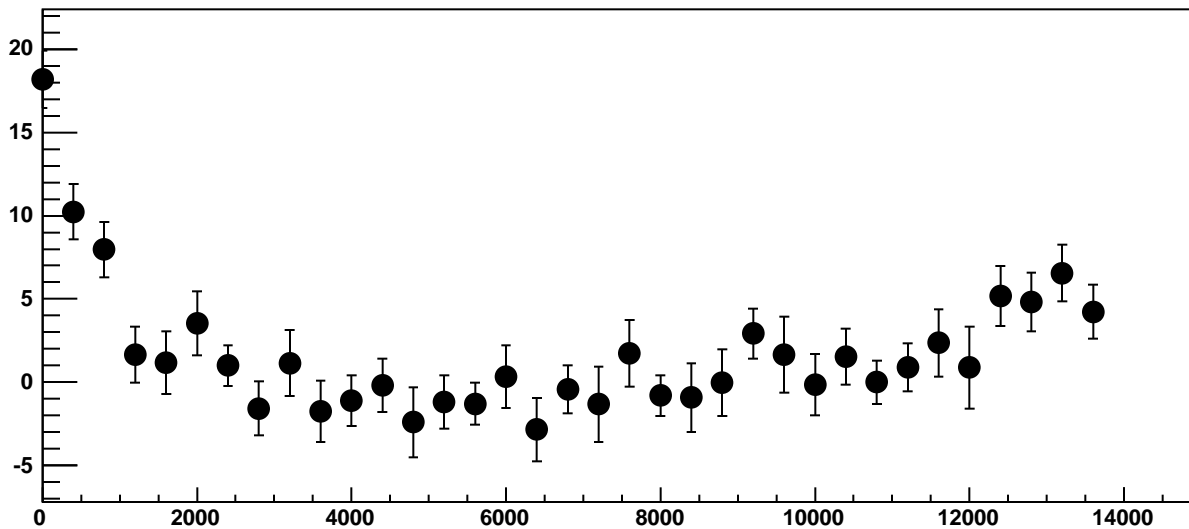


χ^2 / ndf 20.38 / 23
p0 204.8 ± 0.7603
p1 0.01857 ± 0.000114

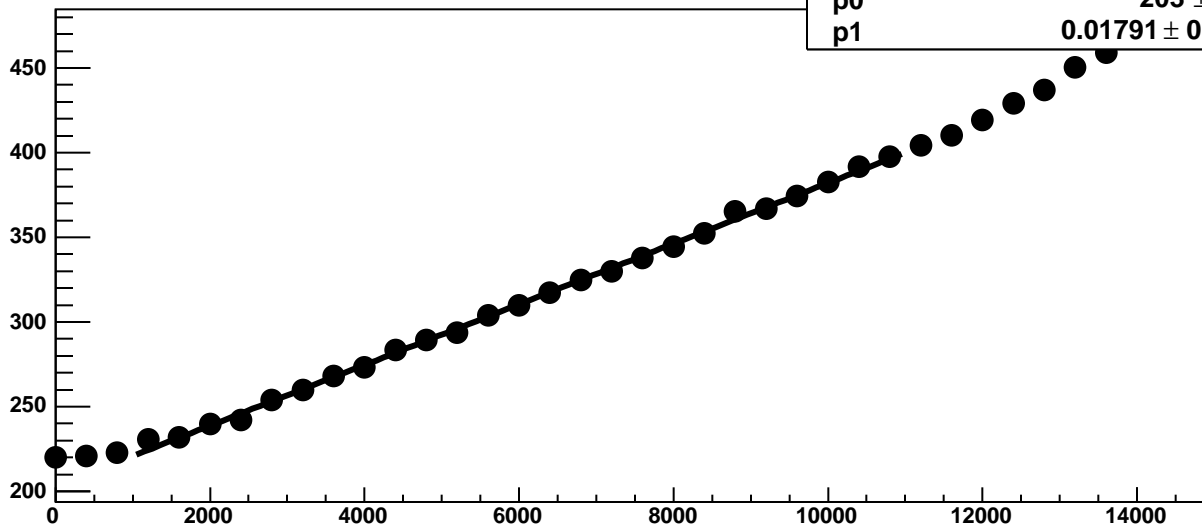
Chip 4, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

31 / 23

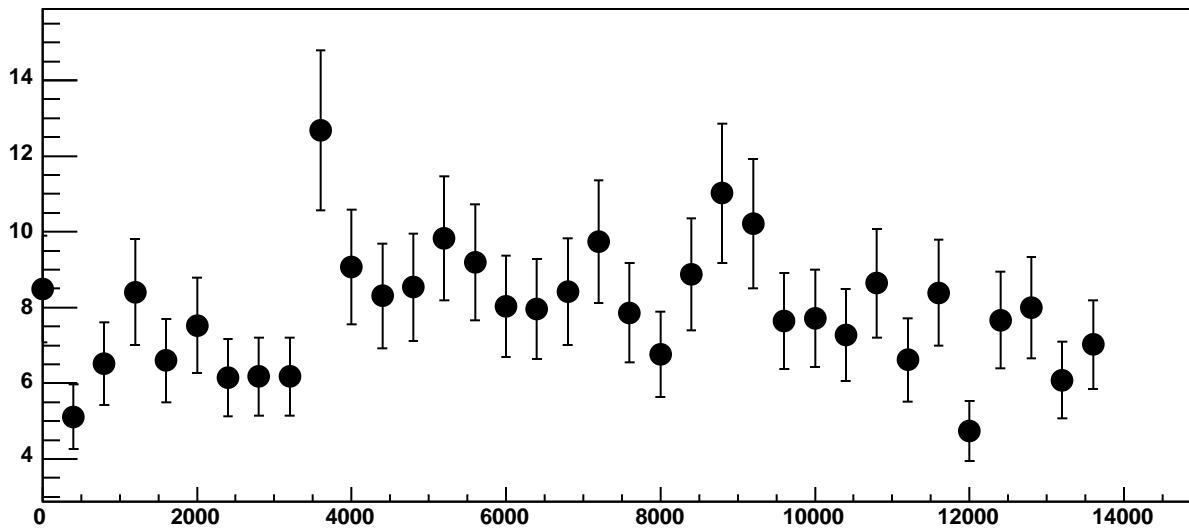
p0

203 ± 0.7845

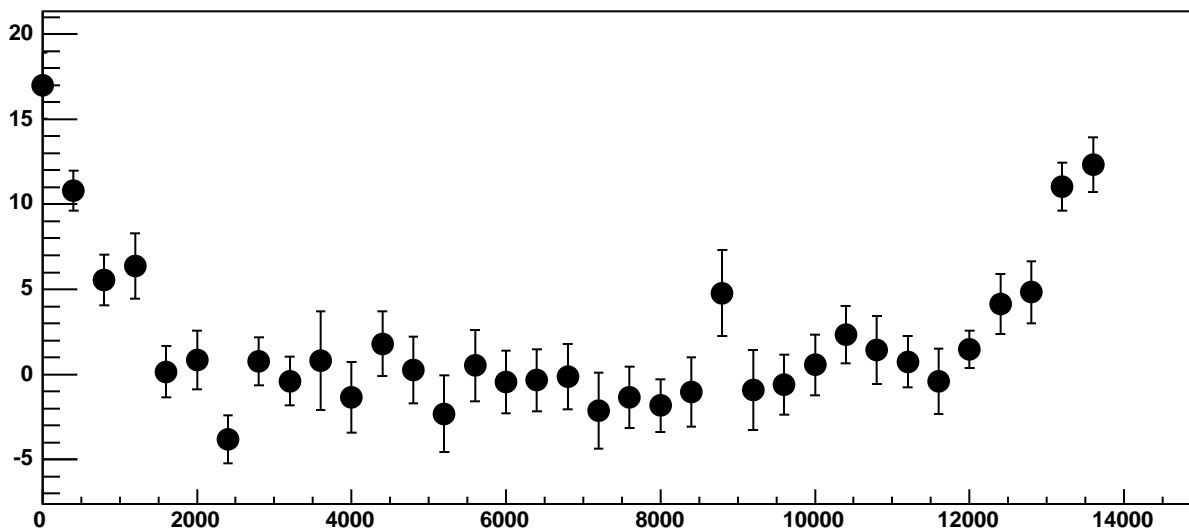
p1

0.01791 ± 0.000122

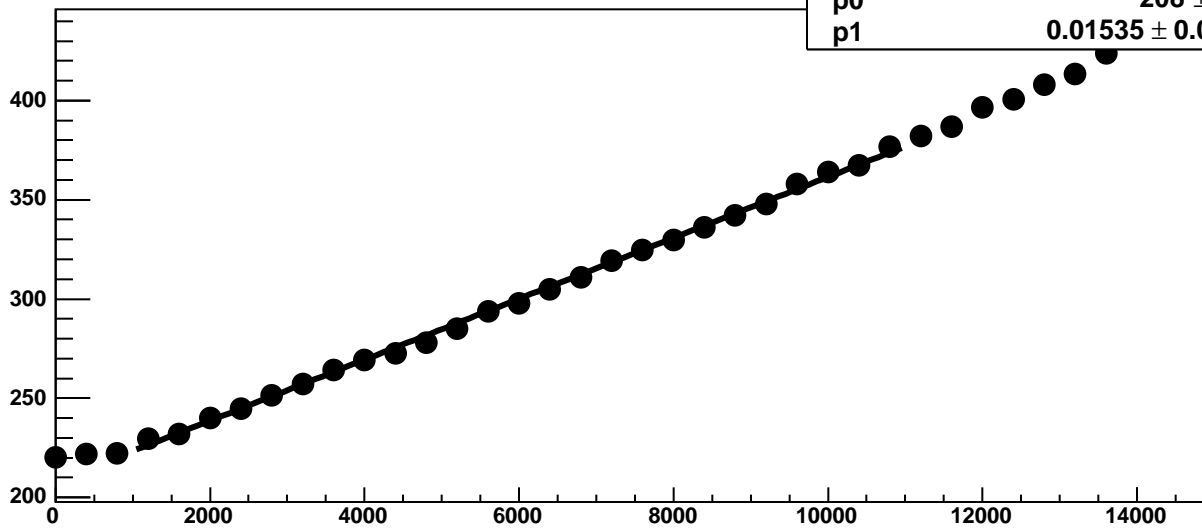
Chip 4, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

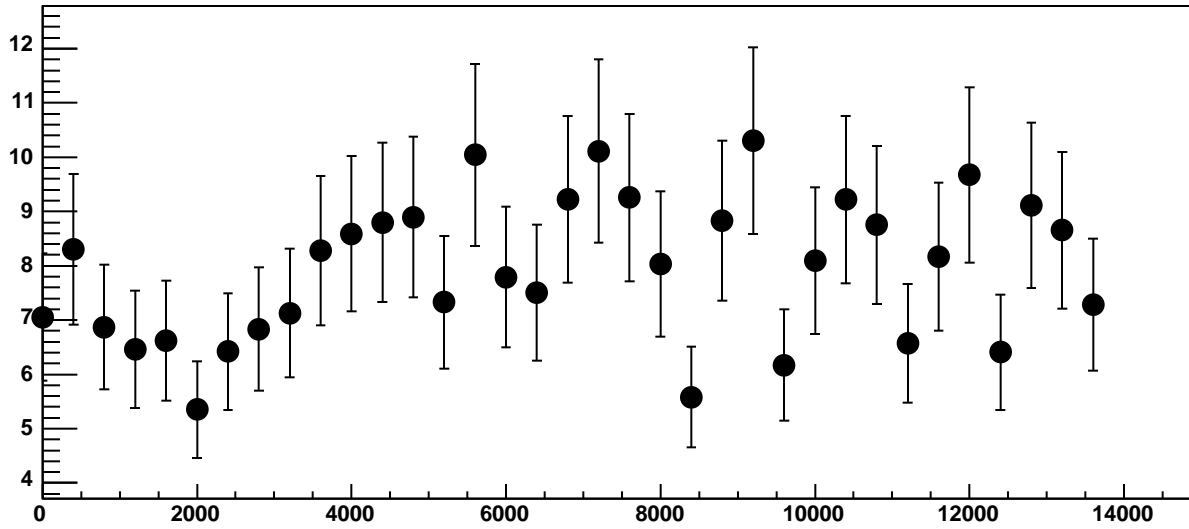


Chip 4, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

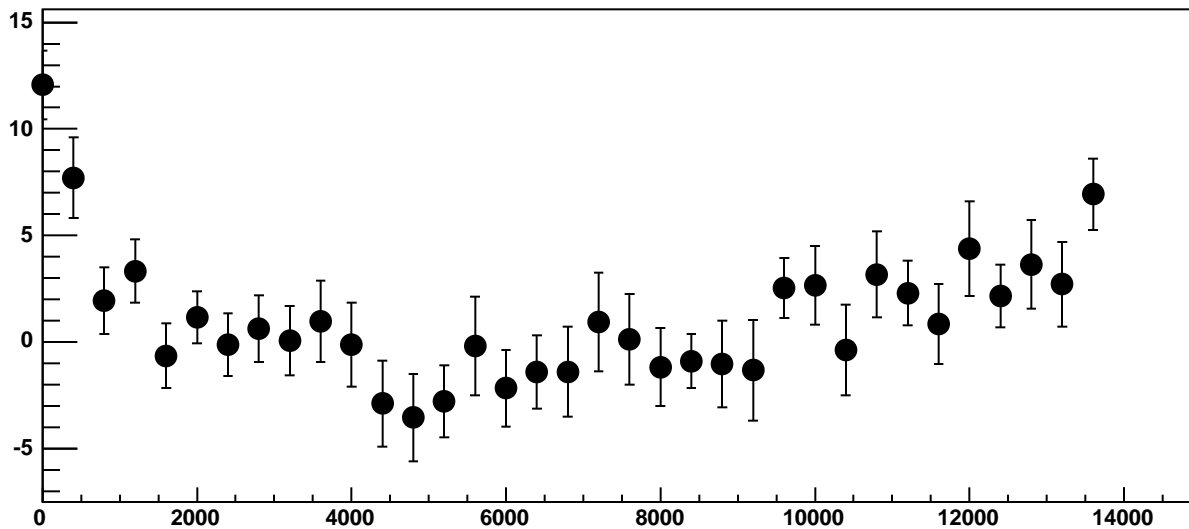


χ^2 / ndf 26.36 / 23
p0 208 ± 0.7287
p1 0.01535 ± 0.0001152

Chip 4, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

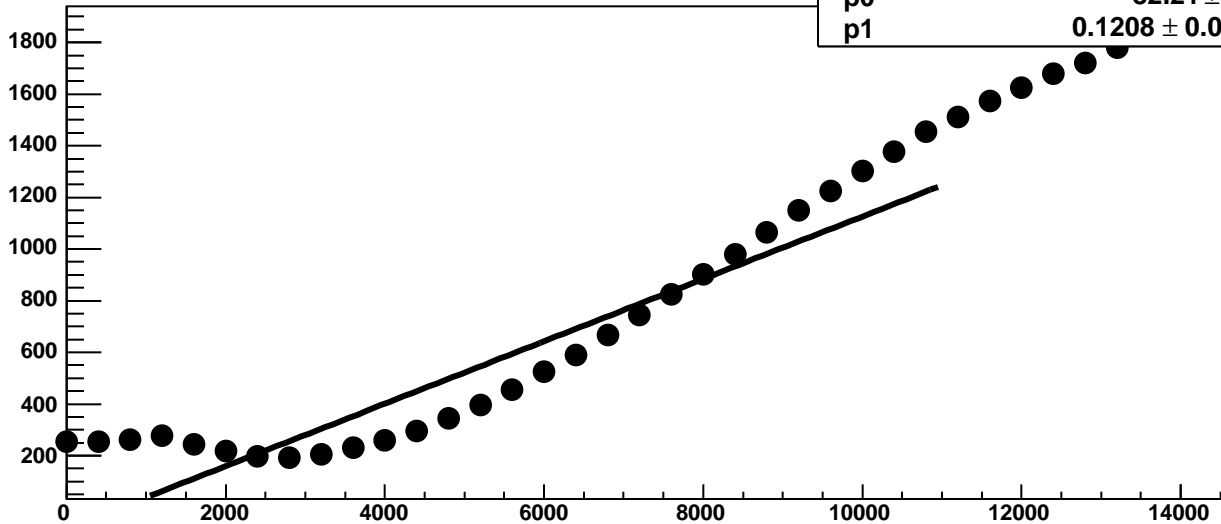


Chip 4, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

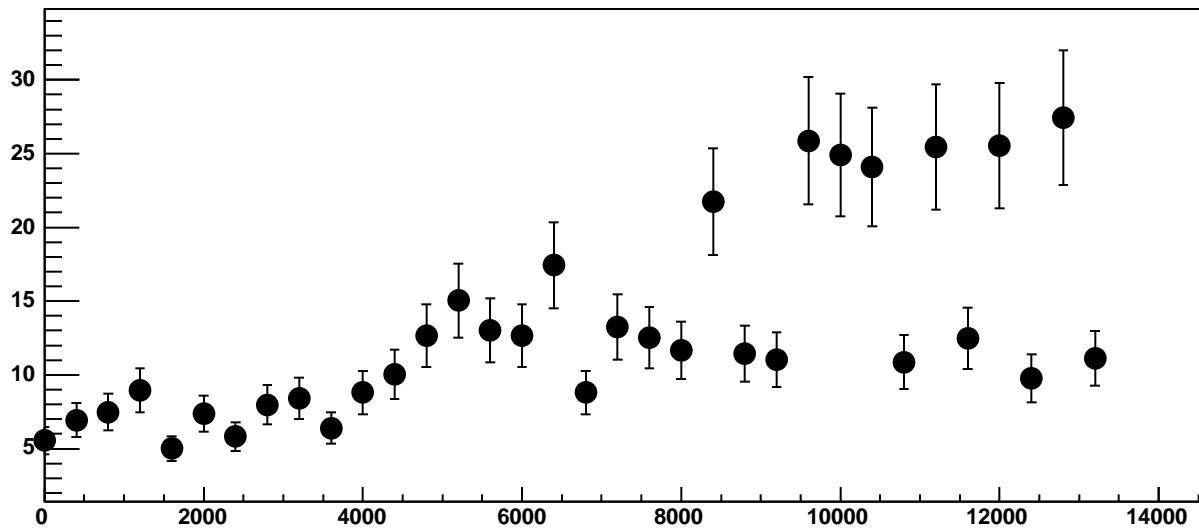


Chip 4, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

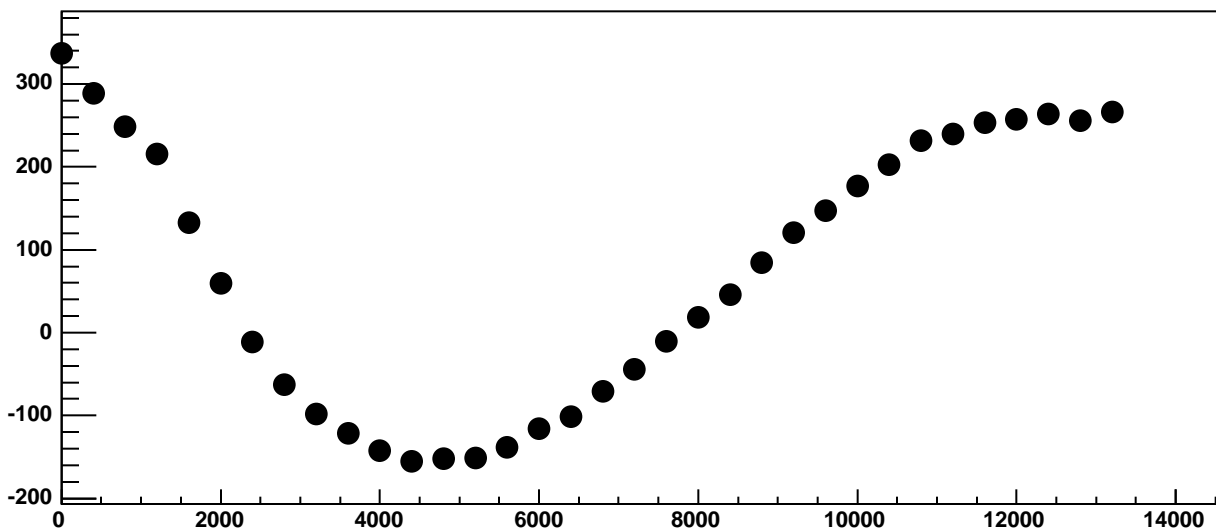
χ^2 / ndf 7.136e+04 / 23
p0 -82.21 ± 0.8326
p1 0.1208 ± 0.0001651



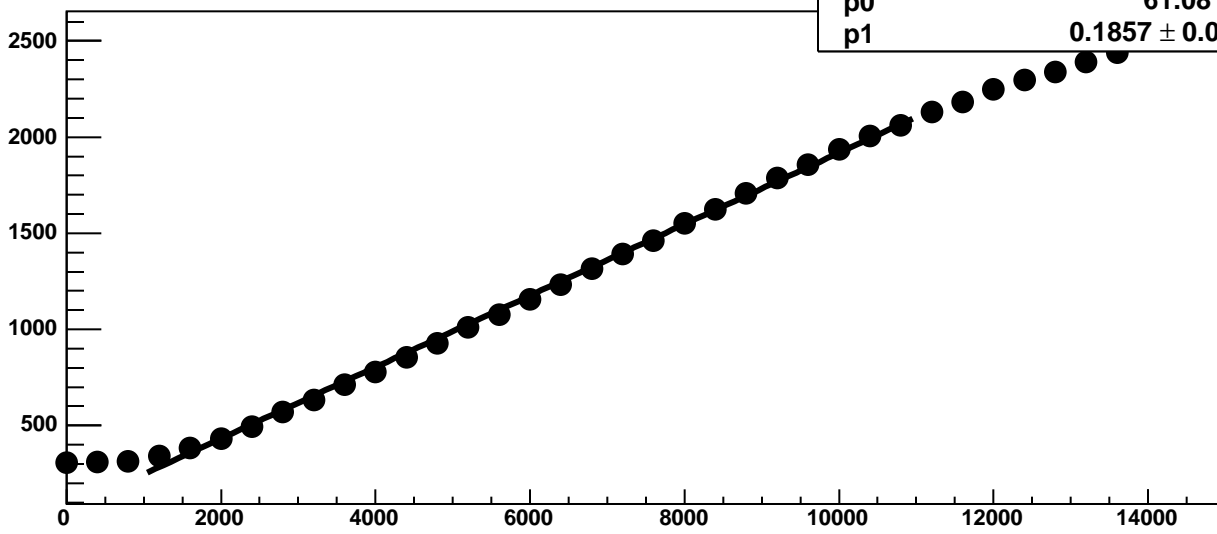
Chip 4, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

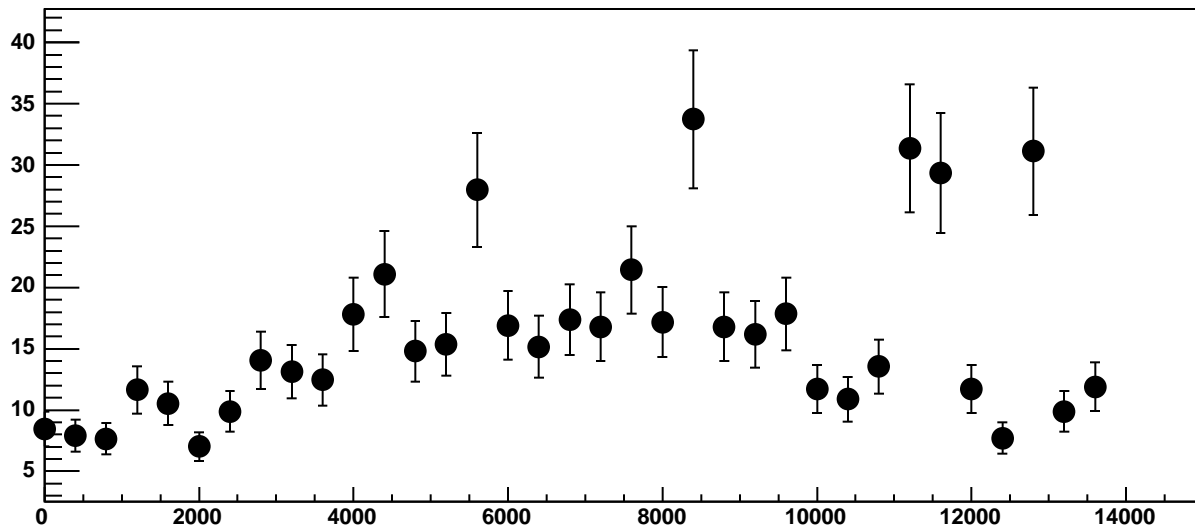


Chip 4, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

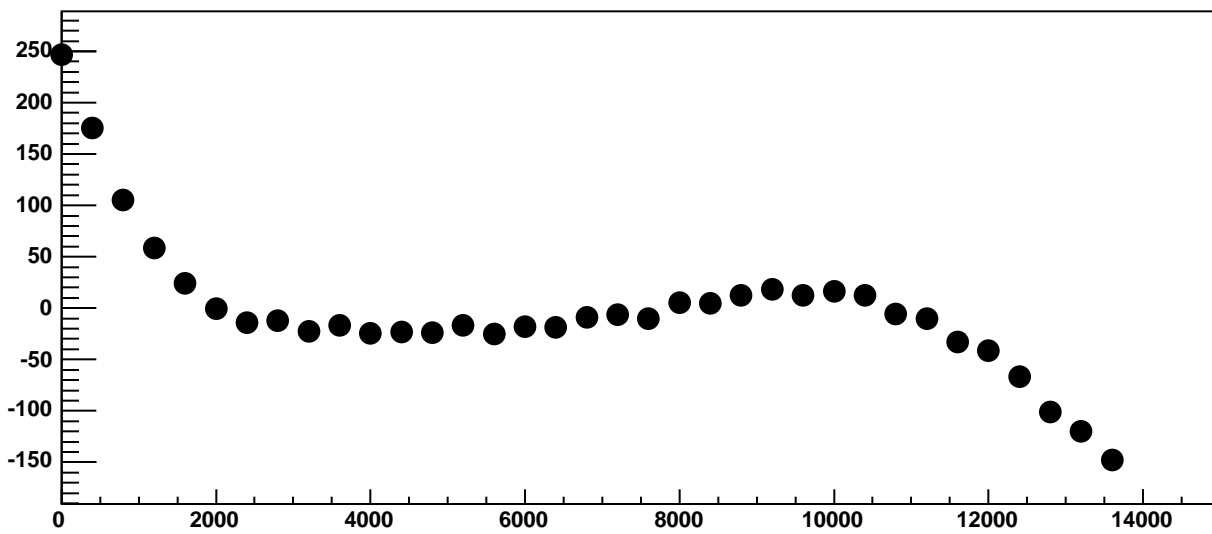


χ^2 / ndf 1045 / 23
p0 61.08 ± 1.175
p1 0.1857 ± 0.0001924

Chip 4, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

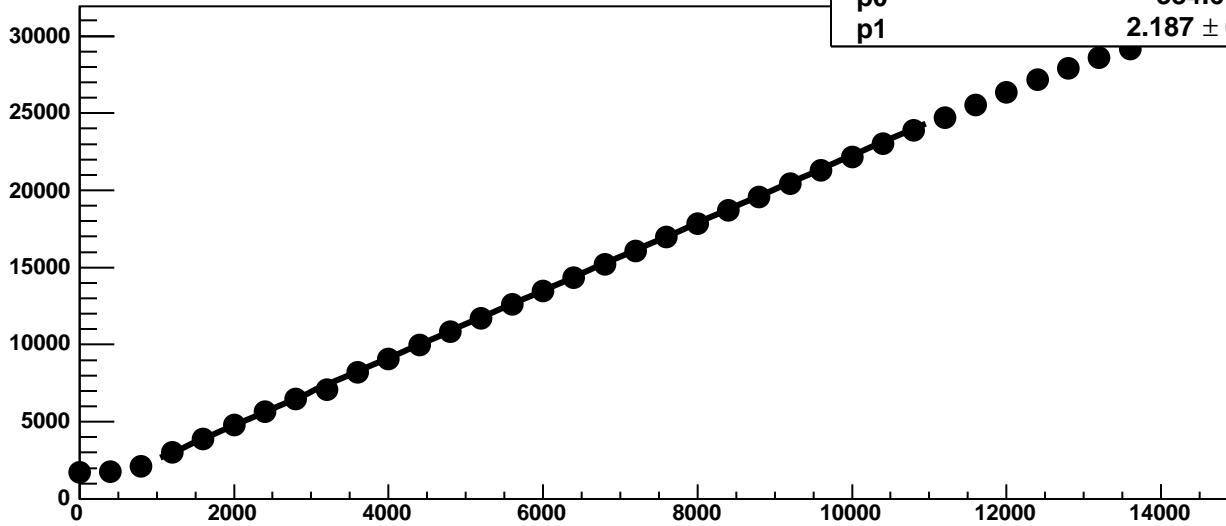


Chip 4, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

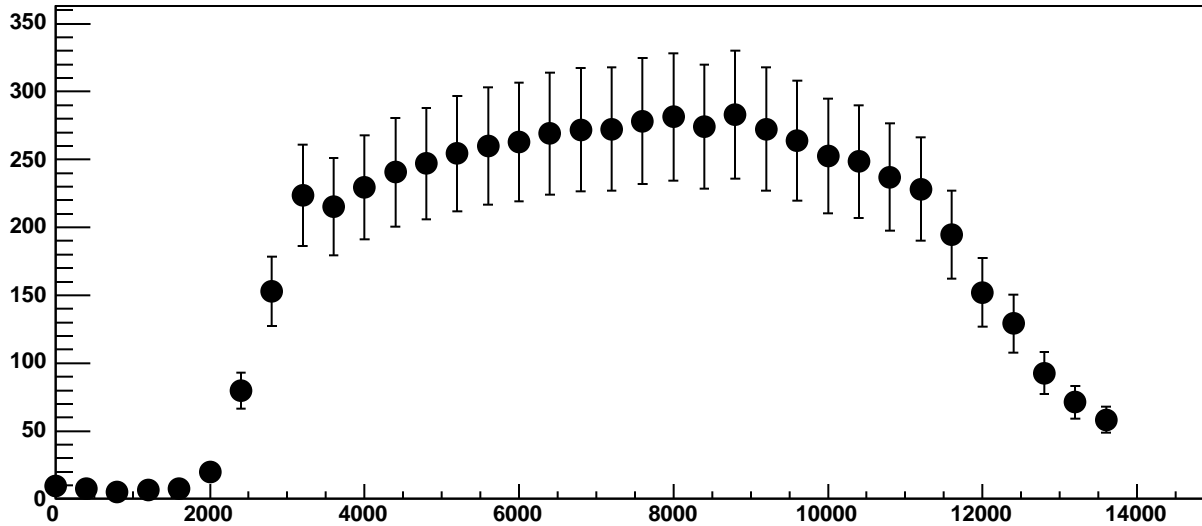


Chip 4, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC

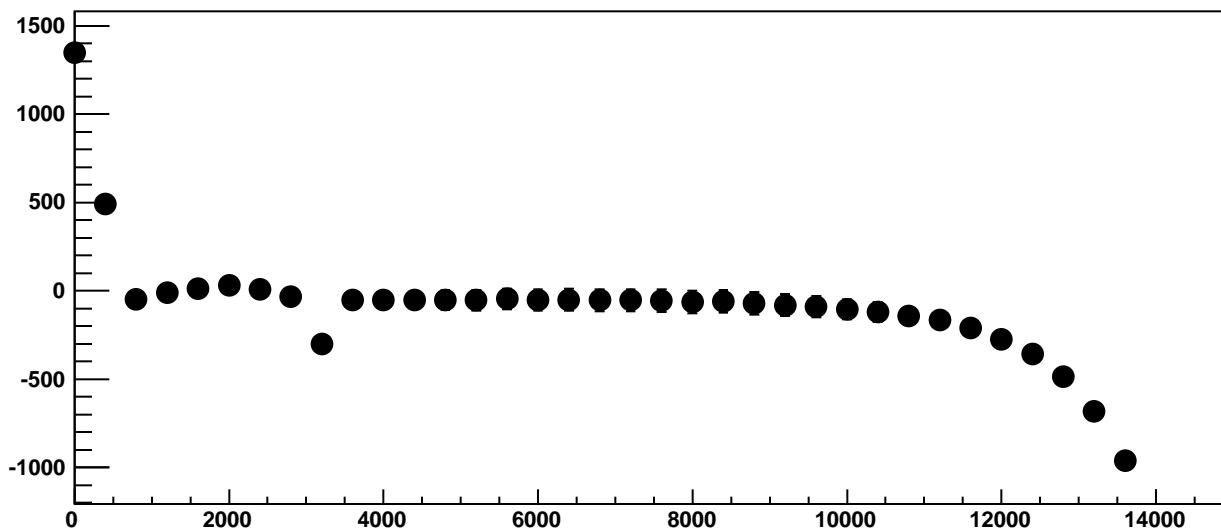
χ^2 / ndf 214.9 / 23
p0 384.6 ± 3.093
p1 2.187 ± 0.00198



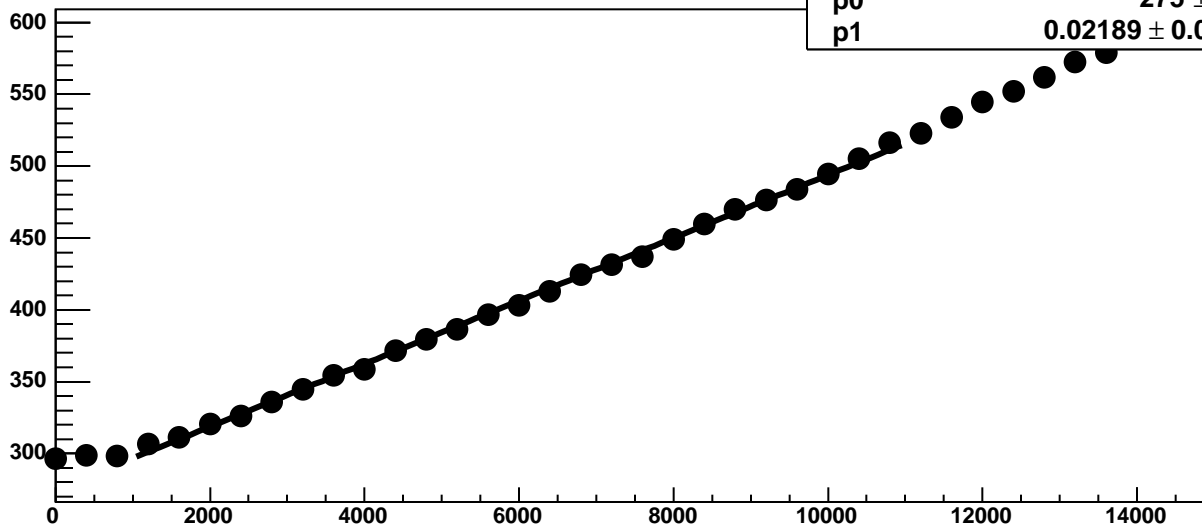
Chip 4, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

43.94 / 23

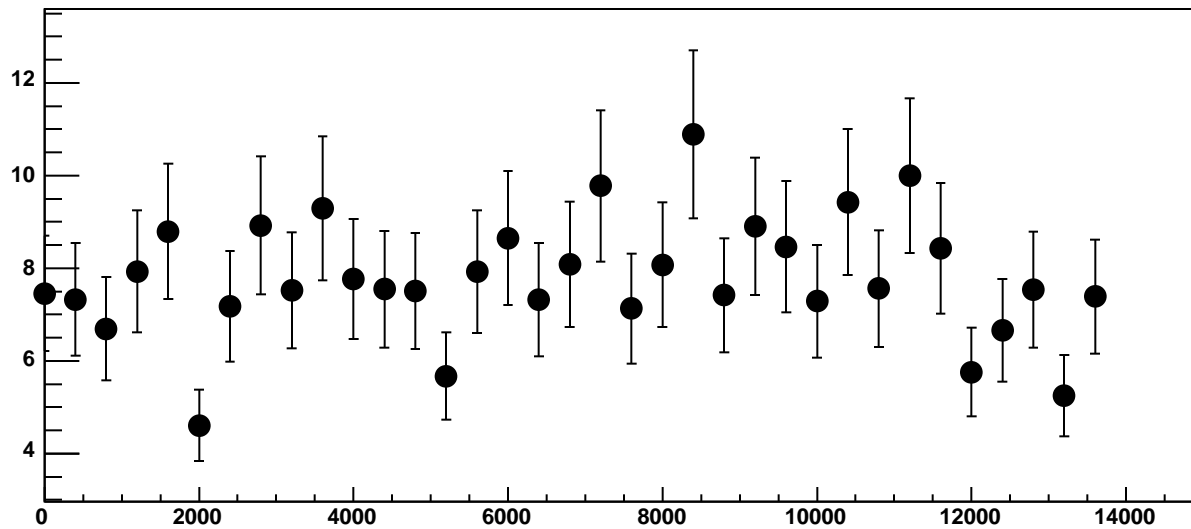
p0

275 ± 0.7643

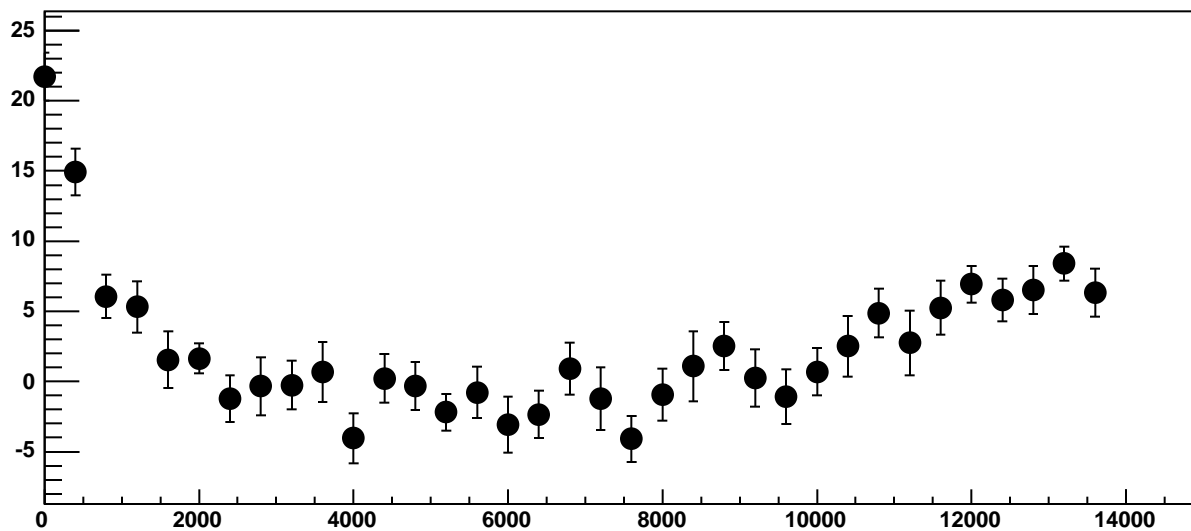
p1

0.02189 ± 0.0001206

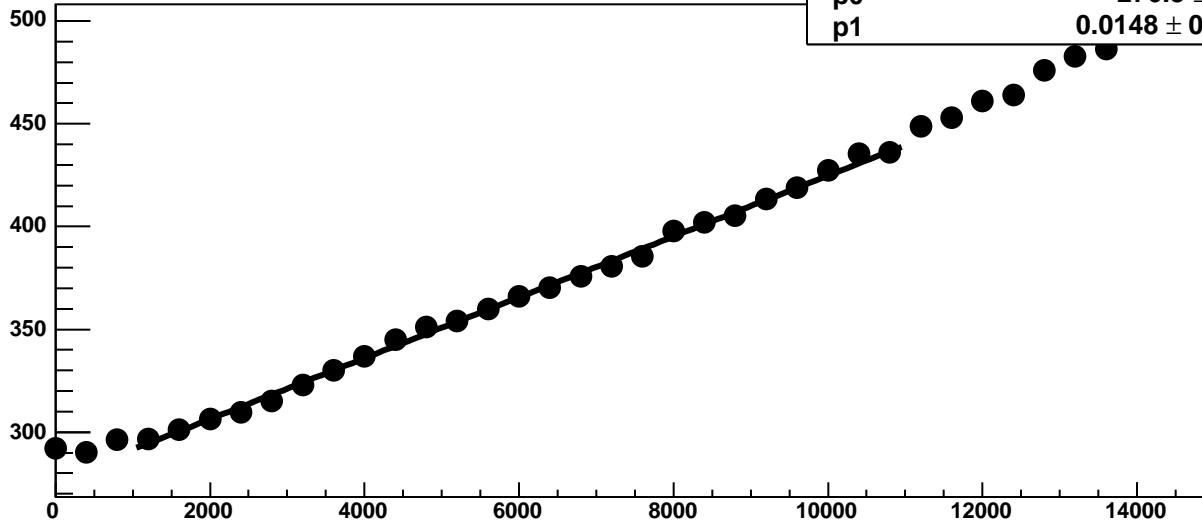
Chip 4, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

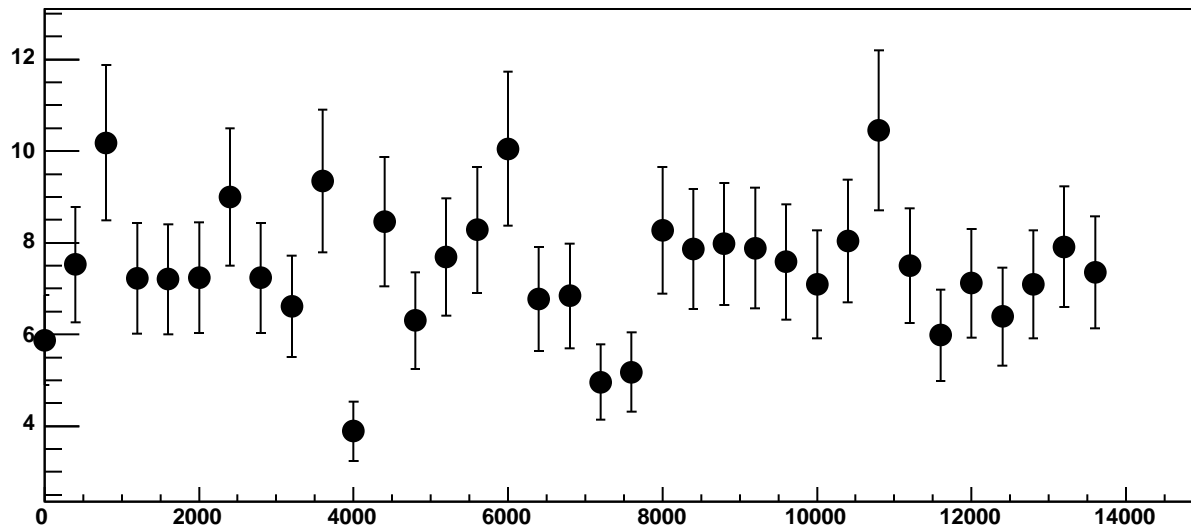


Chip 4, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

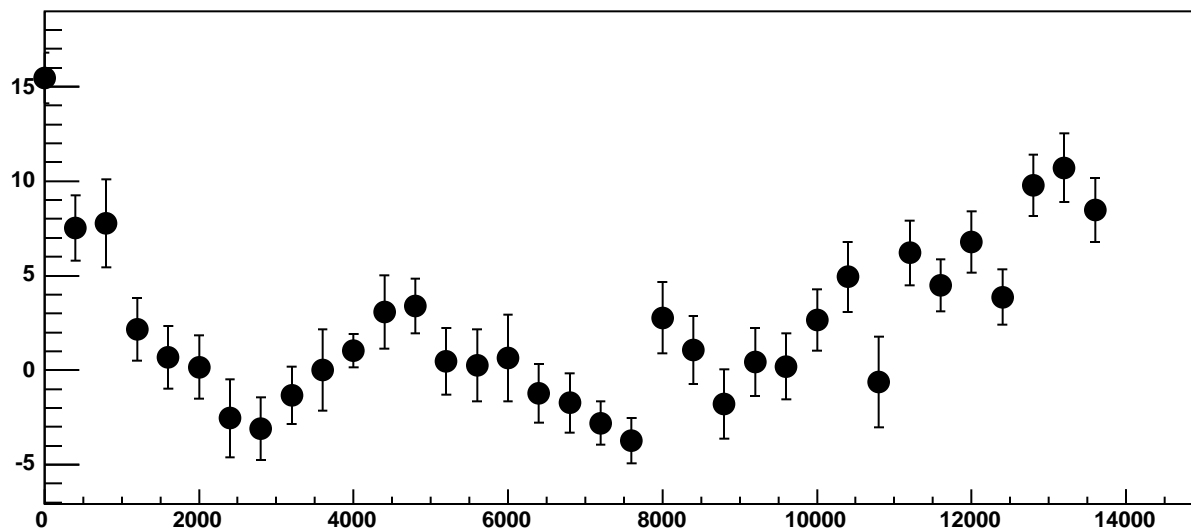


χ^2 / ndf 48.33 / 23
p0 276.8 ± 0.7609
p1 0.0148 ± 0.000119

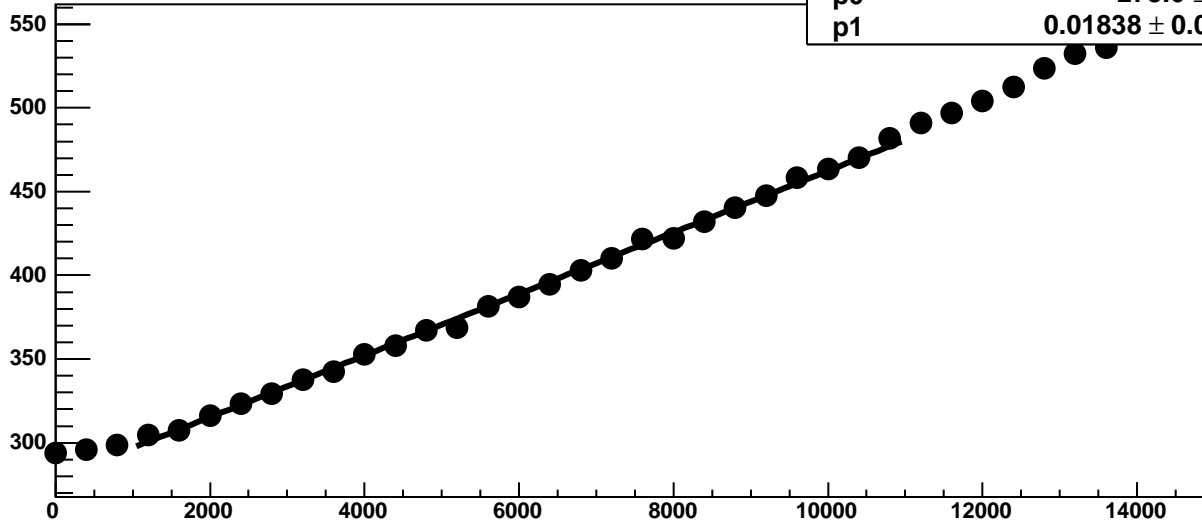
Chip 4, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

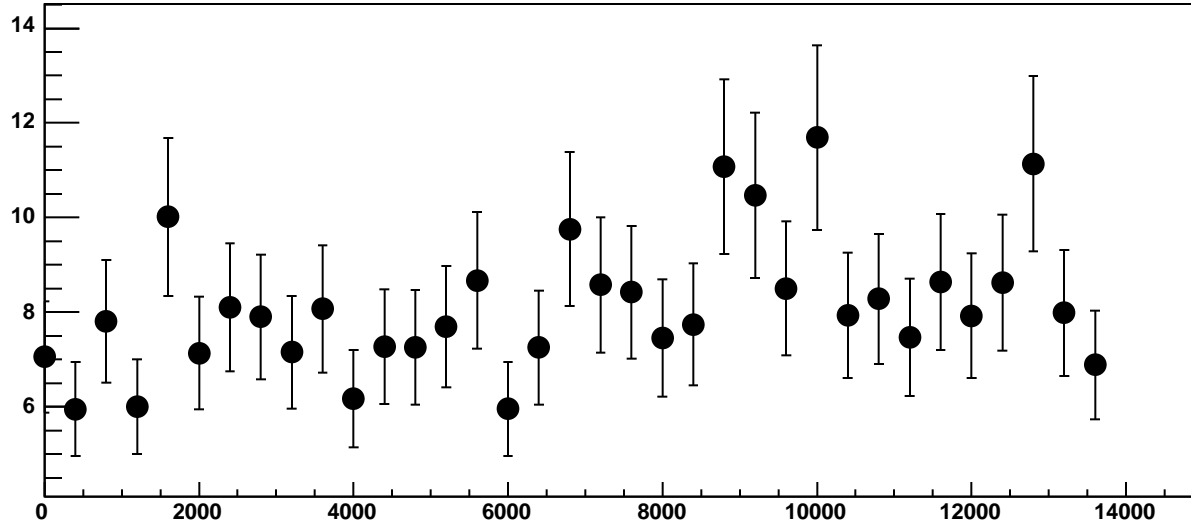


Chip 4, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

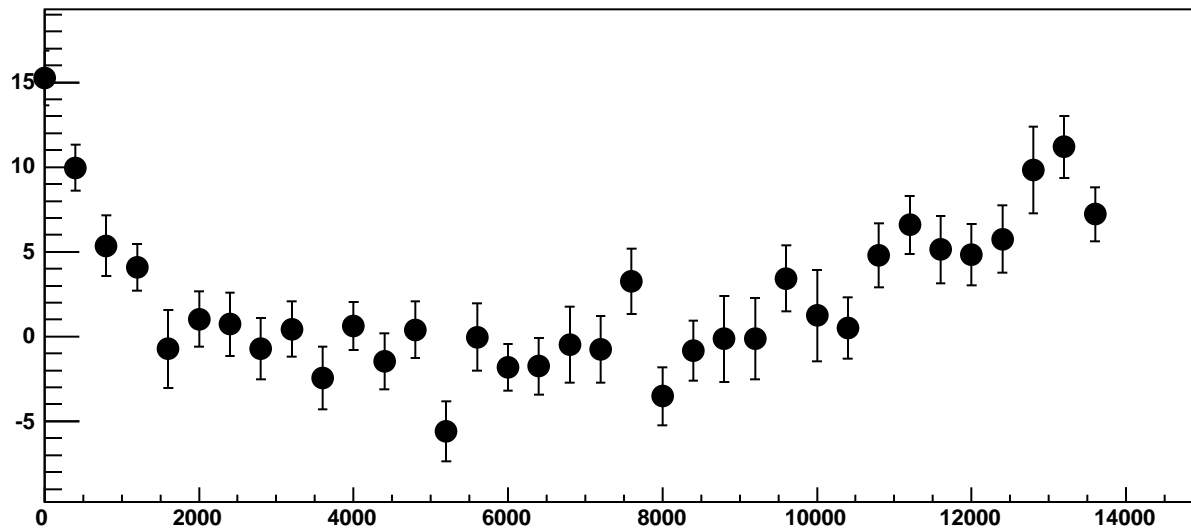


χ^2 / ndf 42.65 / 23
p0 278.6 ± 0.7995
p1 0.01838 ± 0.0001283

Chip 4, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC

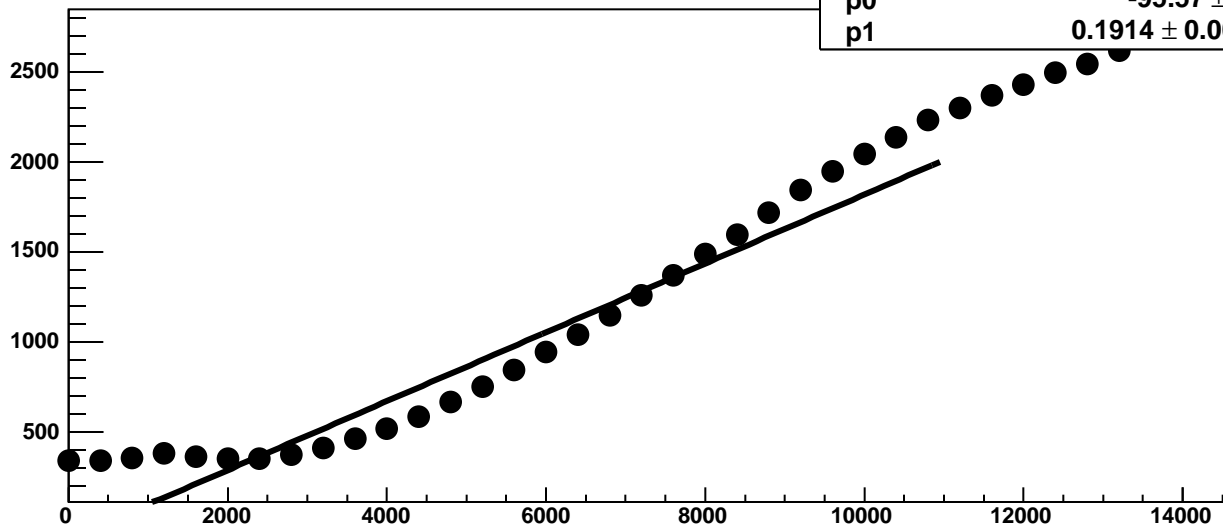


Chip 4, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC

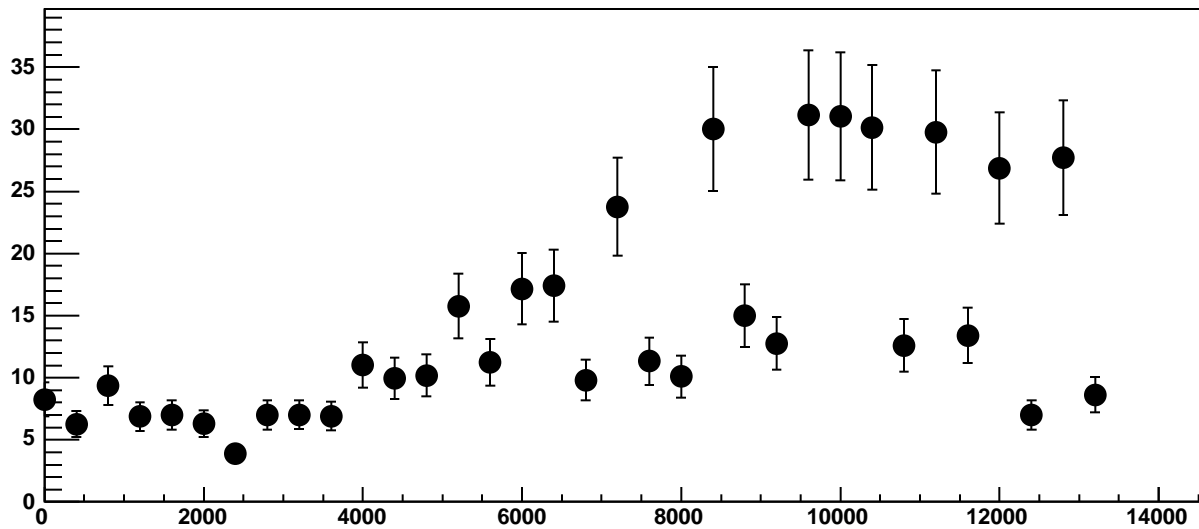


Chip 4, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

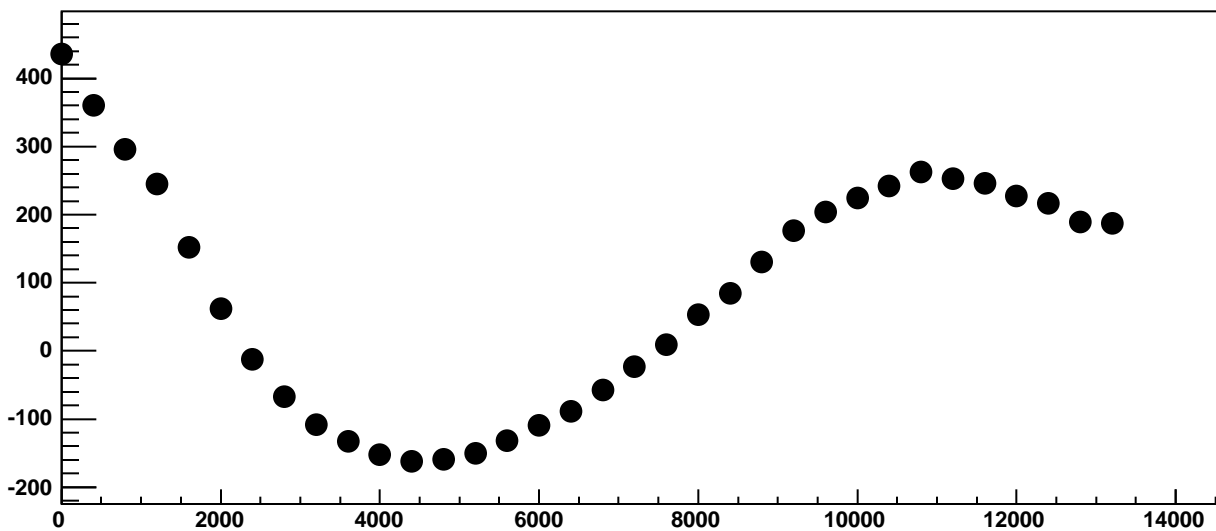
χ^2 / ndf 8.503e+04 / 23
p0 -95.57 ± 0.7951
p1 0.1914 ± 0.0001753



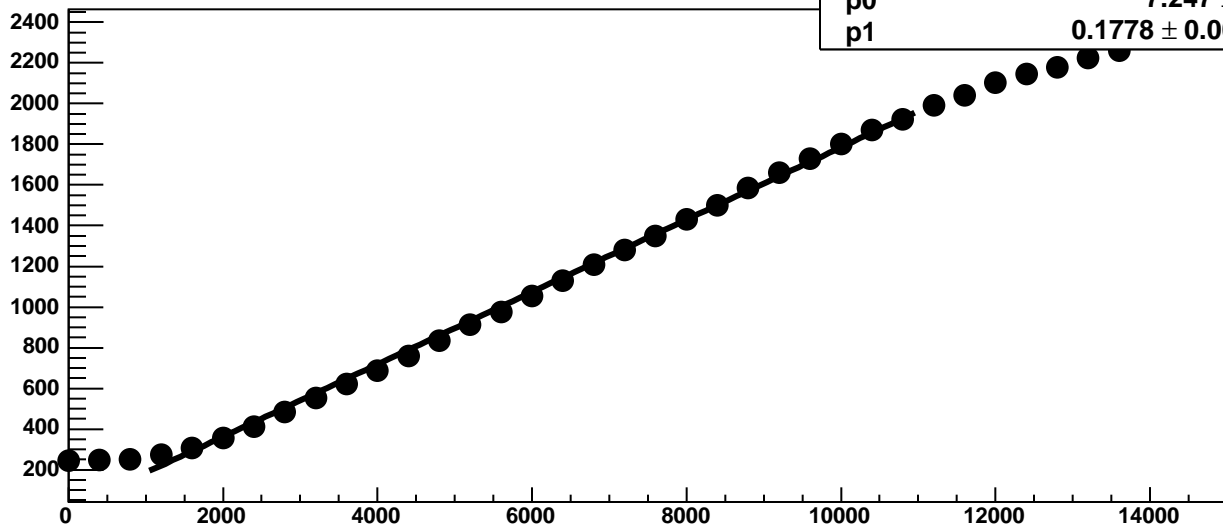
Chip 4, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



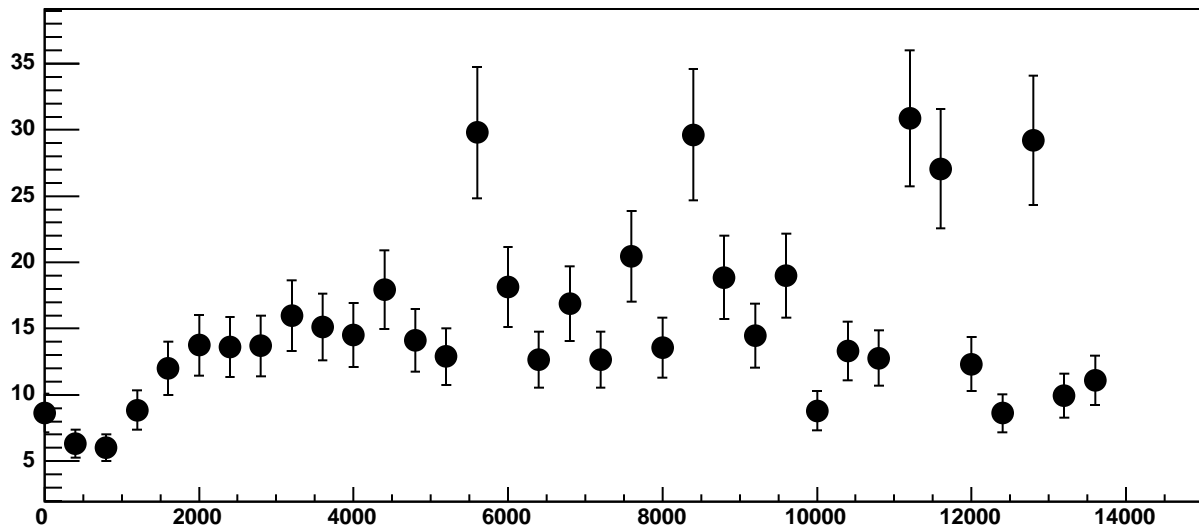
Chip 4, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



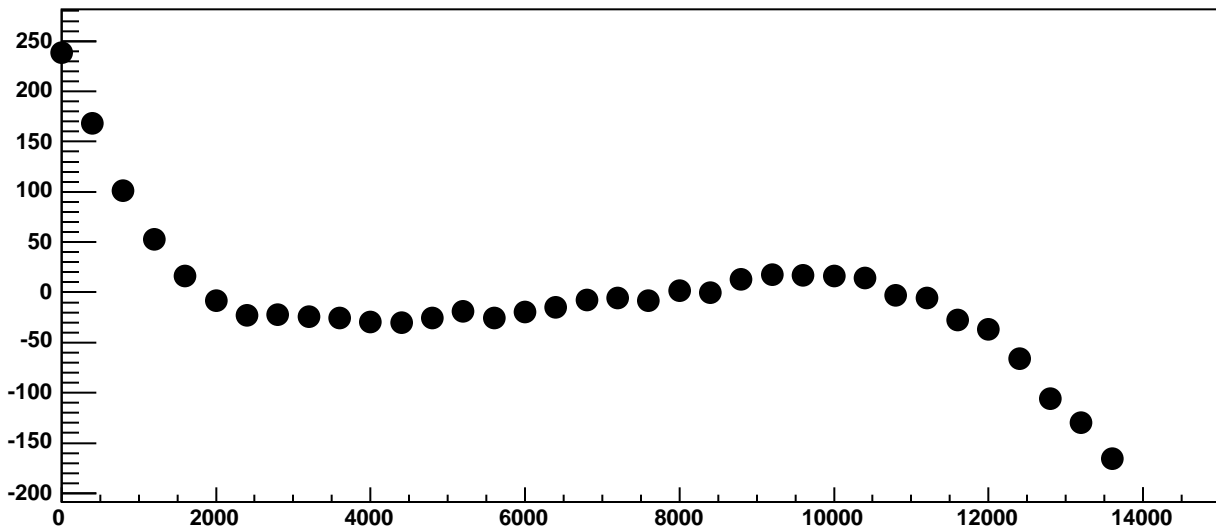
Chip 4, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



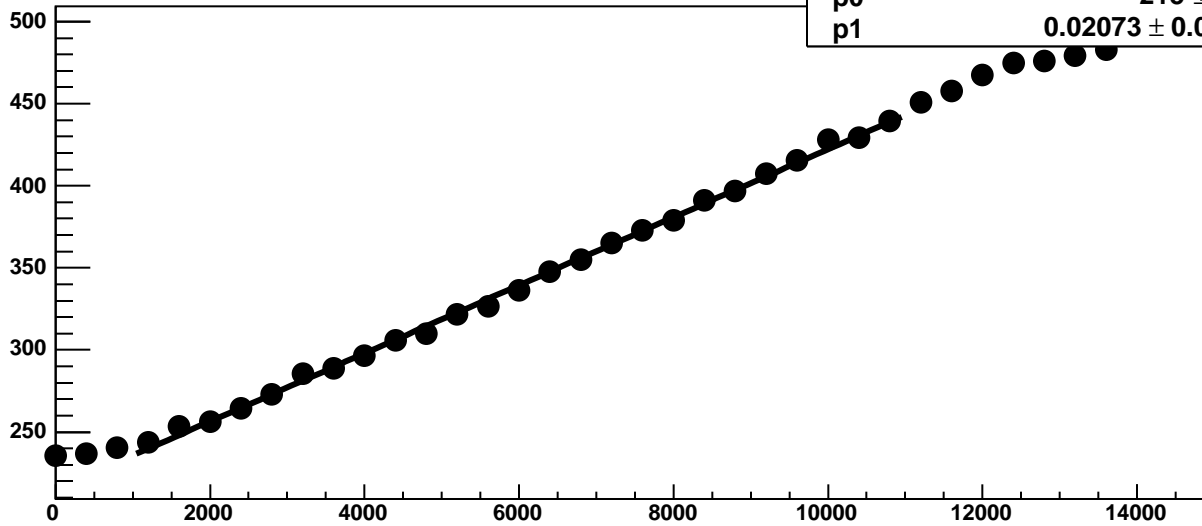
Chip 4, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

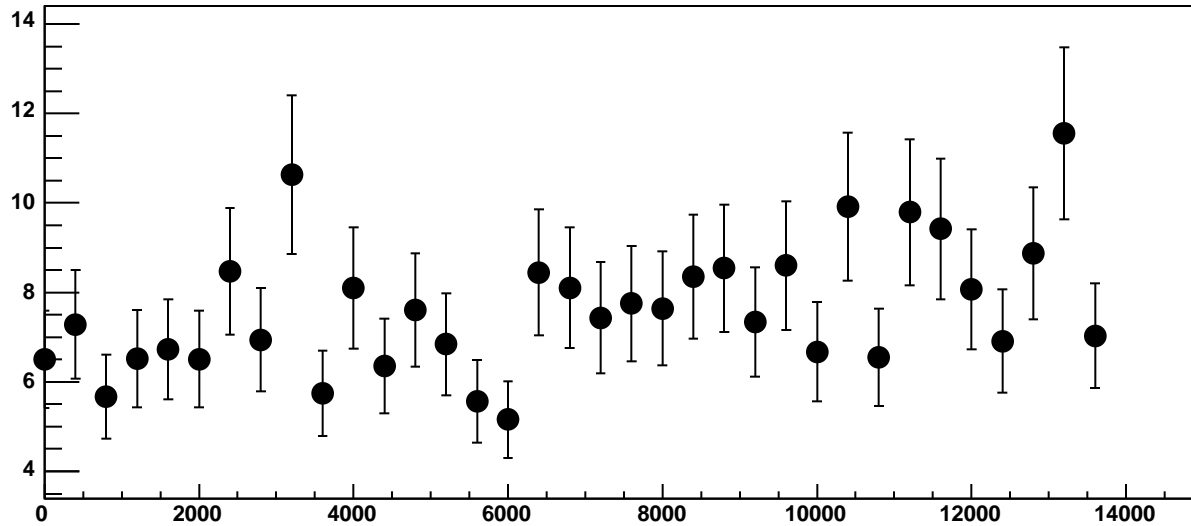


Chip 4, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

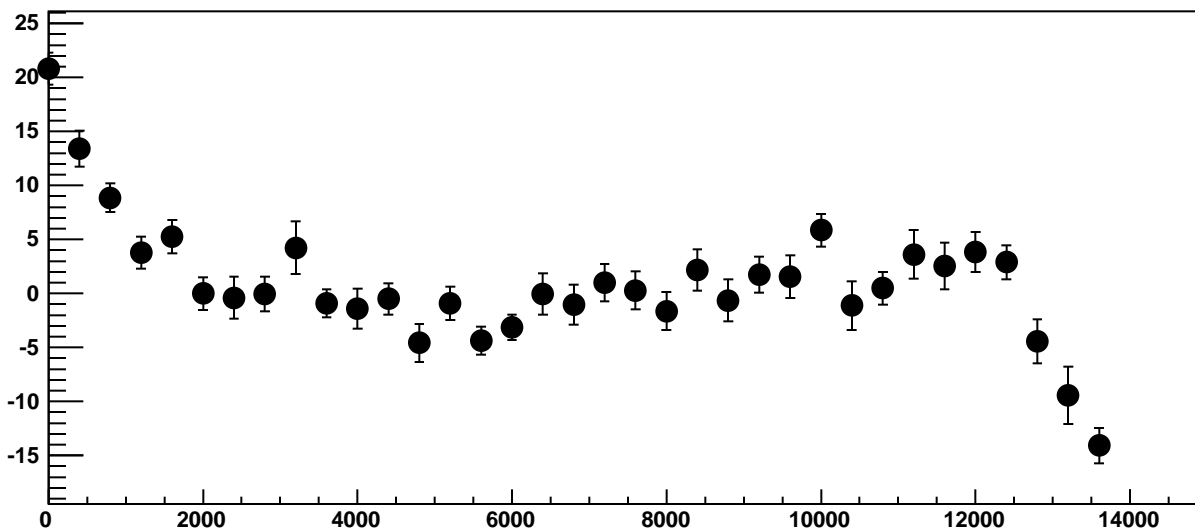


χ^2 / ndf 67.58 / 23
p0 215 ± 0.7446
p1 0.02073 ± 0.0001158

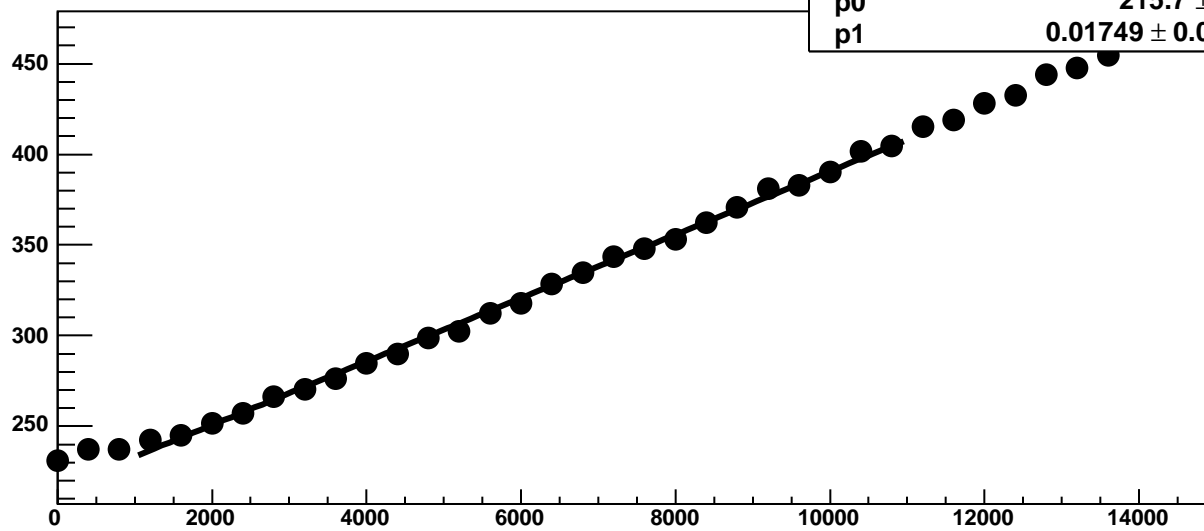
Chip 4, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC

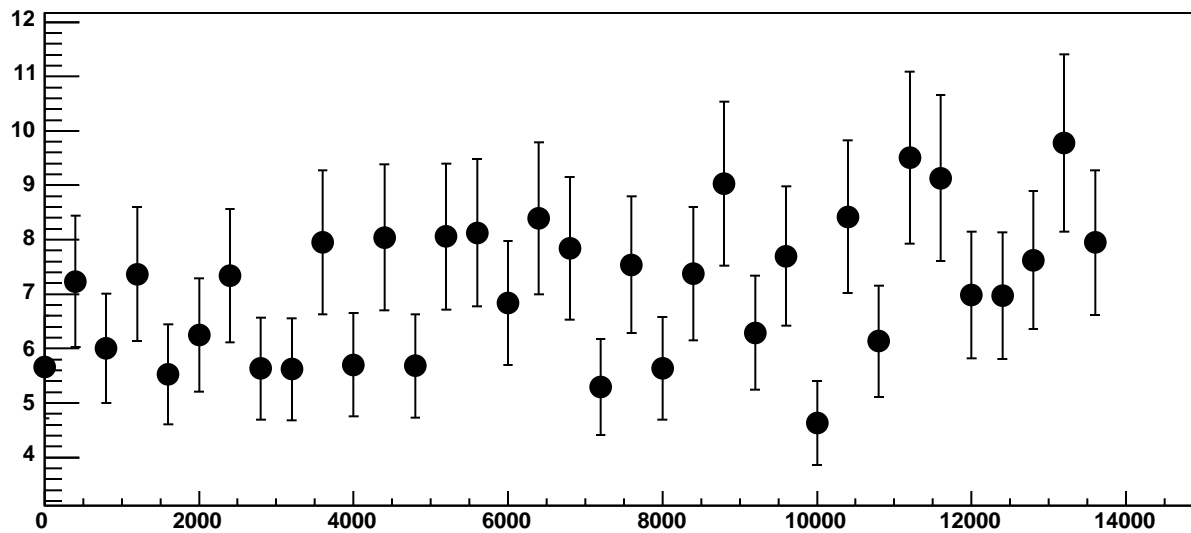


Chip 4, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

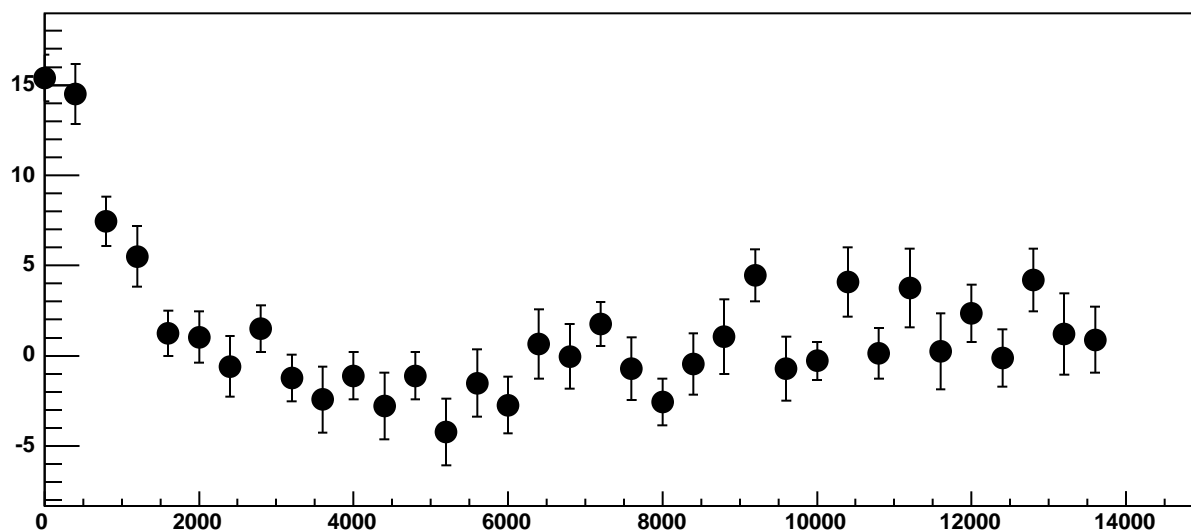


χ^2 / ndf 49.71 / 23
p0 215.7 ± 0.6746
p1 0.01749 ± 0.0001012

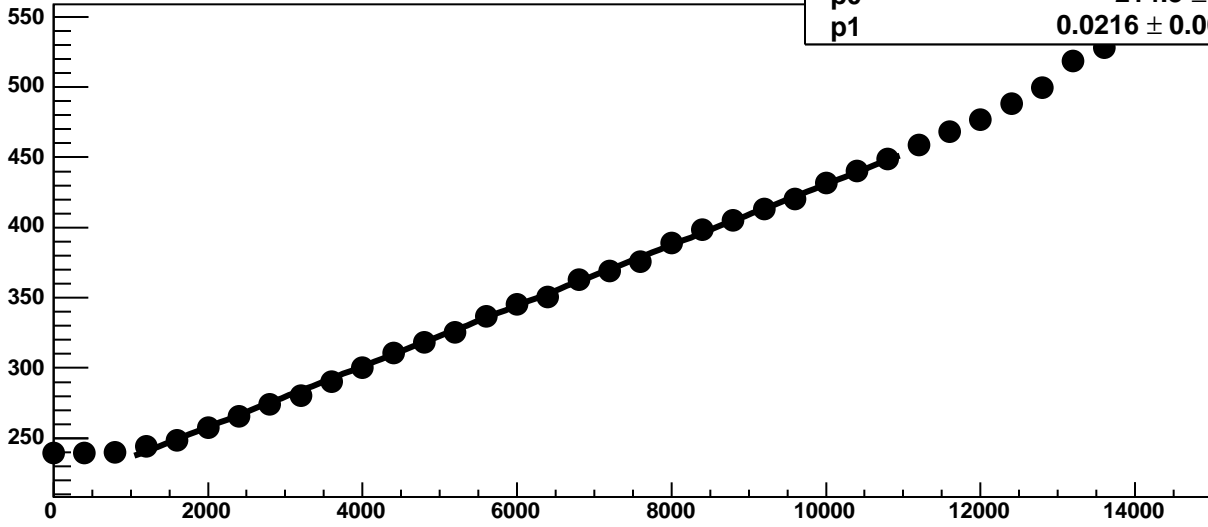
Chip 4, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

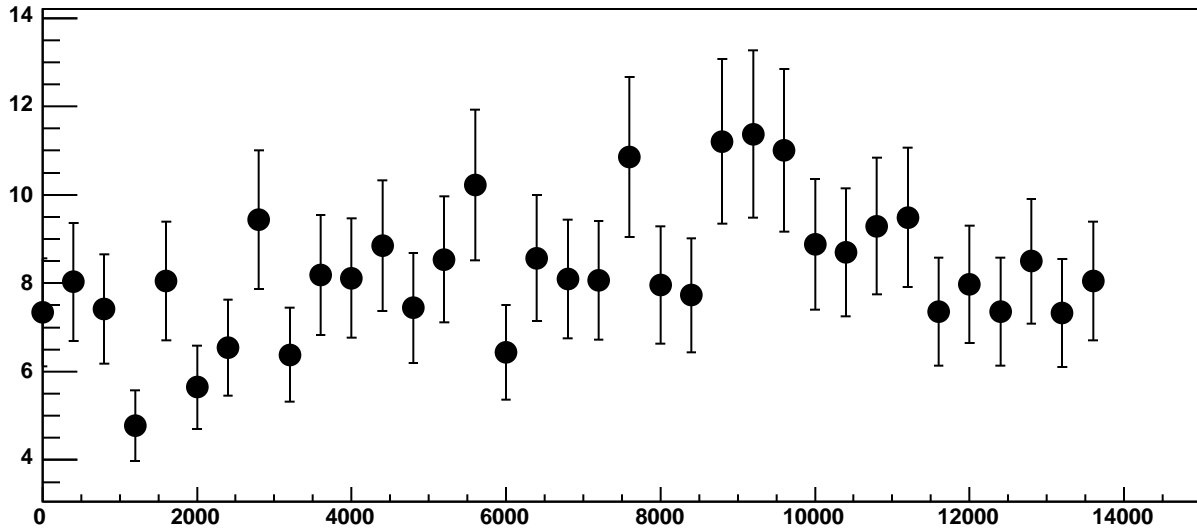


Chip 4, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

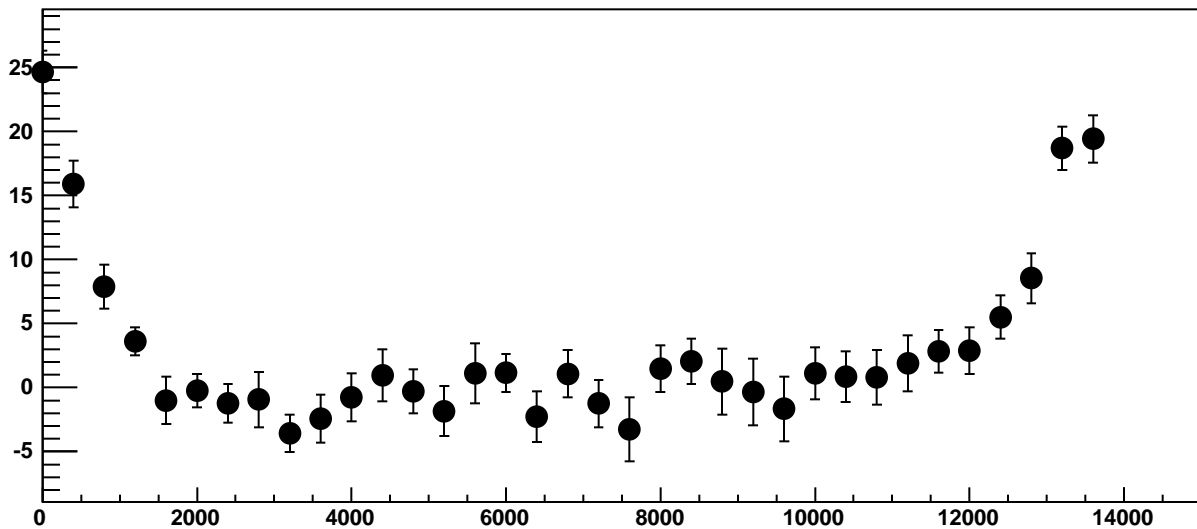


χ^2 / ndf 28.87 / 23
p0 214.8 ± 0.7232
p1 0.0216 ± 0.0001223

Chip 4, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

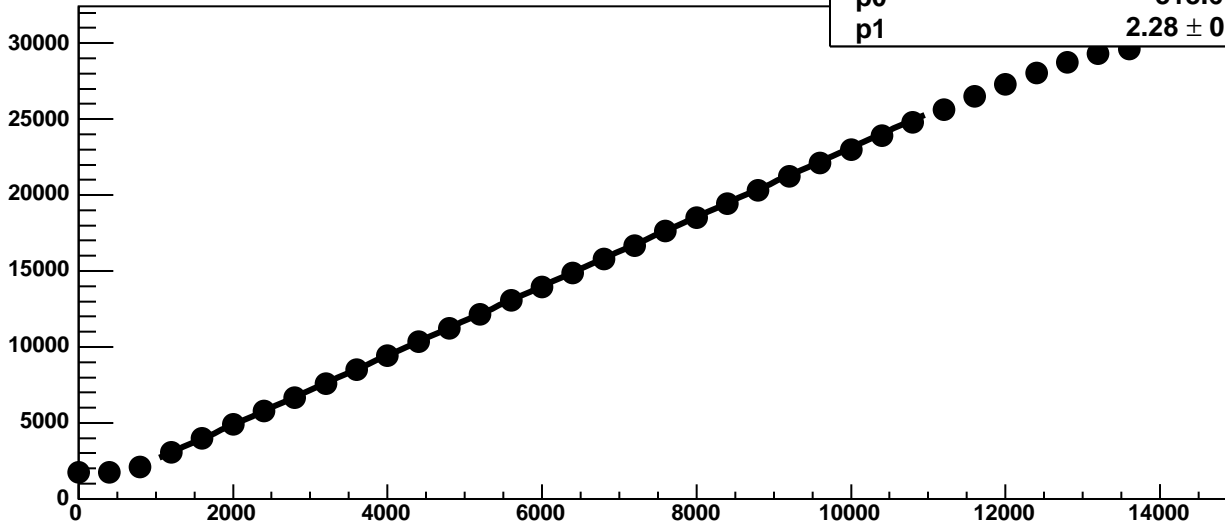


Chip 4, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

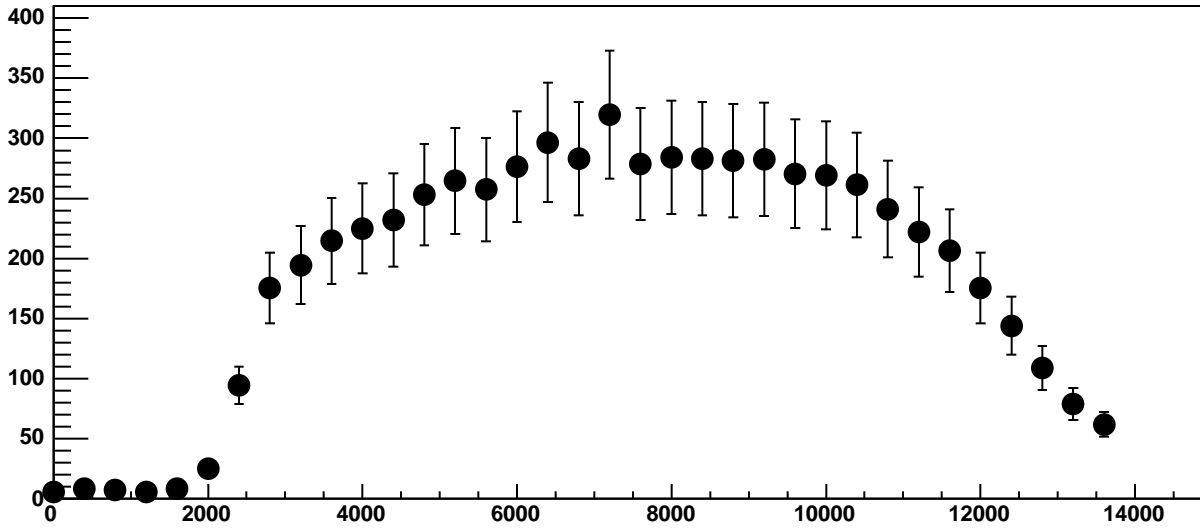


Chip 4, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC

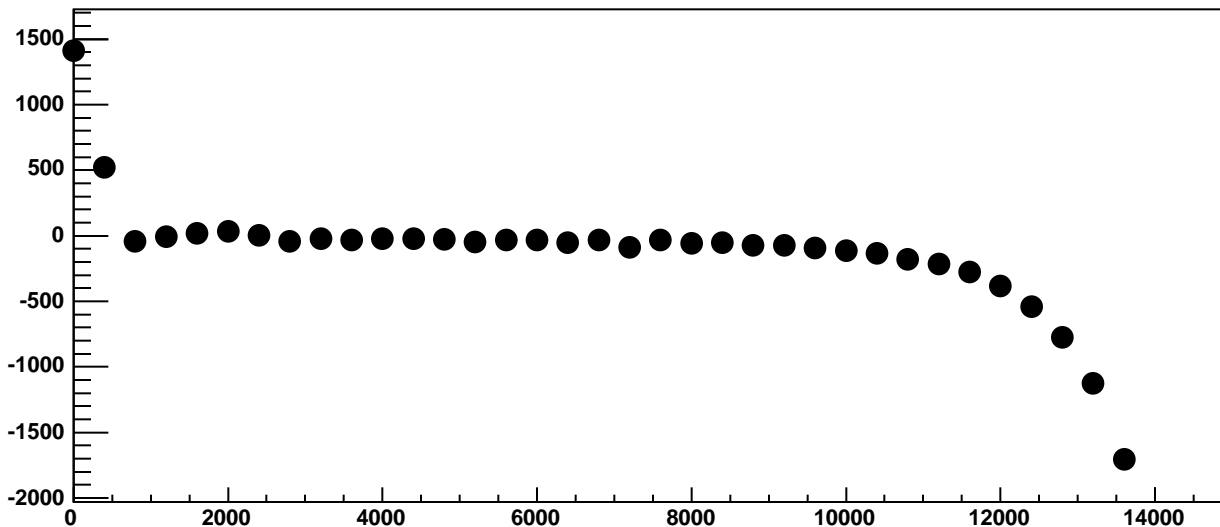
χ^2 / ndf 165.8 / 23
p0 318.6 ± 3.047
p1 2.28 ± 0.002049



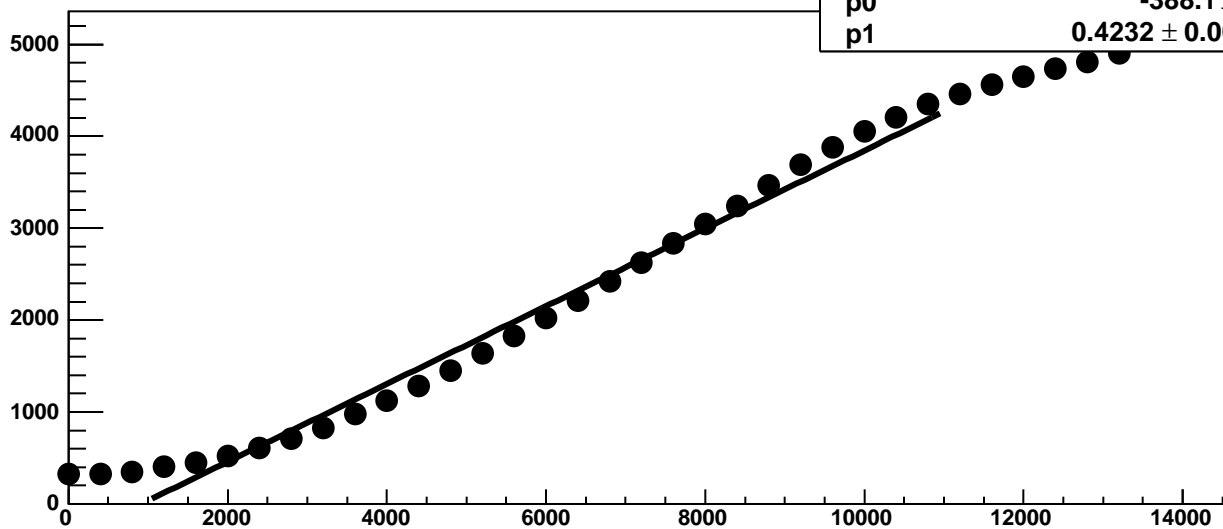
Chip 4, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



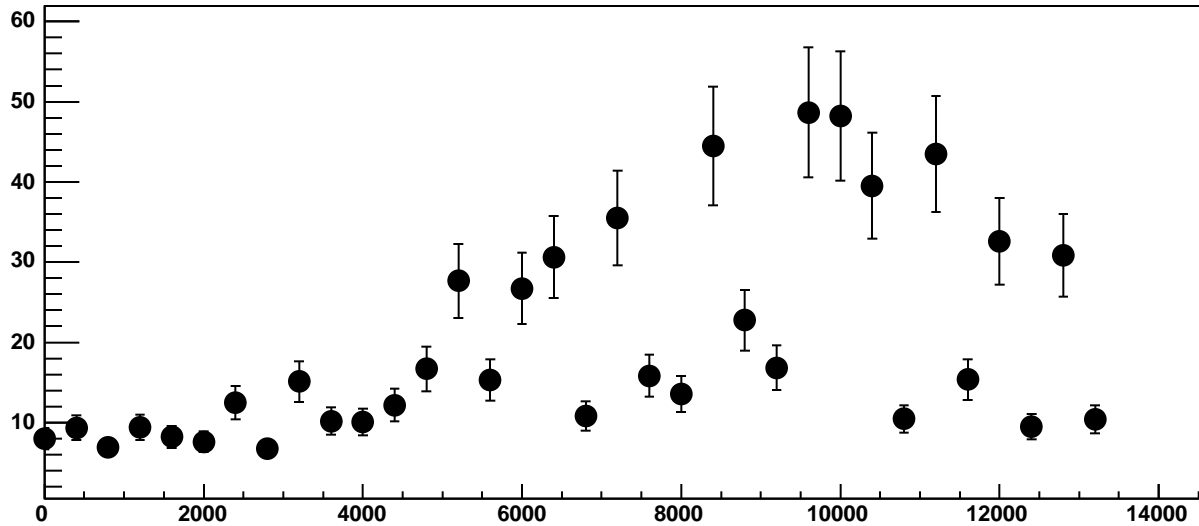
Chip 4, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC



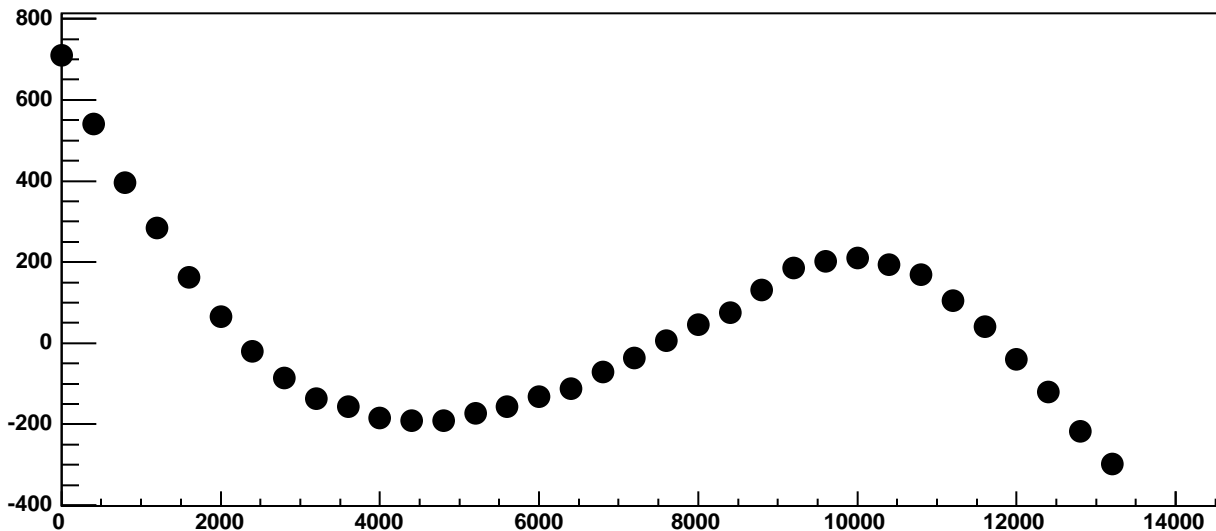
Chip 4, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



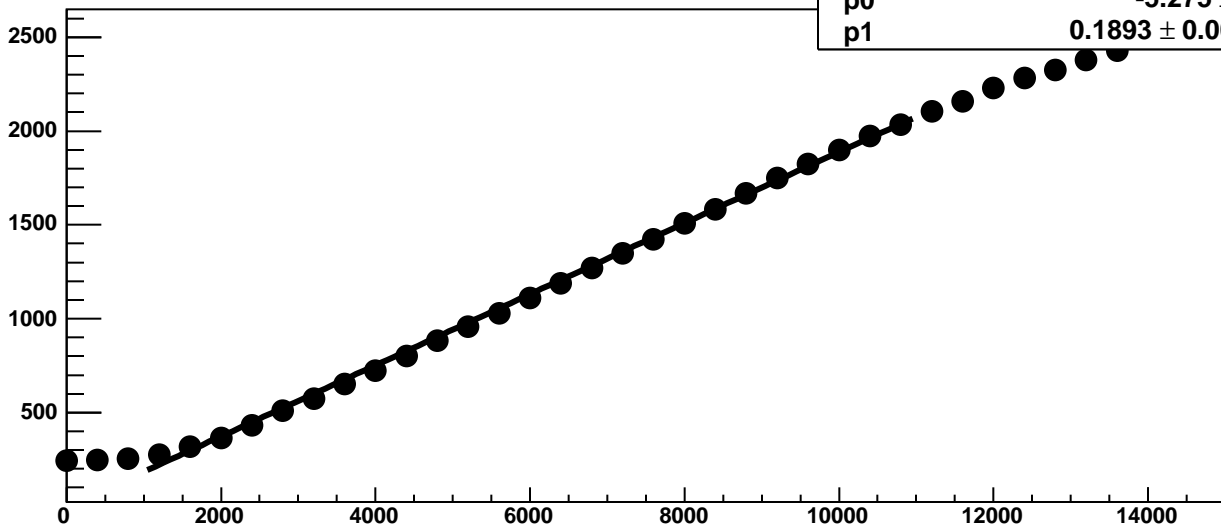
Chip 4, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

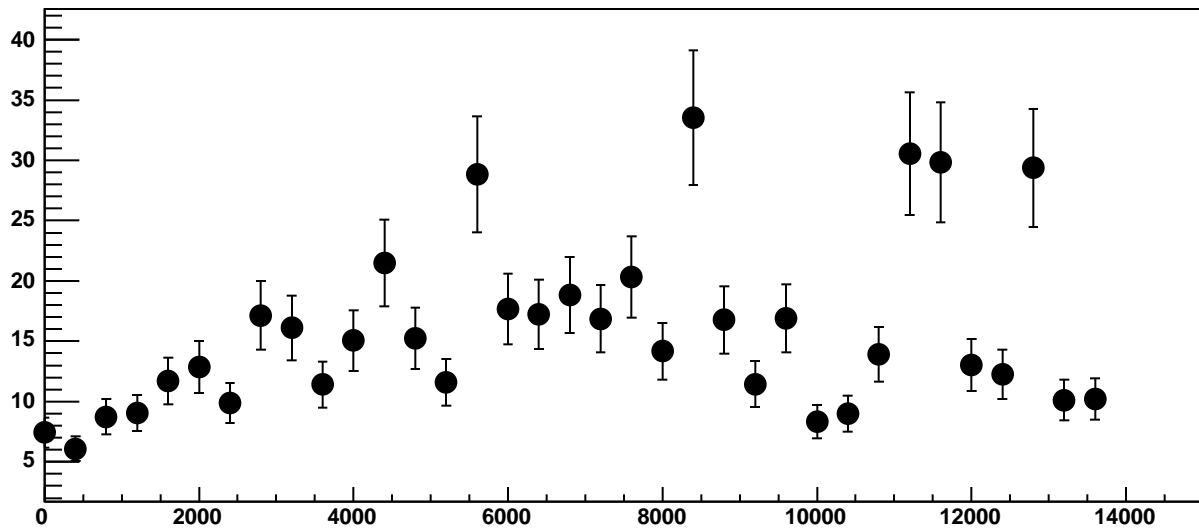


Chip 4, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

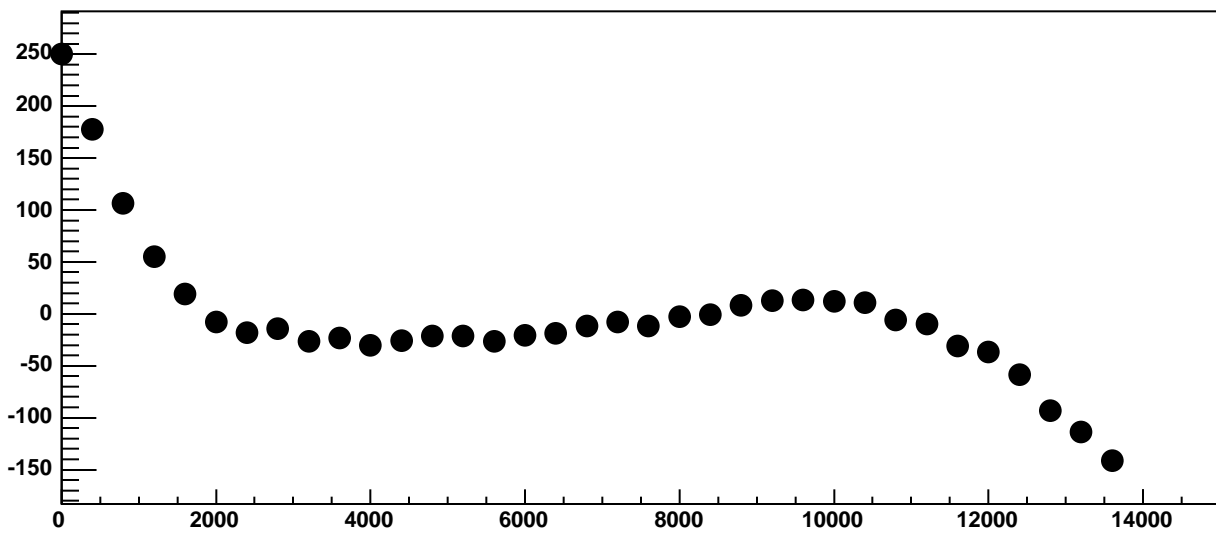


χ^2 / ndf 1379 / 23
p0 -5.275 ± 1.249
p1 0.1893 ± 0.0001825

Chip 4, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC

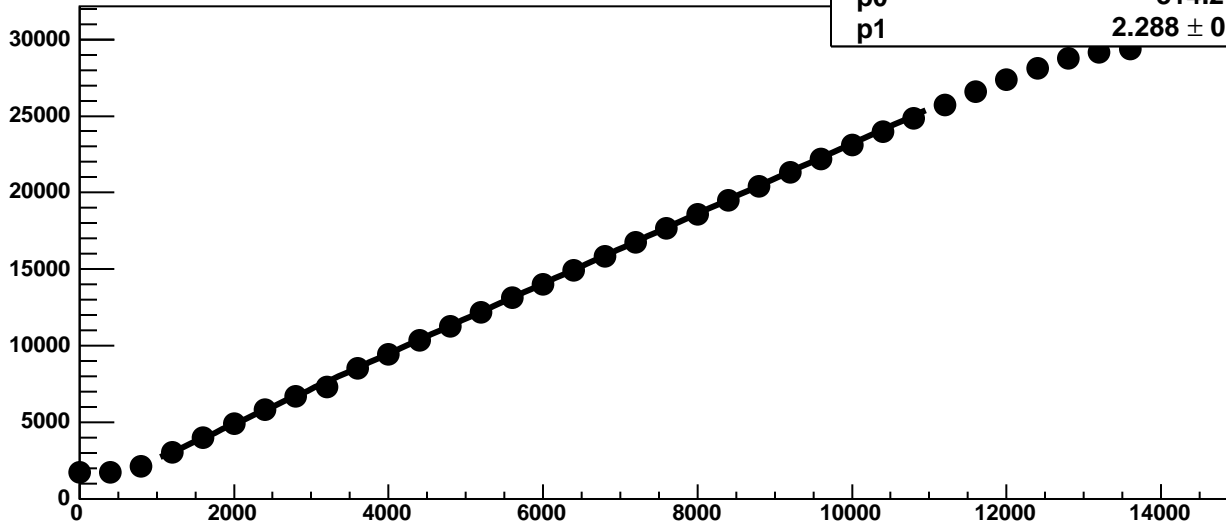


Chip 4, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

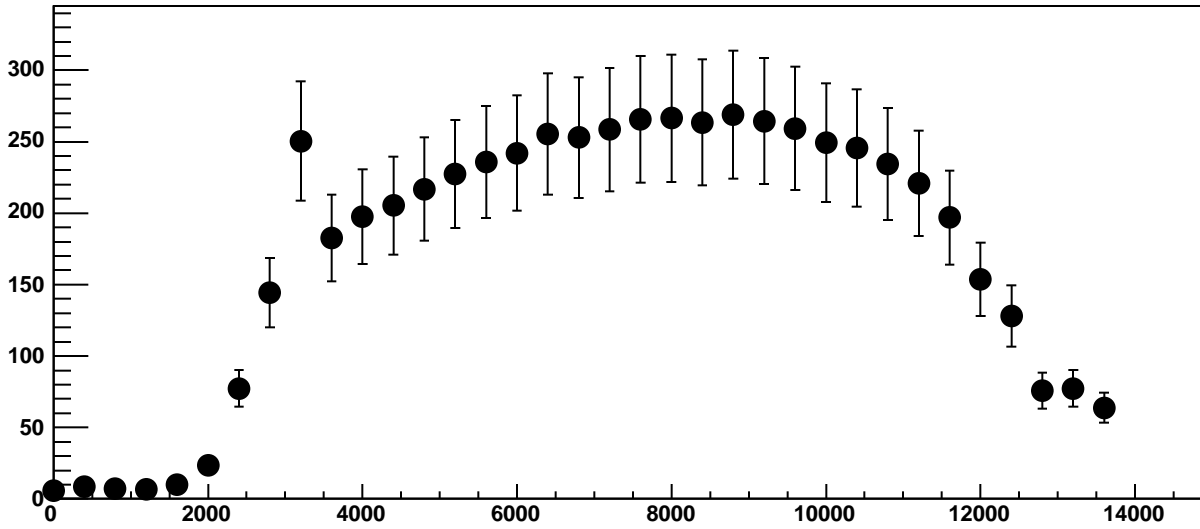


Chip 4, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC

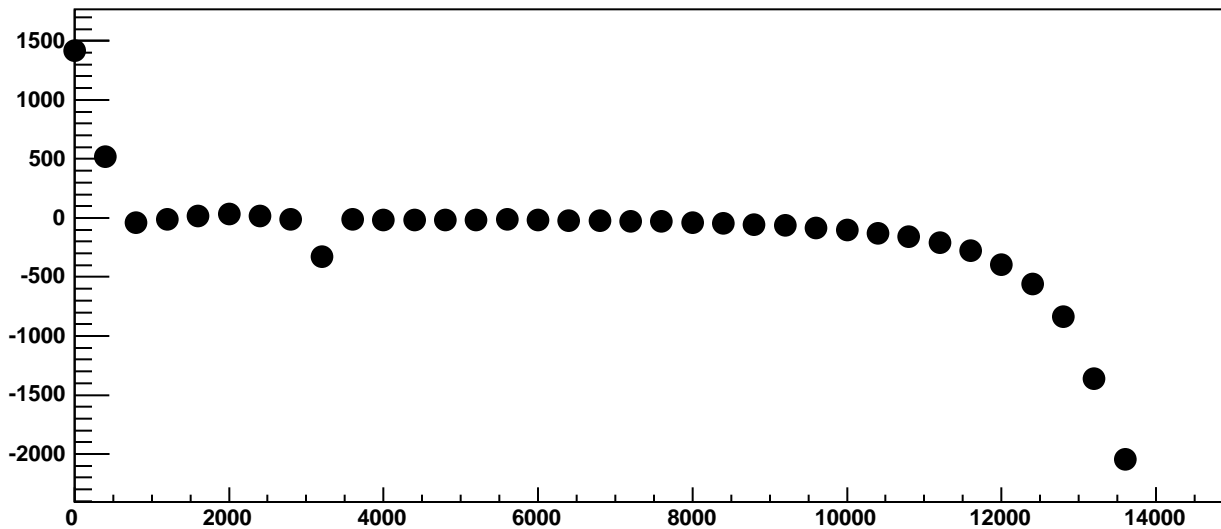
χ^2 / ndf 196.9 / 23
p0 314.2 ± 3.006
p1 2.288 ± 0.001939



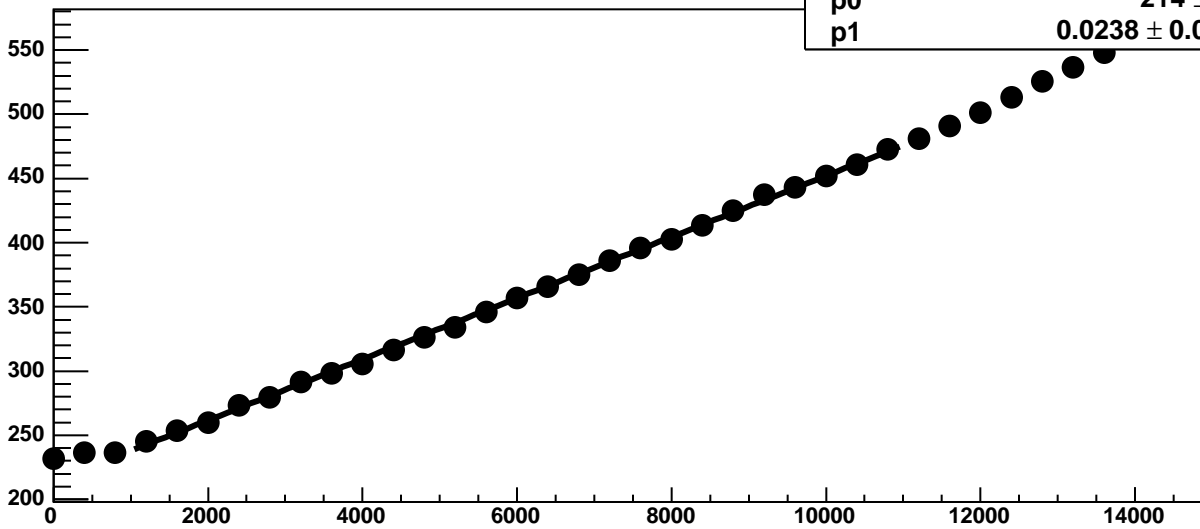
Chip 4, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC

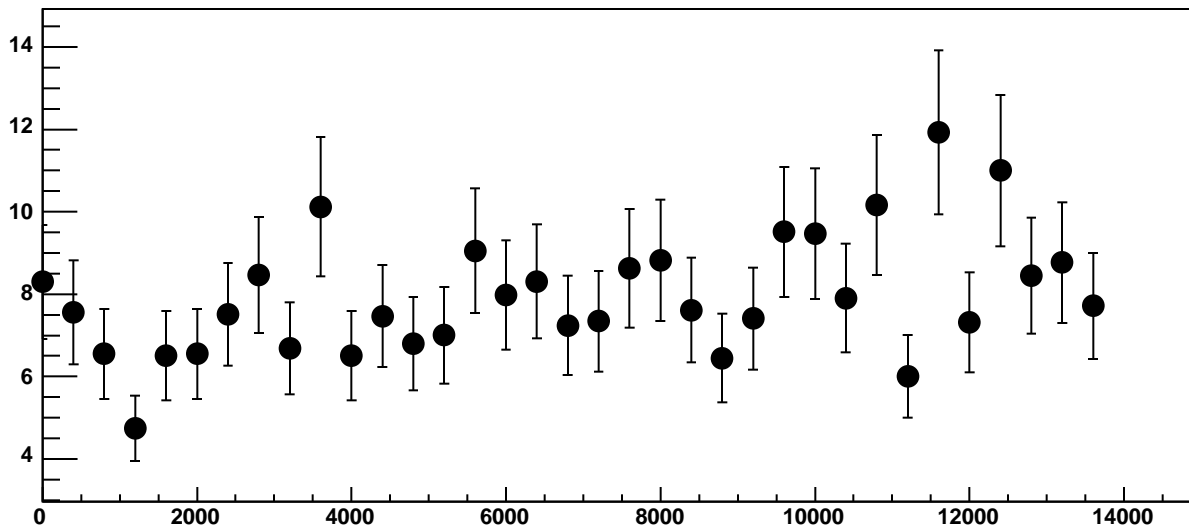


Chip 4, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

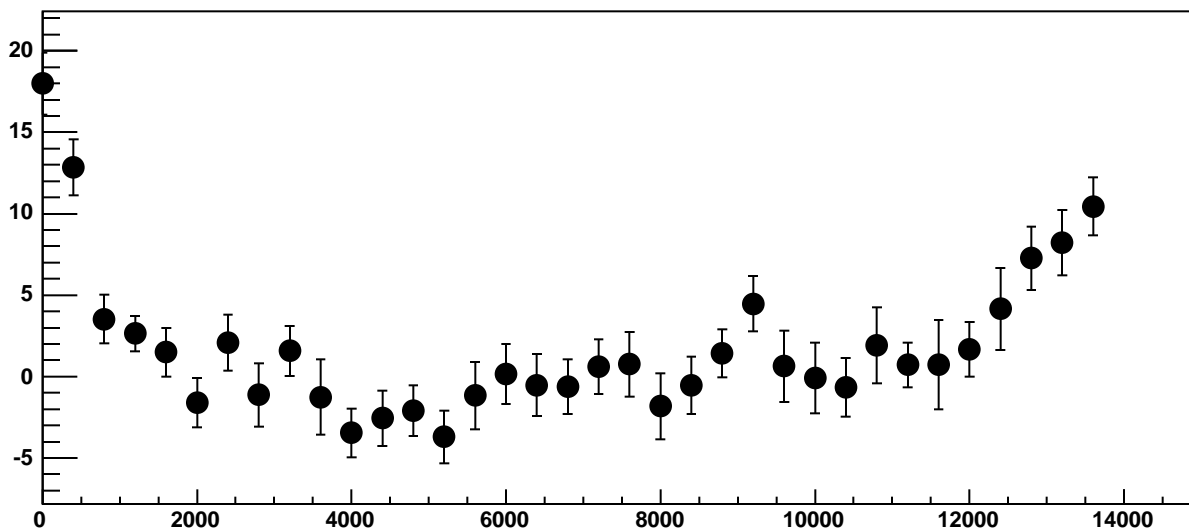


χ^2 / ndf 36.33 / 23
p0 214 ± 0.7117
p1 0.0238 ± 0.0001156

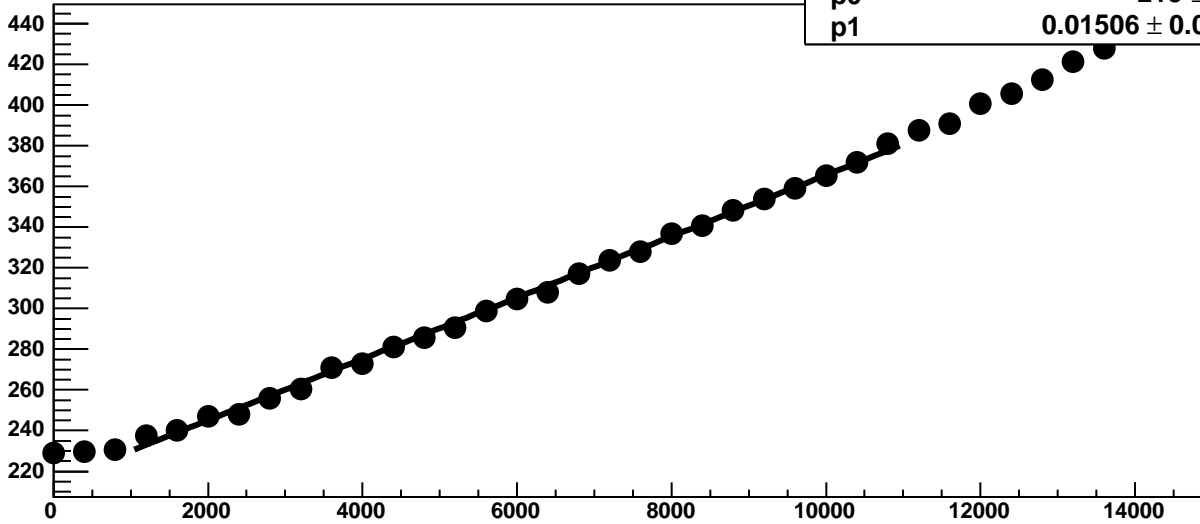
Chip 4, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

47.7 / 23

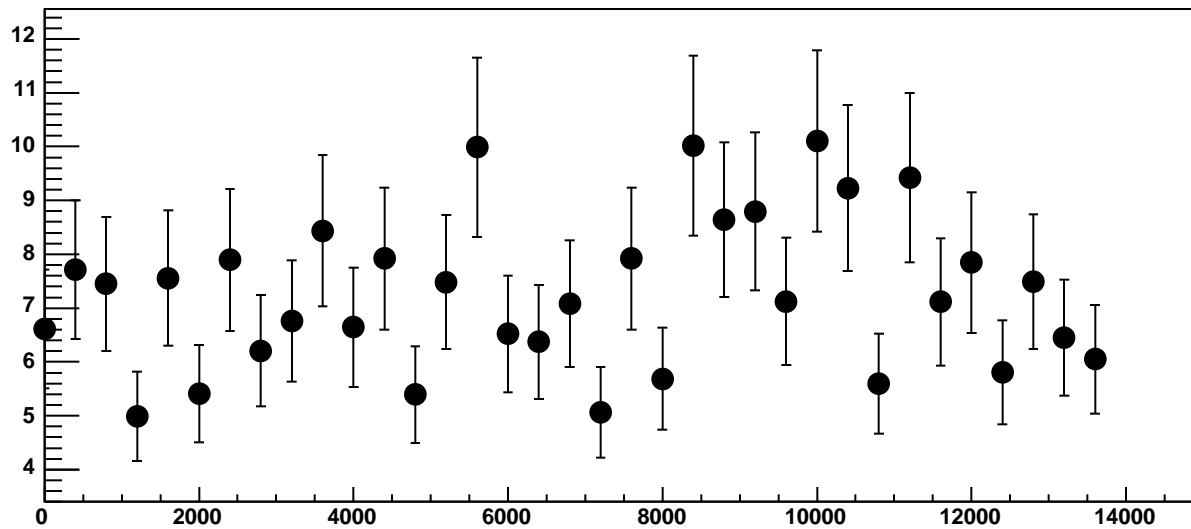
p0

215 ± 0.6816

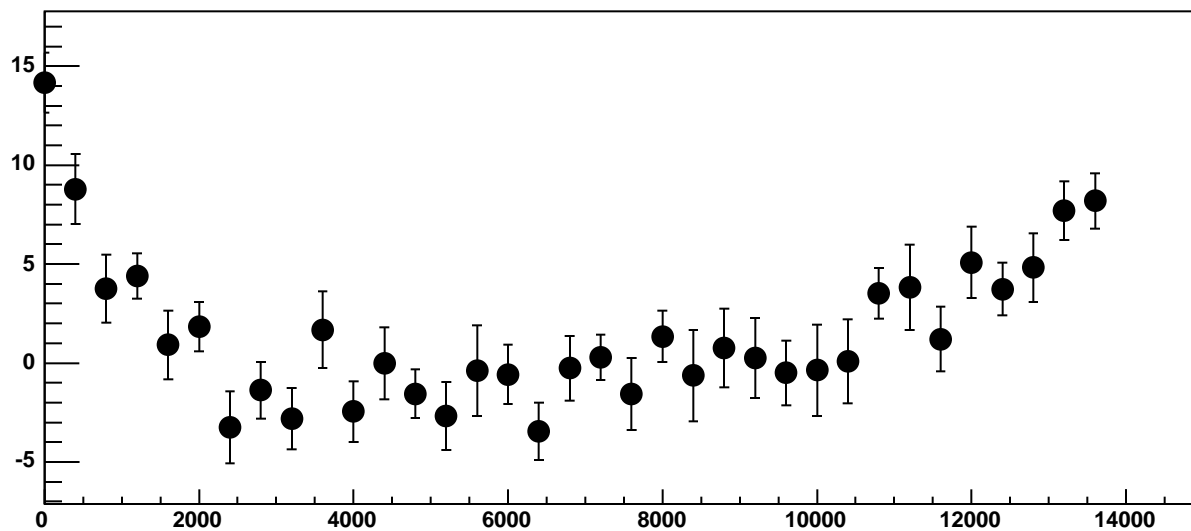
p1

0.01506 ± 0.0001074

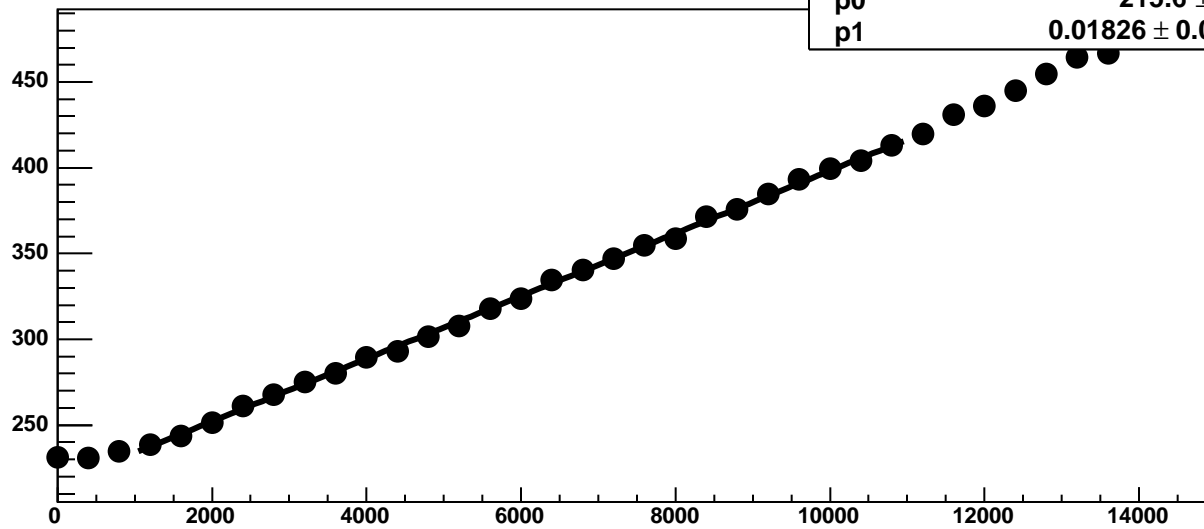
Chip 4, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

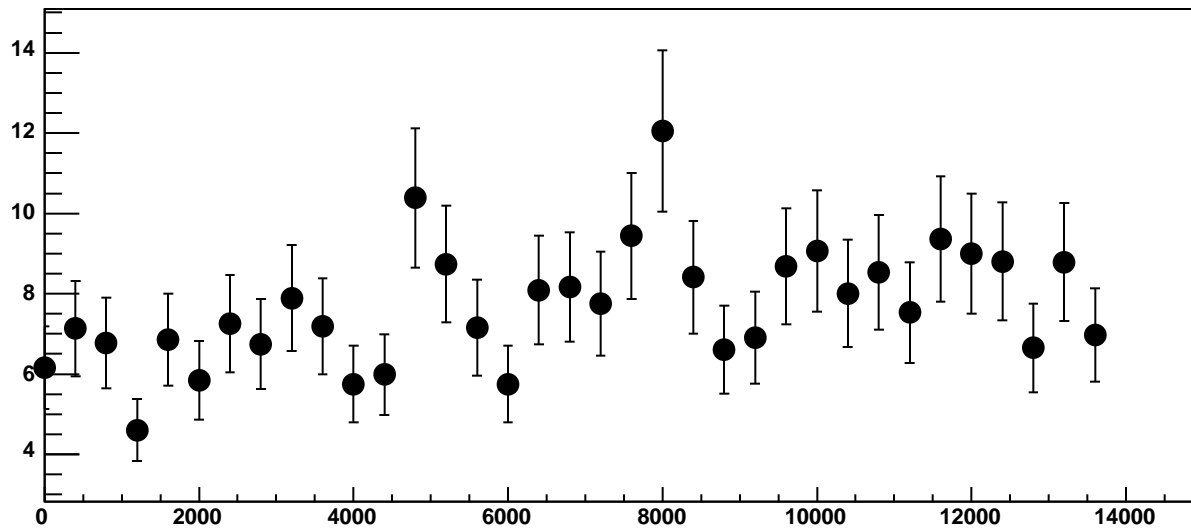


Chip 4, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

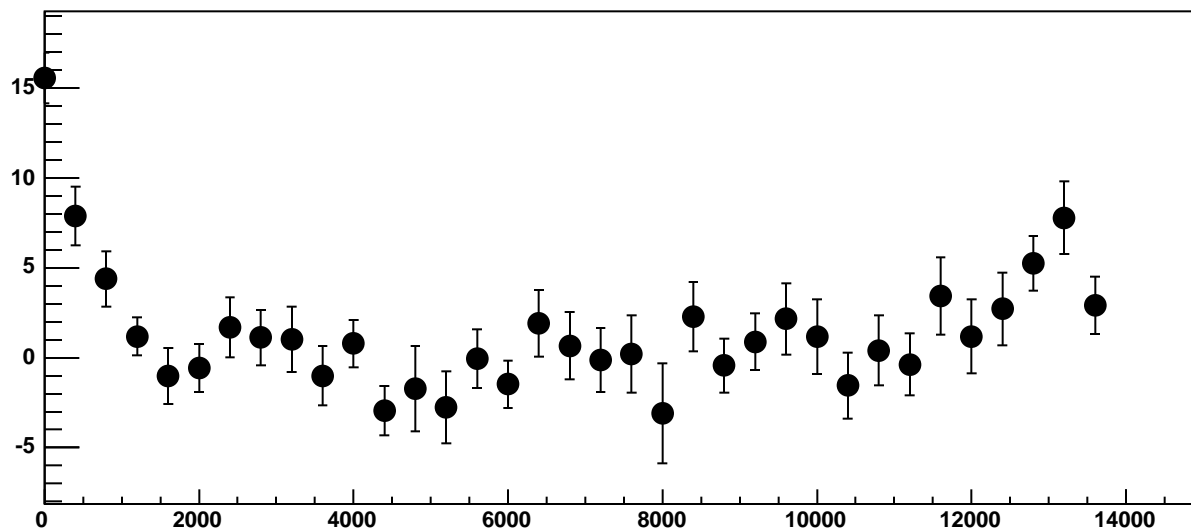


χ^2 / ndf 19.19 / 23
p0 215.6 ± 0.6788
p1 0.01826 ± 0.0001118

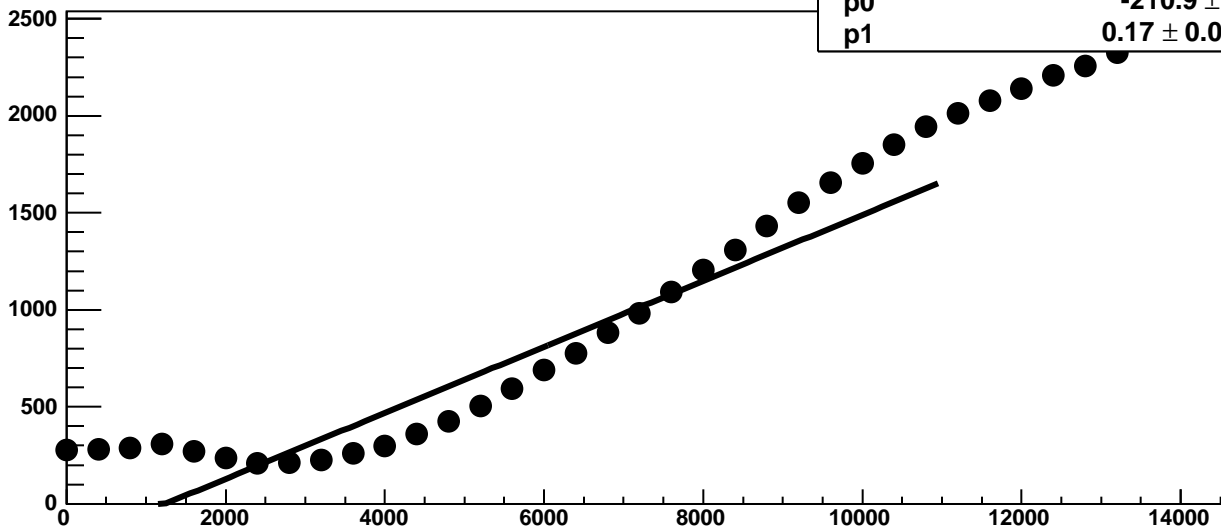
Chip 4, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC

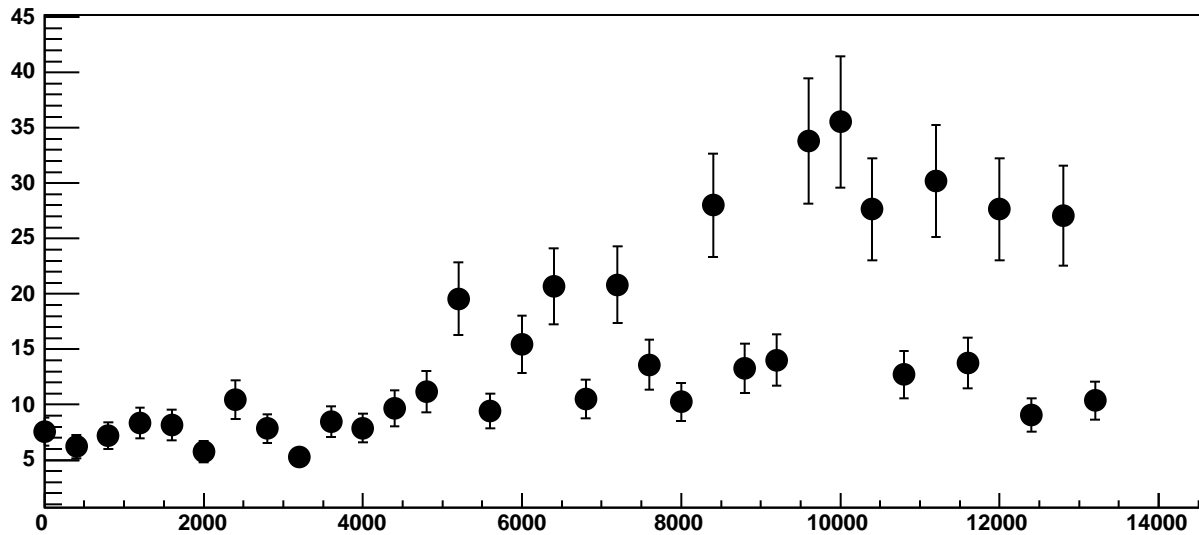


Chip 4, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

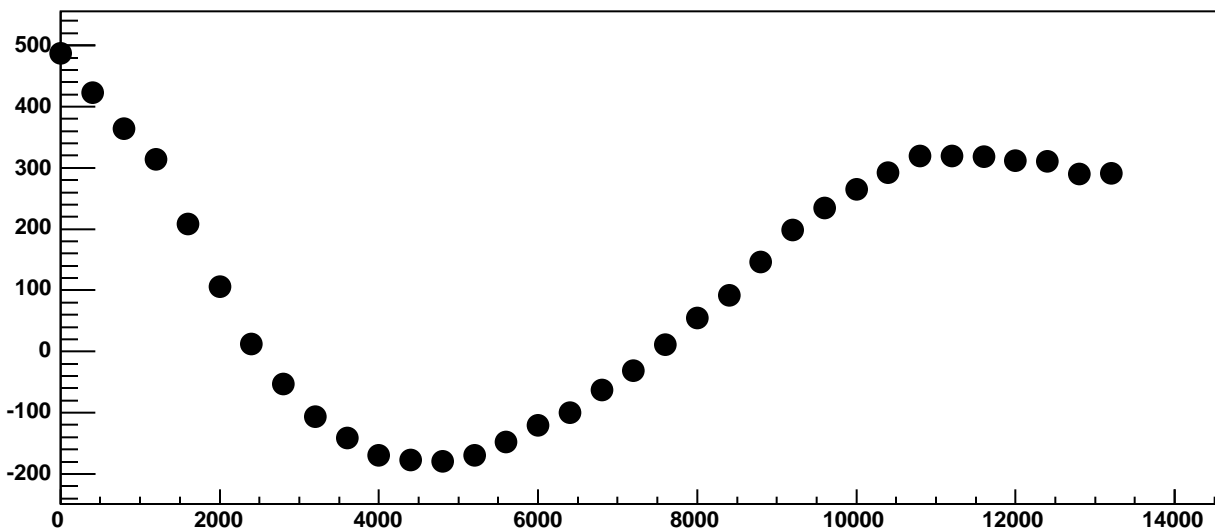


χ^2 / ndf 1.114e+05 / 23
p0 -210.9 ± 0.9284
p1 0.17 ± 0.0001901

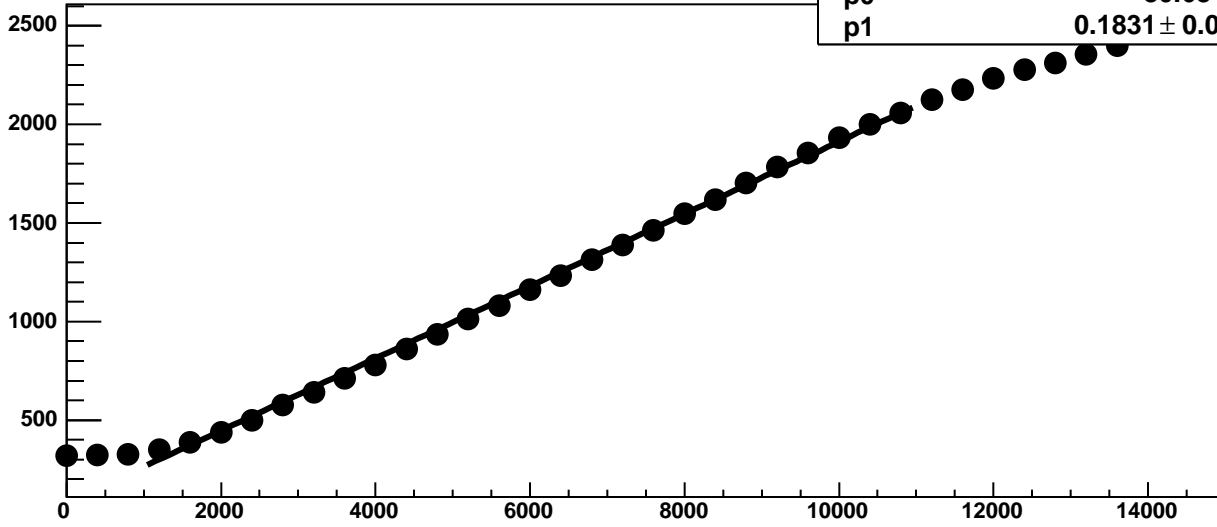
Chip 4, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1528 / 23

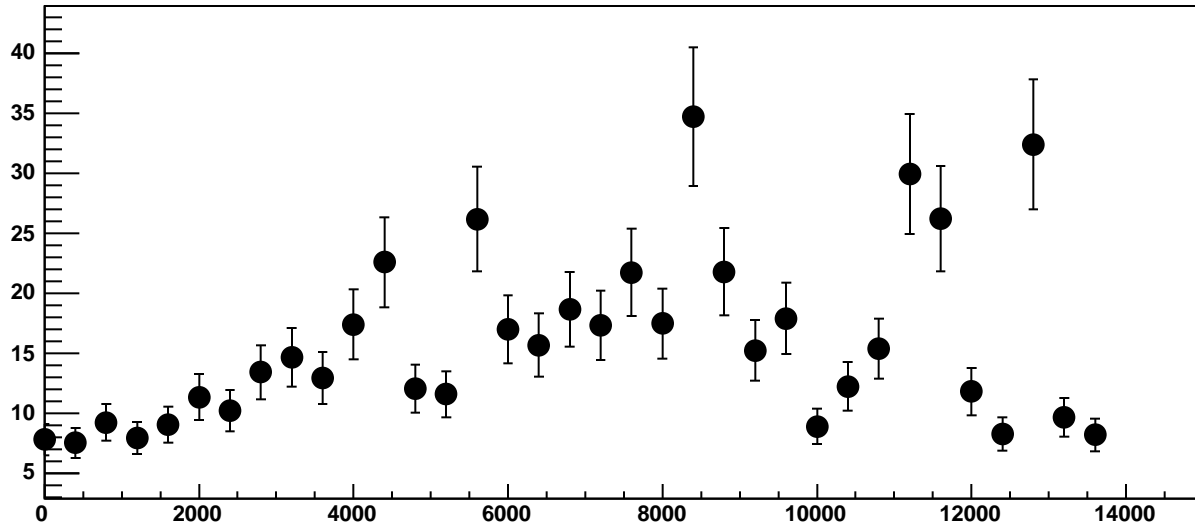
p0

80.68 ± 1.157

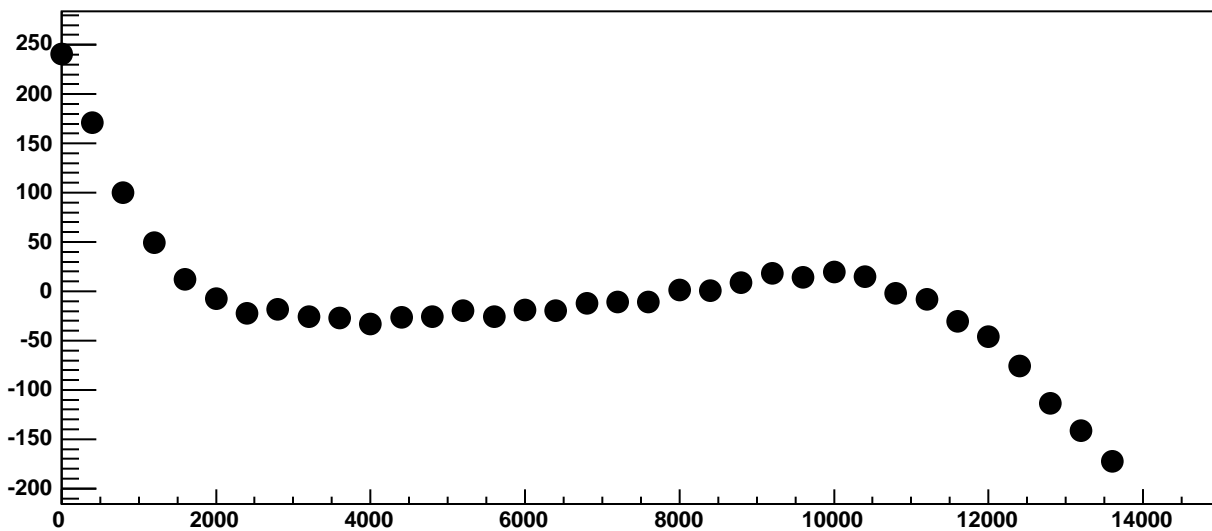
p1

0.1831 ± 0.0001887

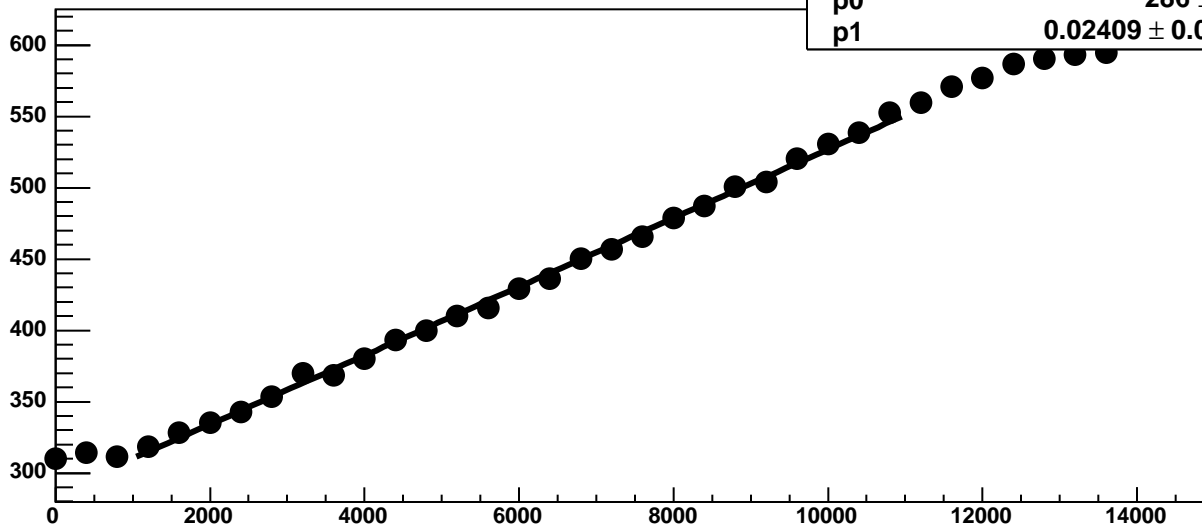
Chip 4, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



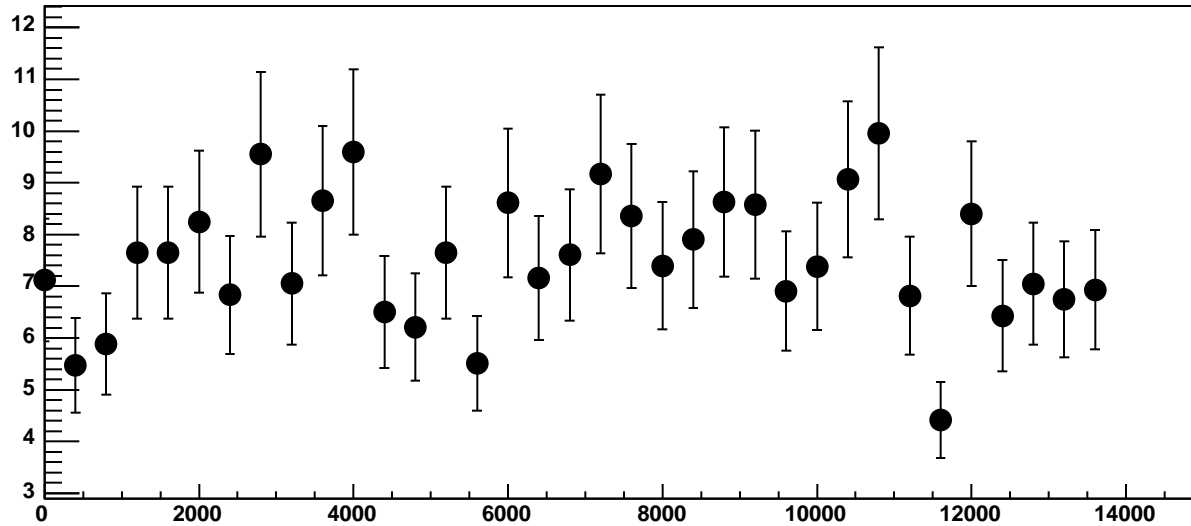
Chip 4, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



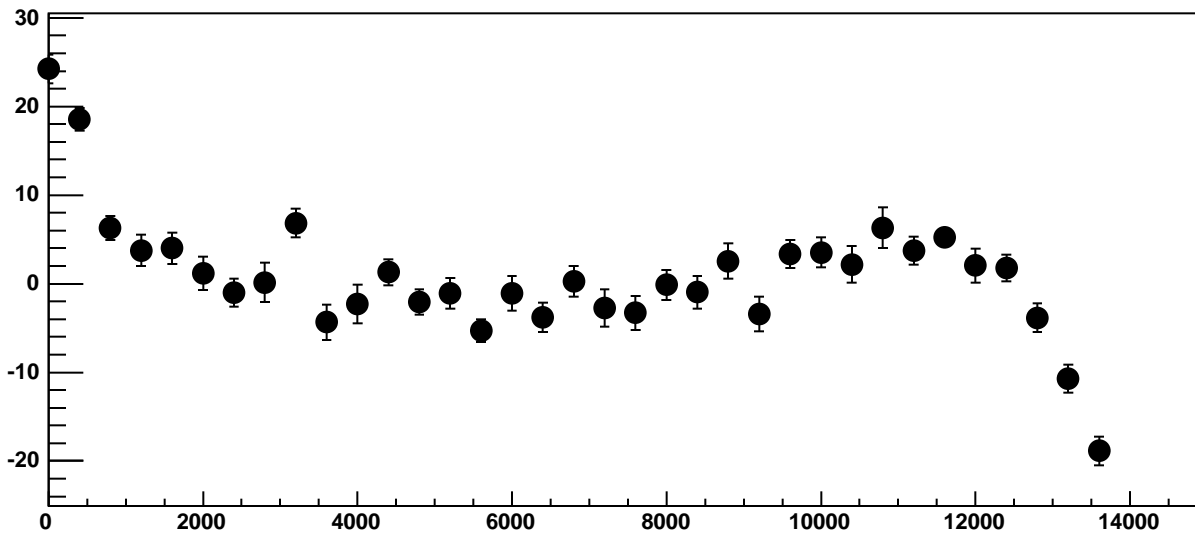
Chip 4, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



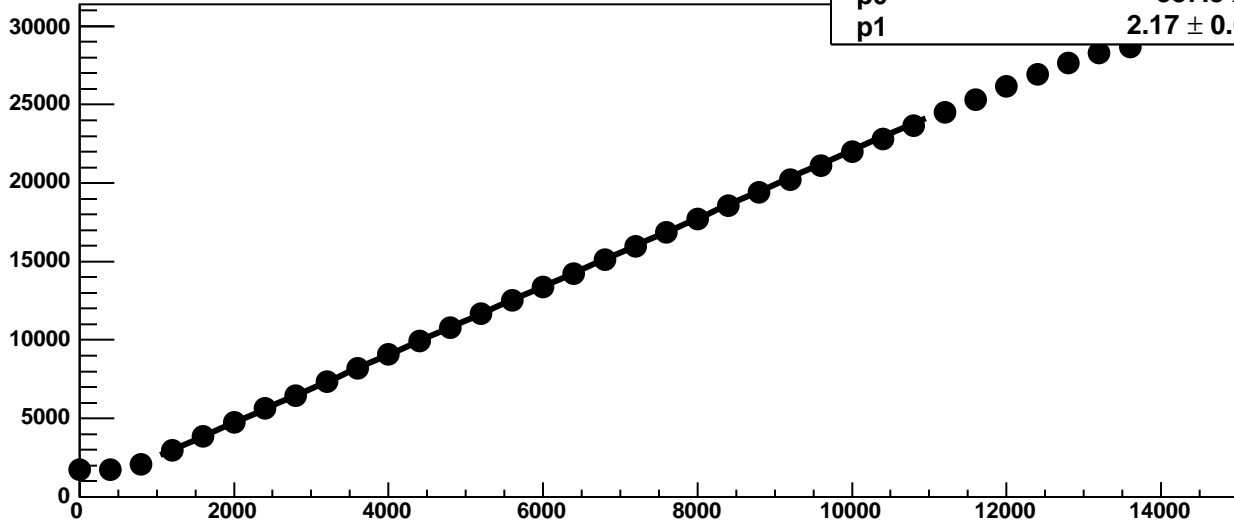
Chip 4, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



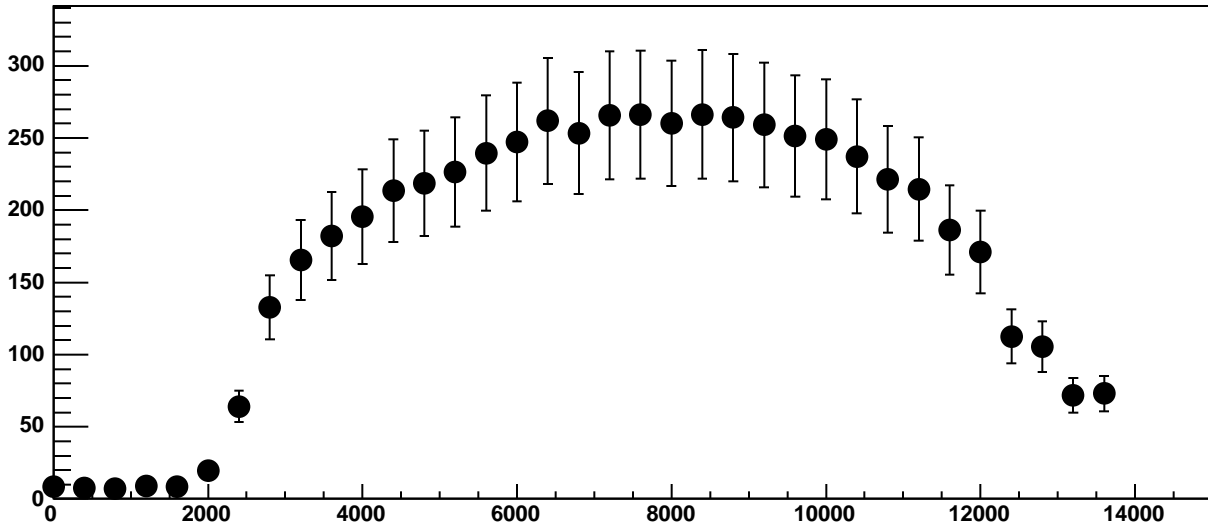
Chip 4, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



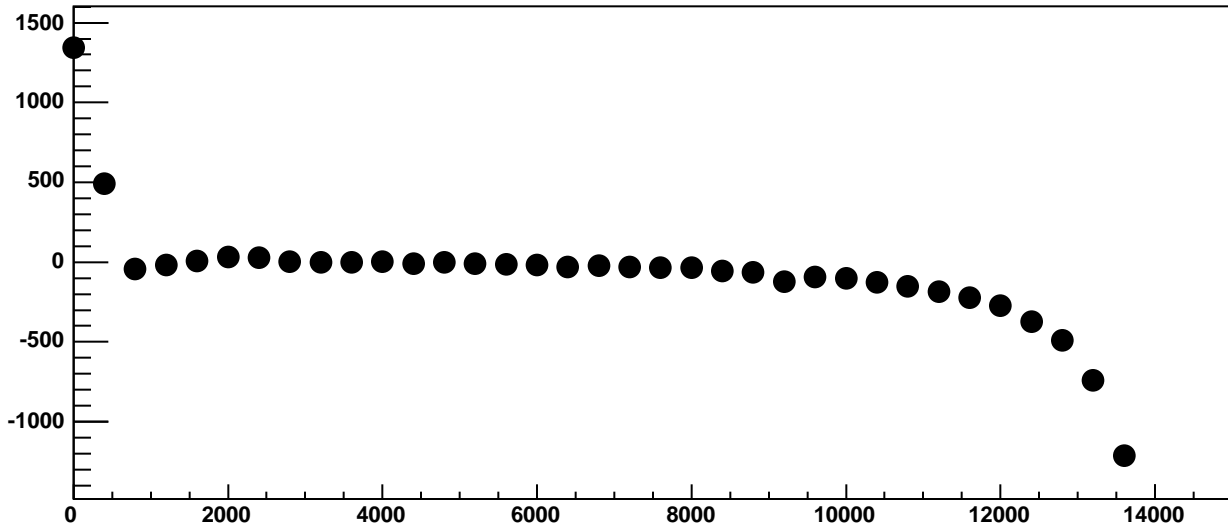
Chip 4, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



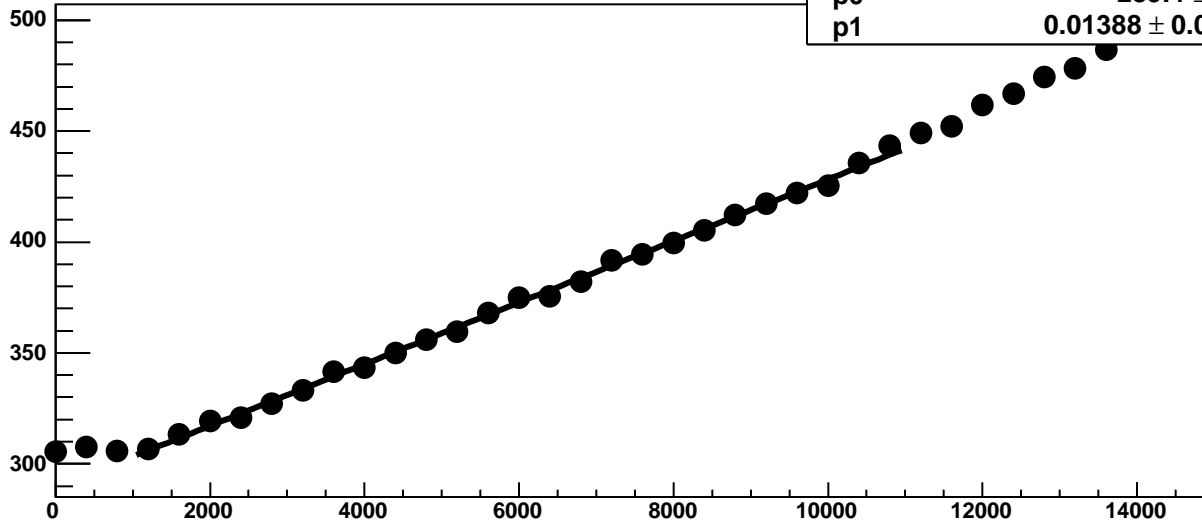
Chip 4, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

25.71 / 23

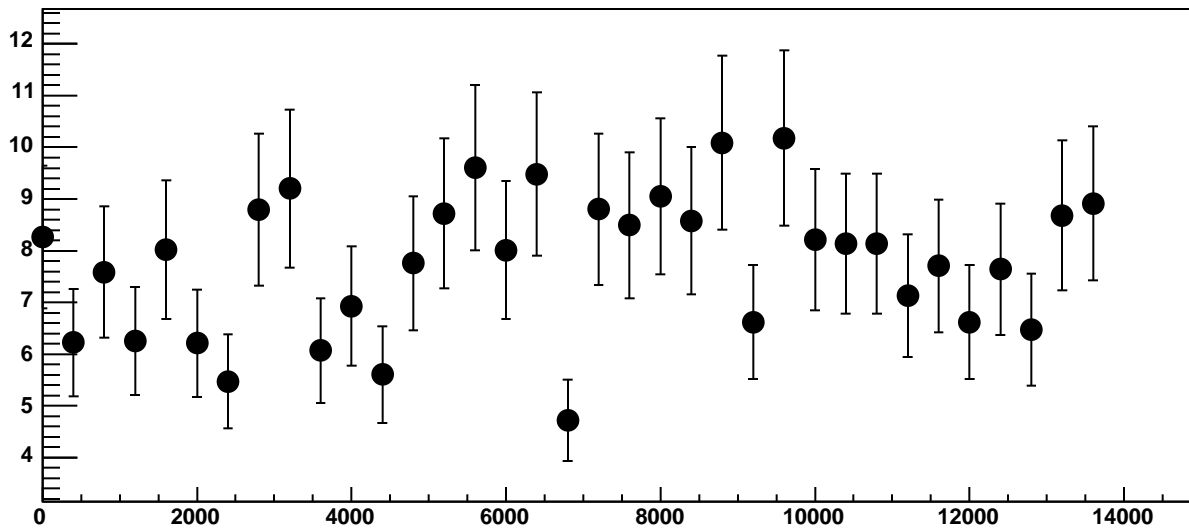
p0

289.4 ± 0.7387

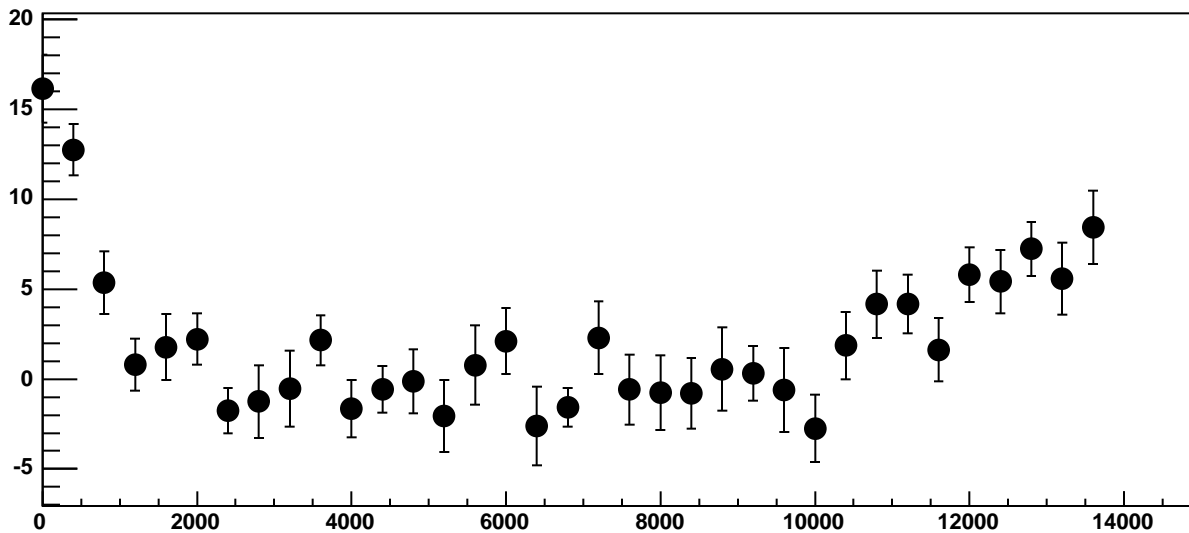
p1

0.01388 ± 0.0001186

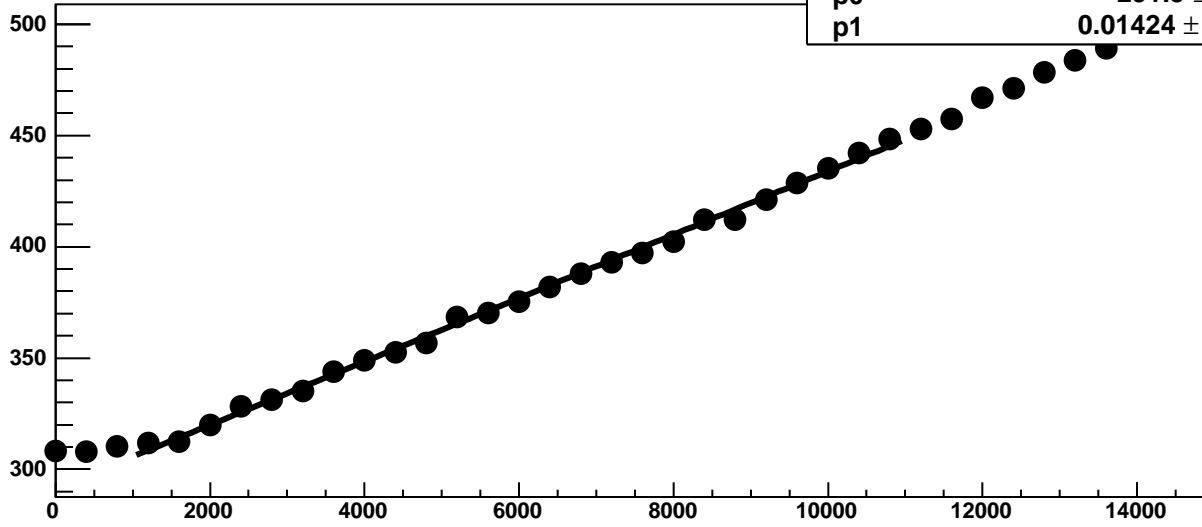
Chip 4, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

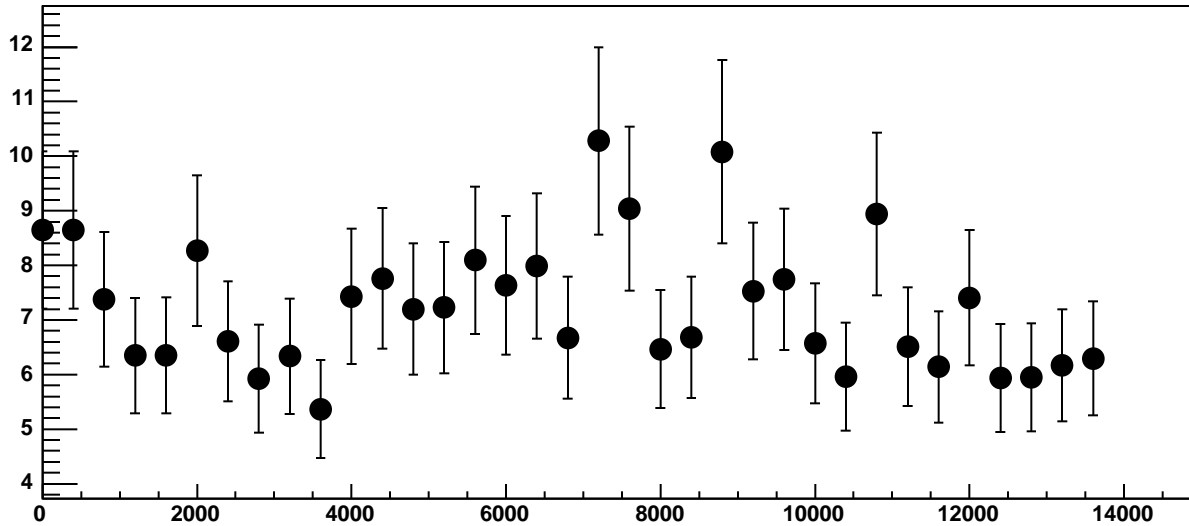


Chip 4, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

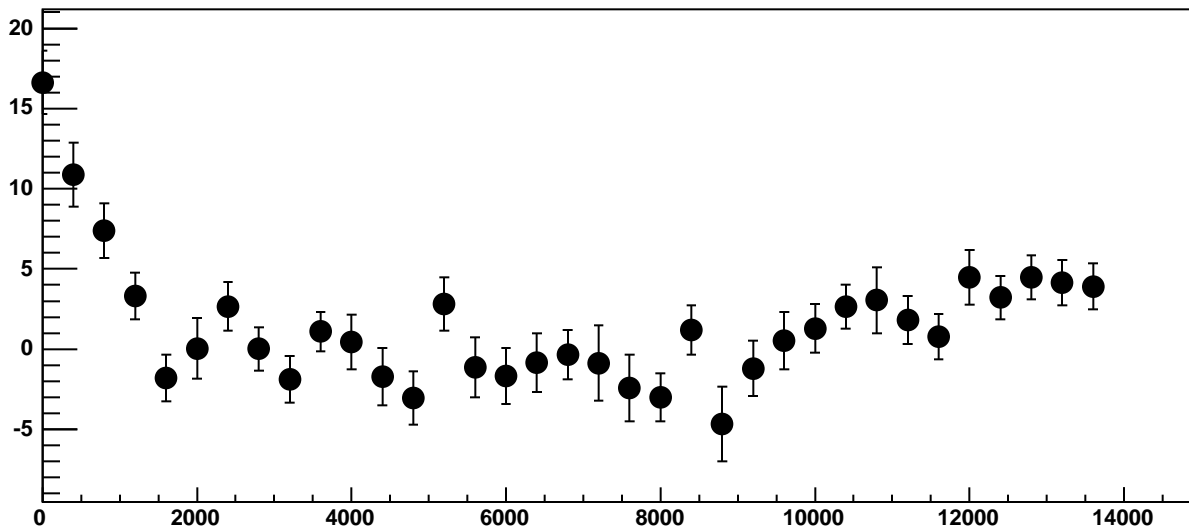


χ^2 / ndf 38.72 / 23
p0 291.5 ± 0.7079
p1 0.01424 ± 0.00011

Chip 4, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

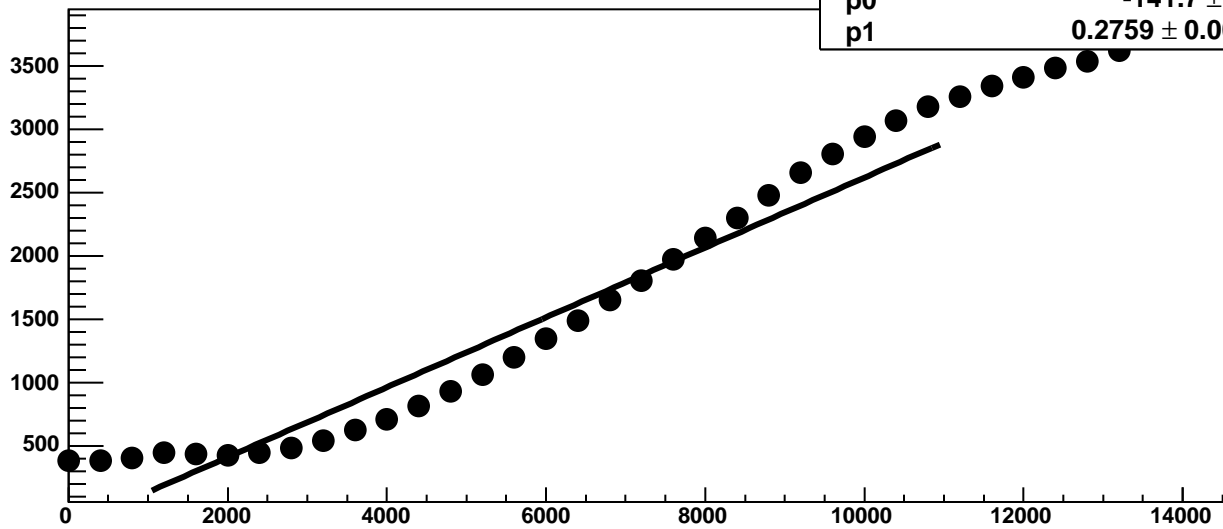


Chip 4, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

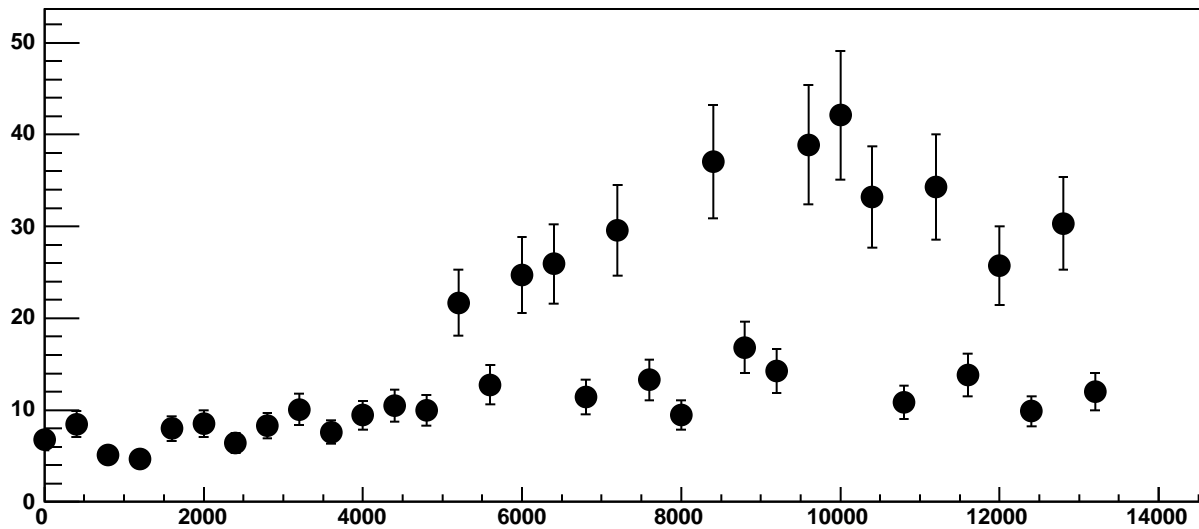


Chip 4, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC

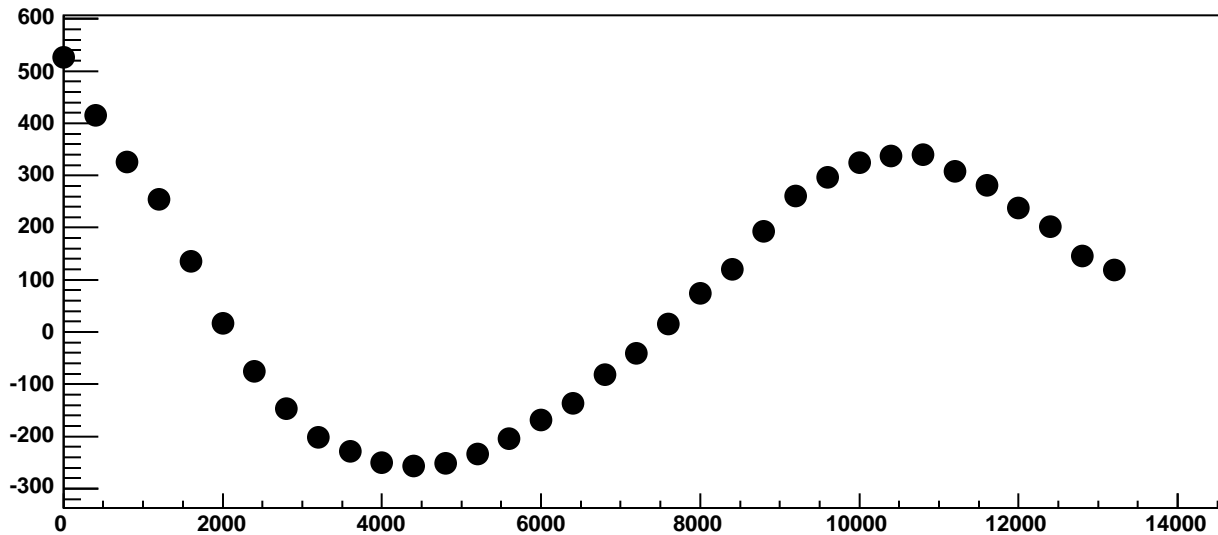
χ^2 / ndf 1.747e+05 / 23
p0 -141.7 ± 0.8429
p1 0.2759 ± 0.0001782



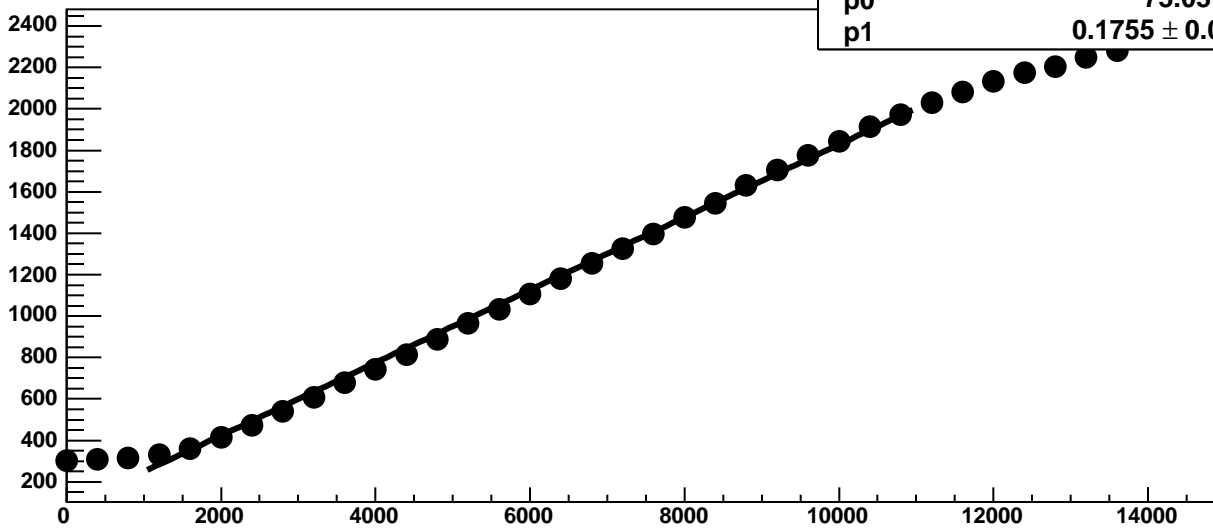
Chip 4, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



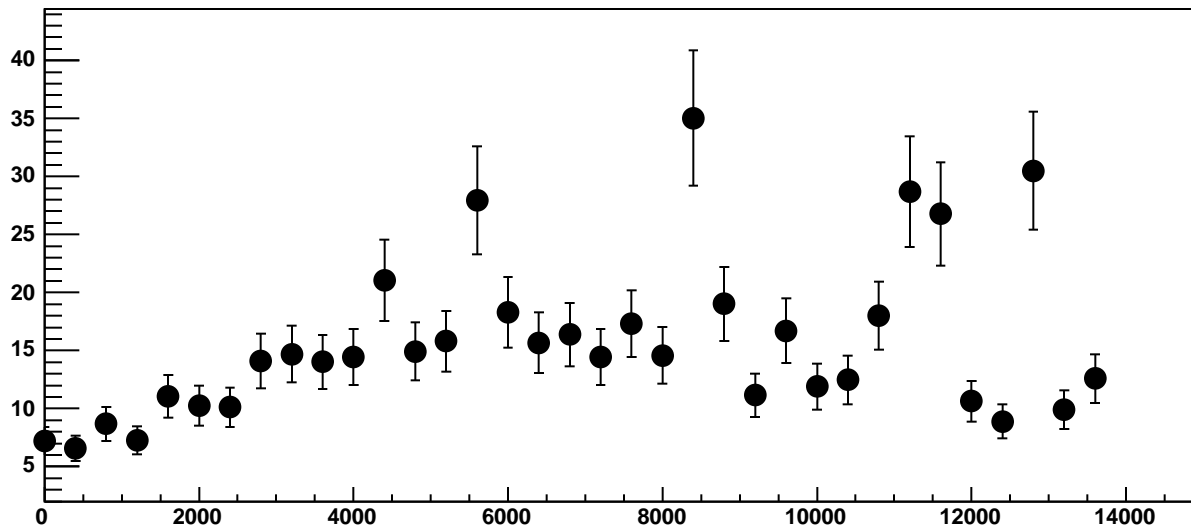
Chip 4, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC



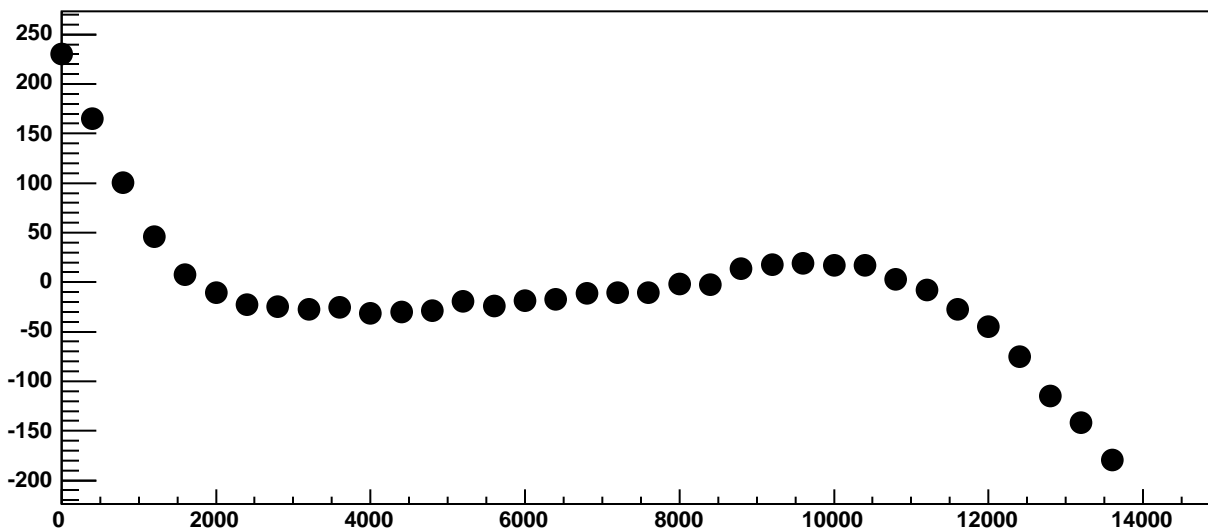
Chip 4, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



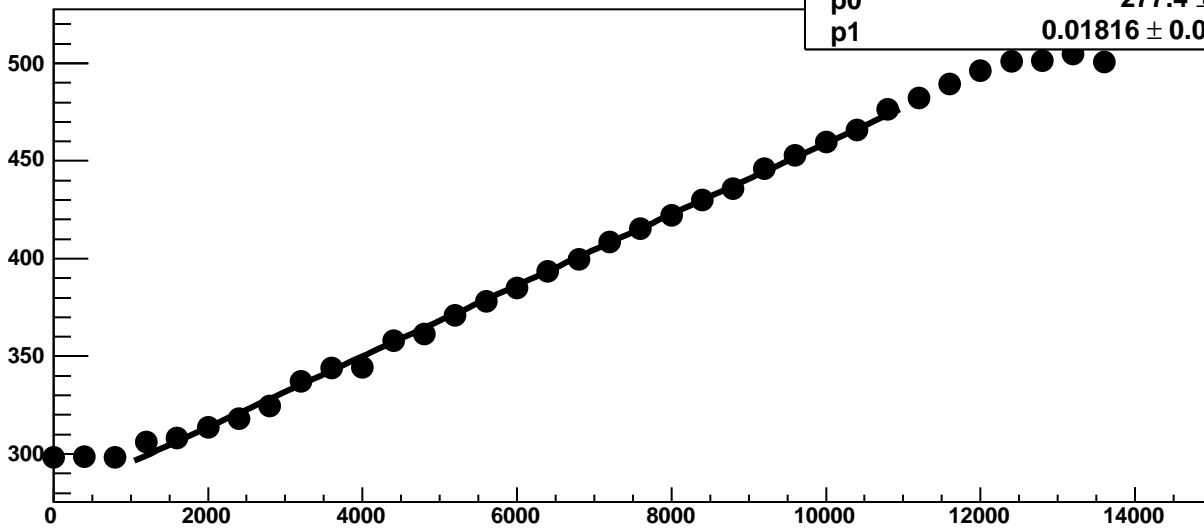
Chip 4, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

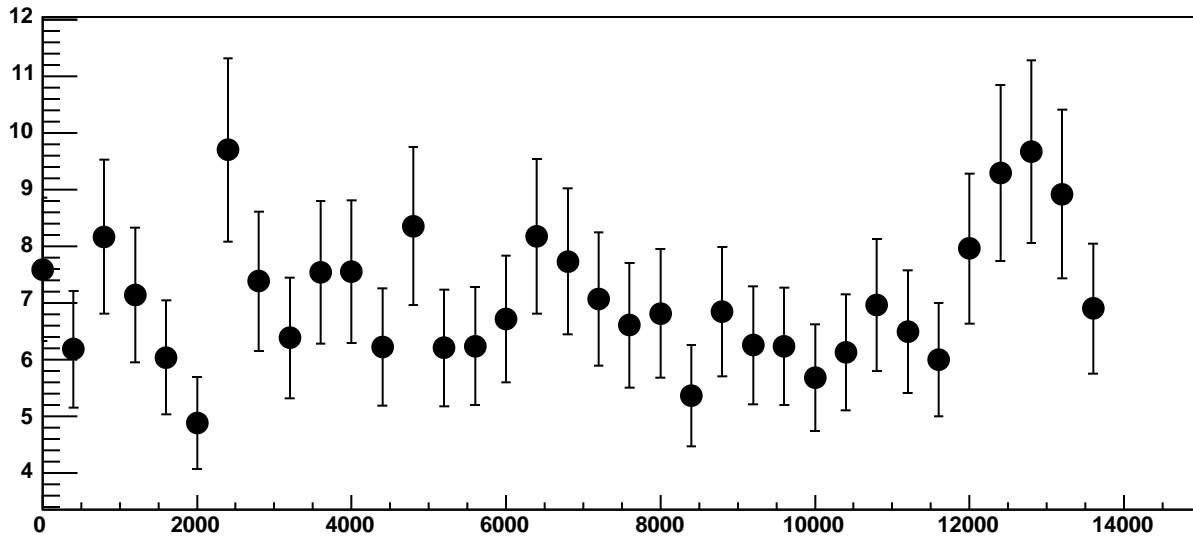


Chip 4, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

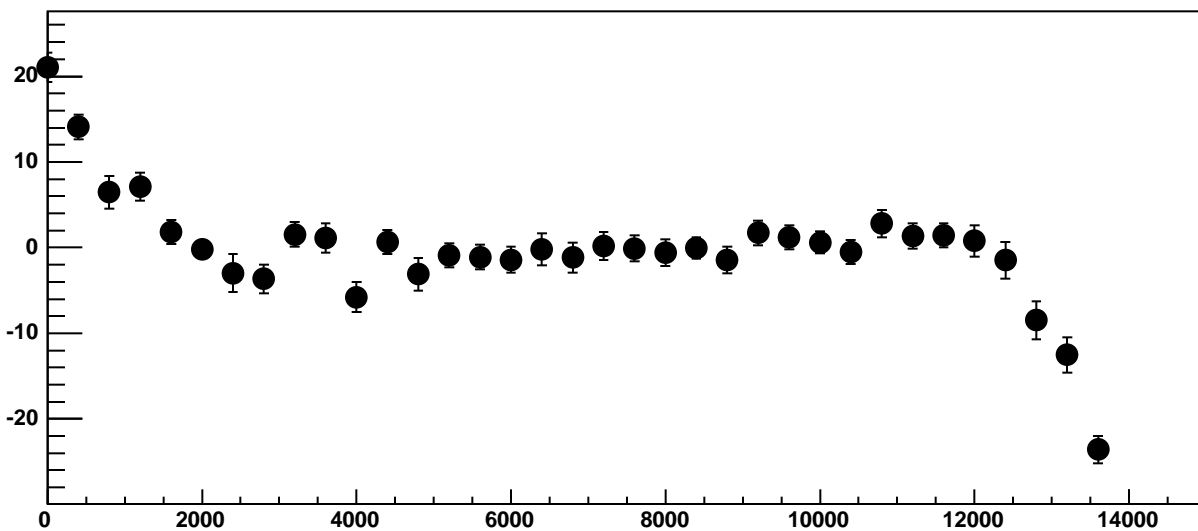


χ^2 / ndf 51.24 / 23
p0 277.4 ± 0.6921
p1 0.01816 ± 0.0001018

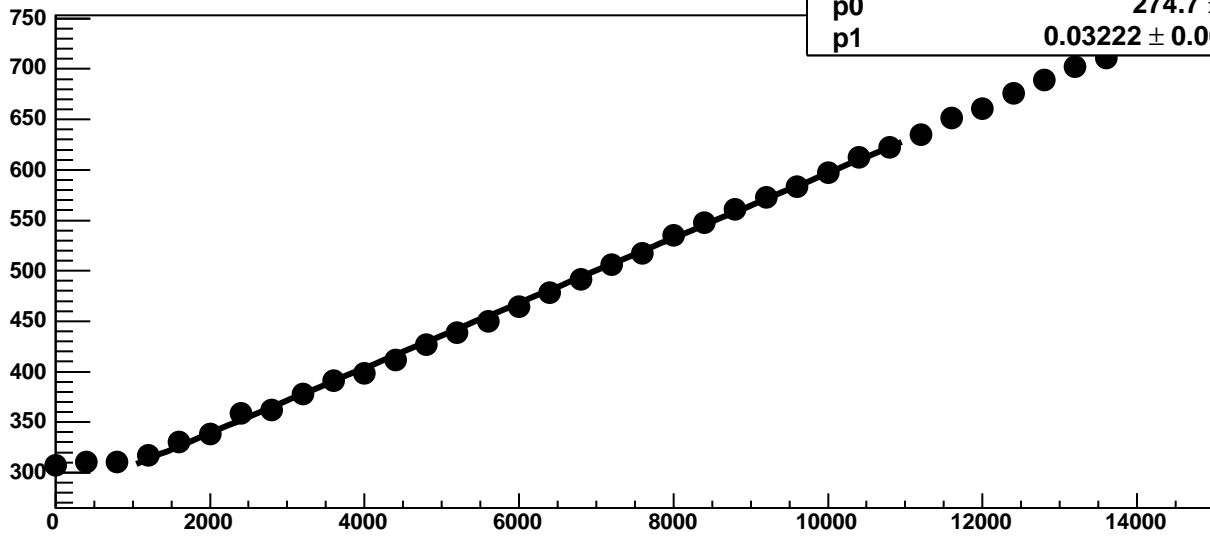
Chip 4, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

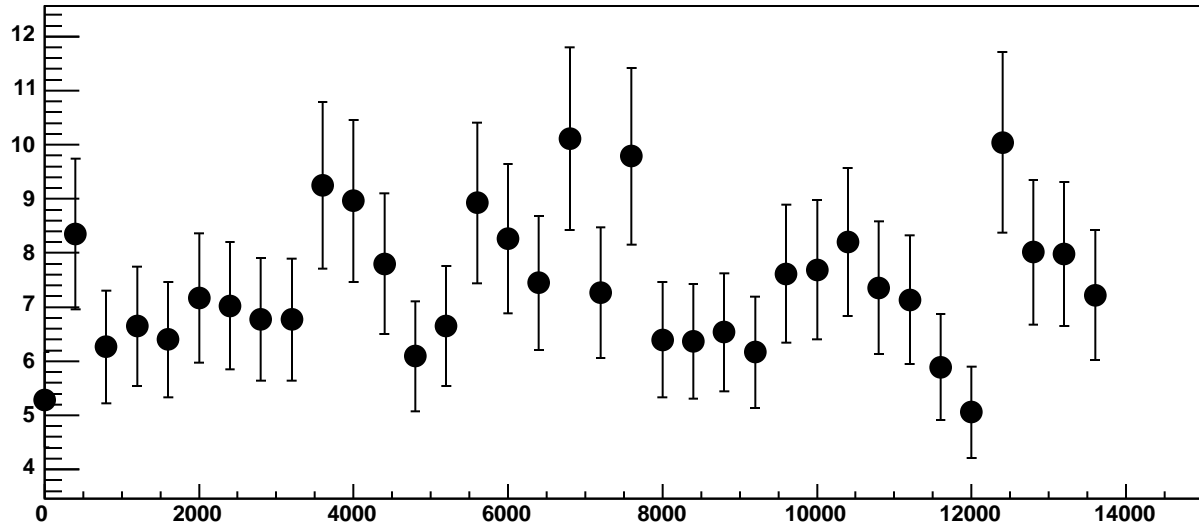


Chip 4, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC

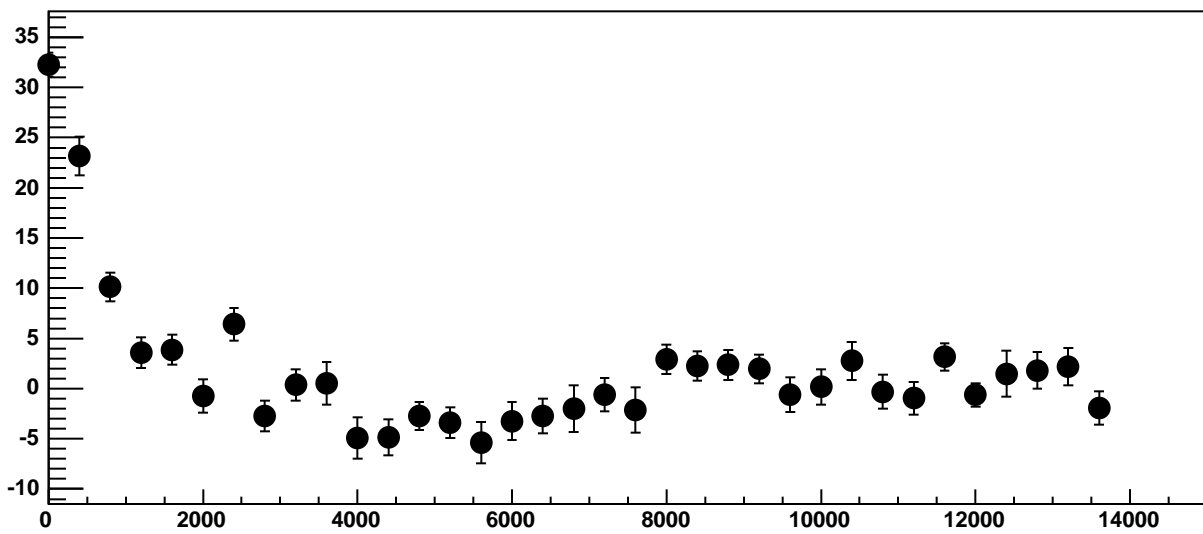


χ^2 / ndf 80.99 / 23
p0 274.7 ± 0.747
p1 0.03222 ± 0.0001128

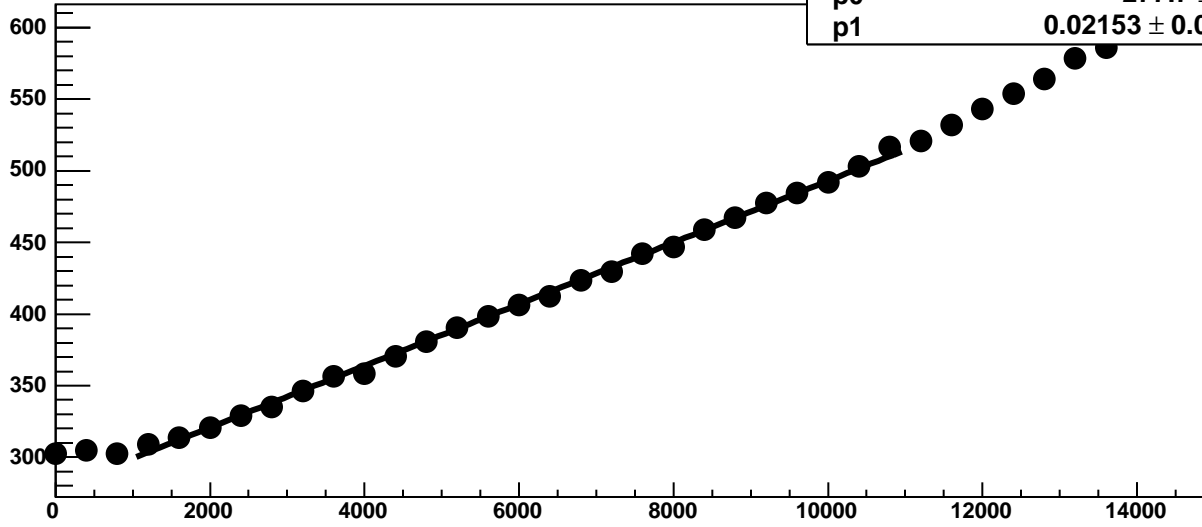
Chip 4, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



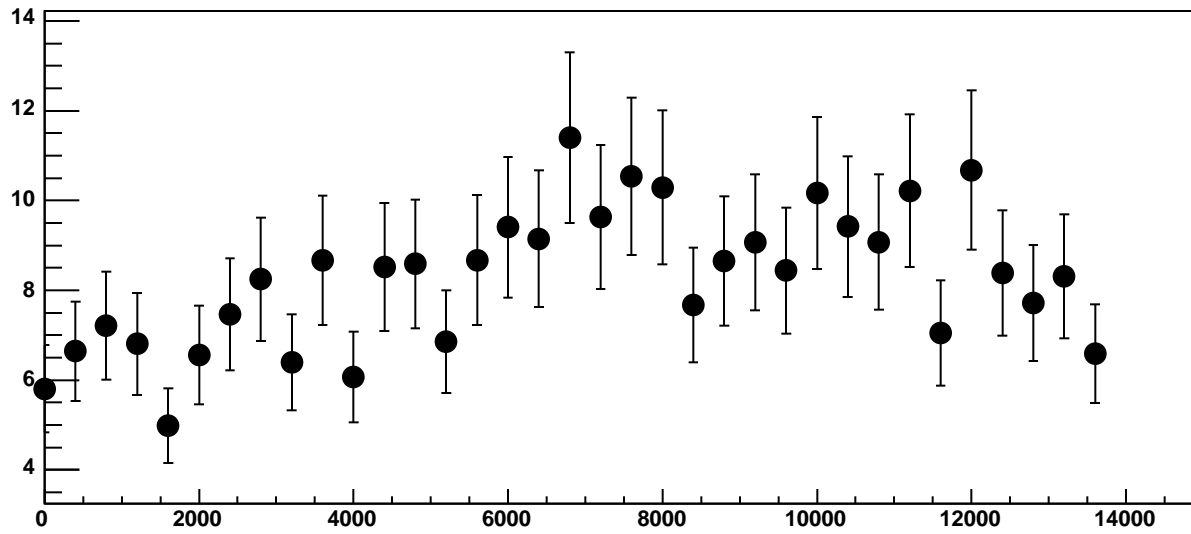
Chip 4, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC



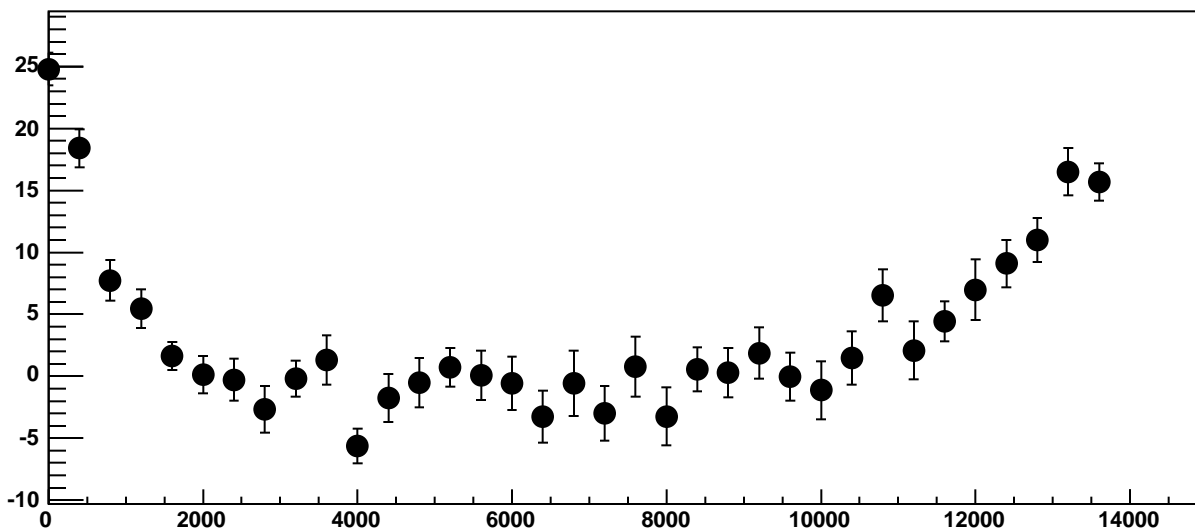
Chip 4, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



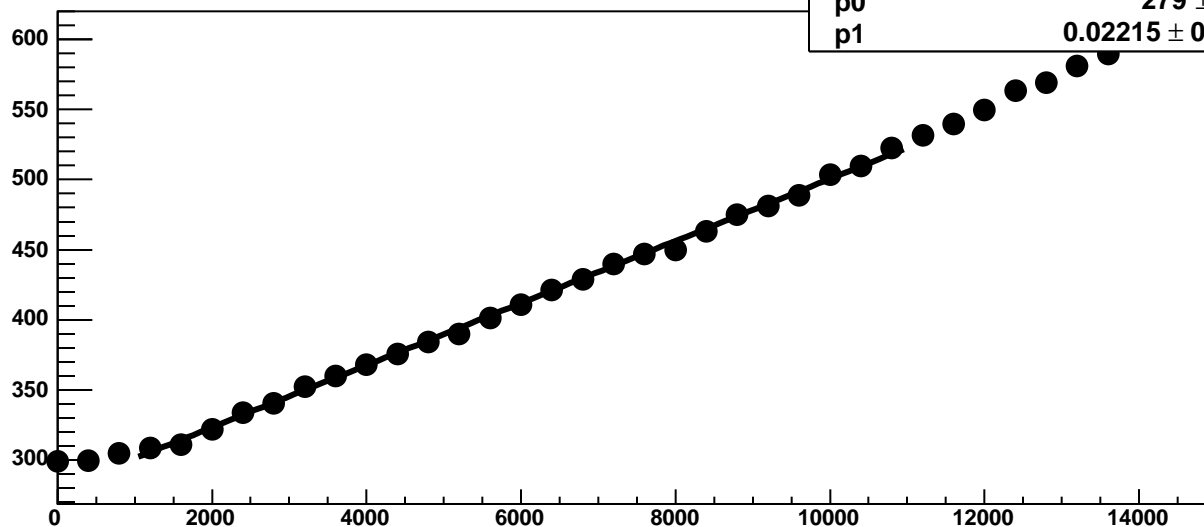
Chip 4, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

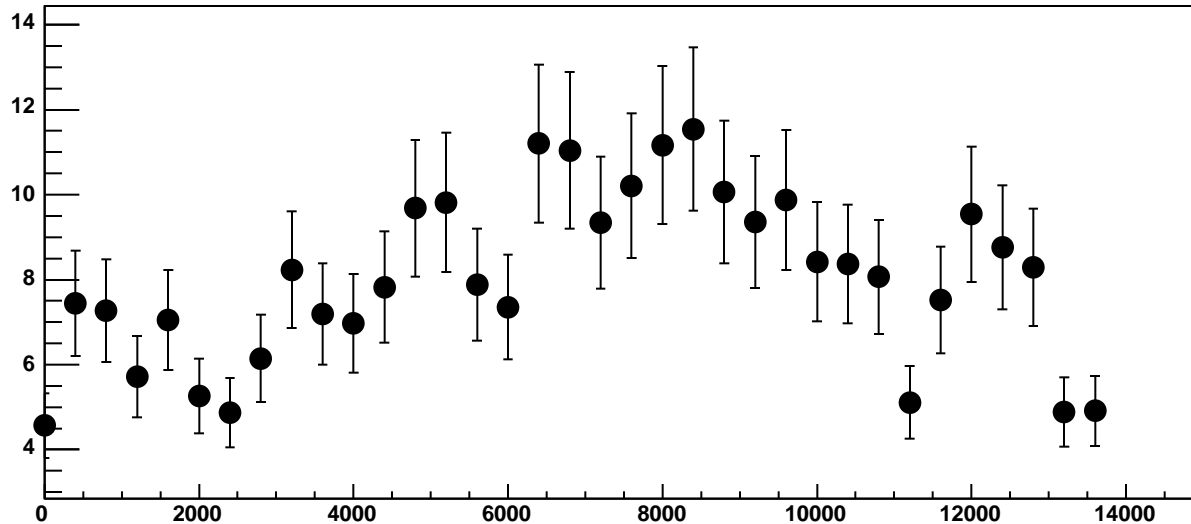


Chip 4, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

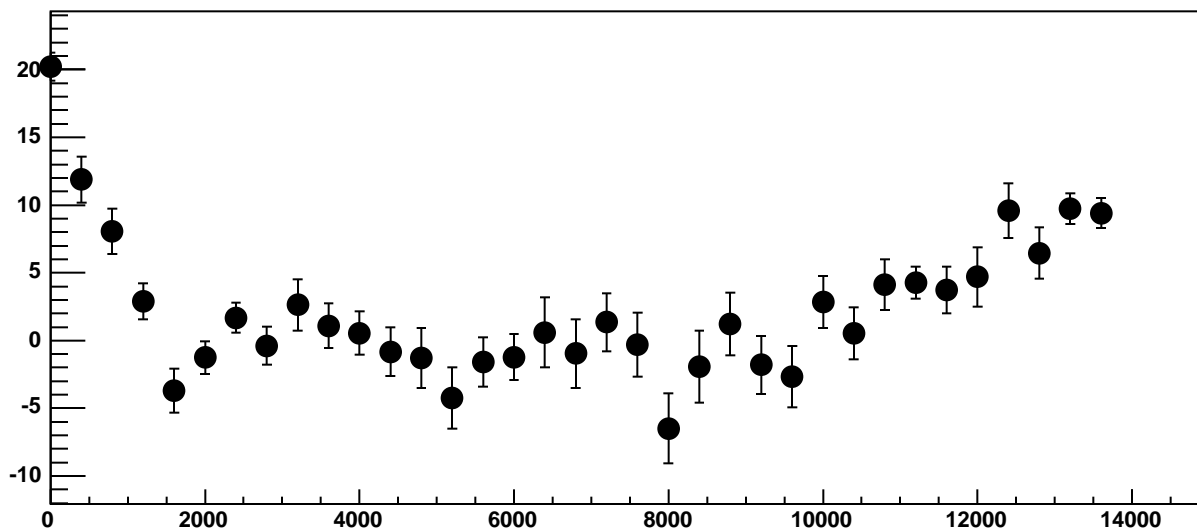


χ^2 / ndf 38.52 / 23
p0 279 ± 0.6892
p1 0.02215 ± 0.000119

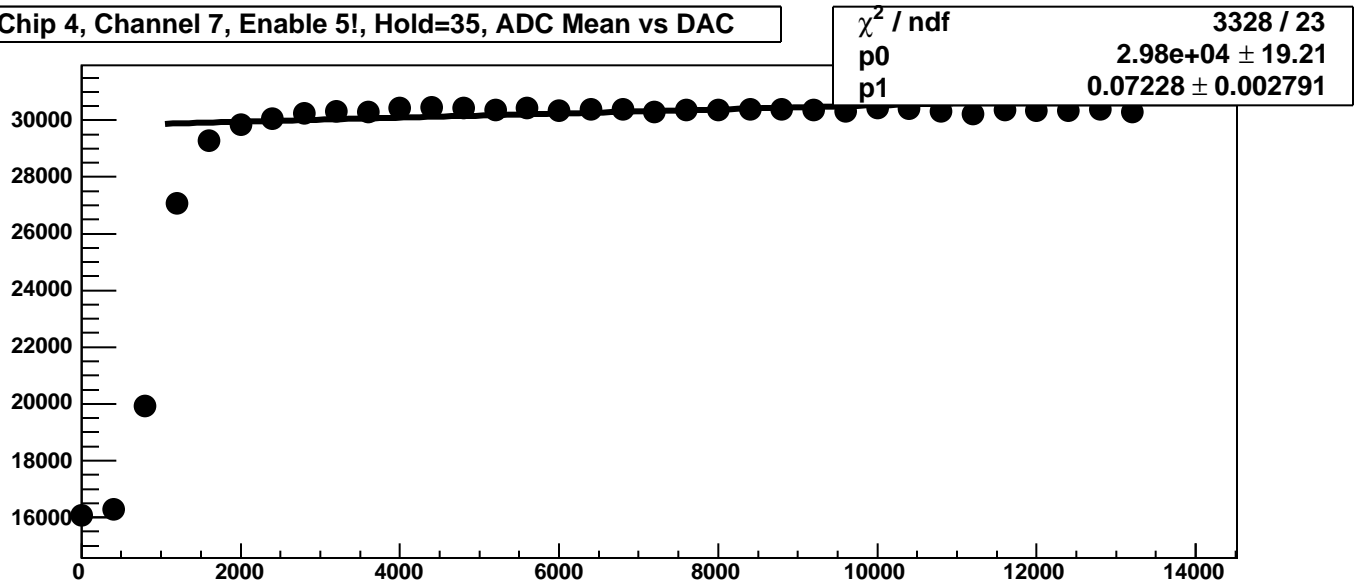
Chip 4, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



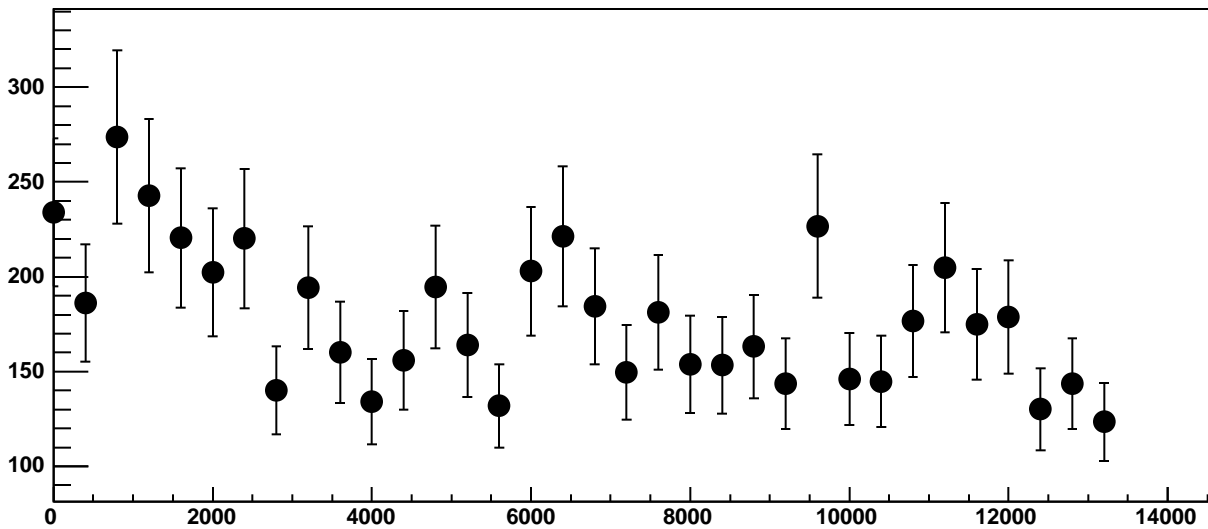
Chip 4, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



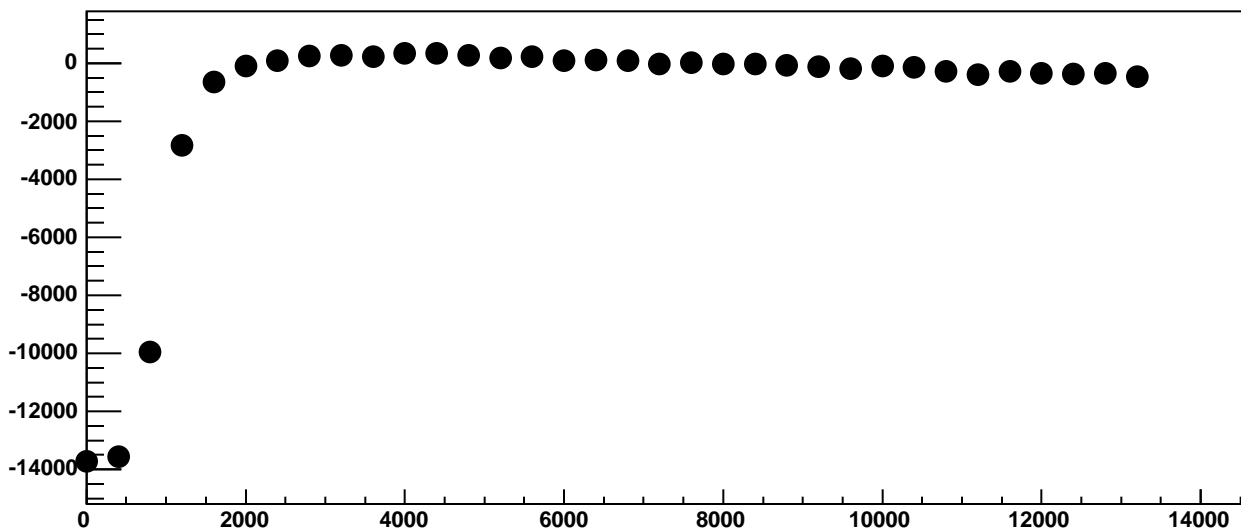
Chip 4, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC



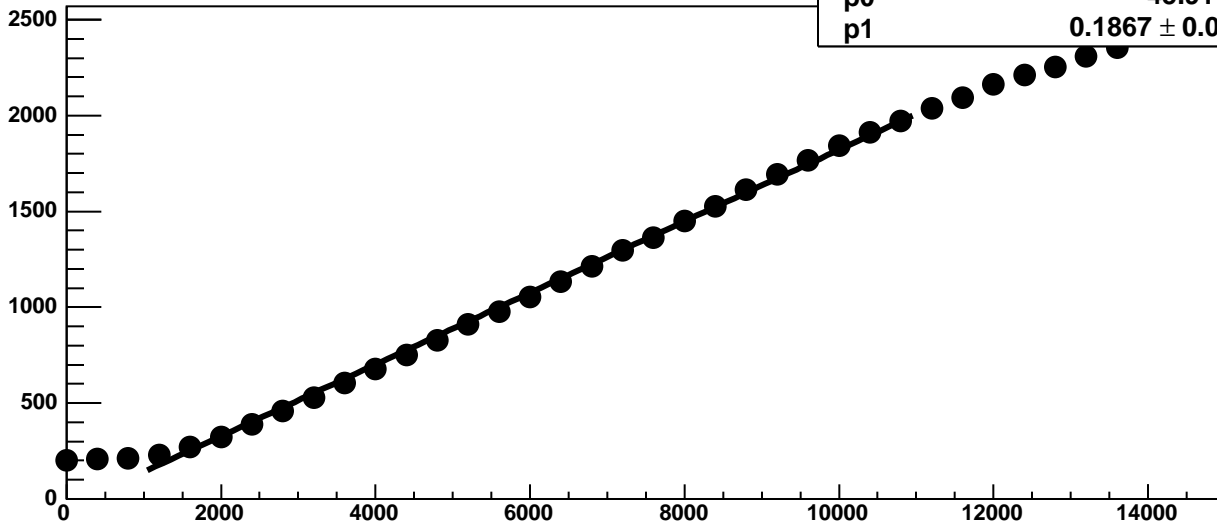
Chip 4, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC

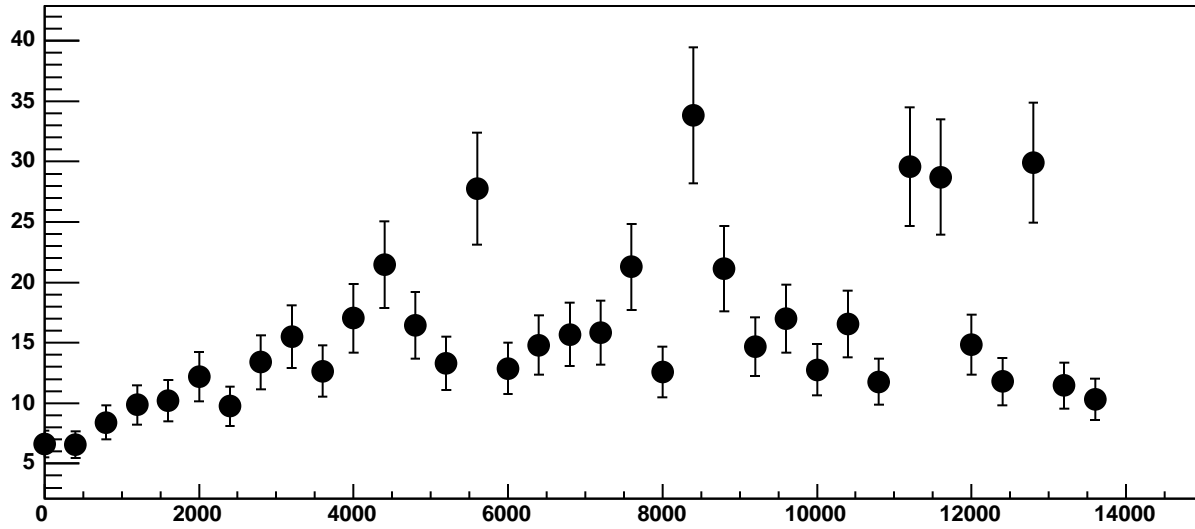


Chip 4, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

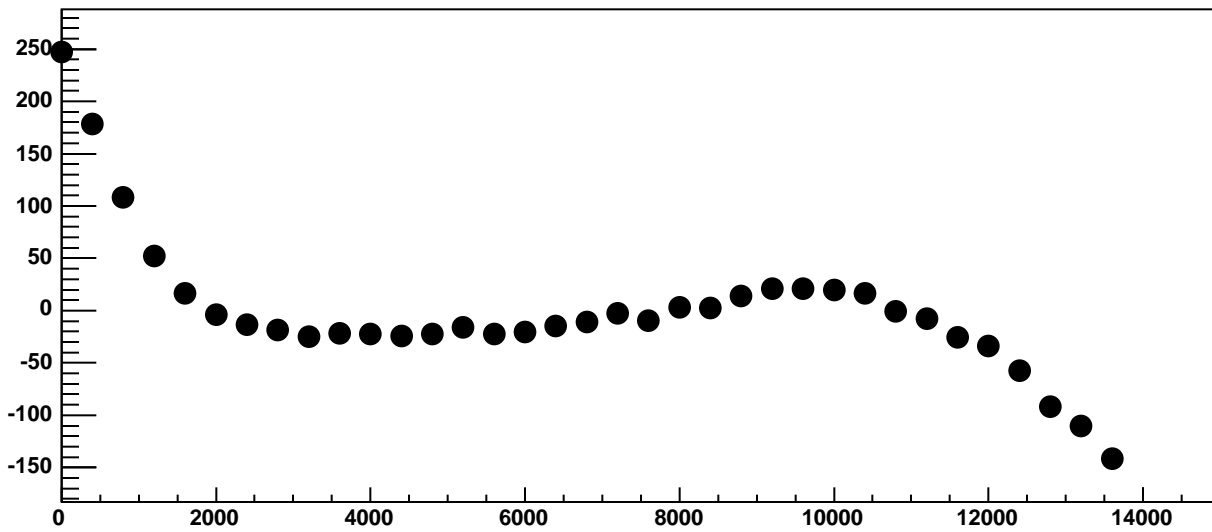


χ^2 / ndf 1118 / 23
p0 -45.91 ± 1.268
p1 0.1867 ± 0.0002039

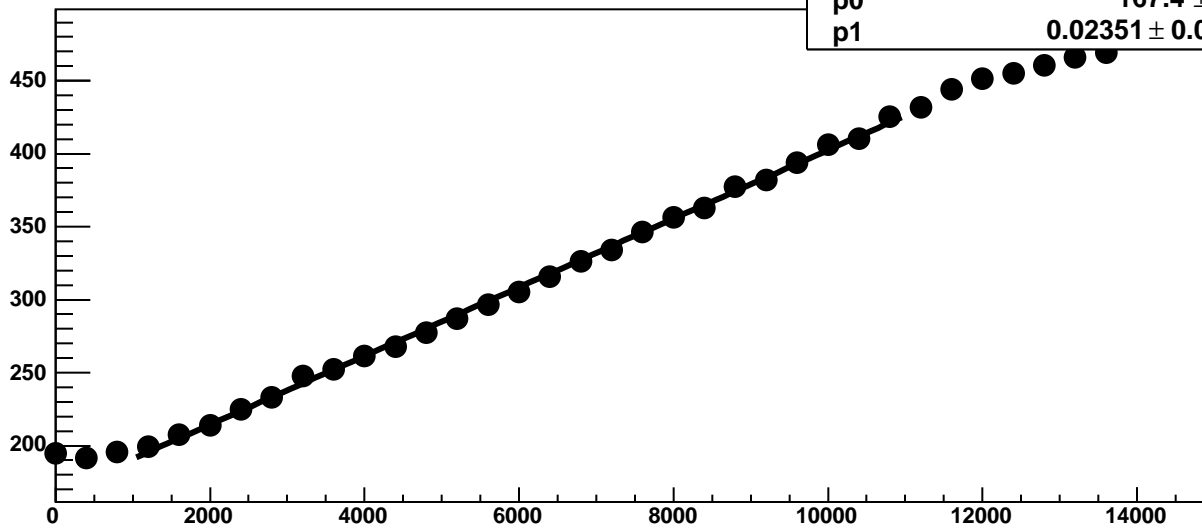
Chip 4, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

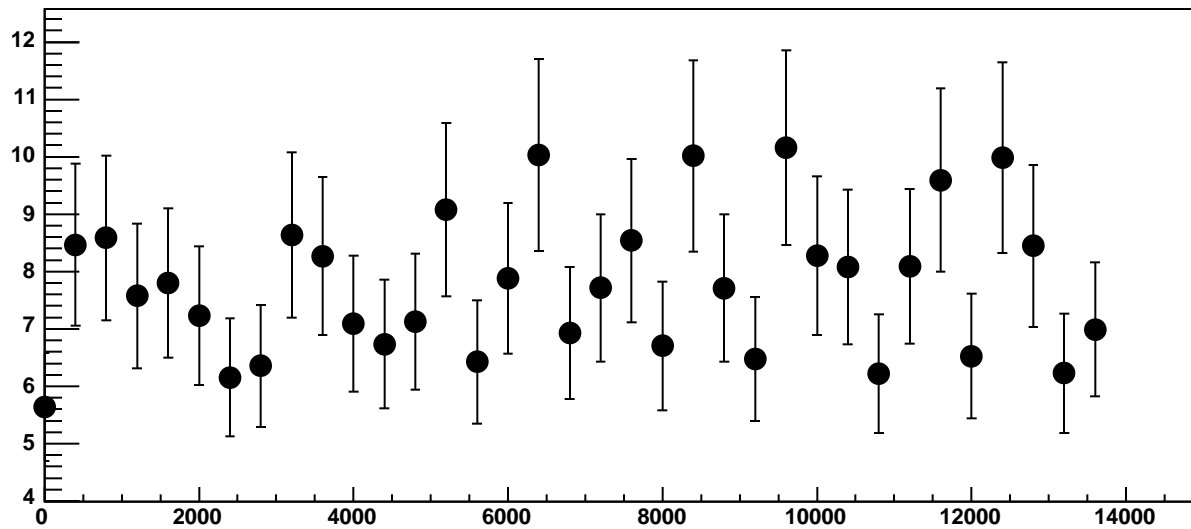


Chip 4, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

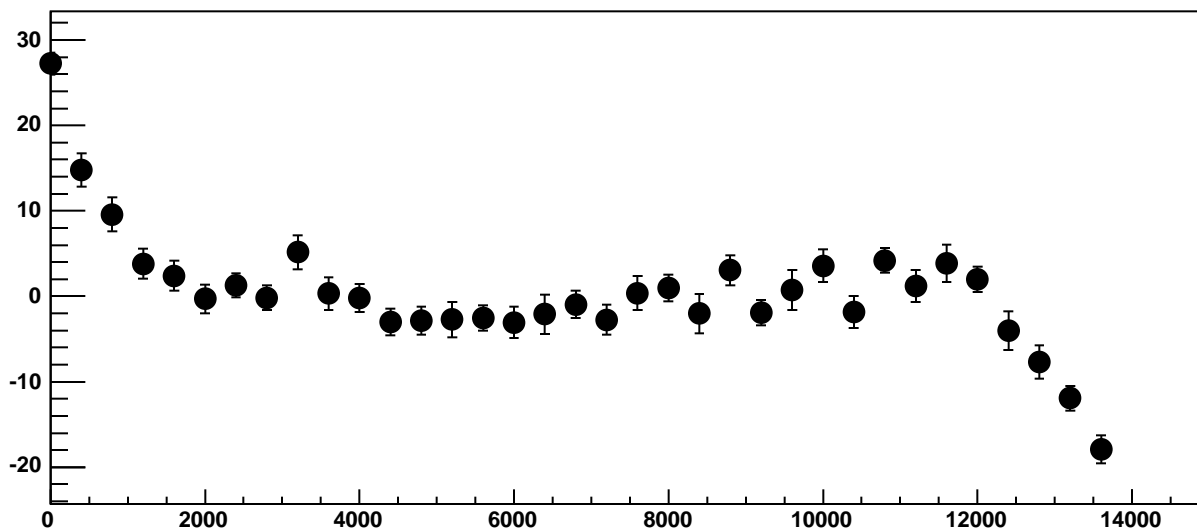


χ^2 / ndf 51.43 / 23
p0 167.4 ± 0.7694
p1 0.02351 ± 0.0001173

Chip 4, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC

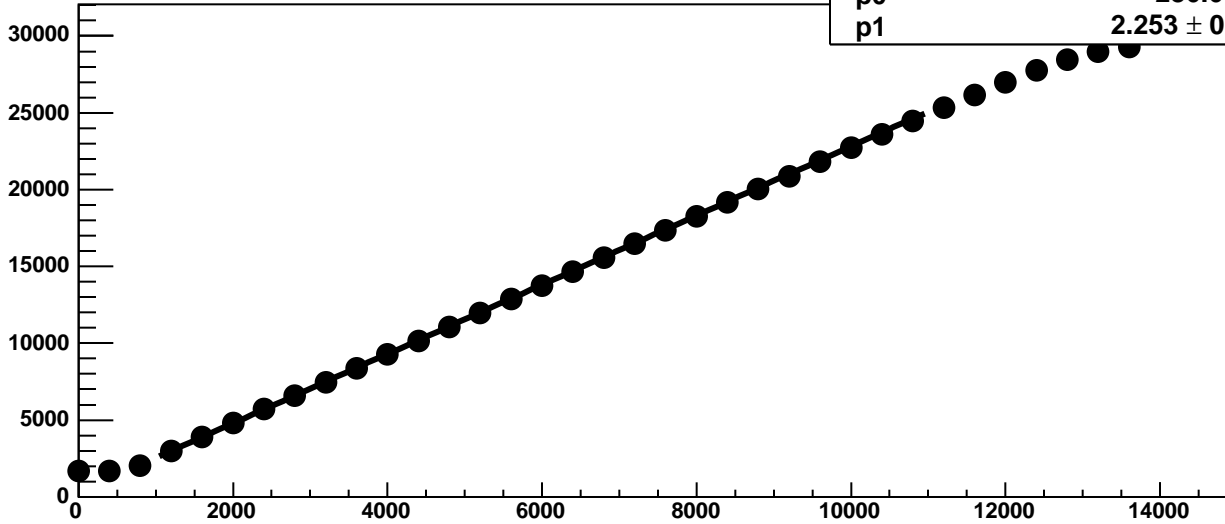


Chip 4, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

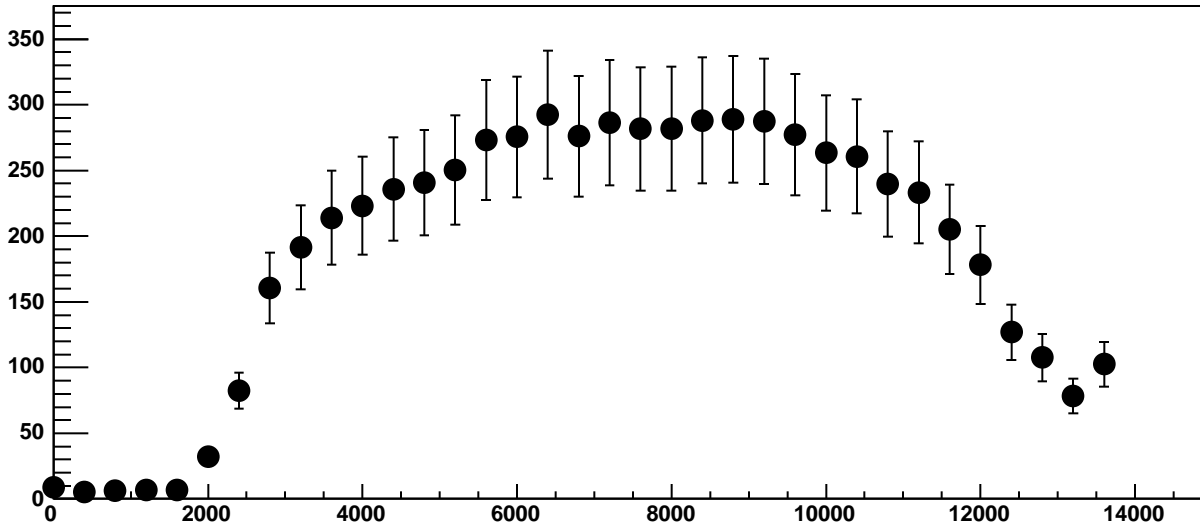


Chip 4, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC

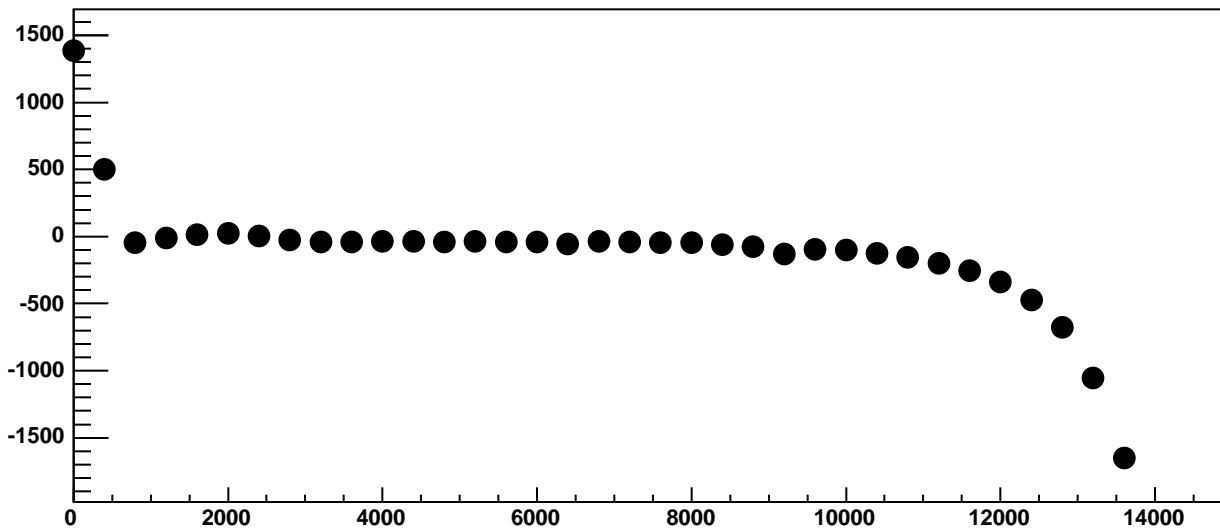
χ^2 / ndf 178.9 / 23
p0 286.6 ± 3.185
p1 2.253 ± 0.002064



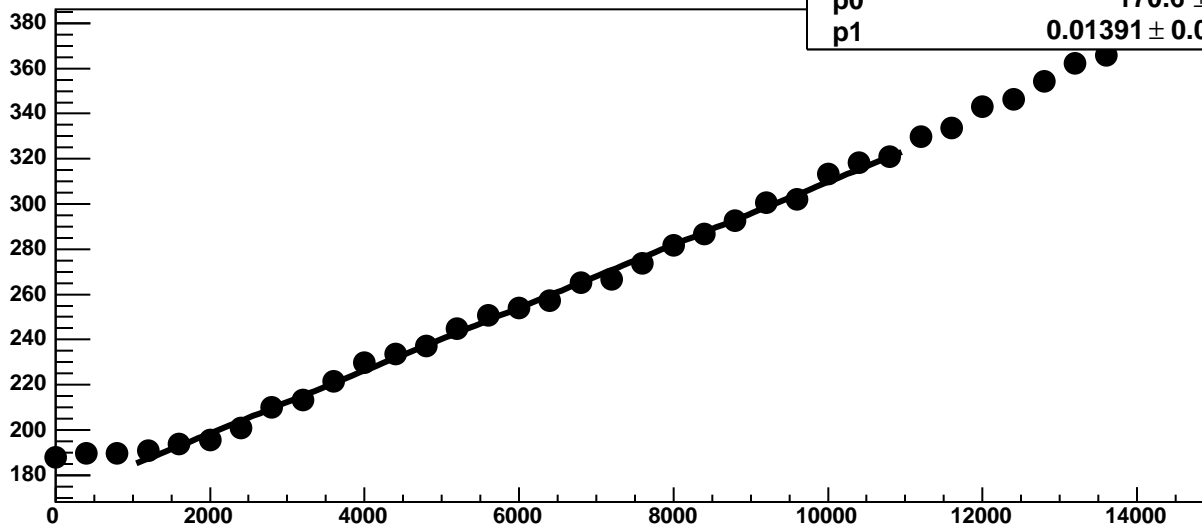
Chip 4, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

49.34 / 23

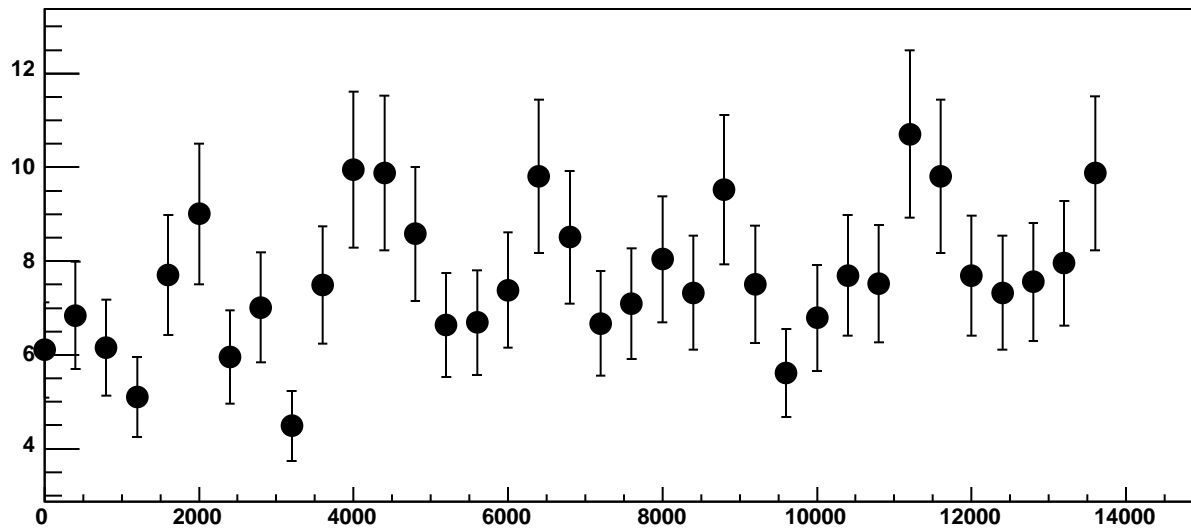
p0

170.6 ± 0.6952

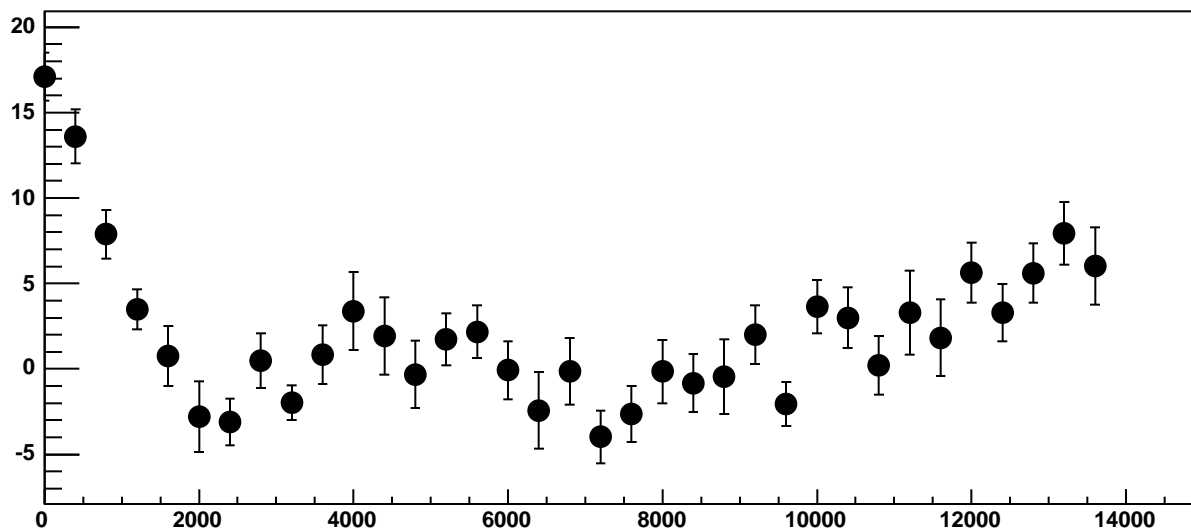
p1

0.01391 ± 0.0001074

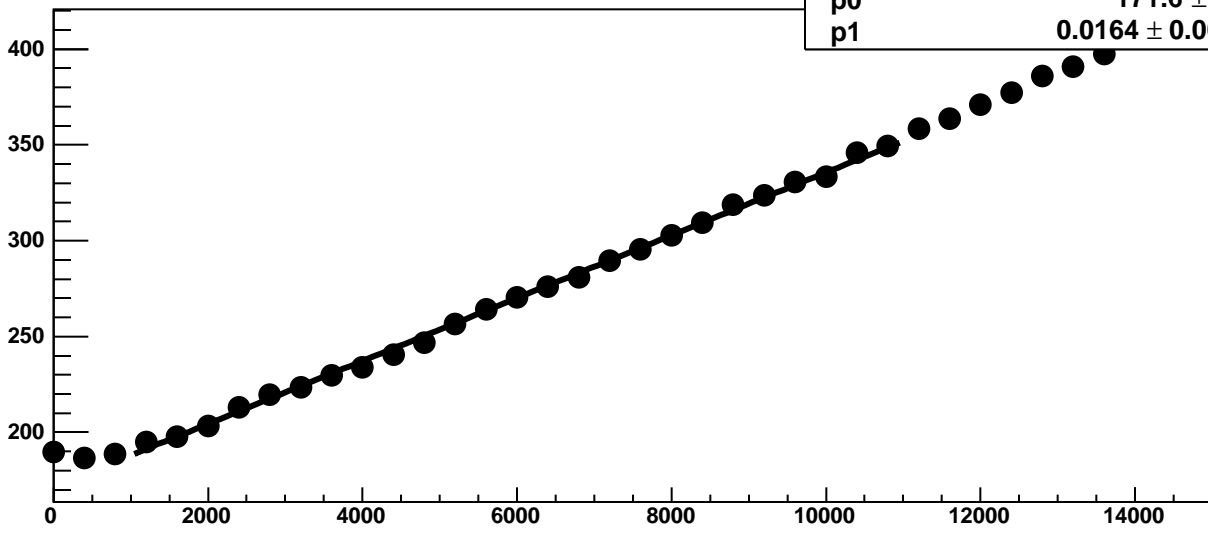
Chip 4, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

32.67 / 23

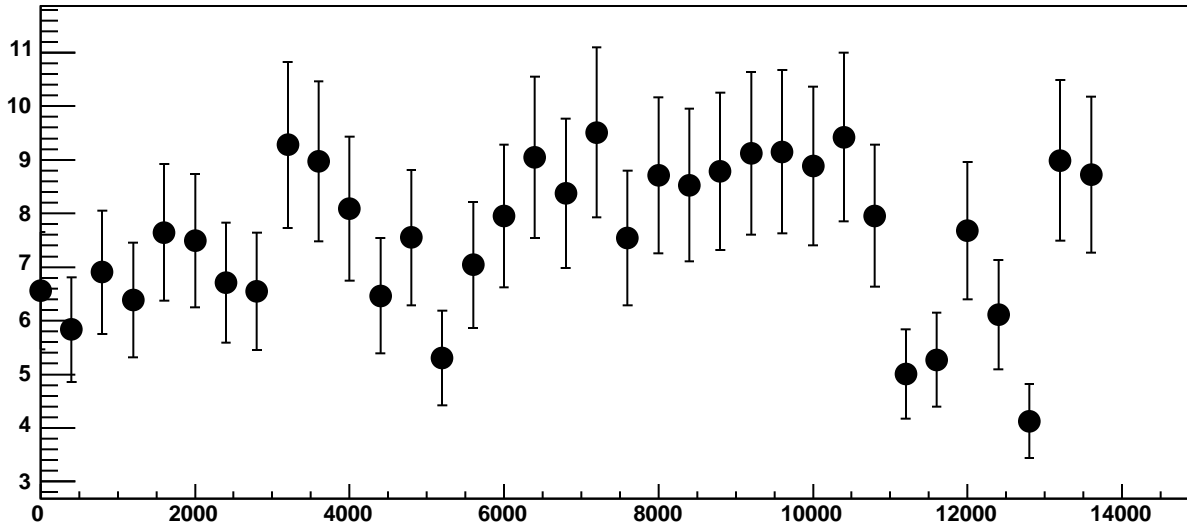
p0

171.6 ± 0.7816

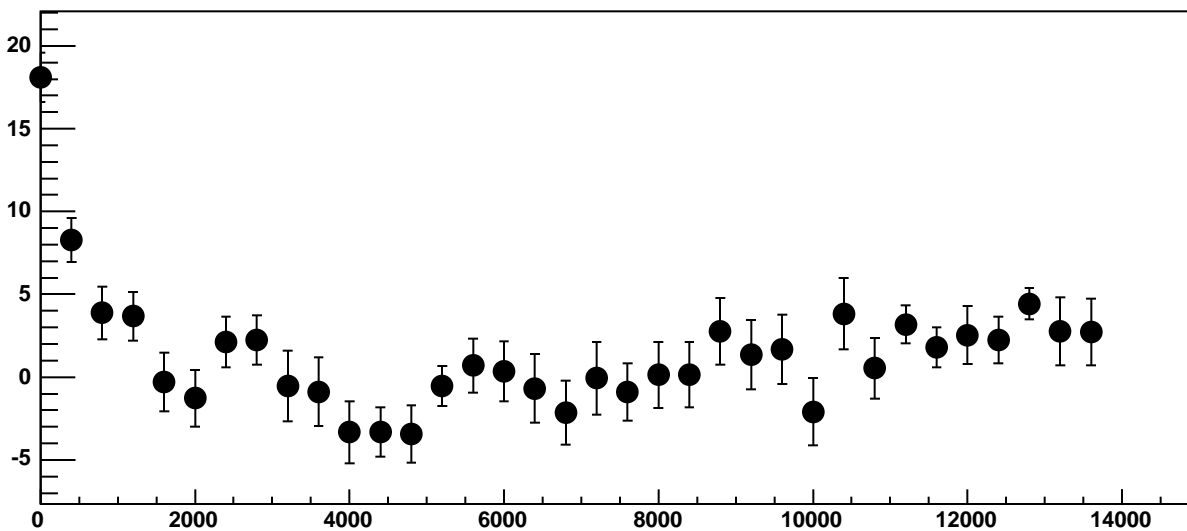
p1

0.0164 ± 0.0001259

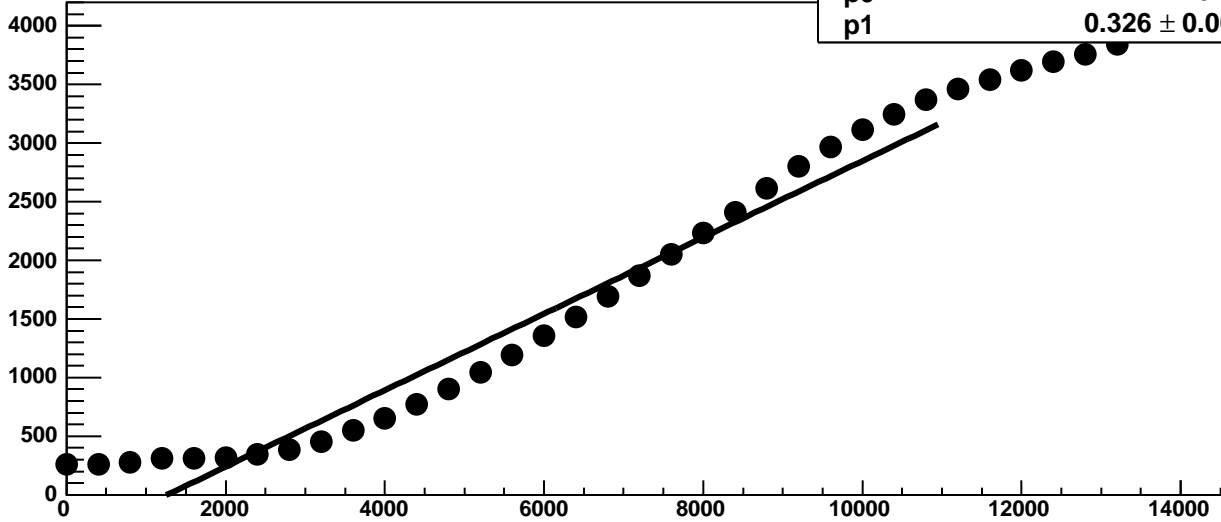
Chip 4, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



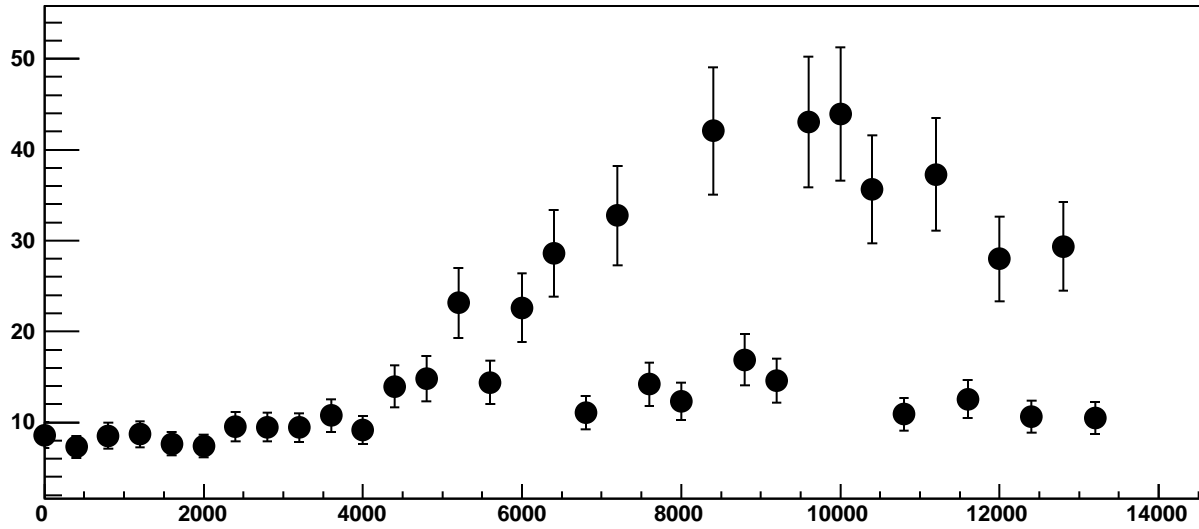
Chip 4, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



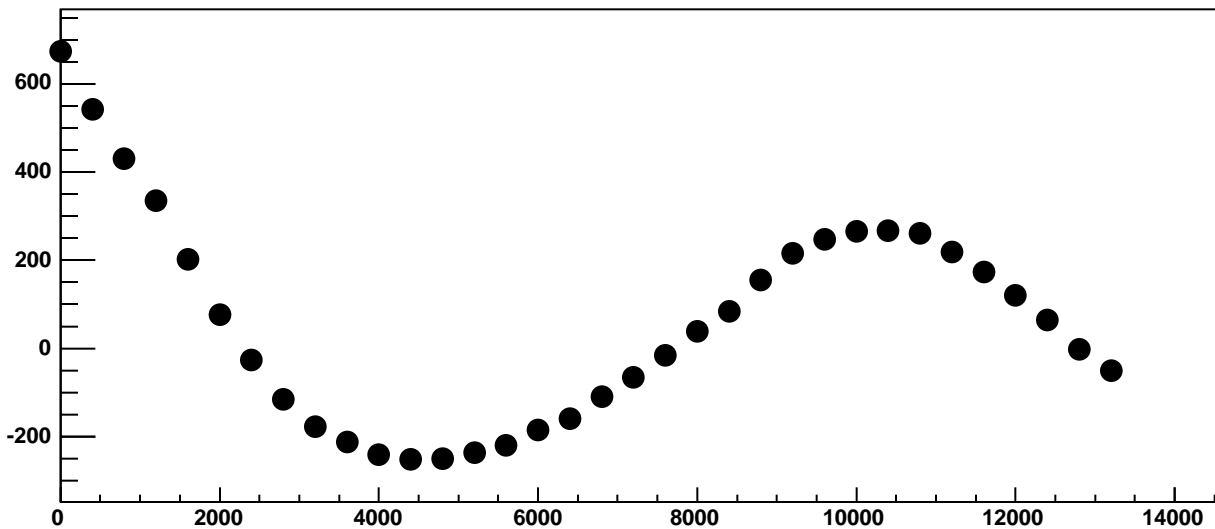
Chip 4, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC



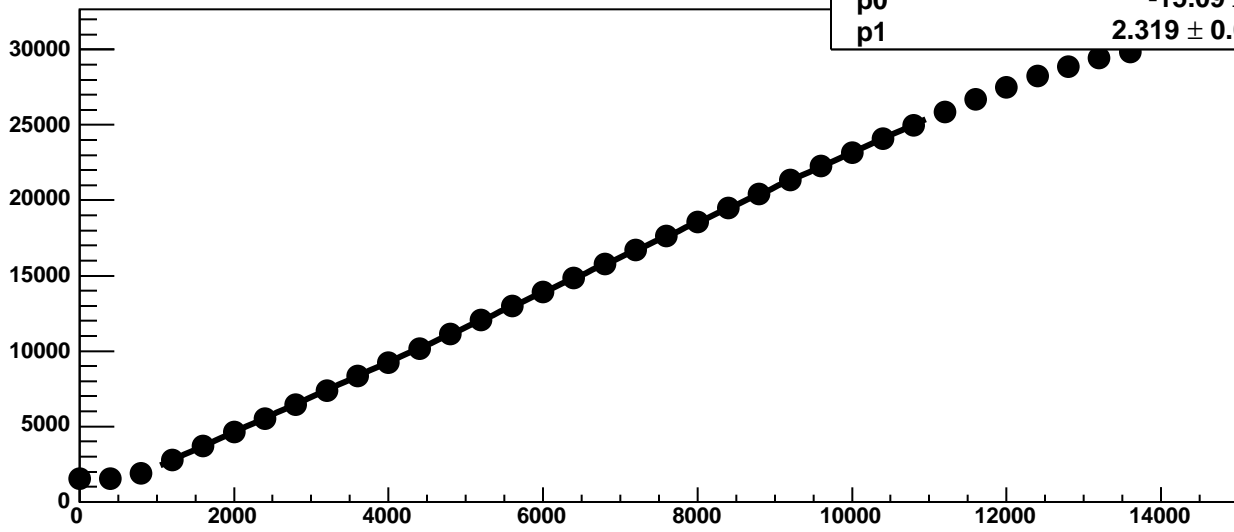
Chip 4, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



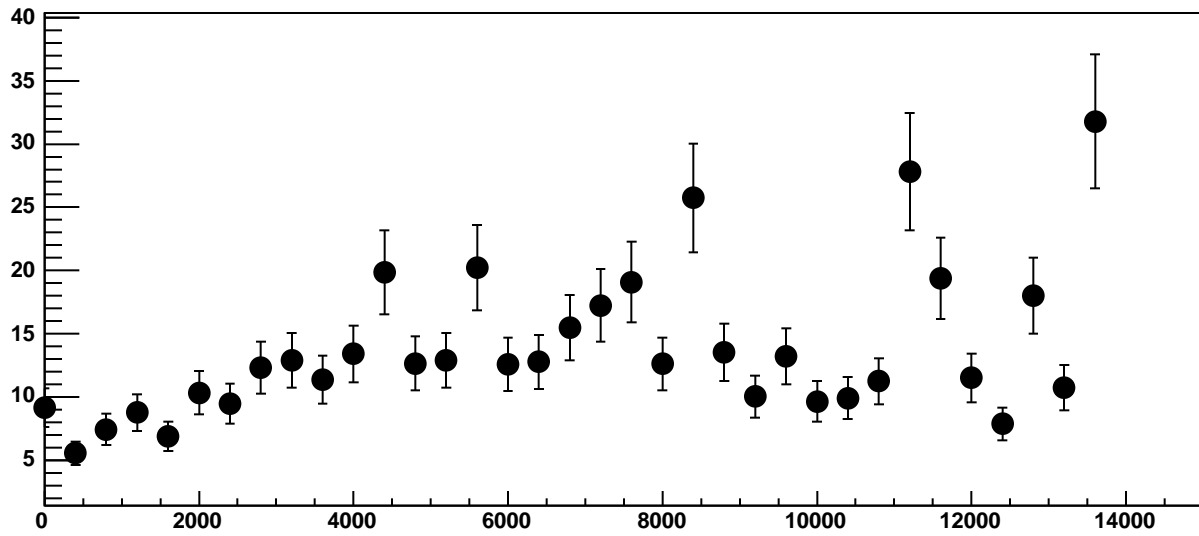
Chip 4, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



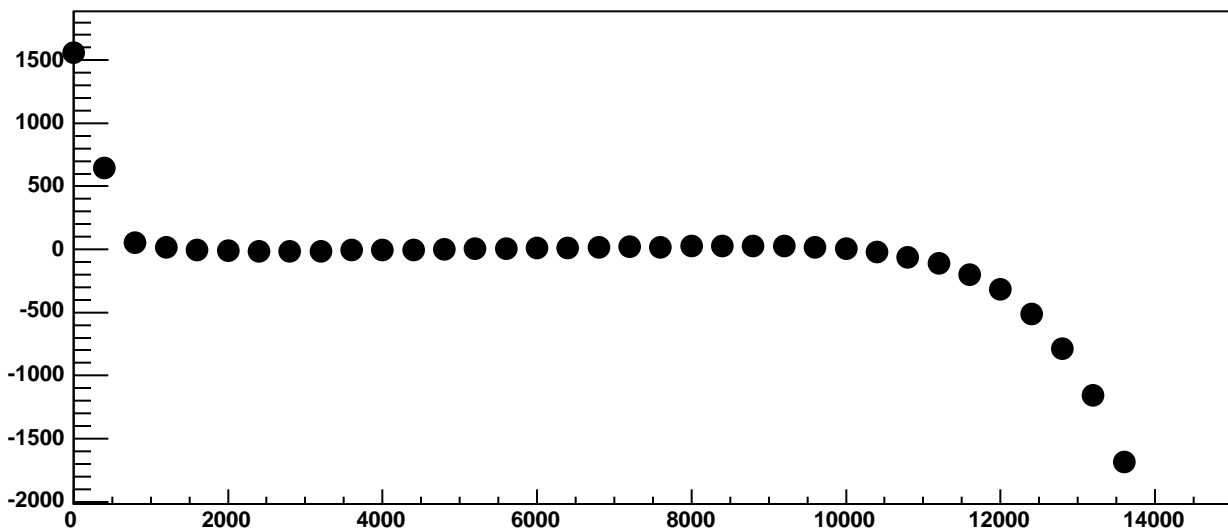
Chip 4, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



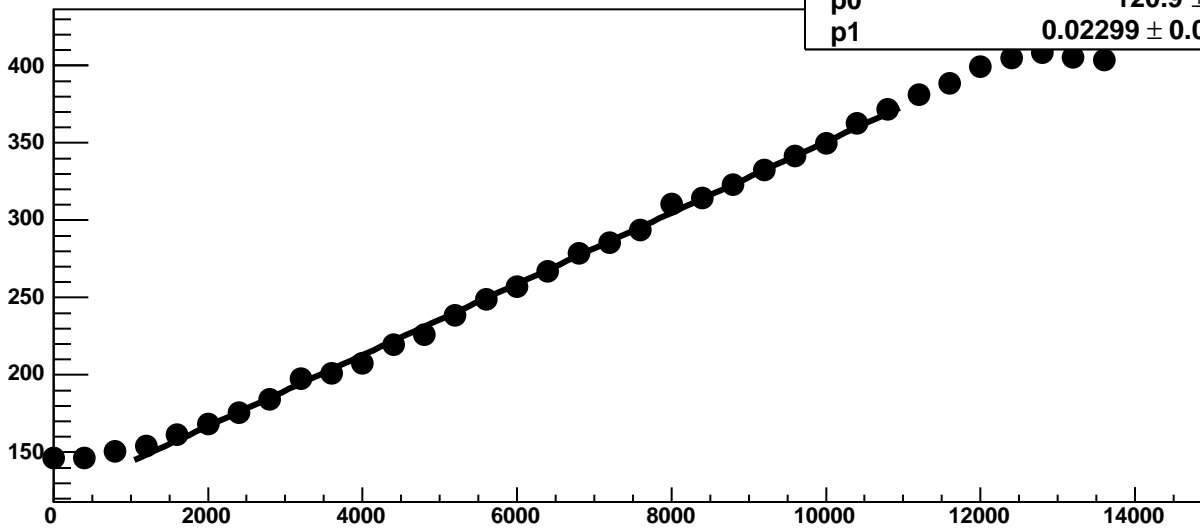
Chip 4, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



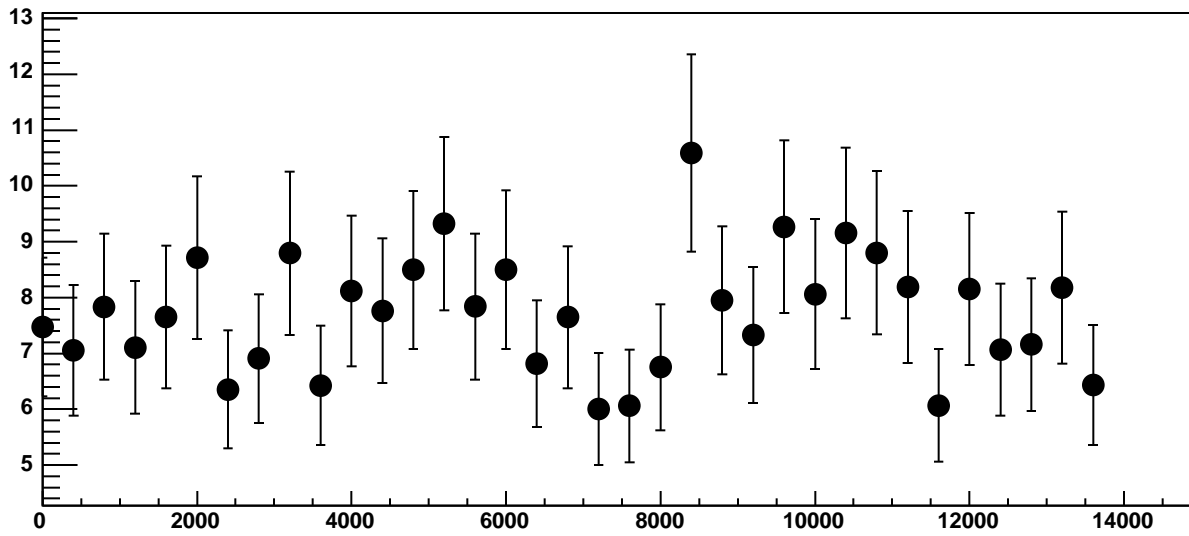
Chip 4, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC



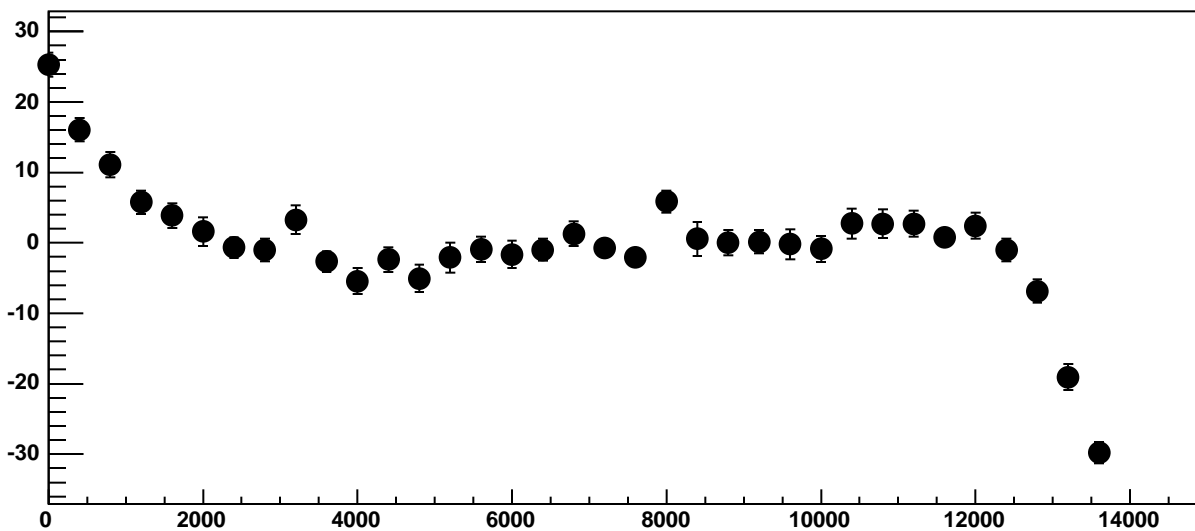
Chip 4, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



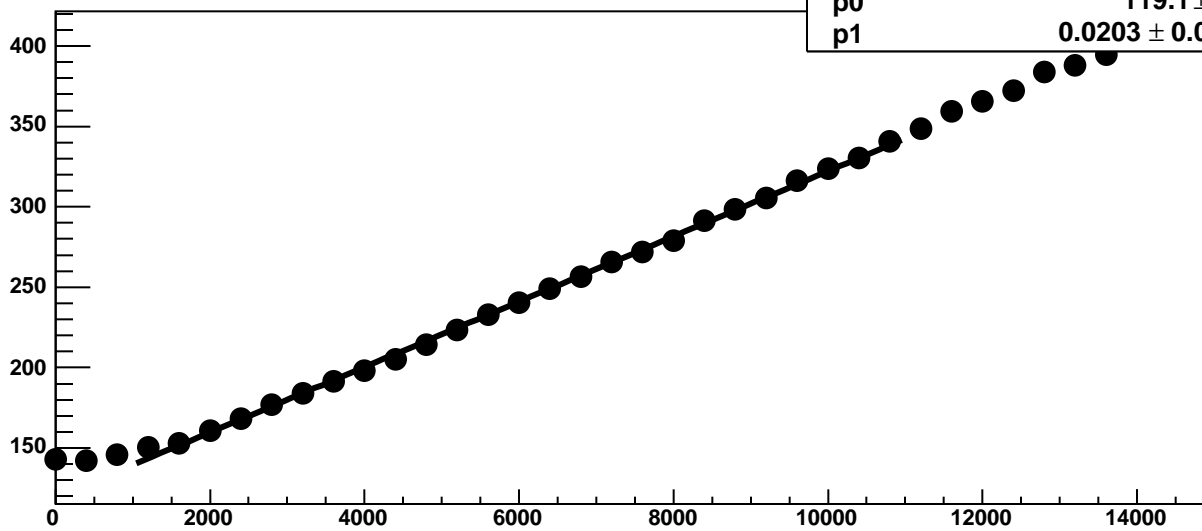
Chip 4, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

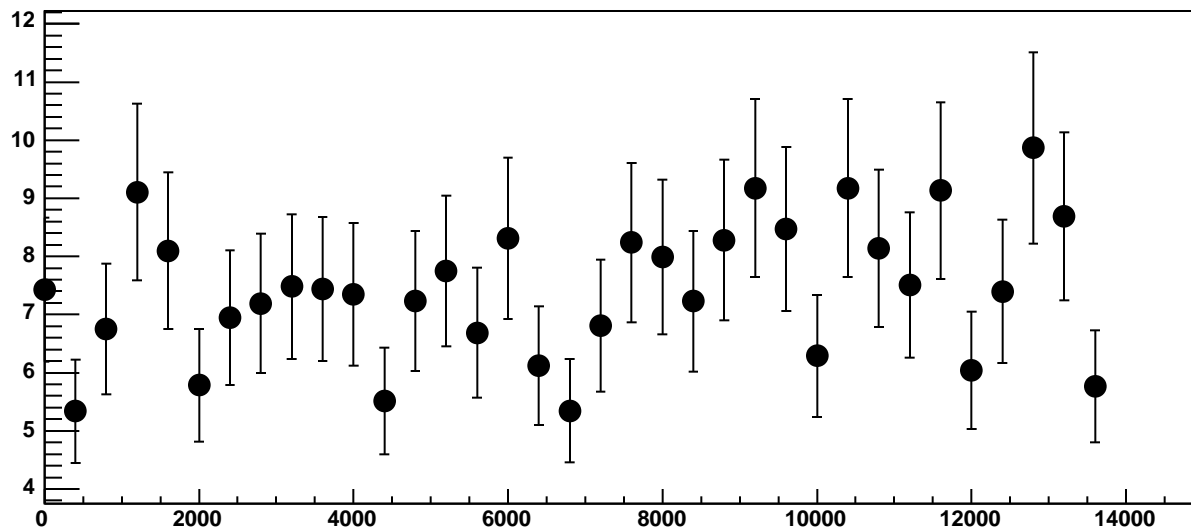


Chip 4, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

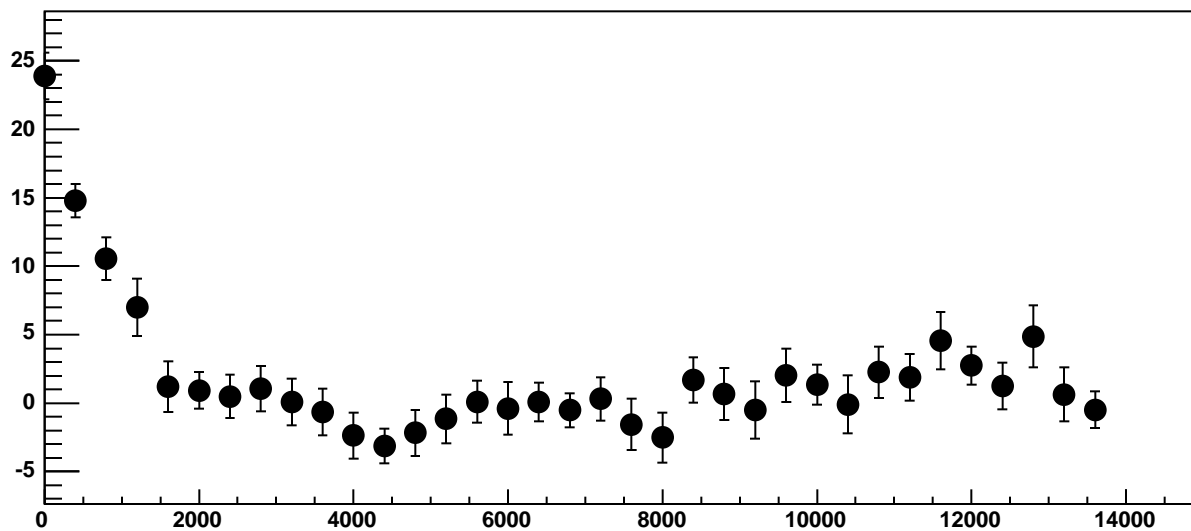


χ^2 / ndf 30.5 / 23
p0 119.1 ± 0.7757
p1 0.0203 ± 0.0001207

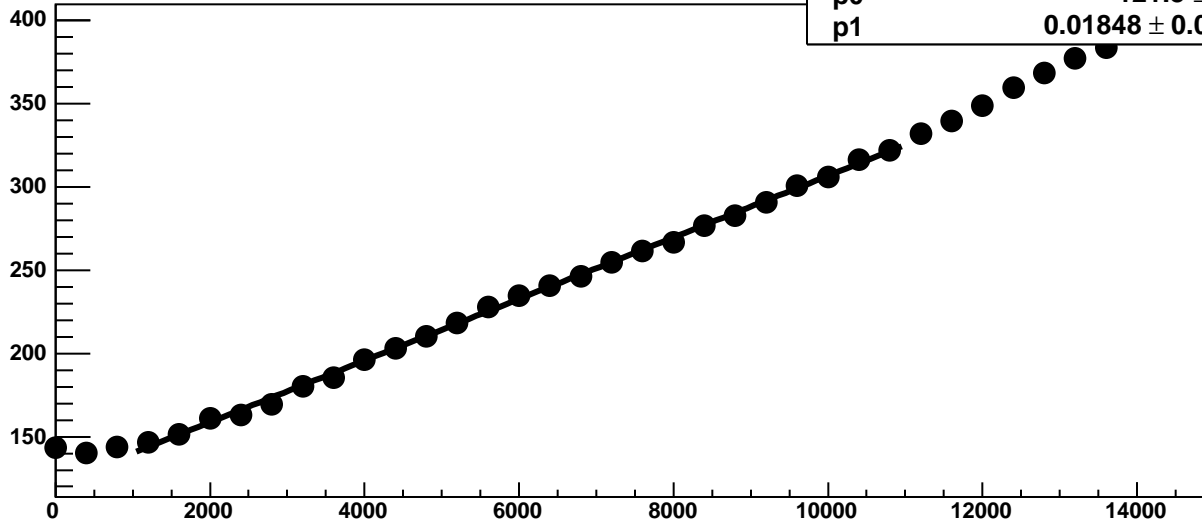
Chip 4, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

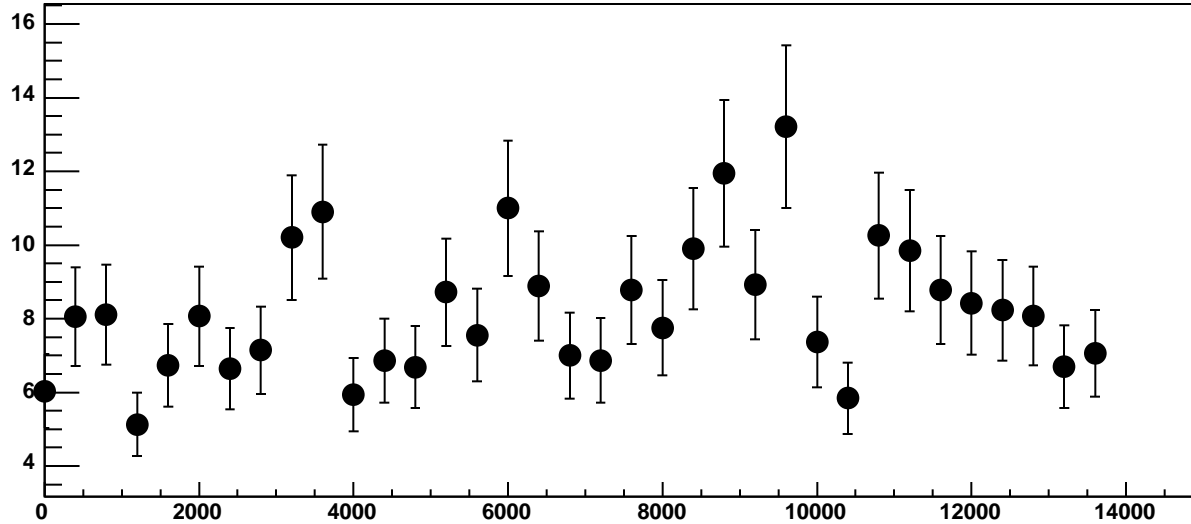


Chip 4, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

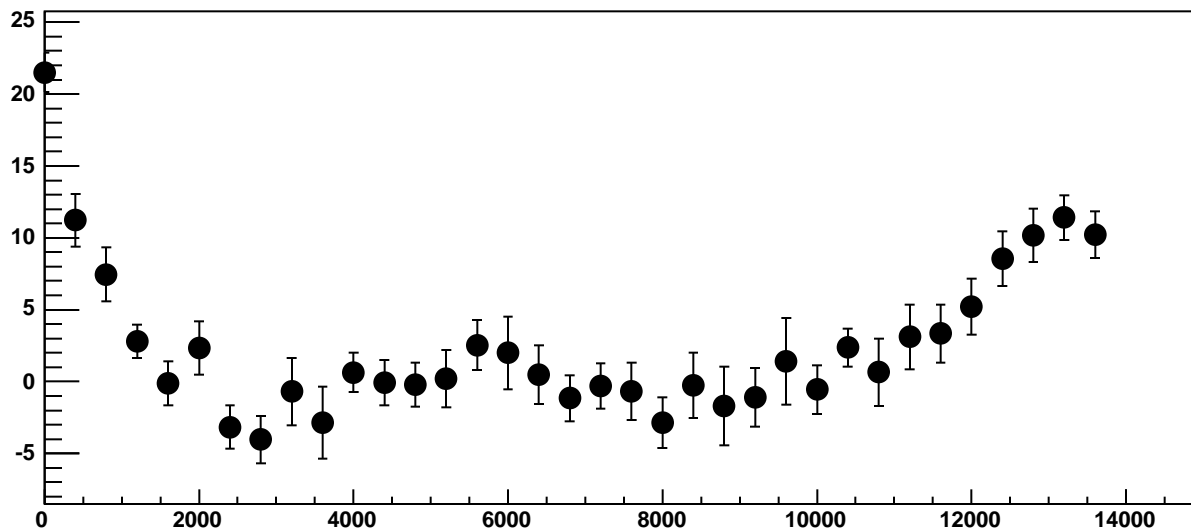


χ^2 / ndf 29.56 / 23
p0 121.8 ± 0.7355
p1 0.01848 ± 0.0001175

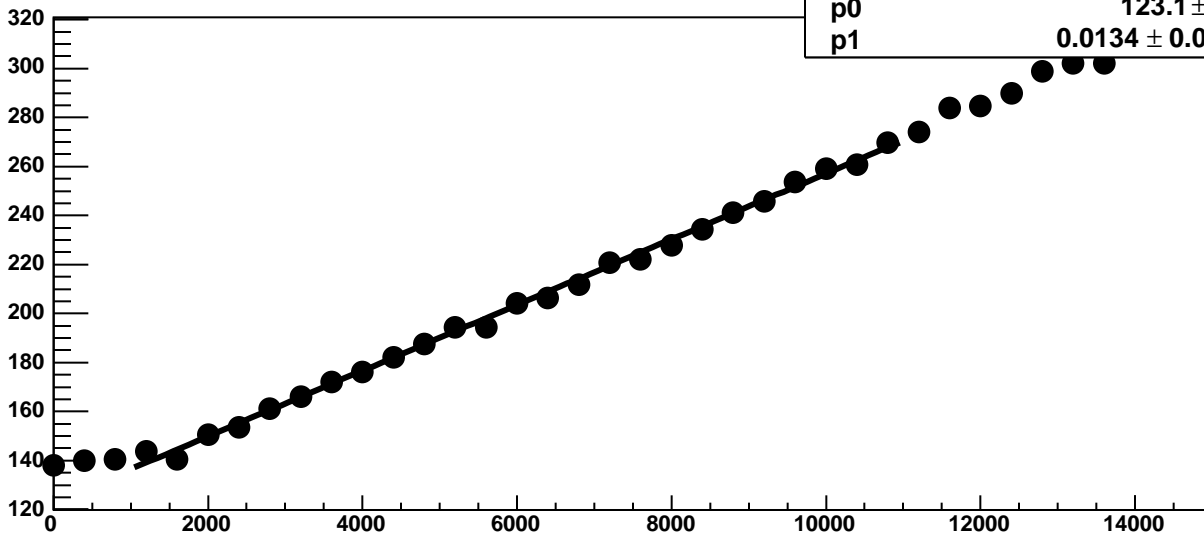
Chip 4, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

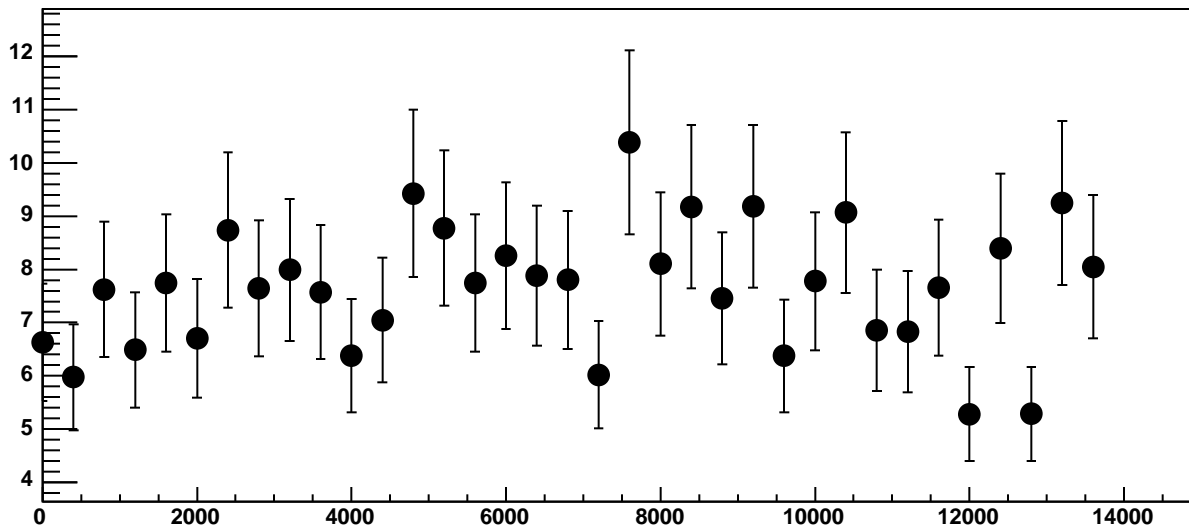


Chip 4, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

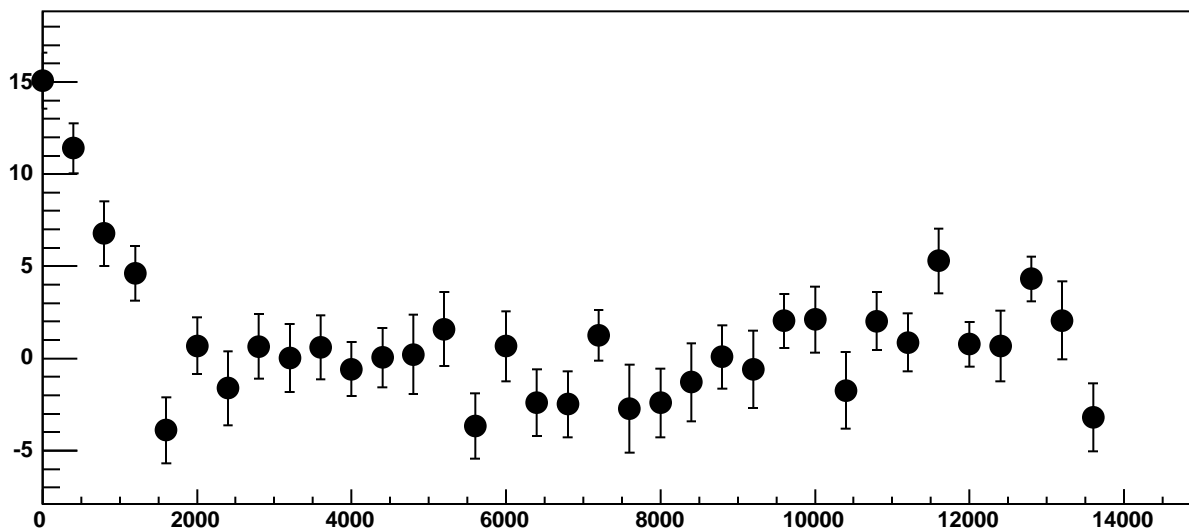


χ^2 / ndf 34.37 / 23
p0 123.1 ± 0.7807
p1 0.0134 ± 0.0001186

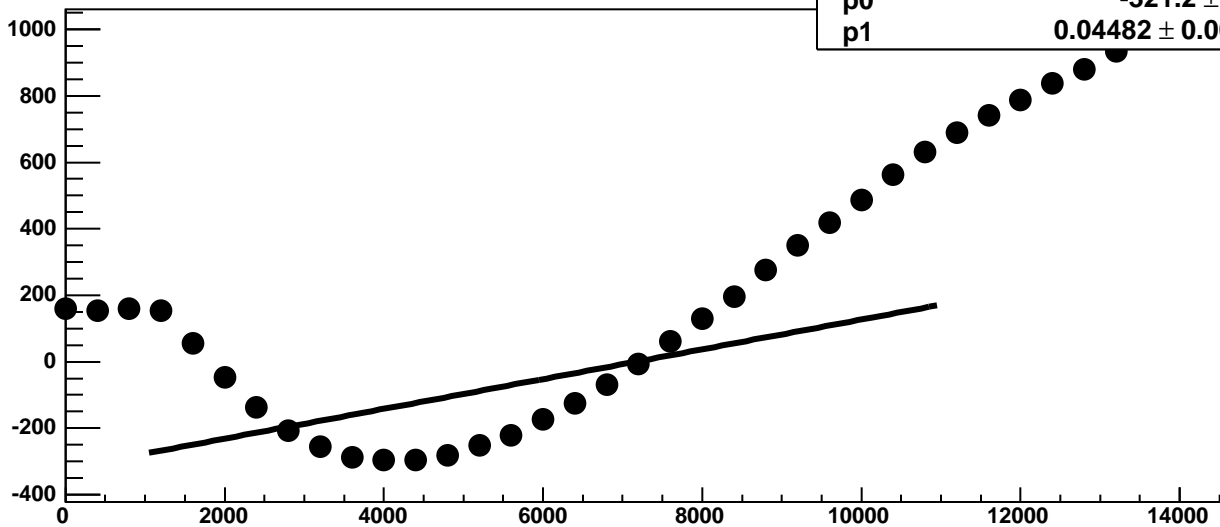
Chip 4, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



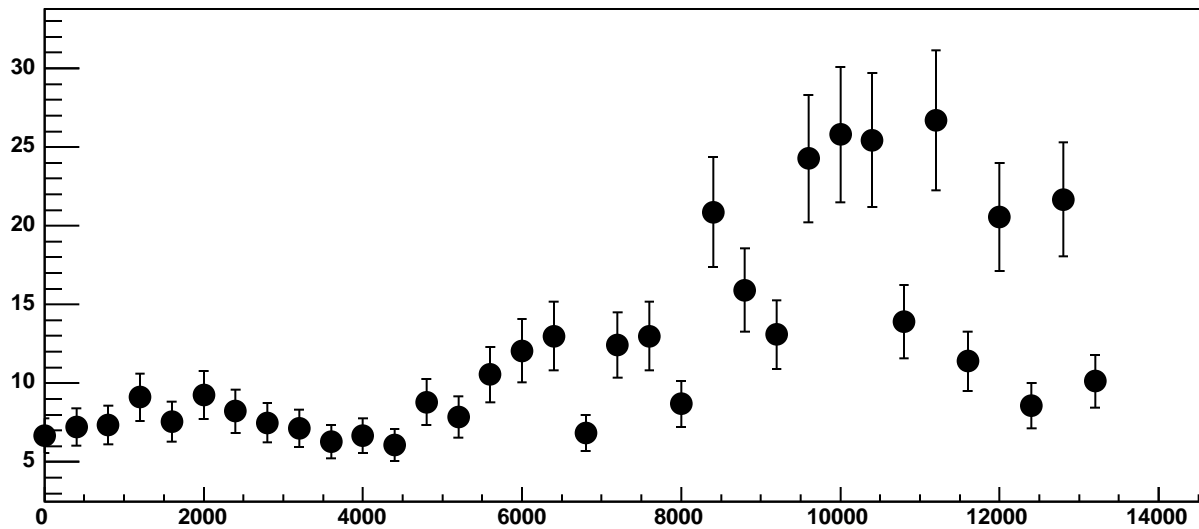
Chip 4, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



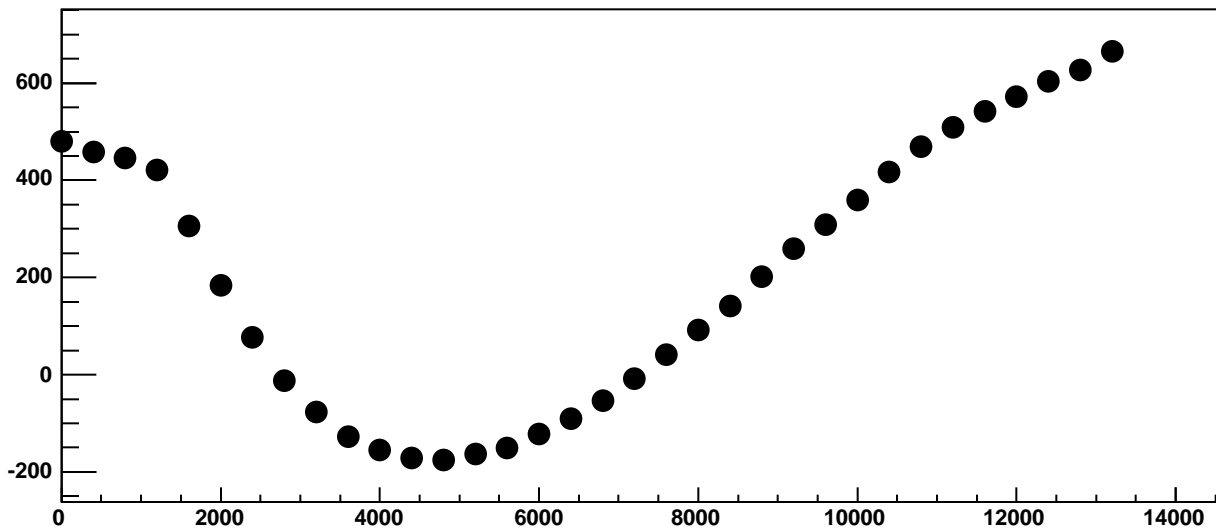
Chip 4, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



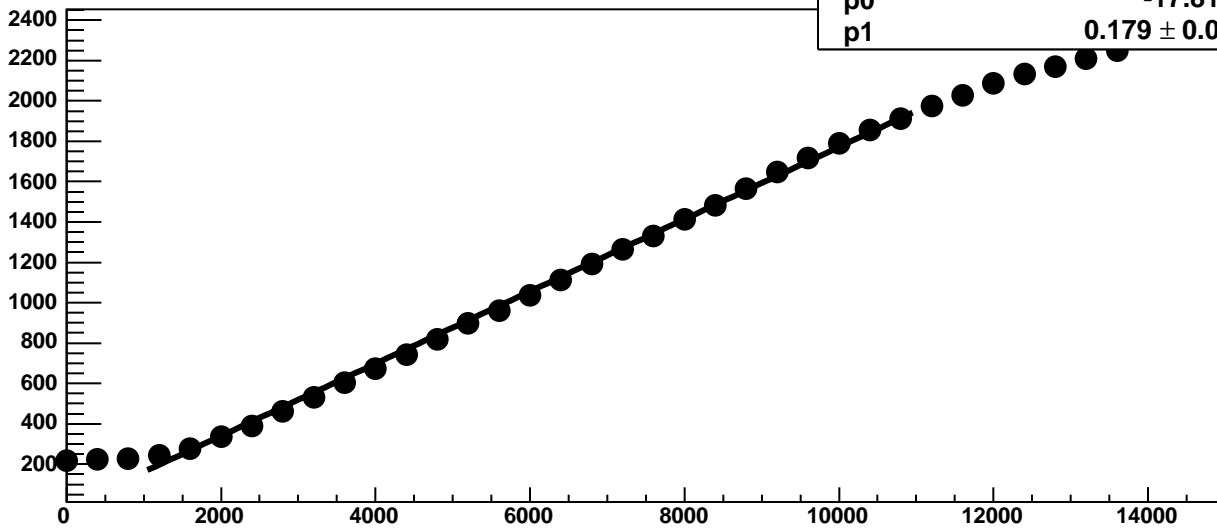
Chip 4, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



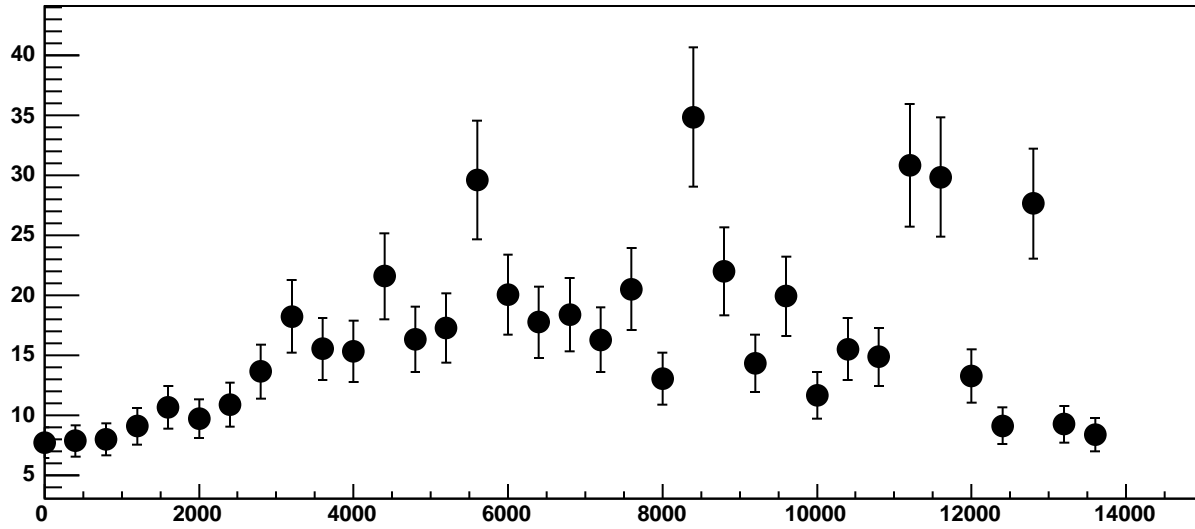
Chip 4, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



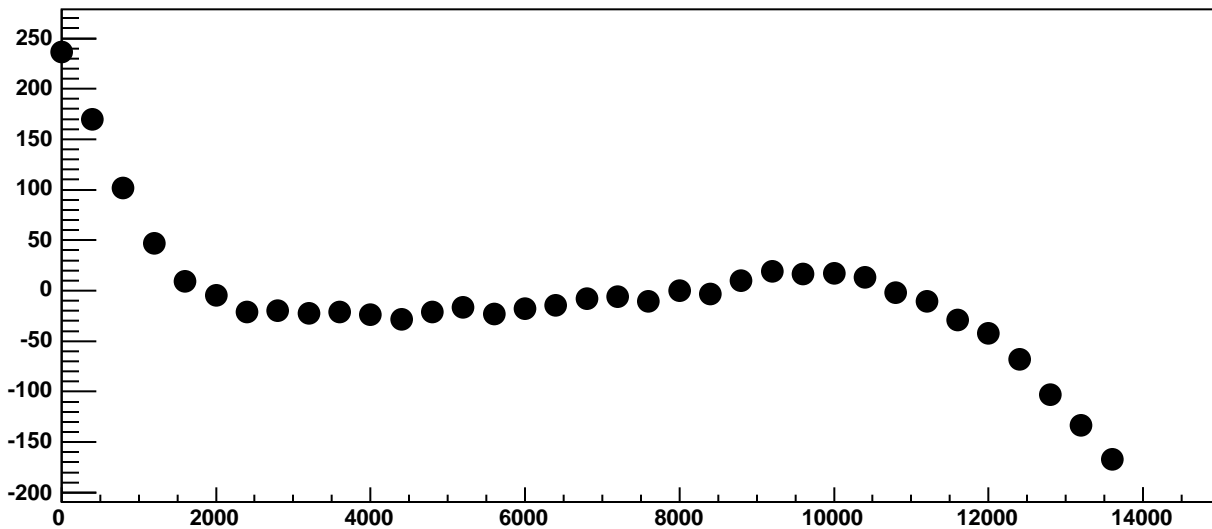
Chip 4, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC



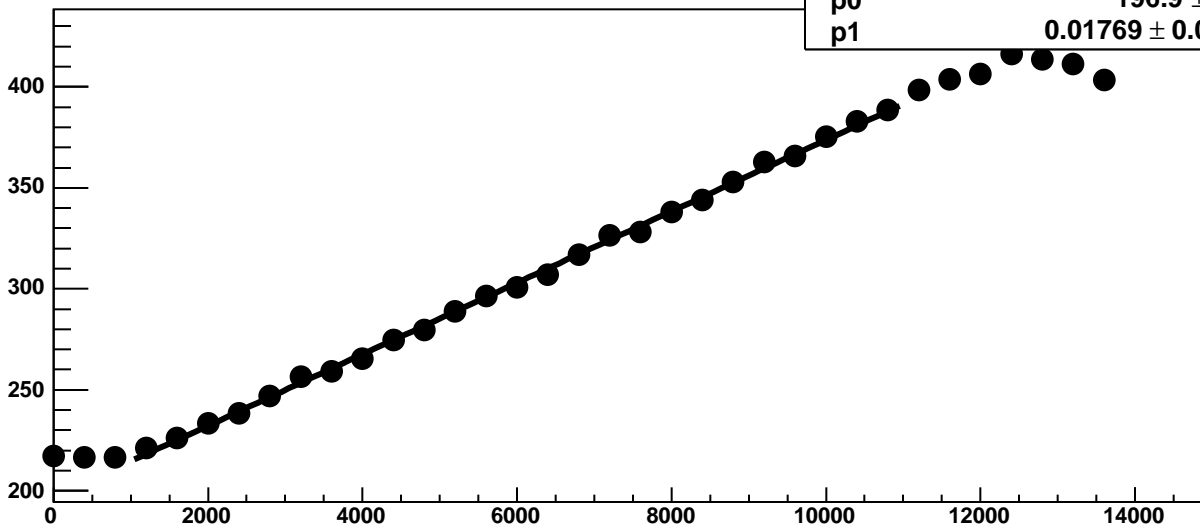
Chip 4, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



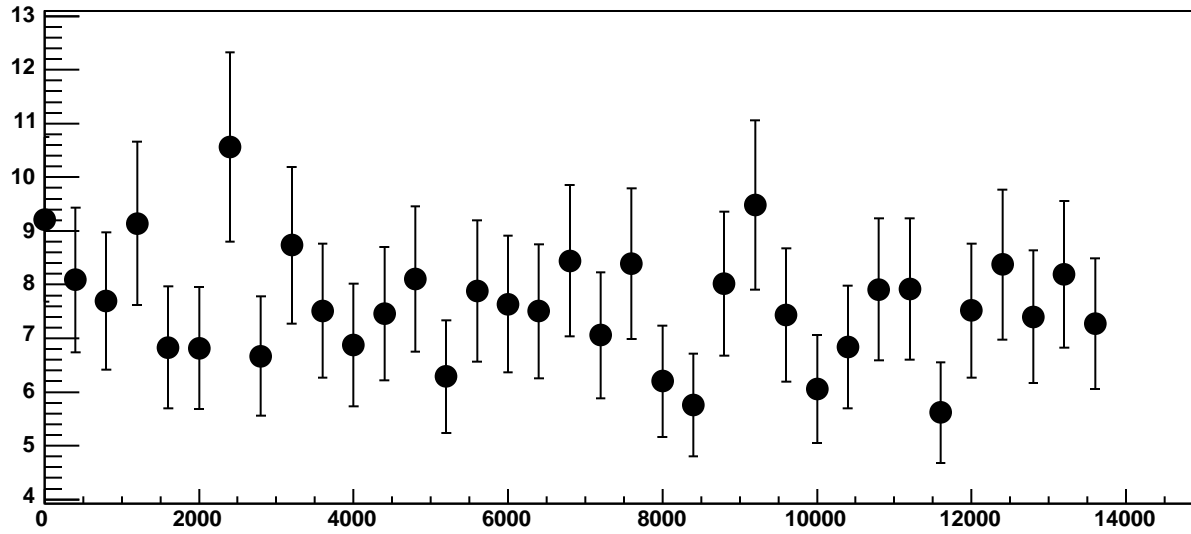
Chip 4, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC



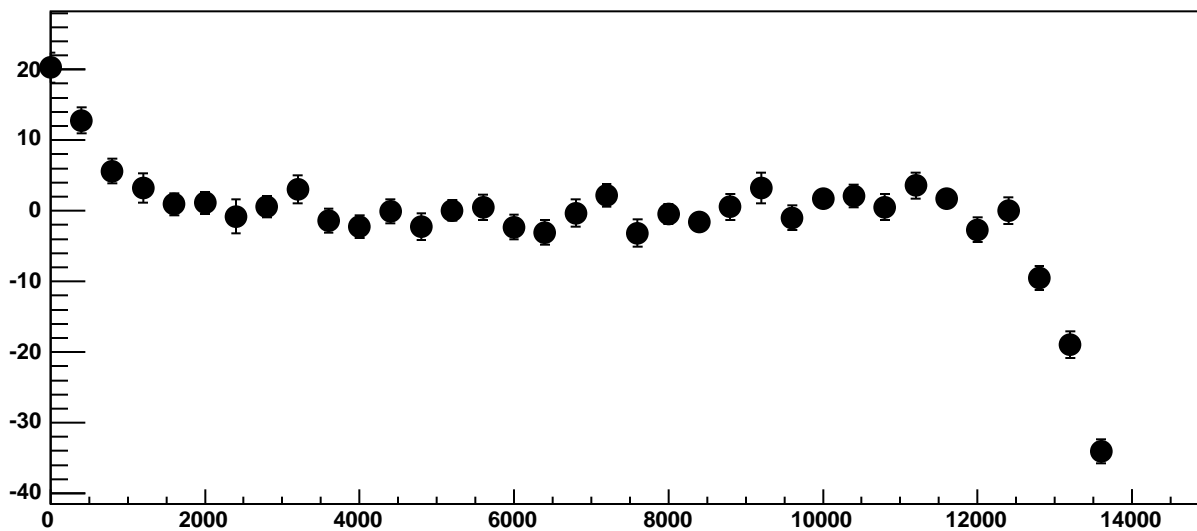
Chip 4, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



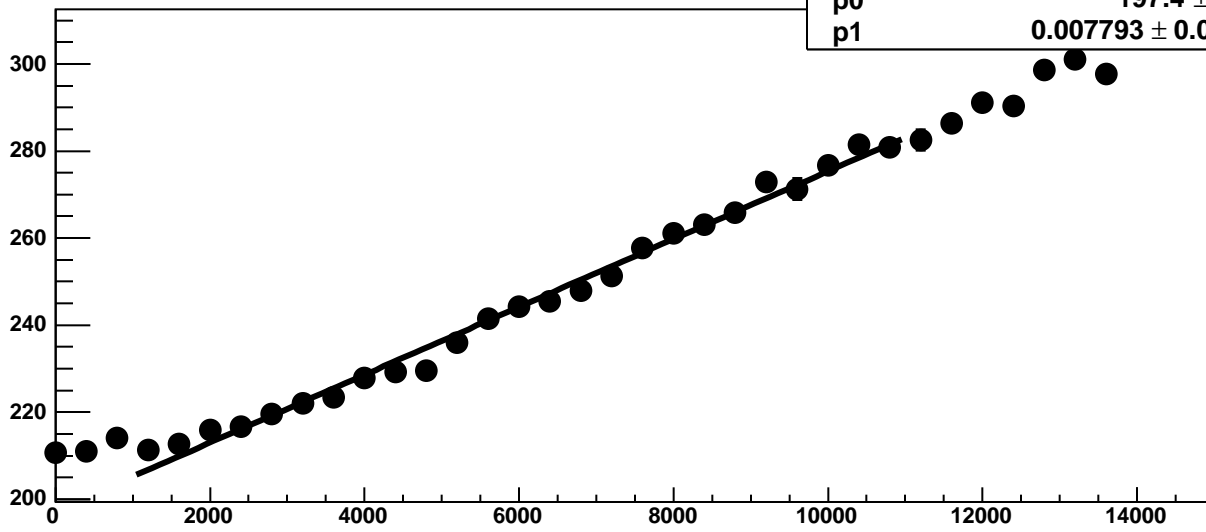
Chip 4, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

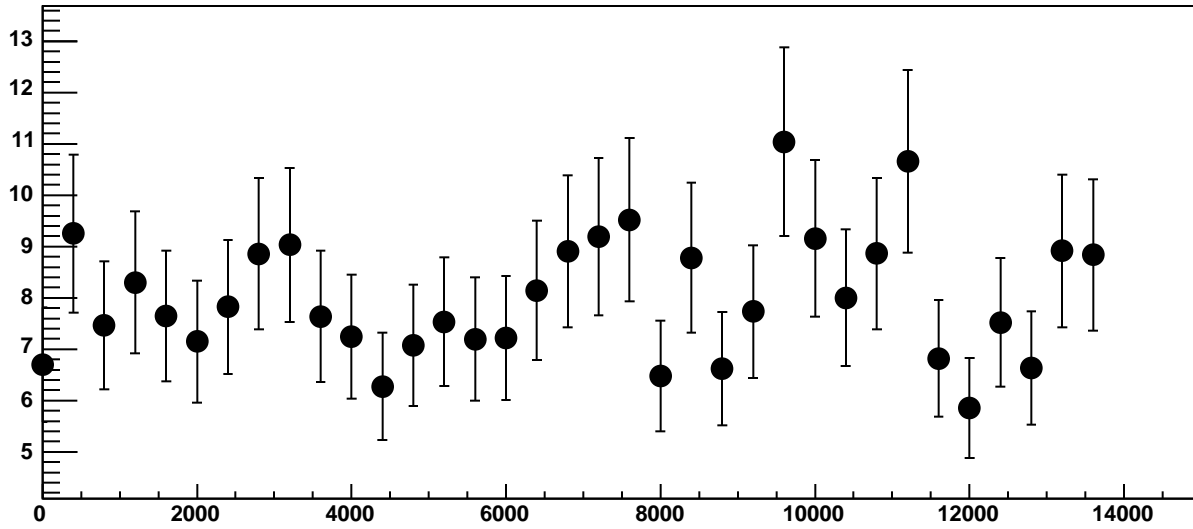


Chip 4, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

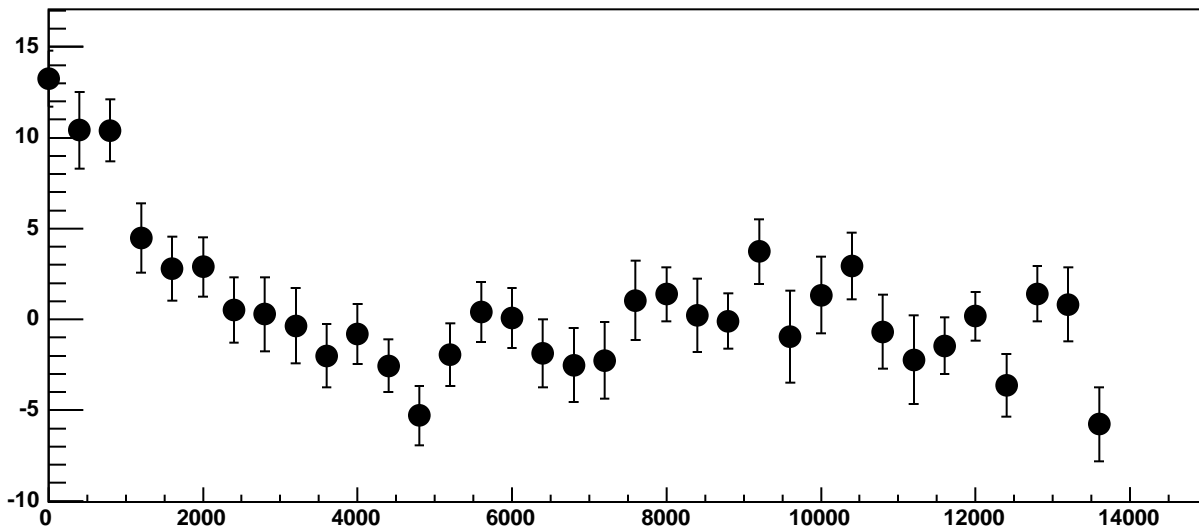


χ^2 / ndf 40.48 / 23
p0 197.4 ± 0.8339
p1 0.007793 ± 0.0001291

Chip 4, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

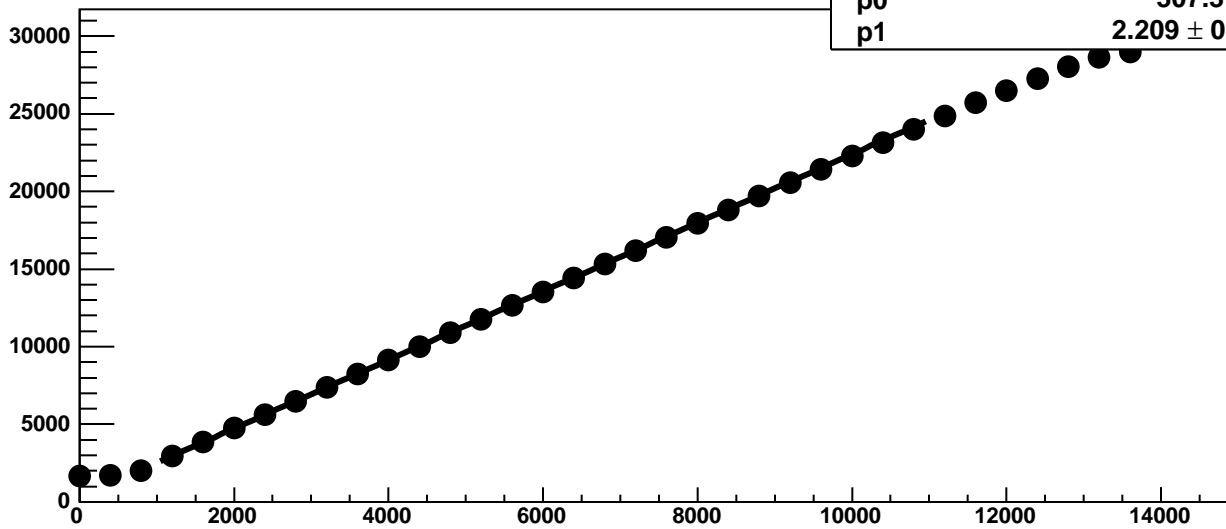


Chip 4, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

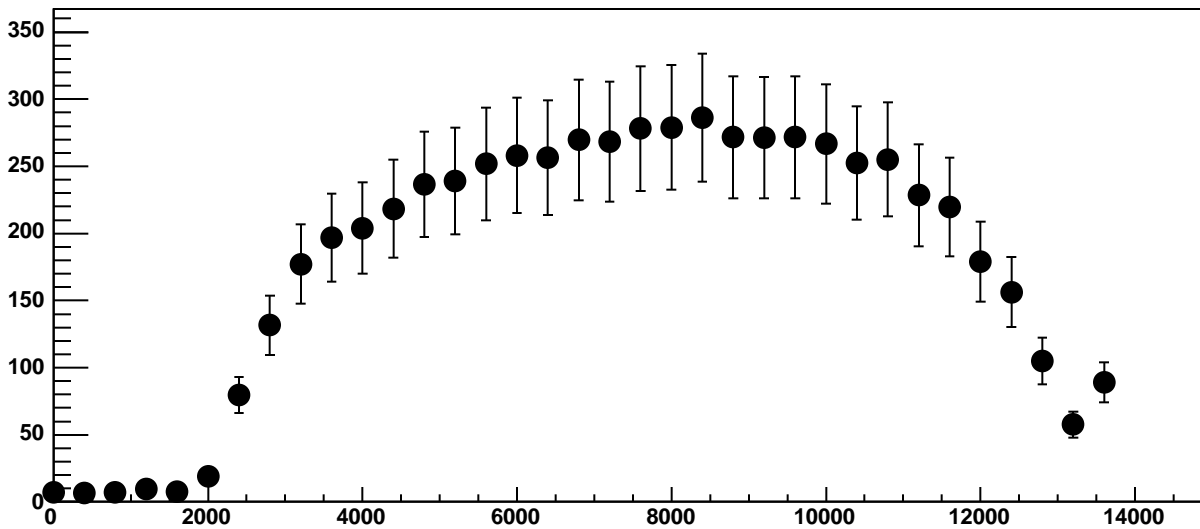


Chip 4, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC

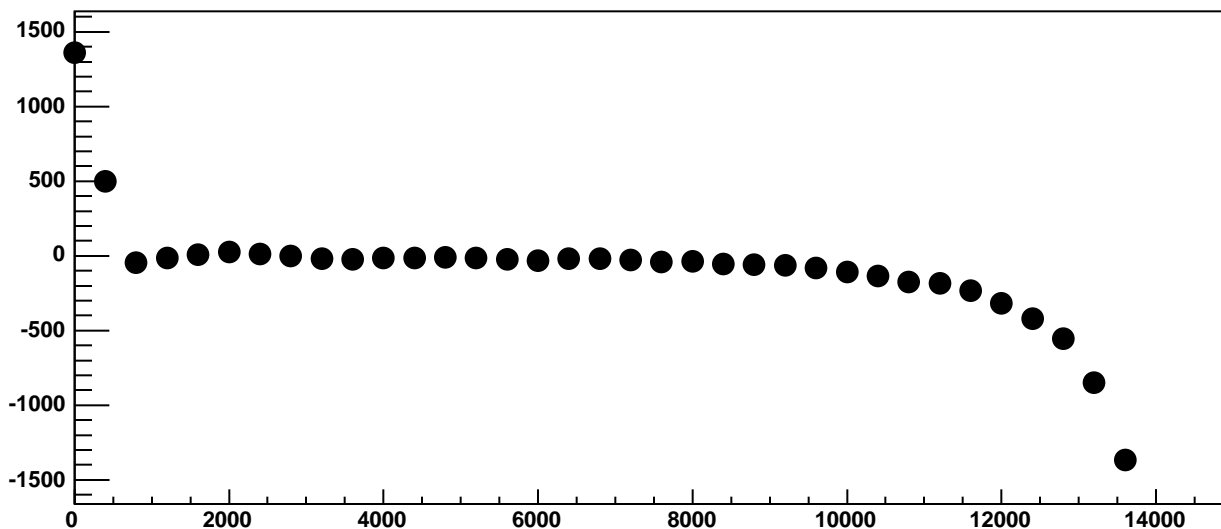
χ^2 / ndf 133.9 / 23
p0 307.3 ± 3.467
p1 2.209 ± 0.002067



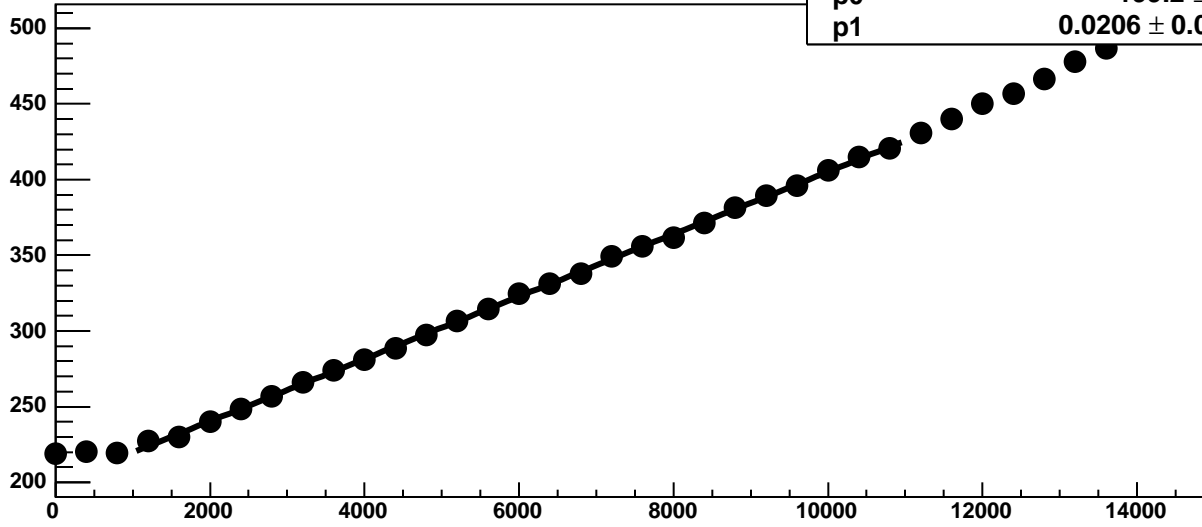
Chip 4, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC

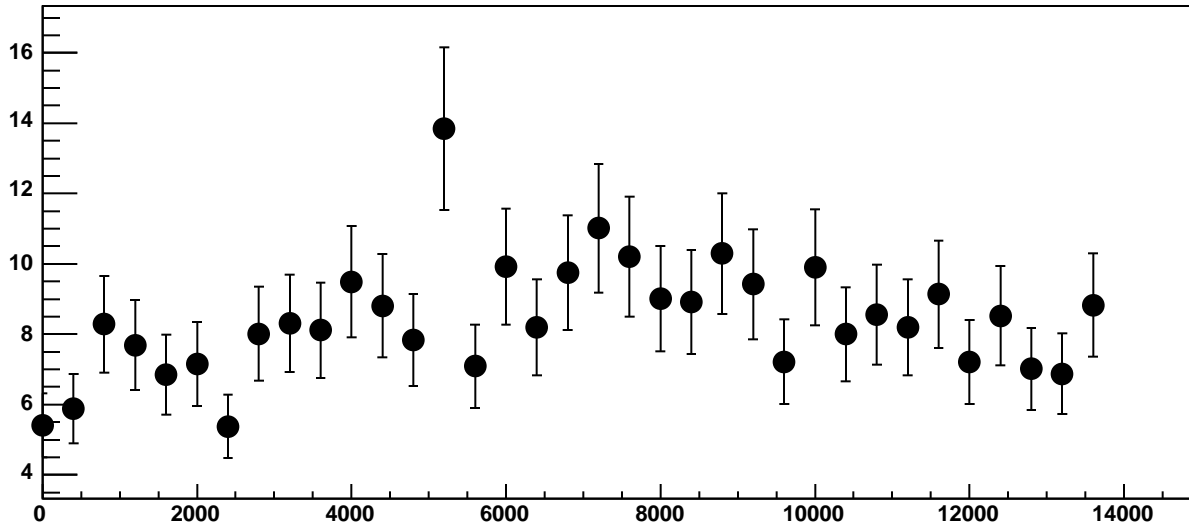


Chip 4, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

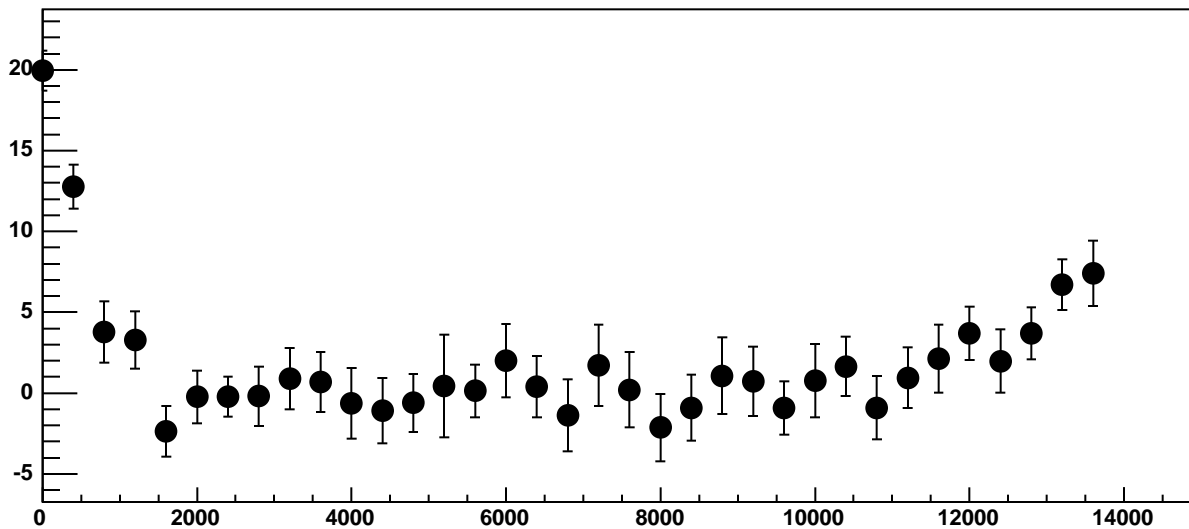


χ^2 / ndf 11.38 / 23
p0 199.2 ± 0.7908
p1 0.0206 ± 0.0001257

Chip 4, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

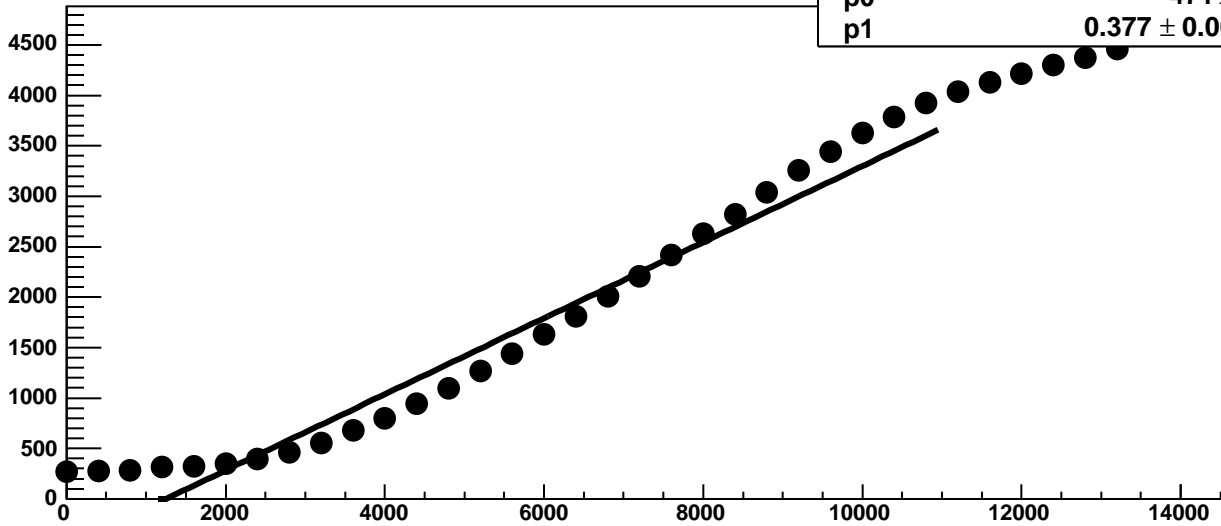


Chip 4, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

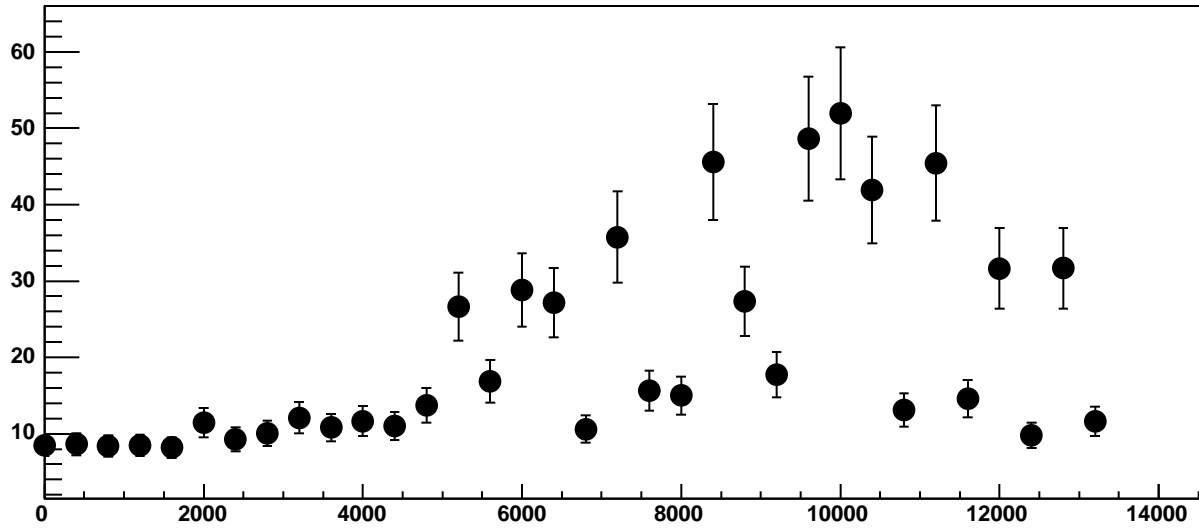


Chip 4, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

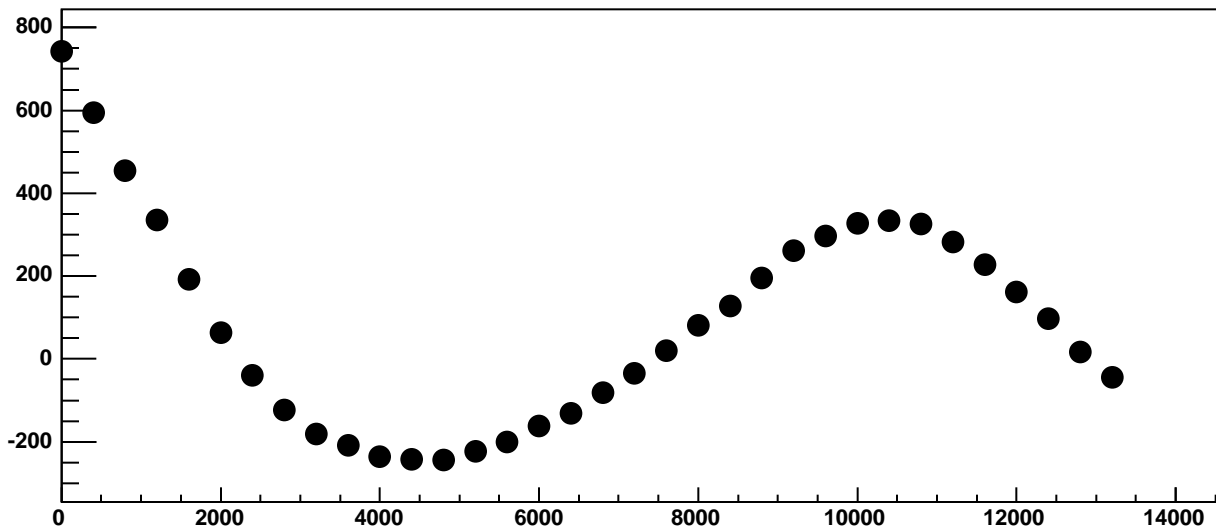
χ^2 / ndf 1.044e+05 / 23
p0 -471 ± 1.173
p1 0.377 ± 0.0002362



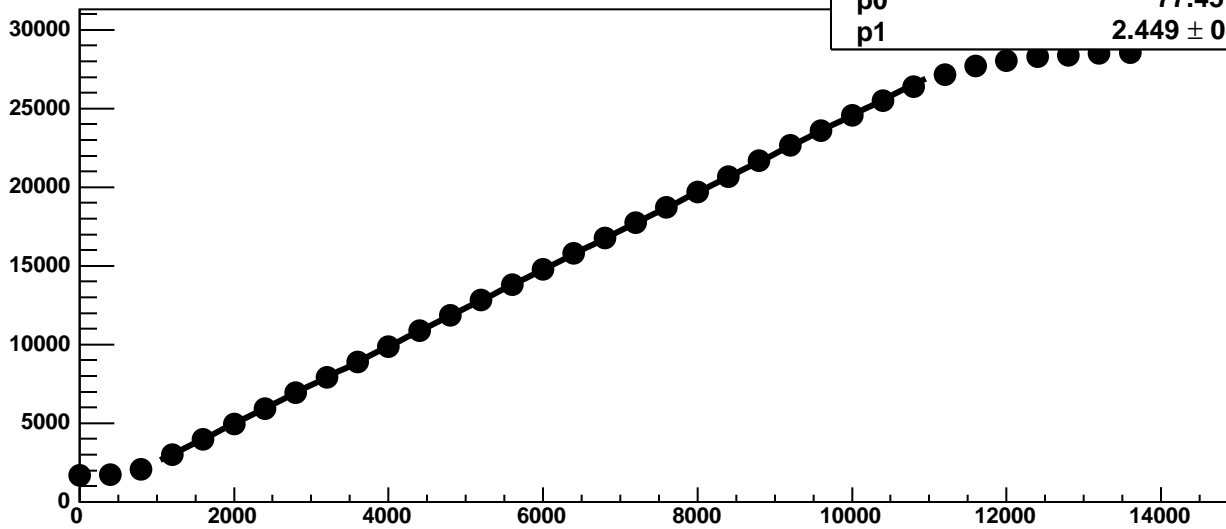
Chip 4, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



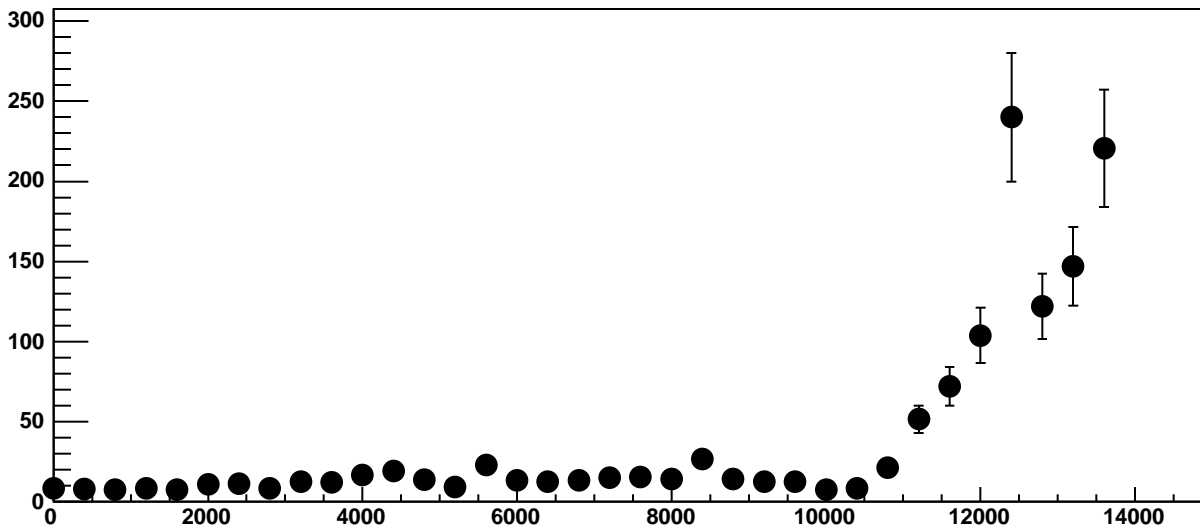
Chip 4, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



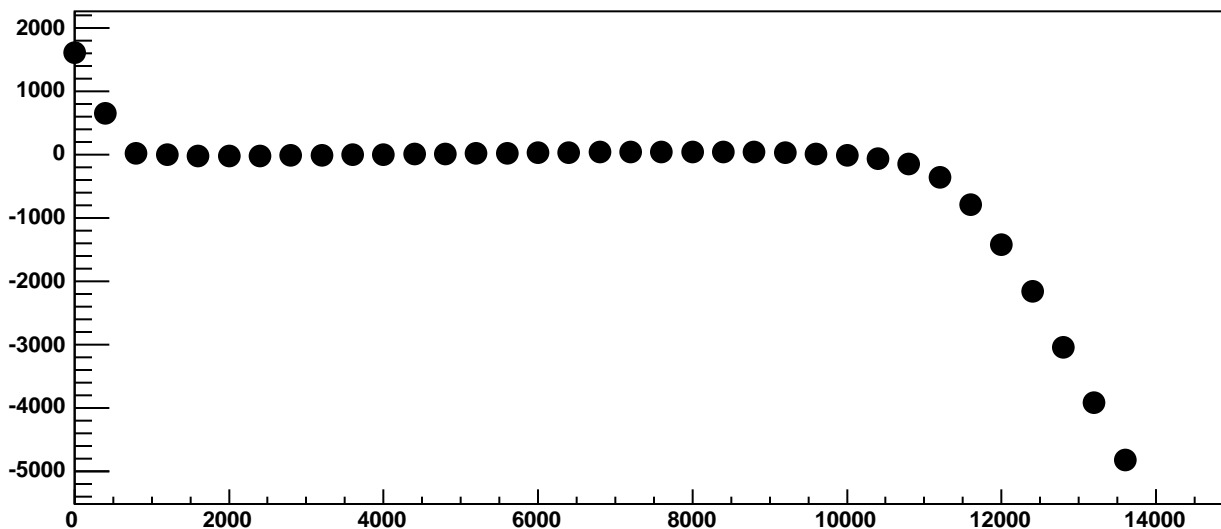
Chip 4, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC



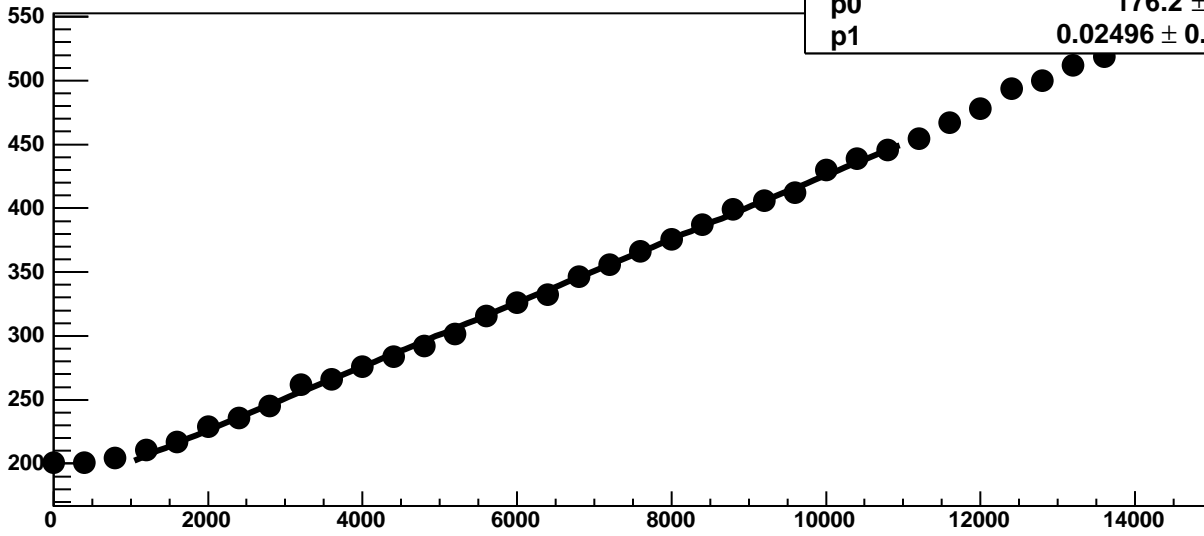
Chip 4, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC

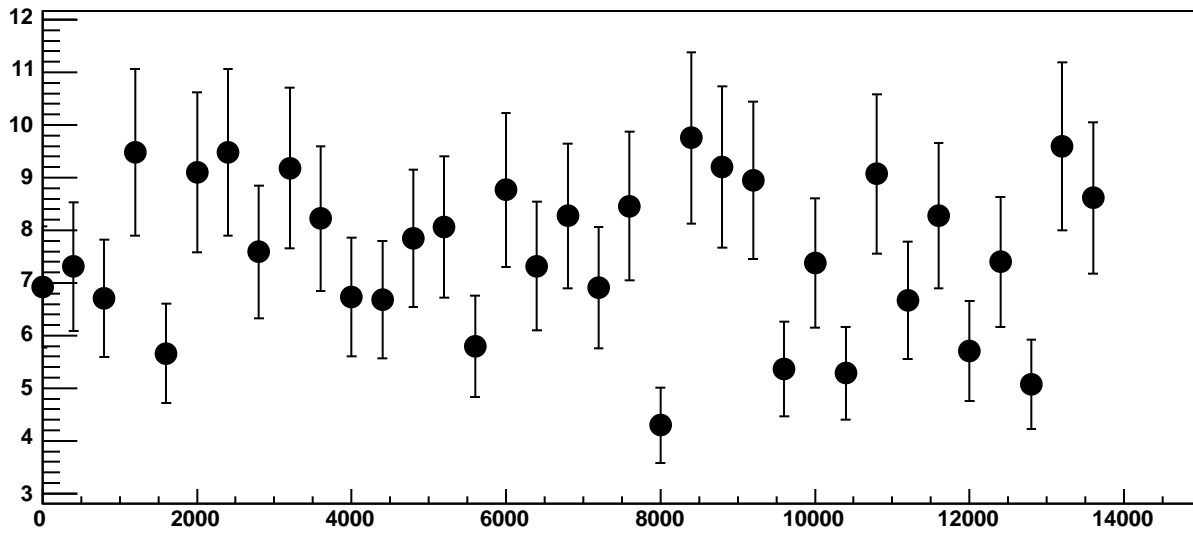


Chip 4, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

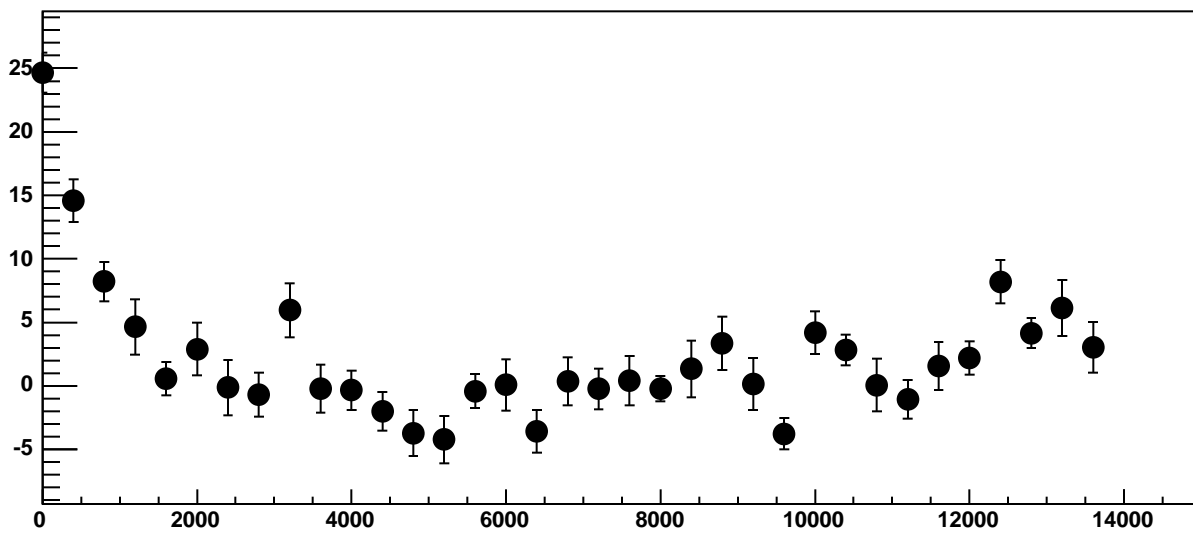


χ^2 / ndf 54.83 / 23
p0 176.2 ± 0.7989
p1 0.02496 ± 0.000115

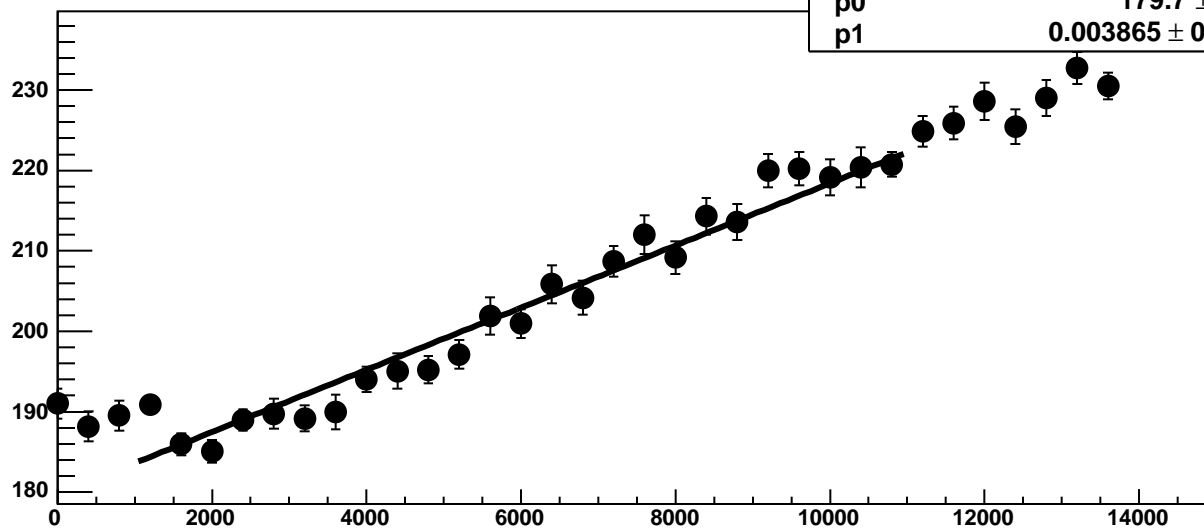
Chip 4, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



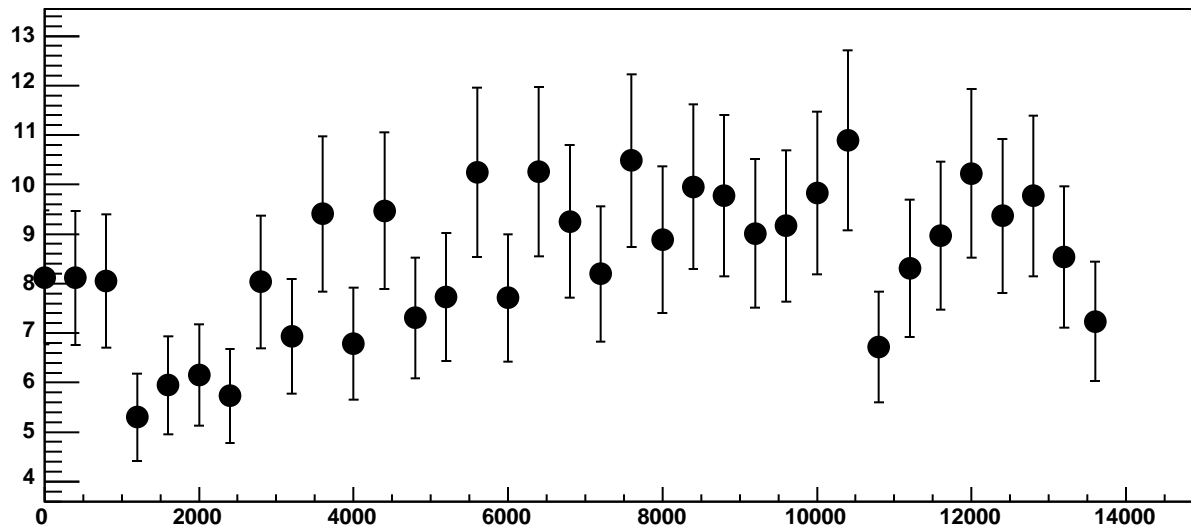
Chip 4, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



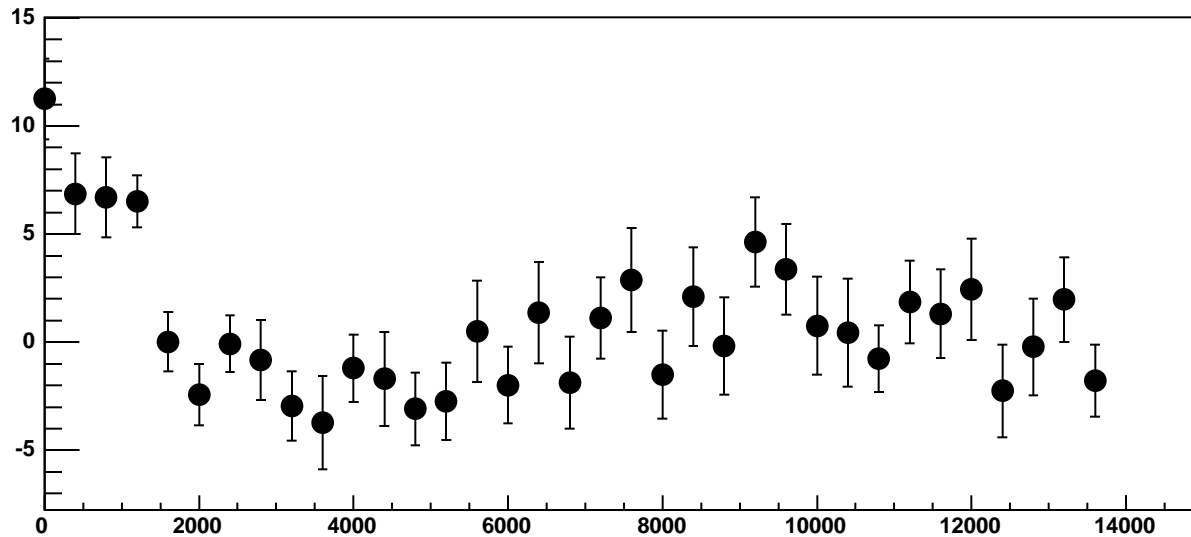
Chip 4, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



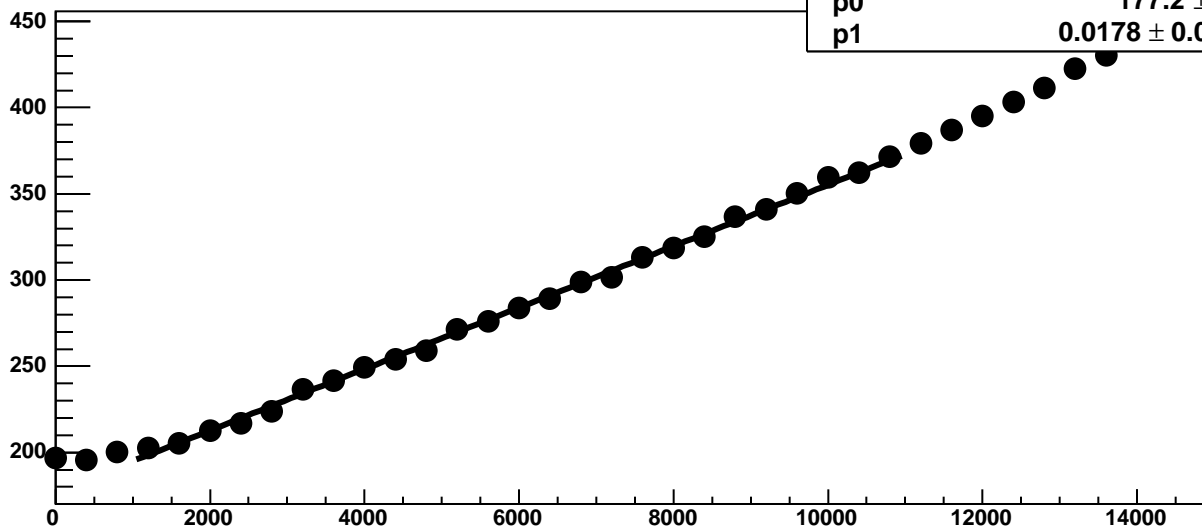
Chip 4, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

46.15 / 23

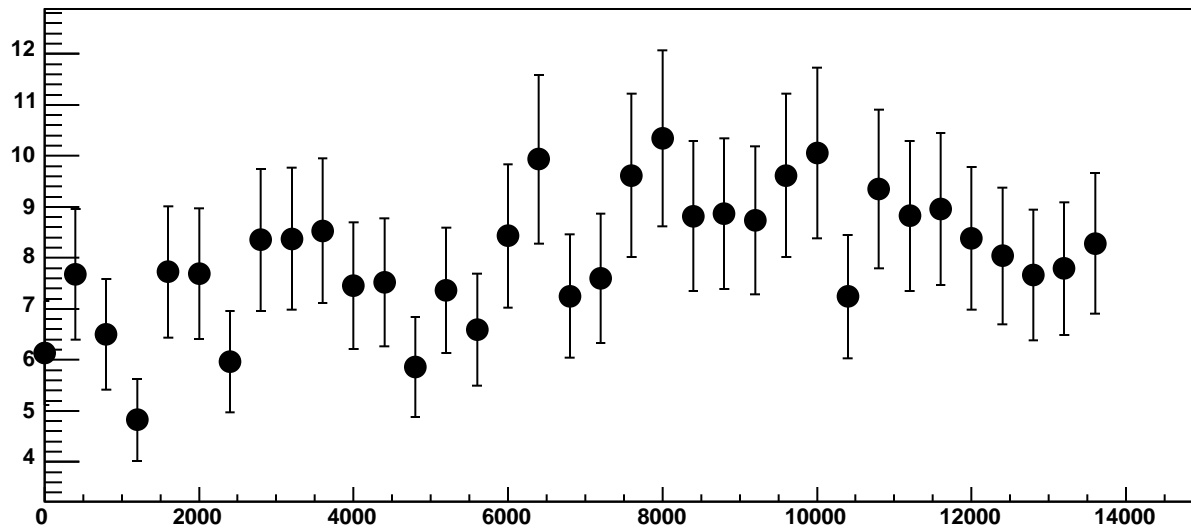
p0

177.2 ± 0.7327

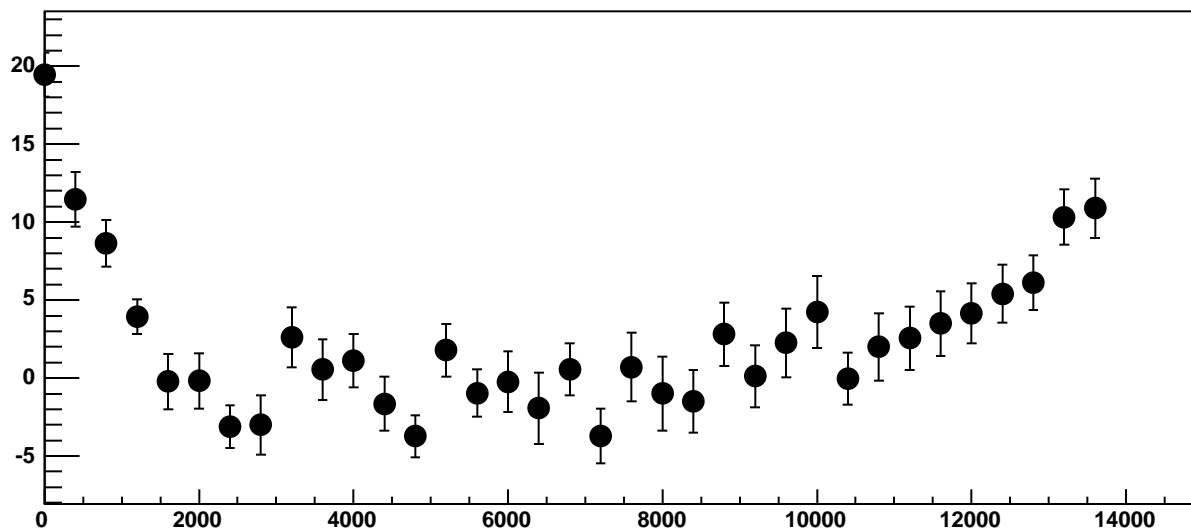
p1

0.0178 ± 0.0001208

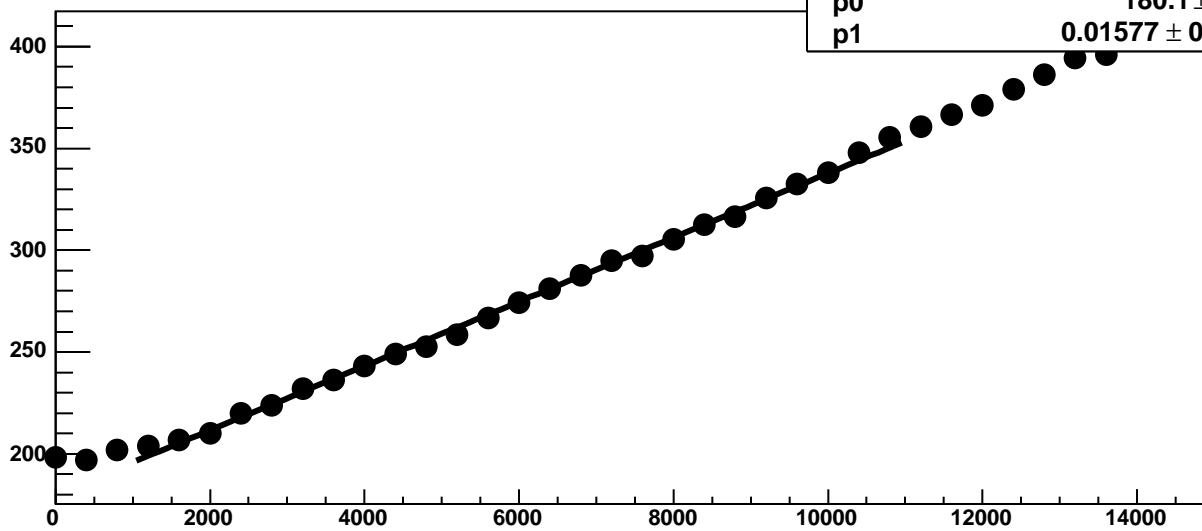
Chip 4, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

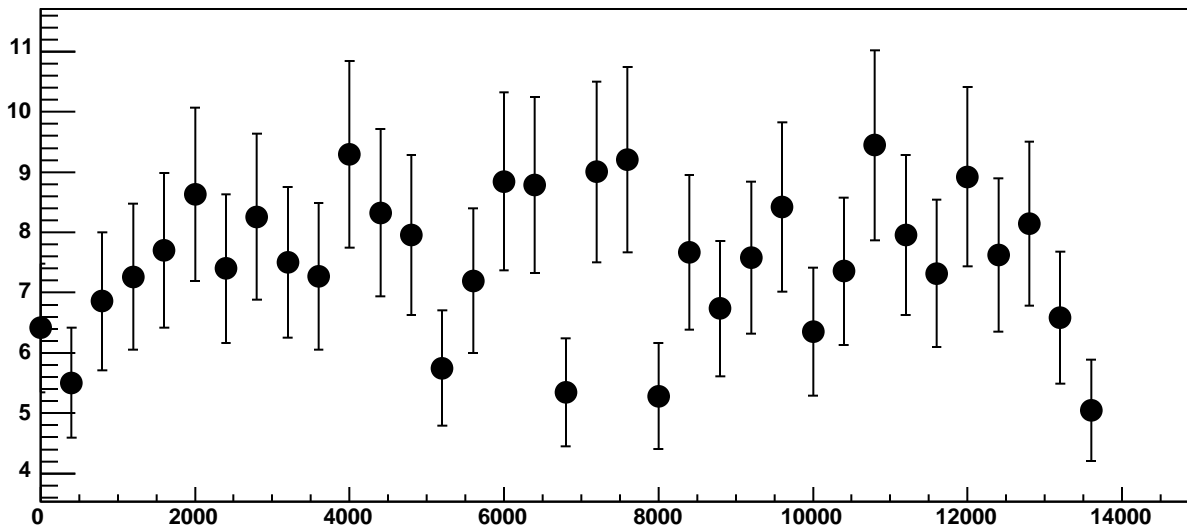


Chip 4, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

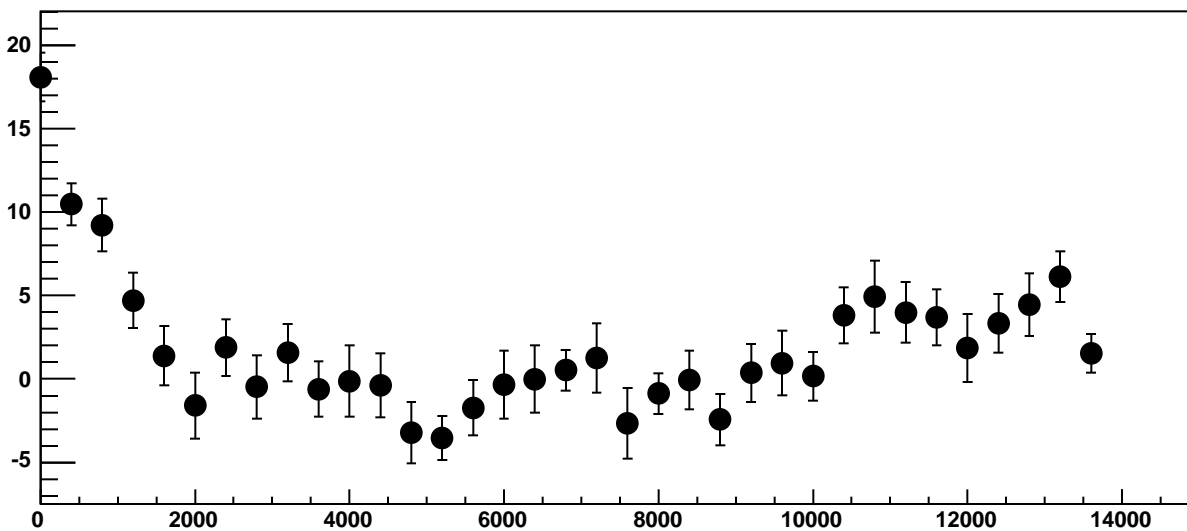


χ^2 / ndf 38.64 / 23
p0 180.1 ± 0.8157
p1 0.01577 ± 0.000121

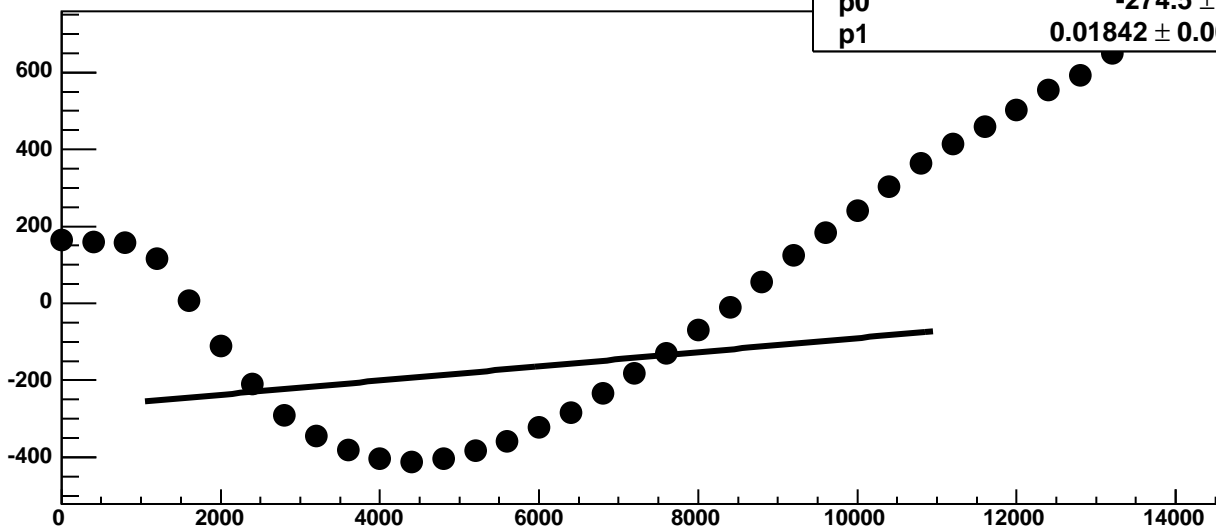
Chip 4, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

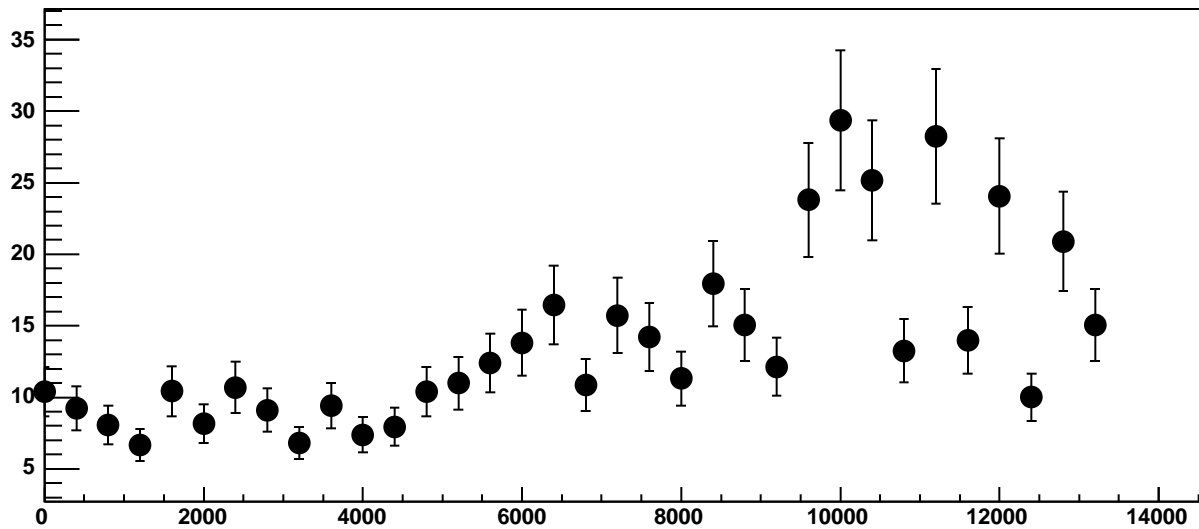


Chip 4, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

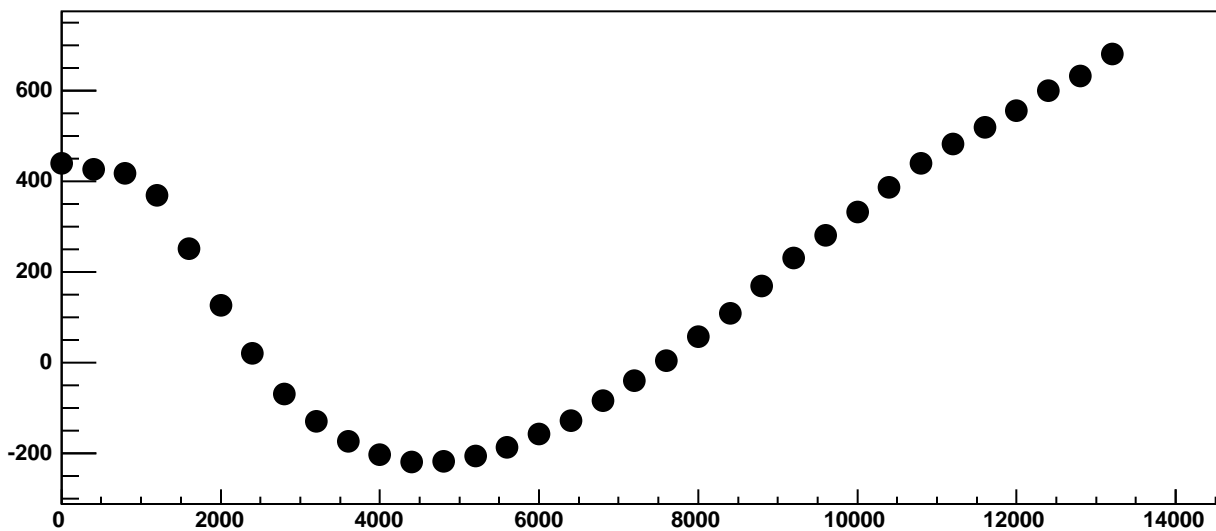


χ^2 / ndf 1.819e+05 / 23
p0 -274.5 ± 0.9932
p1 0.01842 ± 0.0001915

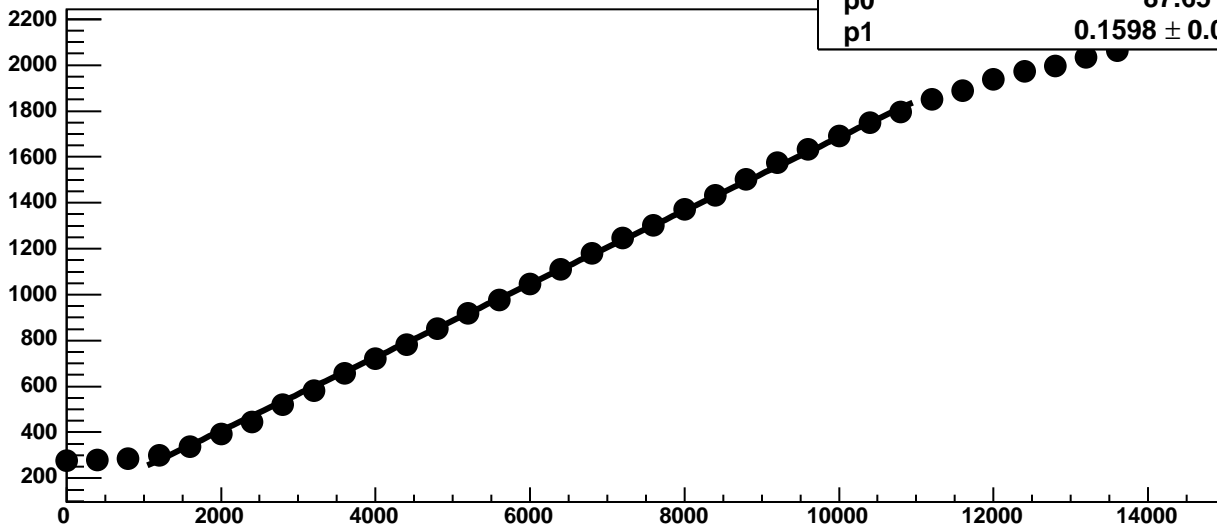
Chip 4, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

473.7 / 23

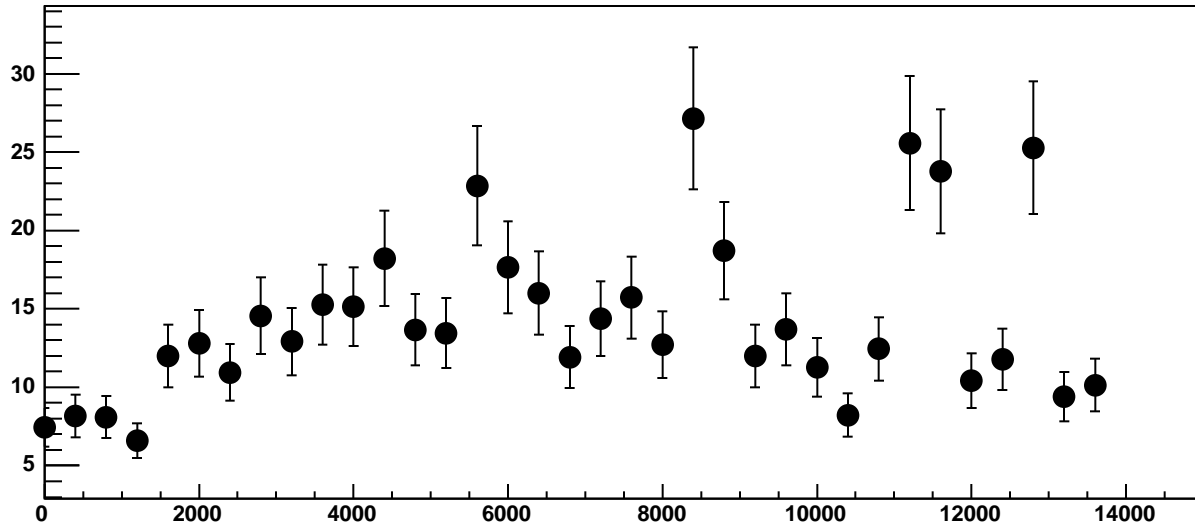
p0

87.65 ± 1.134

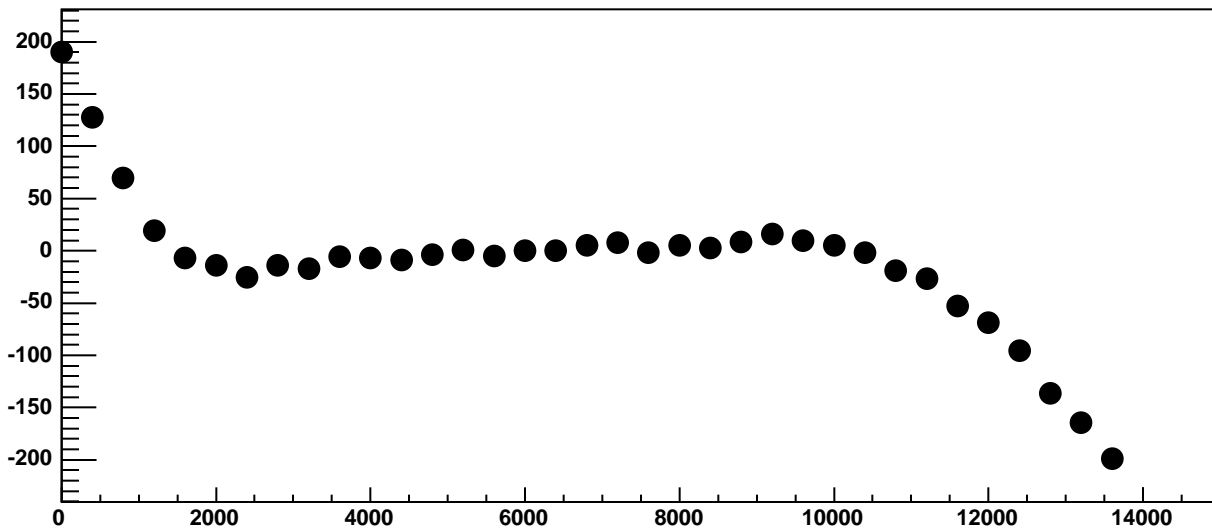
p1

0.1598 ± 0.0001711

Chip 4, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

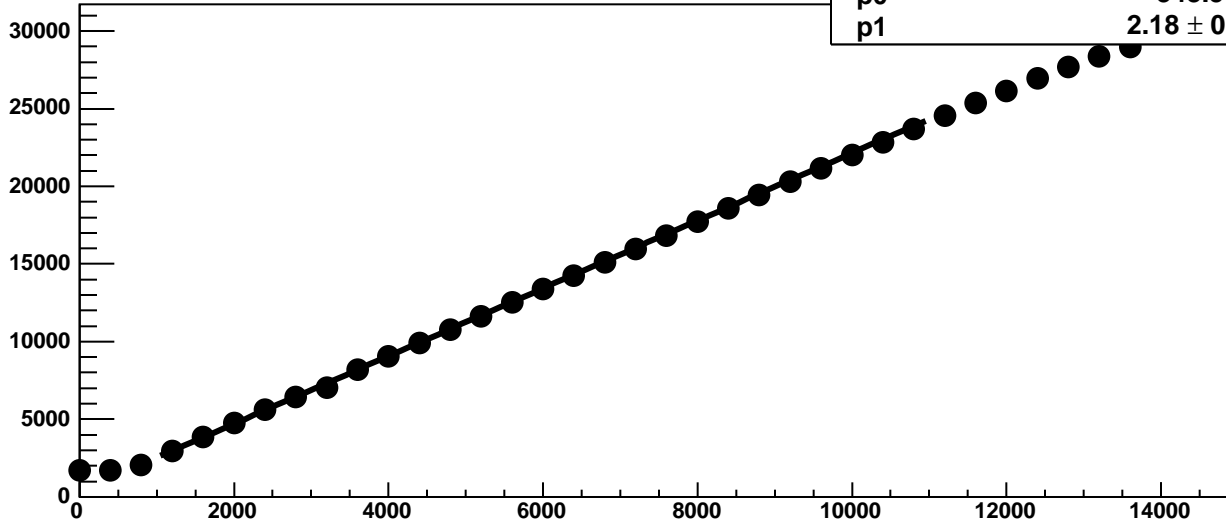


Chip 4, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

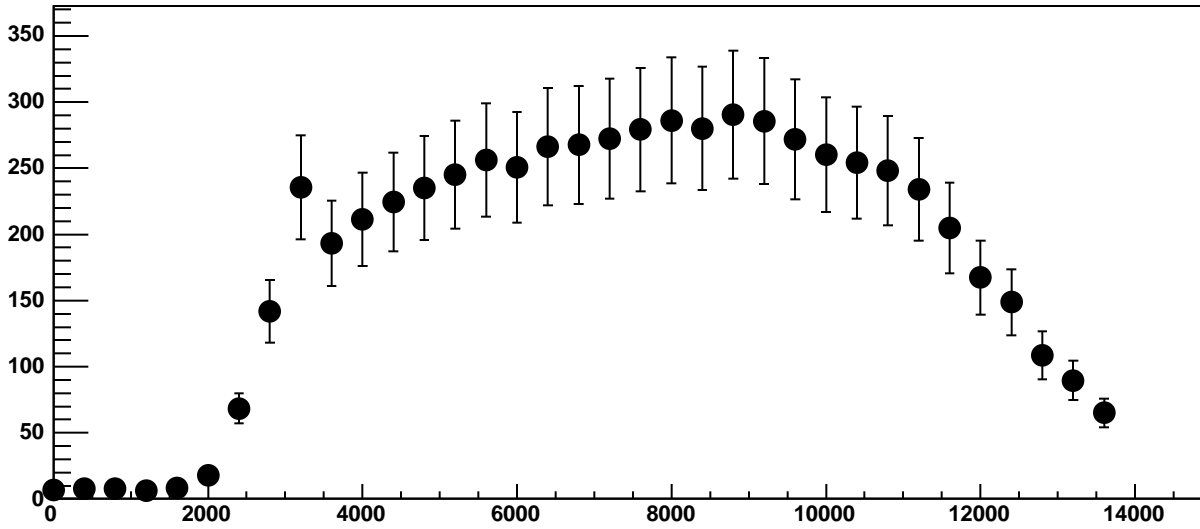


Chip 4, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC

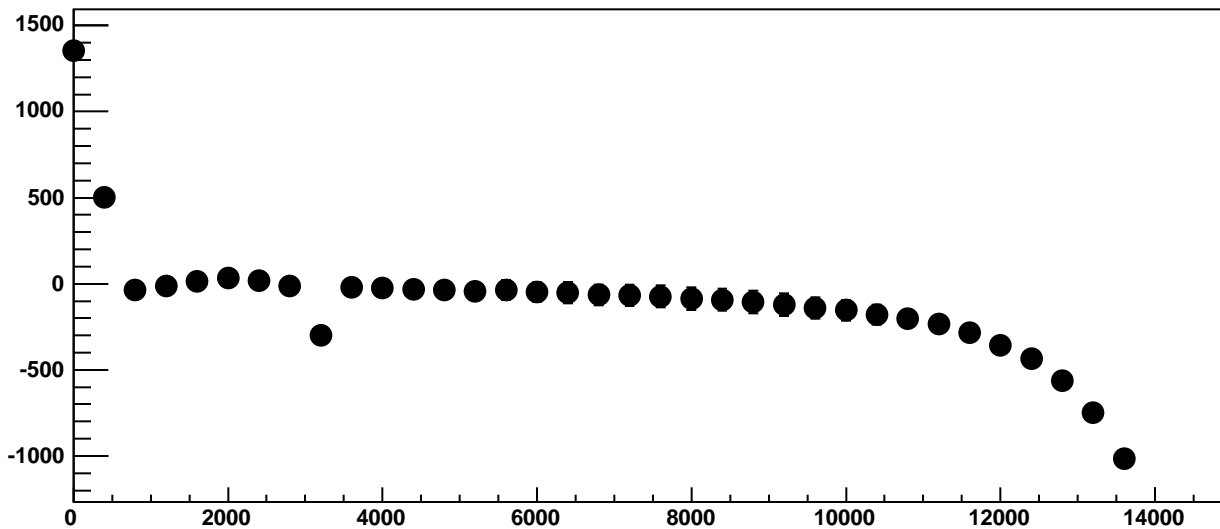
χ^2 / ndf 283.8 / 23
p0 348.5 ± 3.045
p1 2.18 ± 0.001974



Chip 4, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC

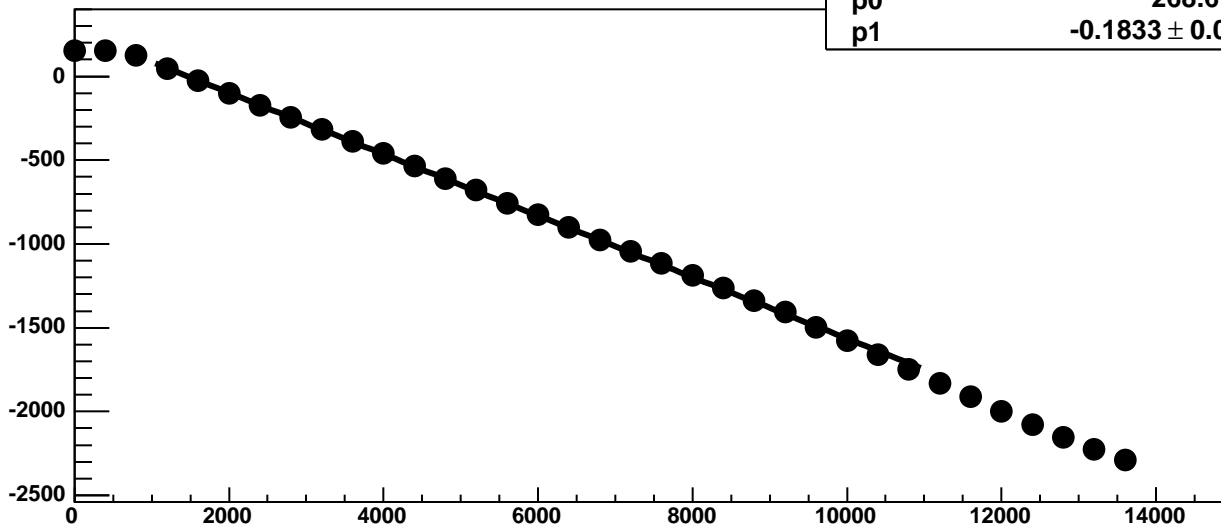


Chip 4, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC

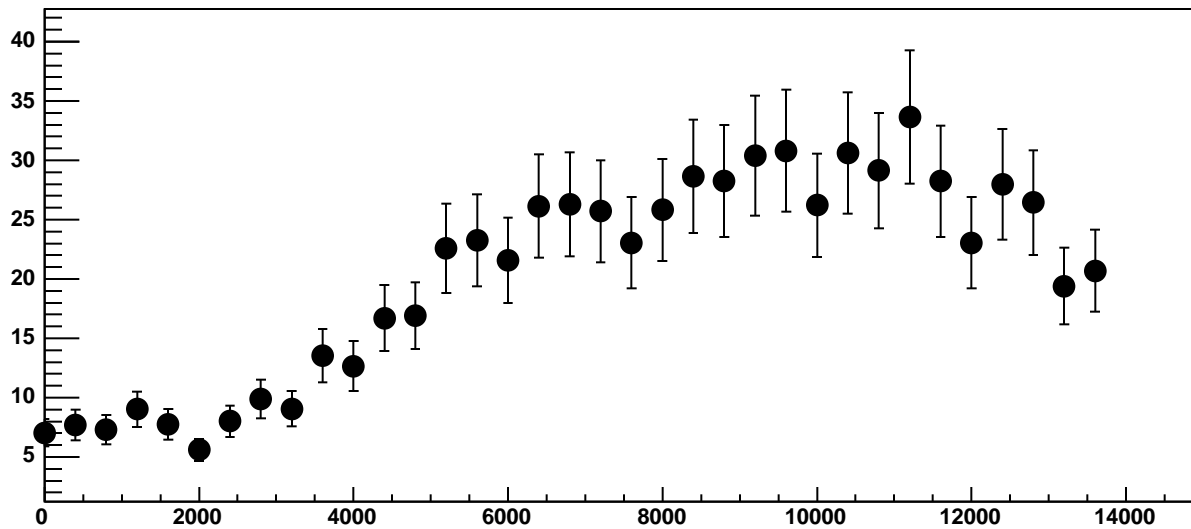


Chip 4, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

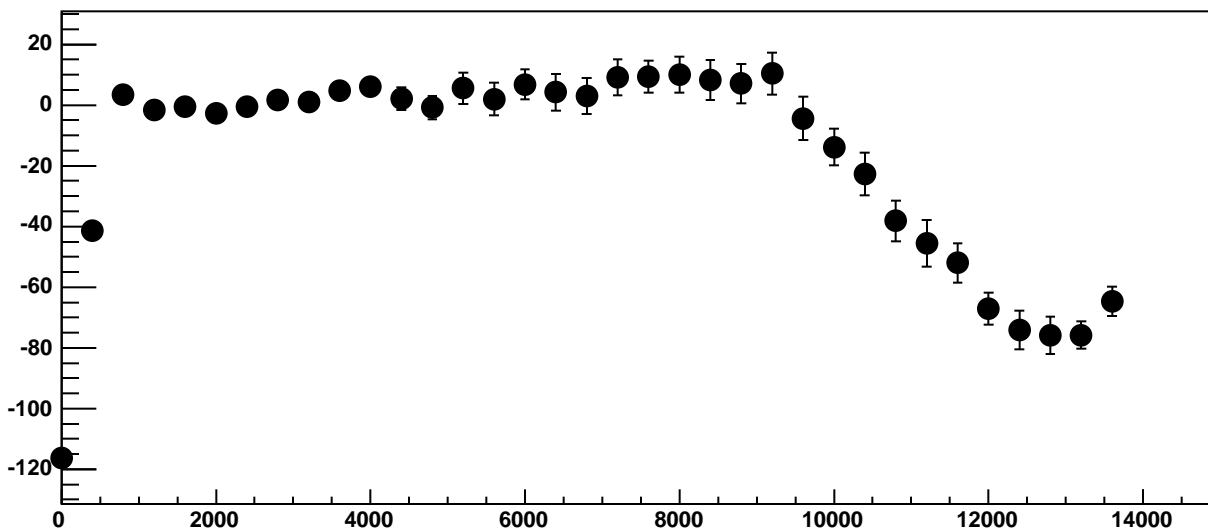
χ^2 / ndf 79.15 / 23
p0 268.6 ± 1.099
p1 -0.1833 ± 0.0002788



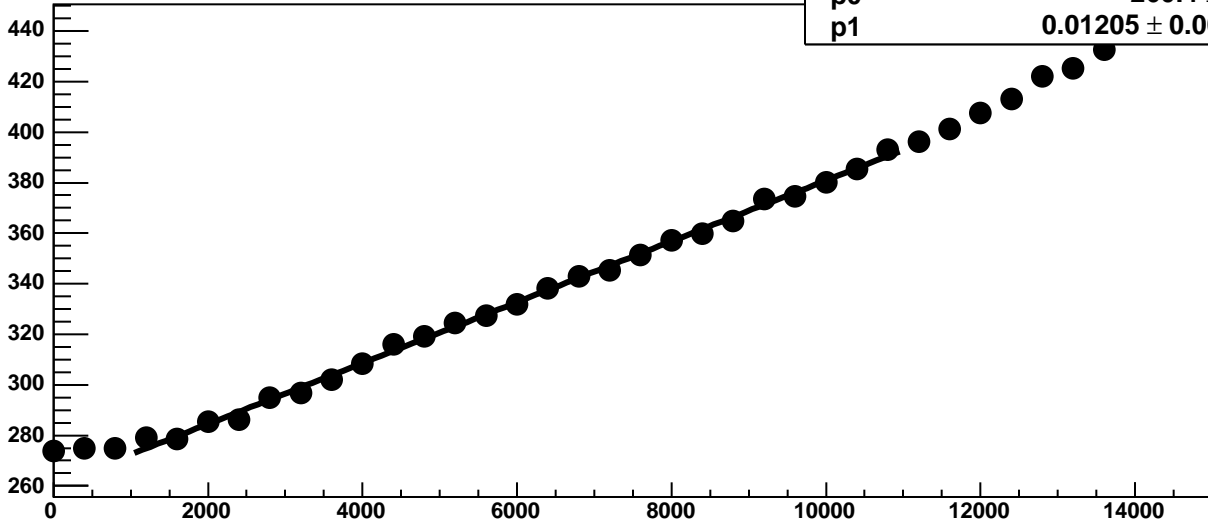
Chip 4, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

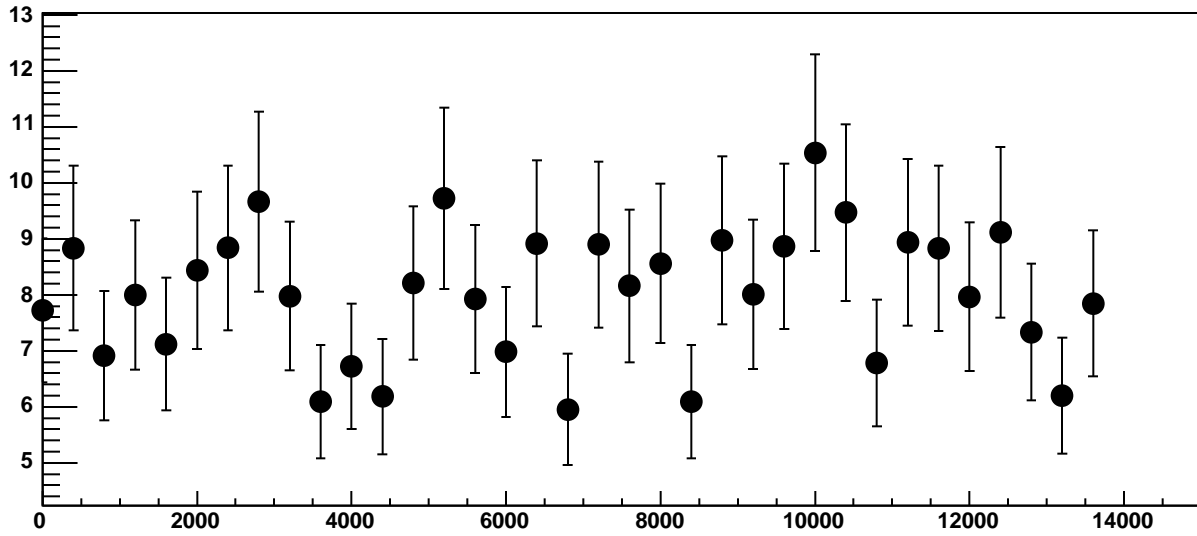


Chip 4, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

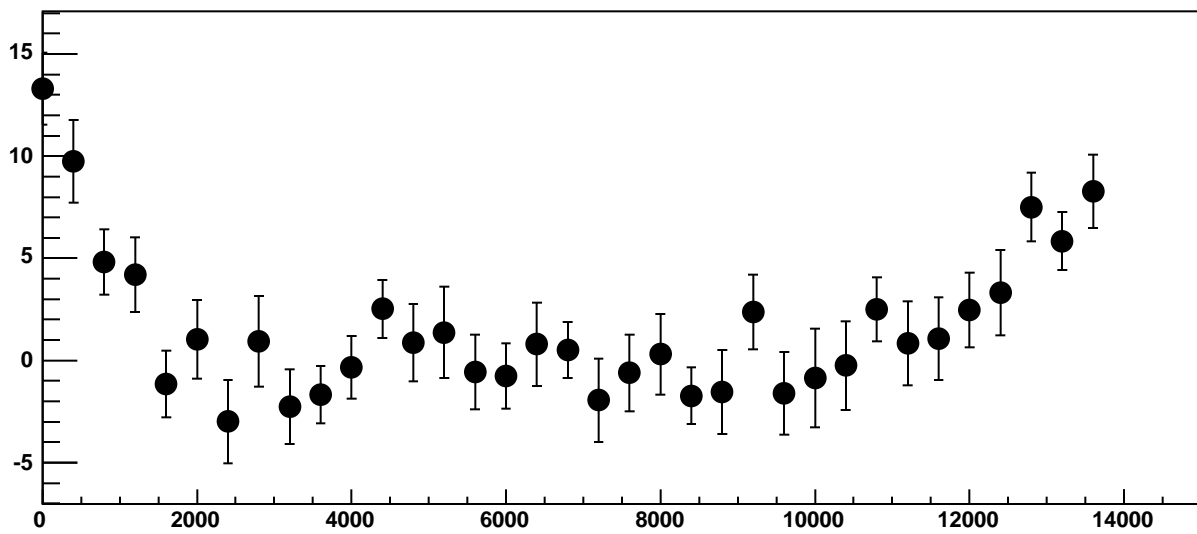


χ^2 / ndf 23.92 / 23
p0 260.4 ± 0.827
p1 0.01205 ± 0.0001269

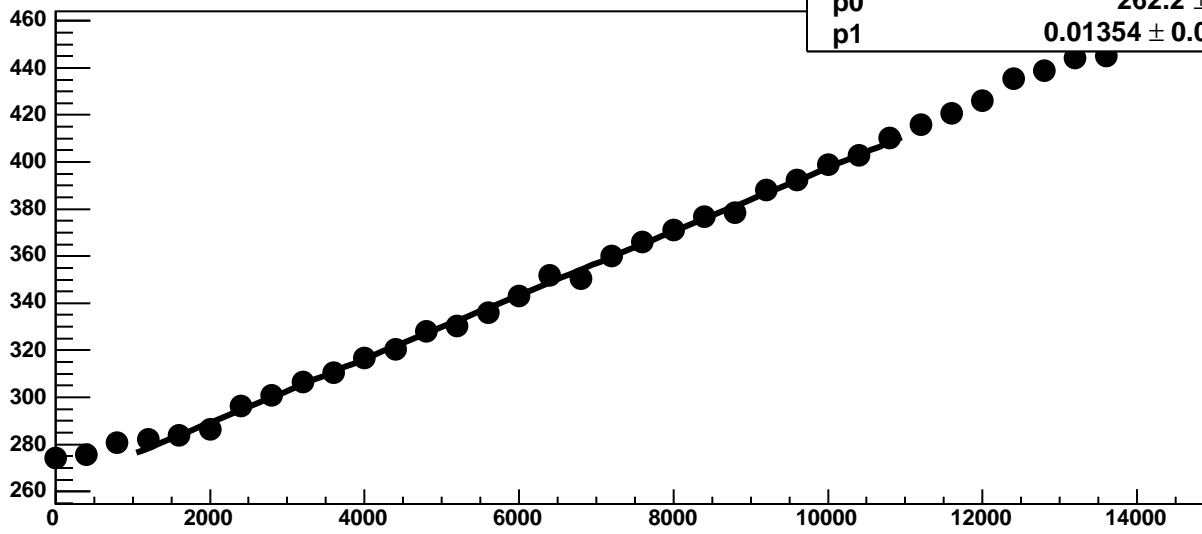
Chip 4, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



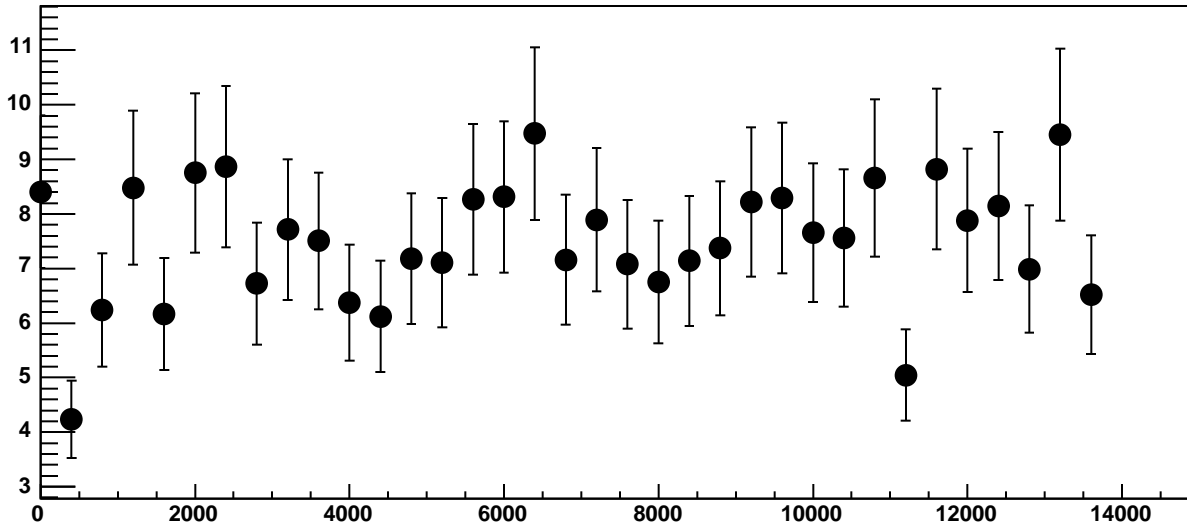
Chip 4, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



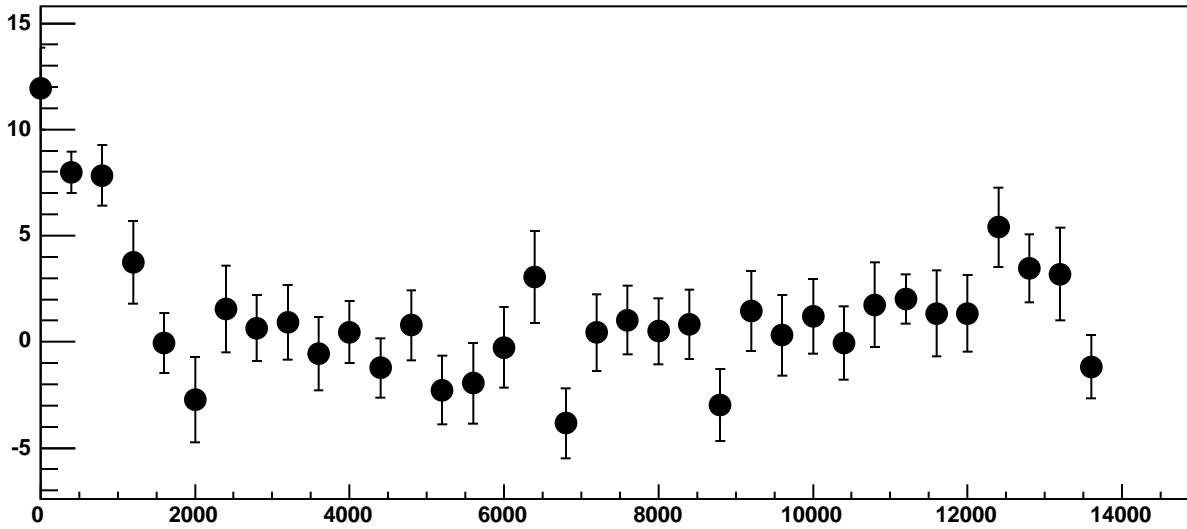
Chip 4, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC



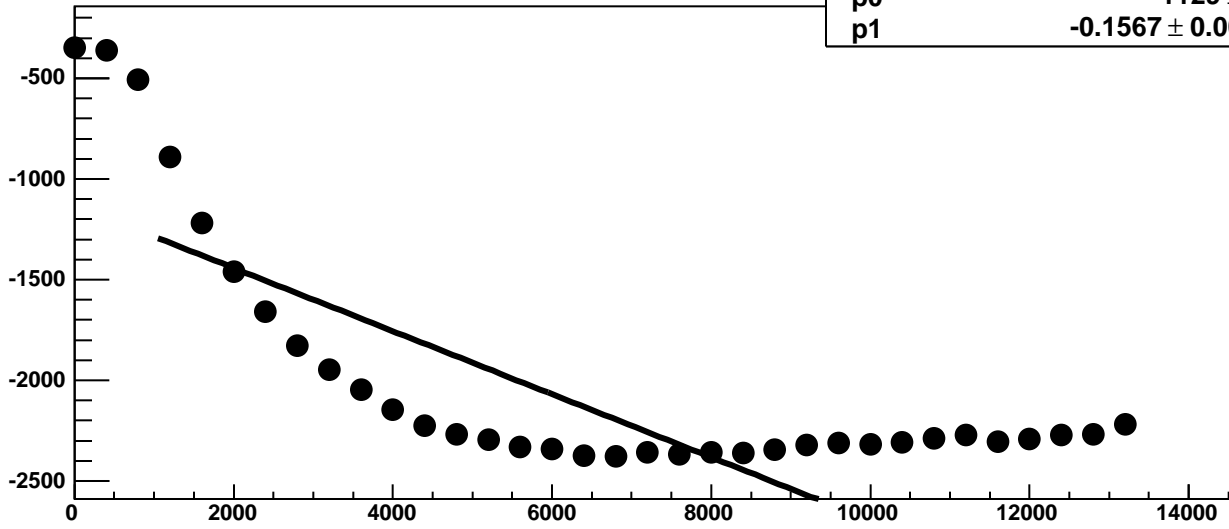
Chip 4, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC

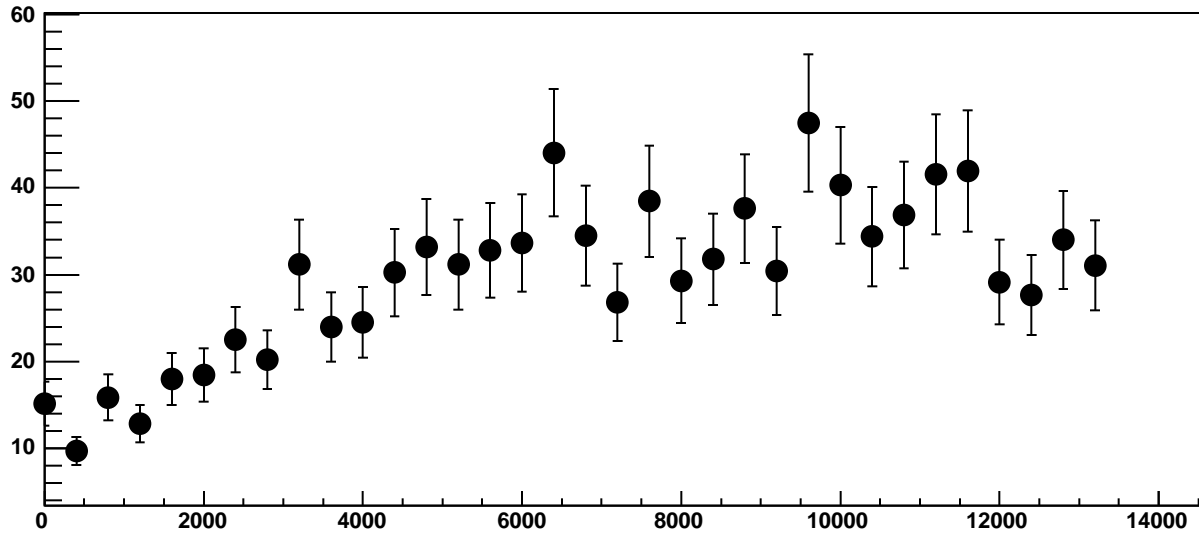


Chip 4, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

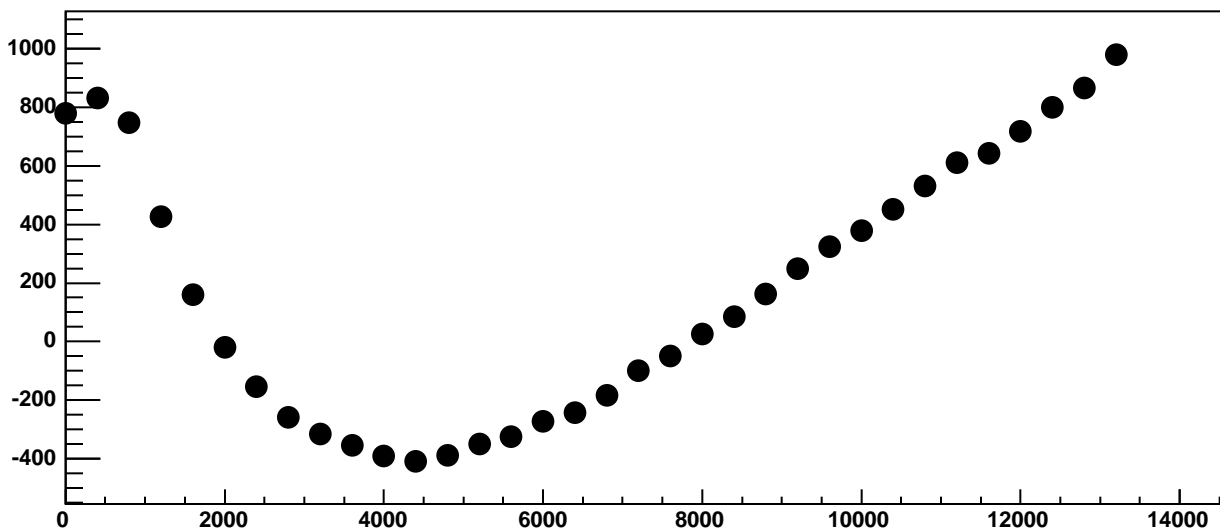


χ^2 / ndf	6.195e+04 / 23
p0	-1129 ± 2.164
p1	-0.1567 ± 0.0004115

Chip 4, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

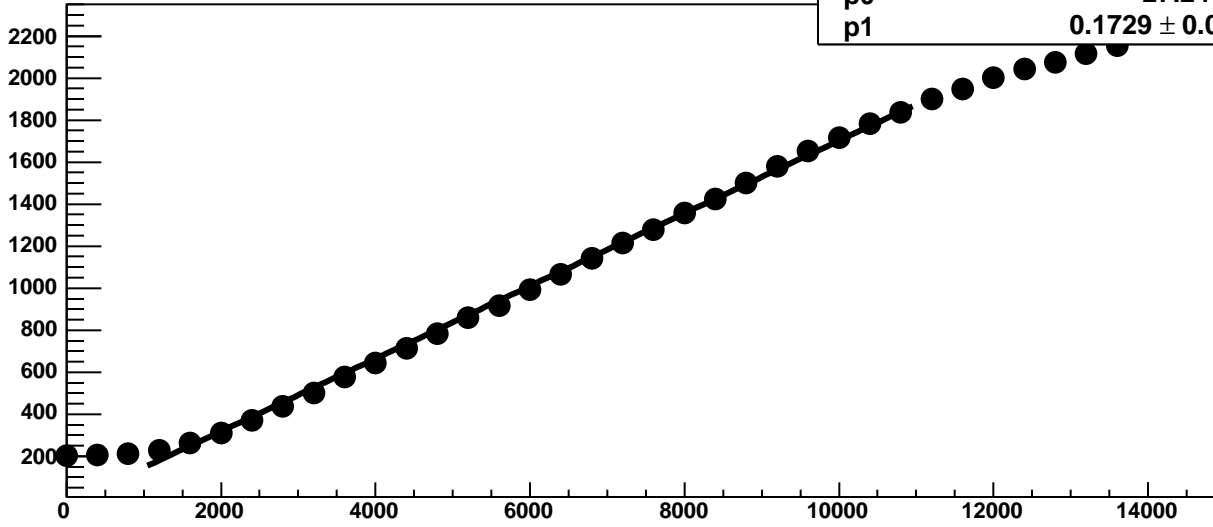


Chip 4, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

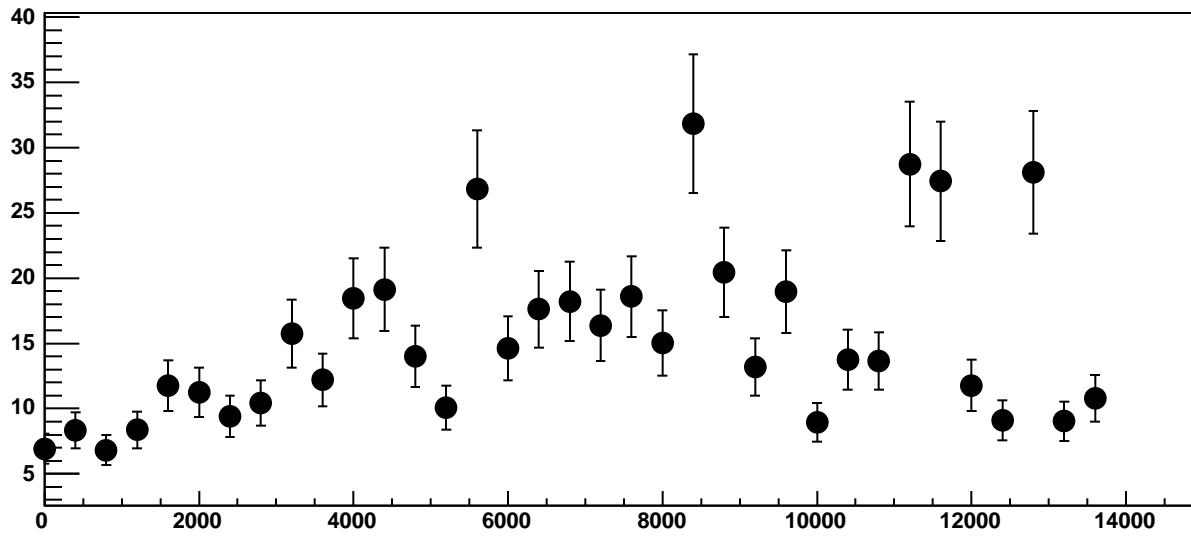


Chip 4, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

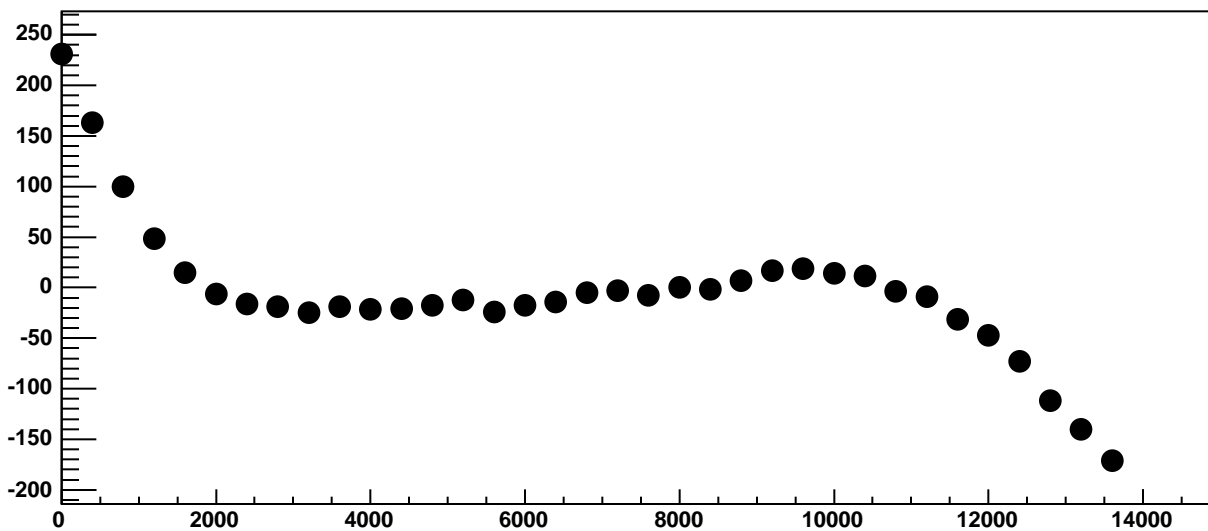
χ^2 / ndf 1162 / 23
p0 -27.24 ± 1.182
p1 0.1729 ± 0.0001899



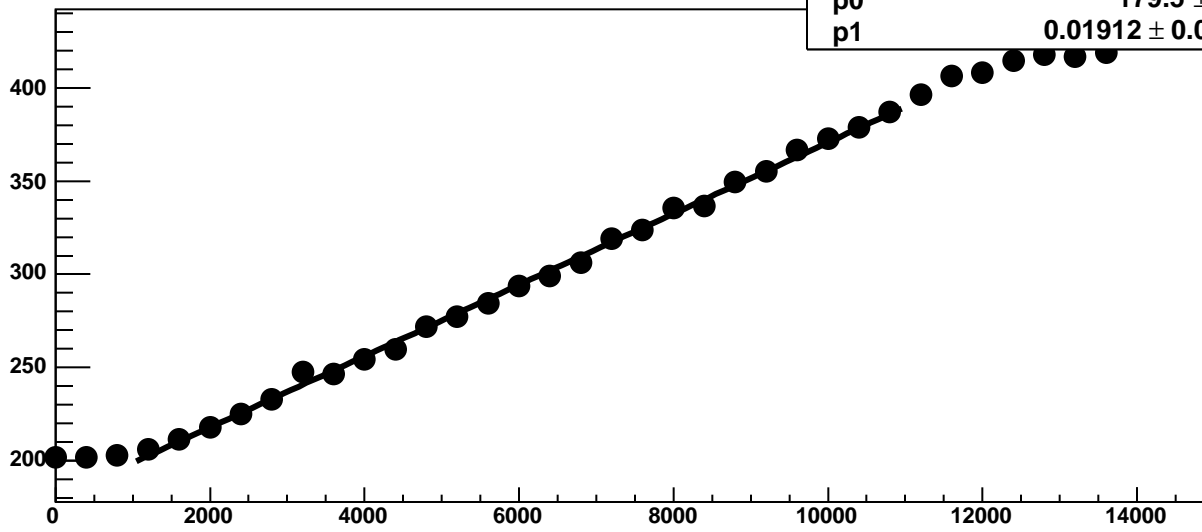
Chip 4, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

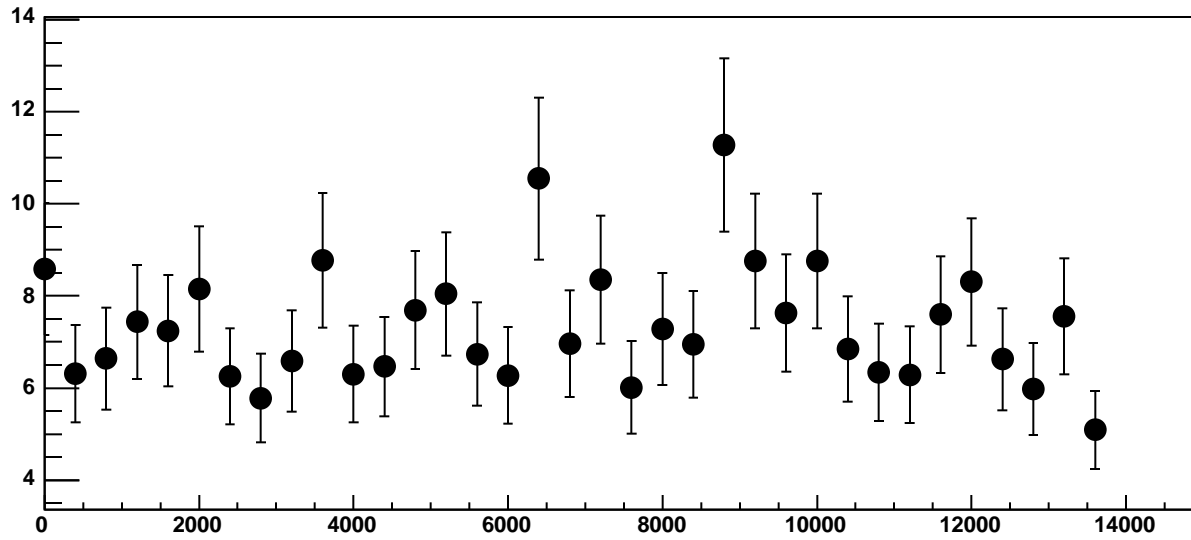


Chip 4, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC

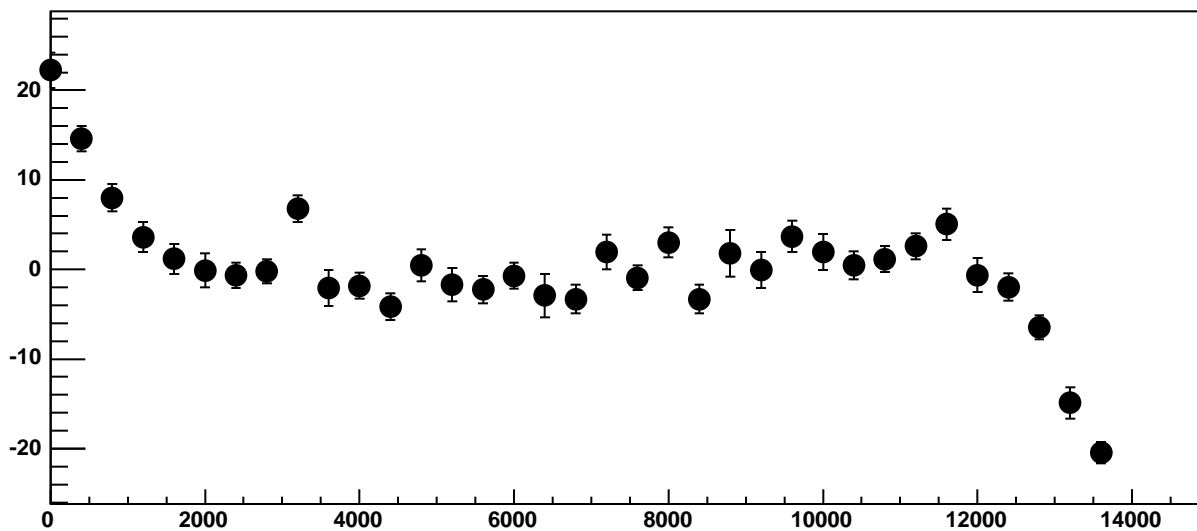


χ^2 / ndf 60.57 / 23
p0 179.5 ± 0.7442
p1 0.01912 ± 0.0001145

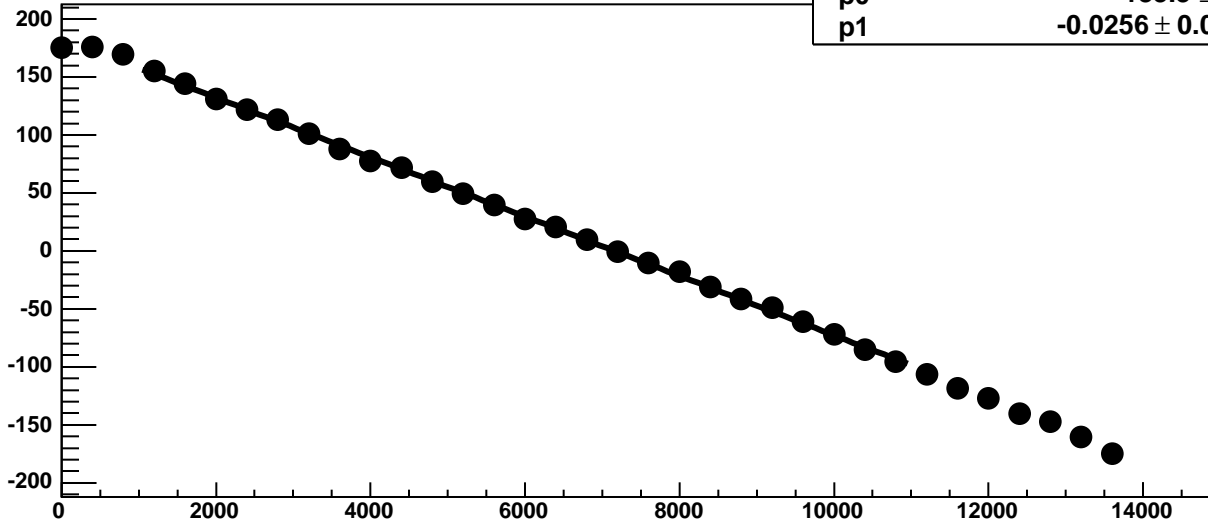
Chip 4, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

29.03 / 23

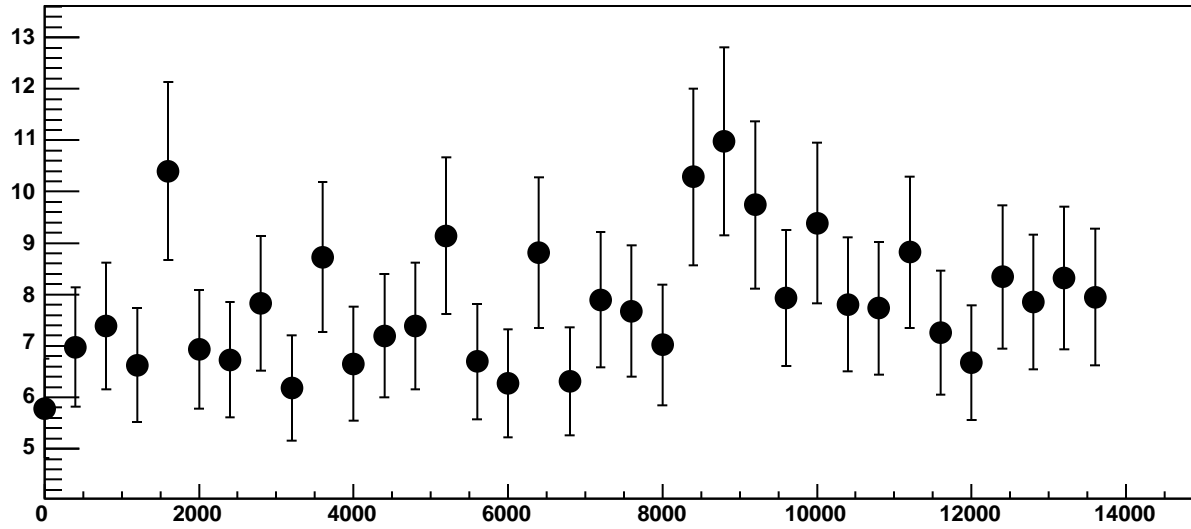
p0

183.3 ± 0.7893

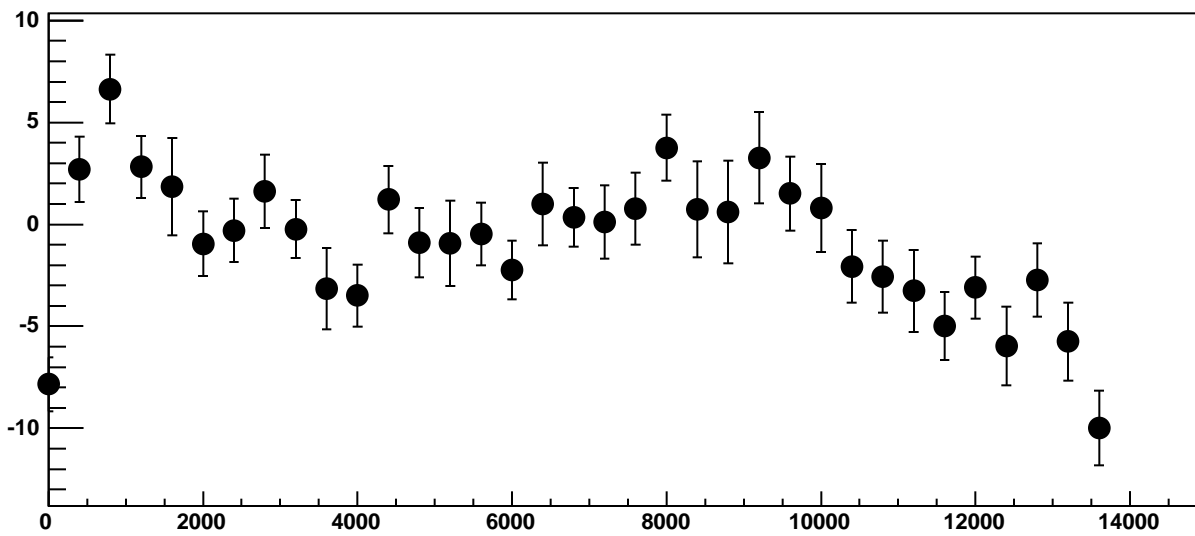
p1

-0.0256 ± 0.0001247

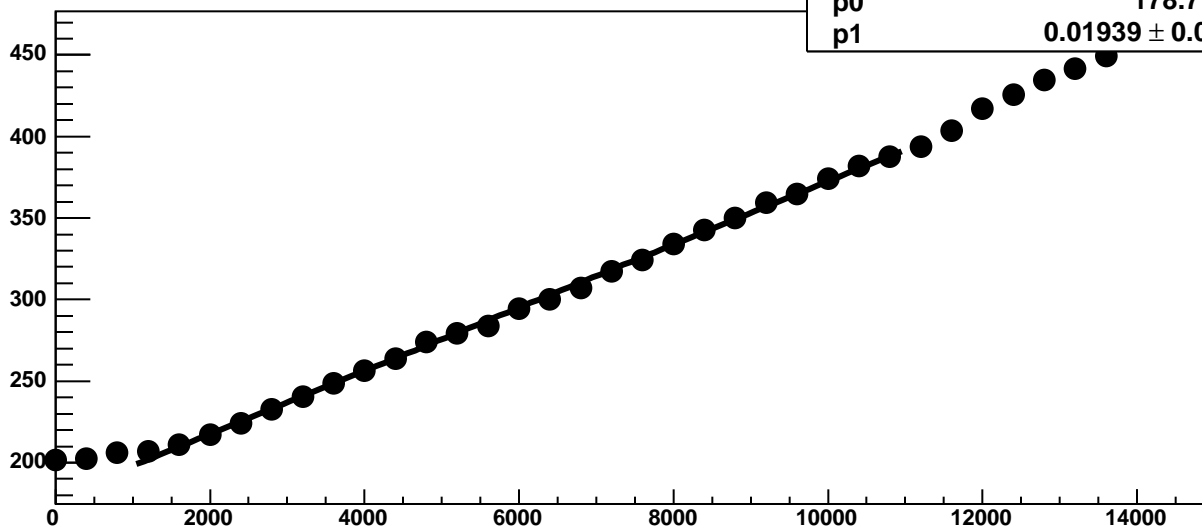
Chip 4, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

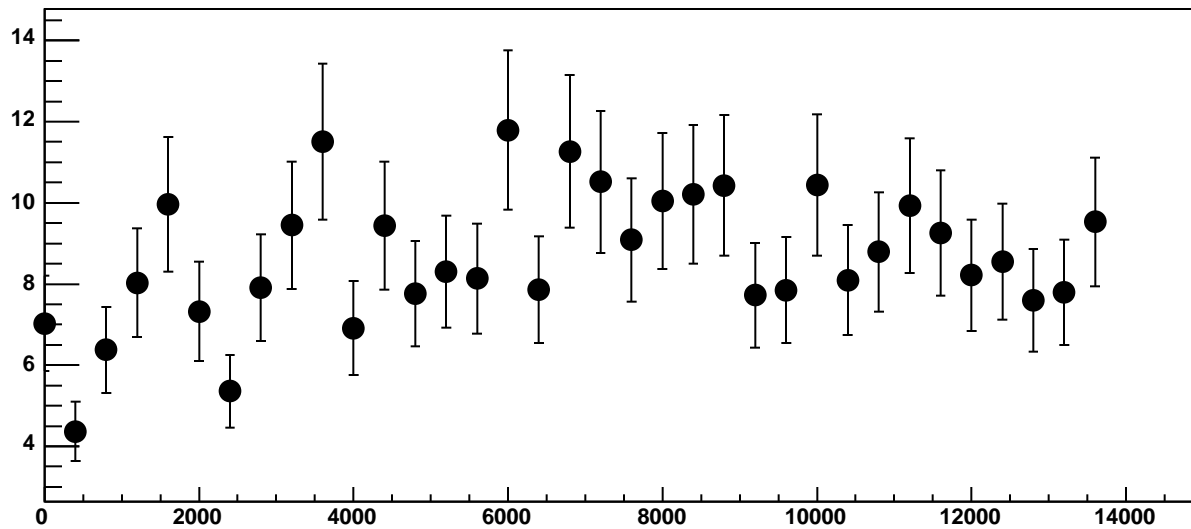


Chip 4, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

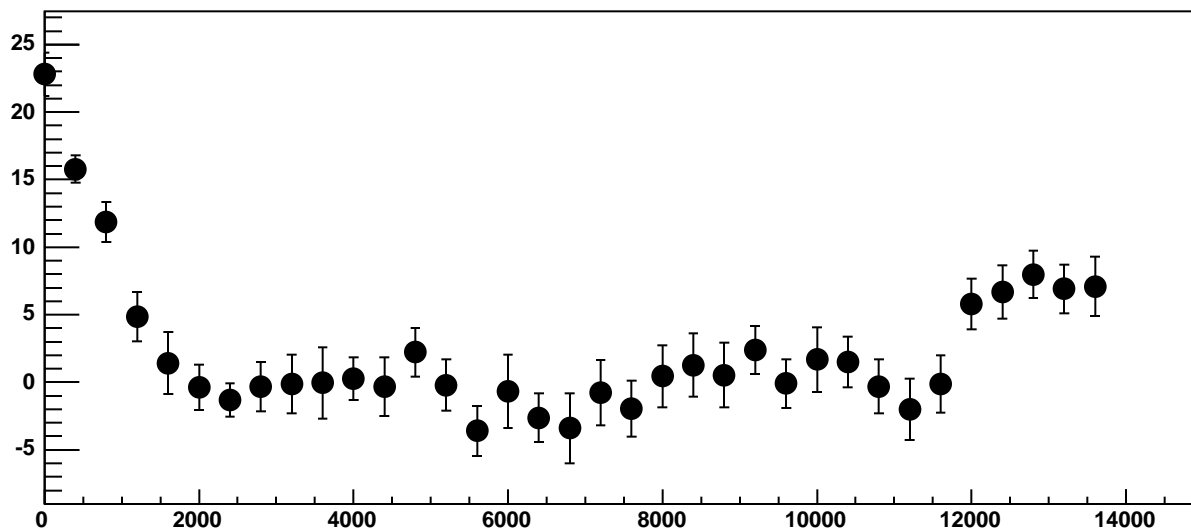


χ^2 / ndf 22.17 / 23
p0 178.7 ± 0.838
p1 0.01939 ± 0.0001316

Chip 4, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

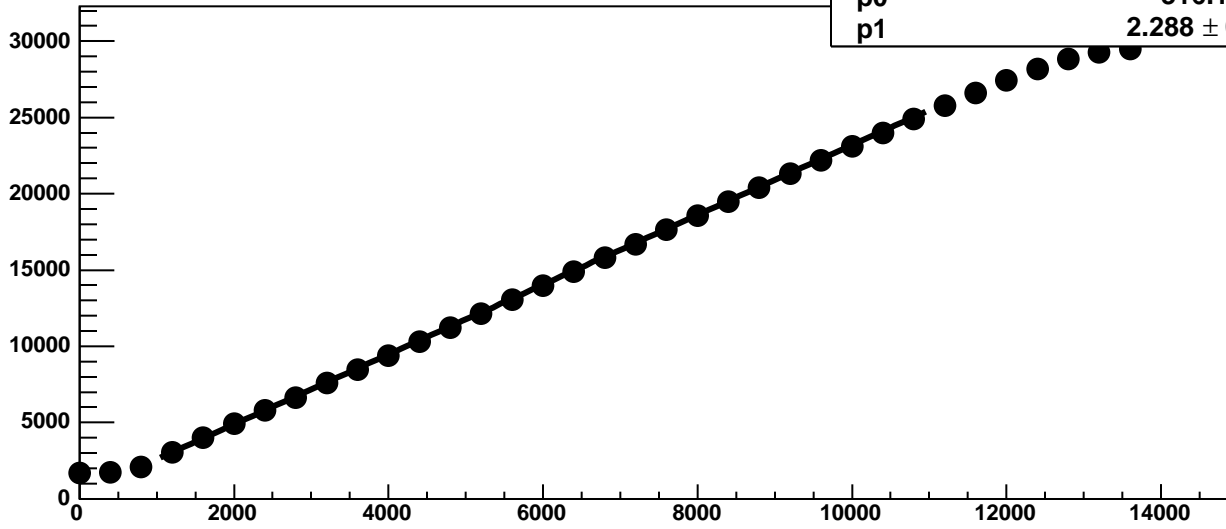


Chip 4, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

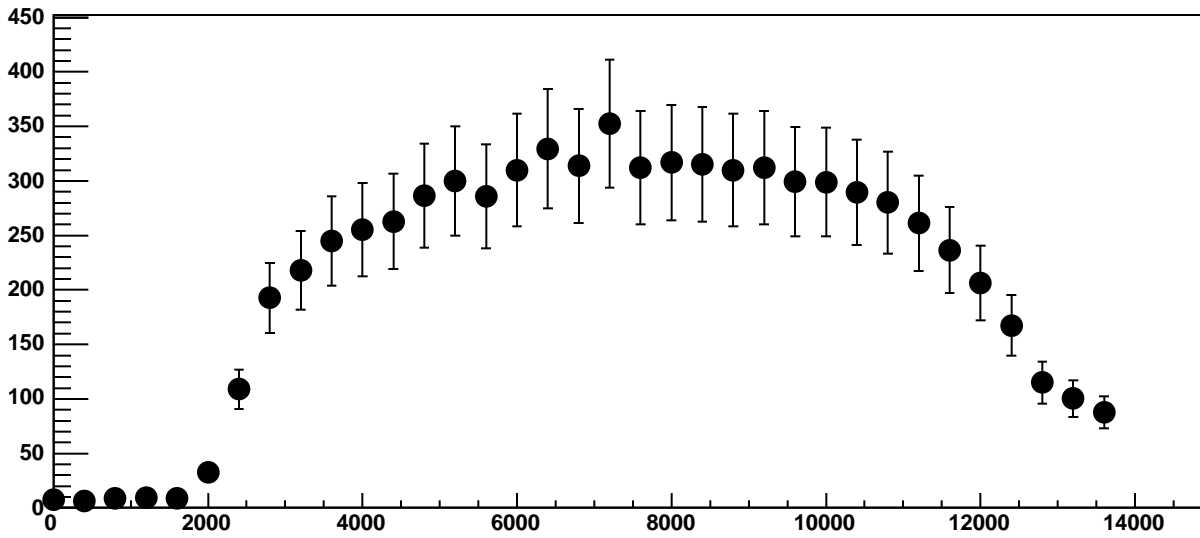


Chip 4, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

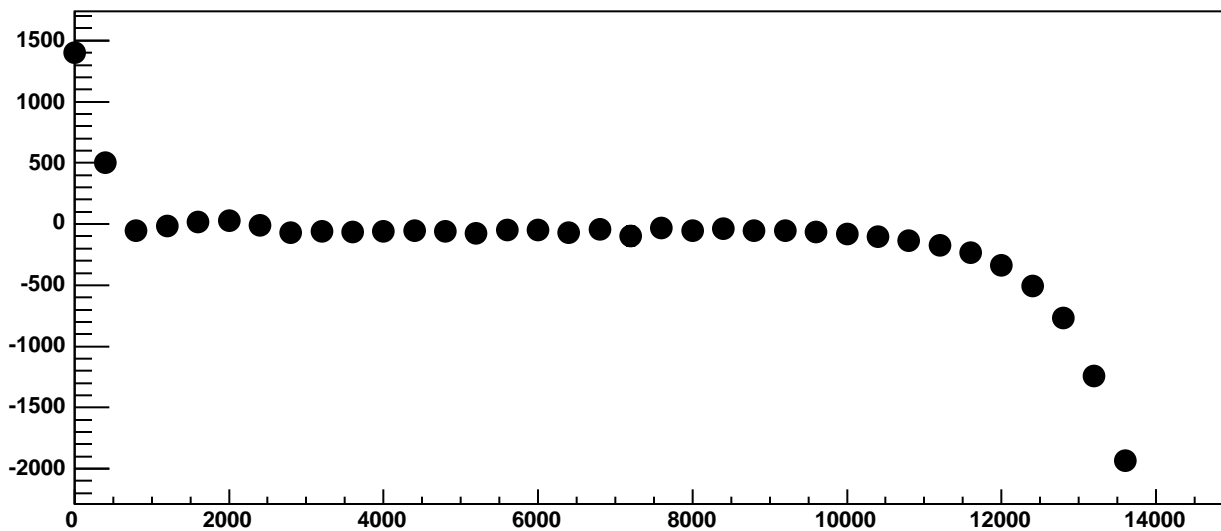
χ^2 / ndf 156.9 / 23
p0 316.1 ± 3.849
p1 2.288 ± 0.00238



Chip 4, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC

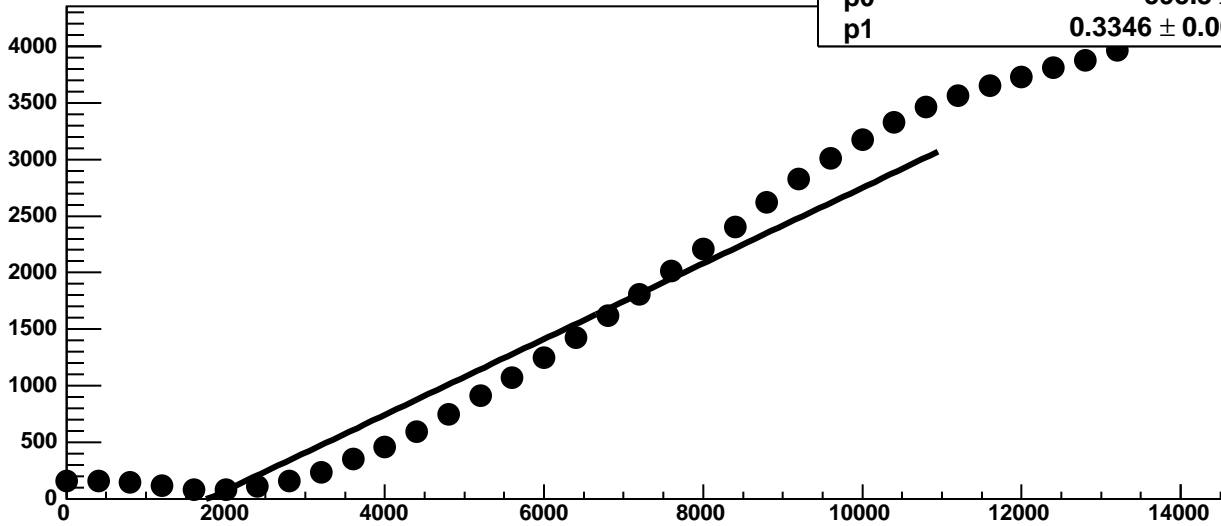


Chip 4, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC

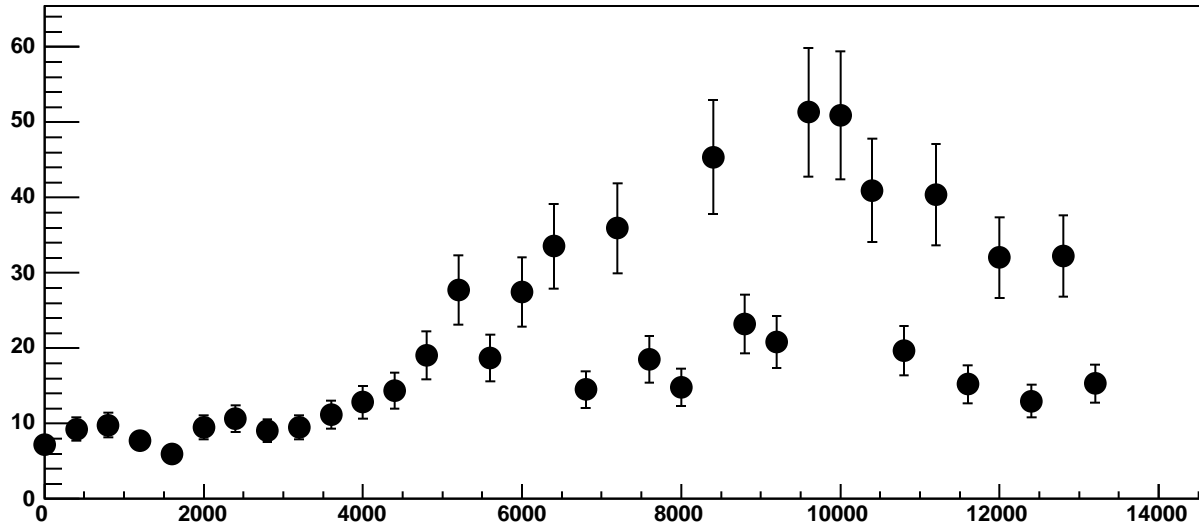


Chip 4, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

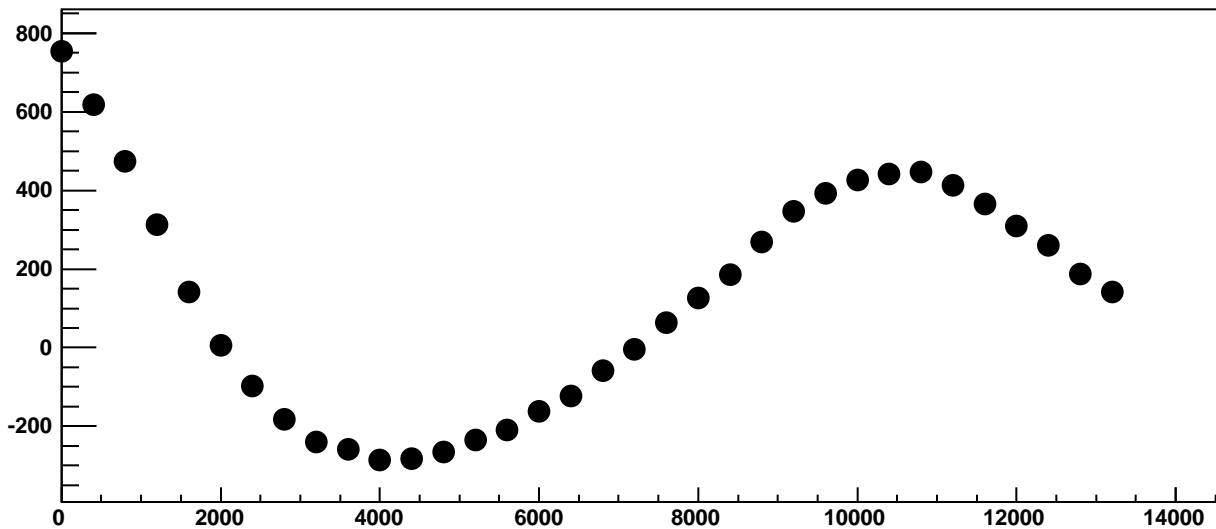
χ^2 / ndf 1.229e+05 / 23
p0 -595.3 ± 1.083
p1 0.3346 ± 0.0002543



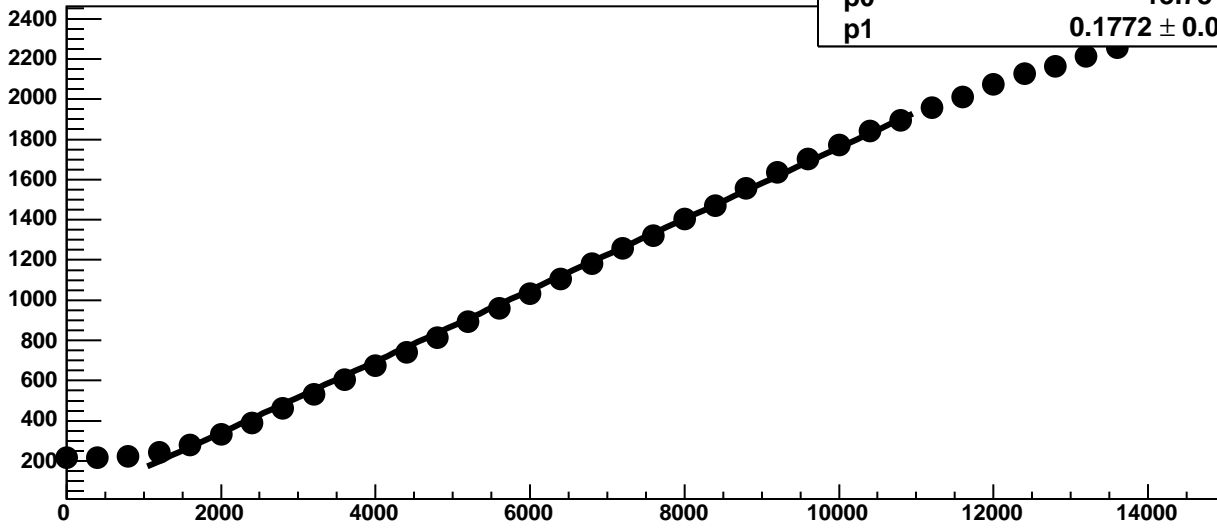
Chip 4, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



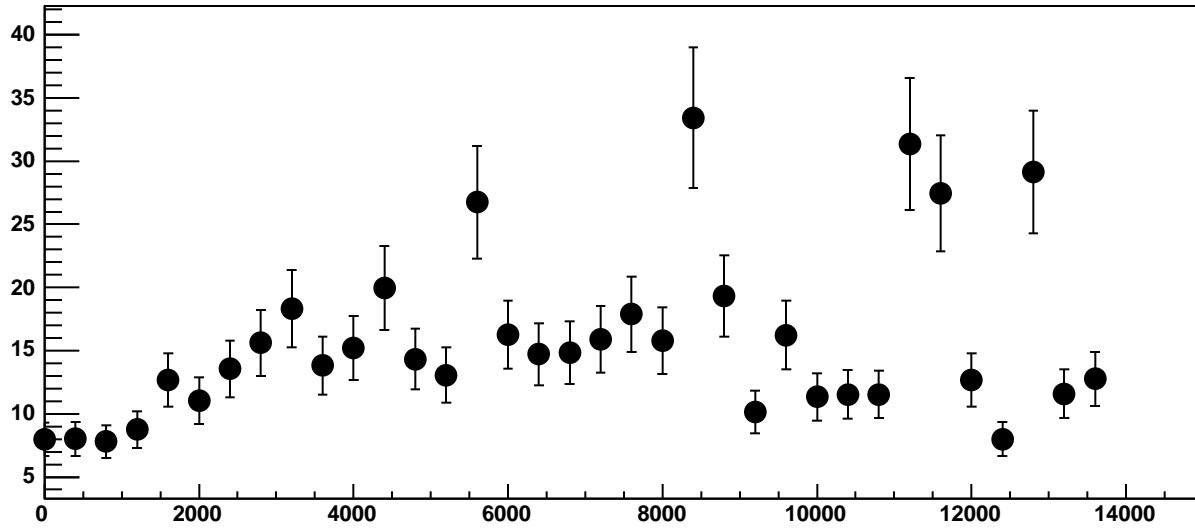
Chip 4, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



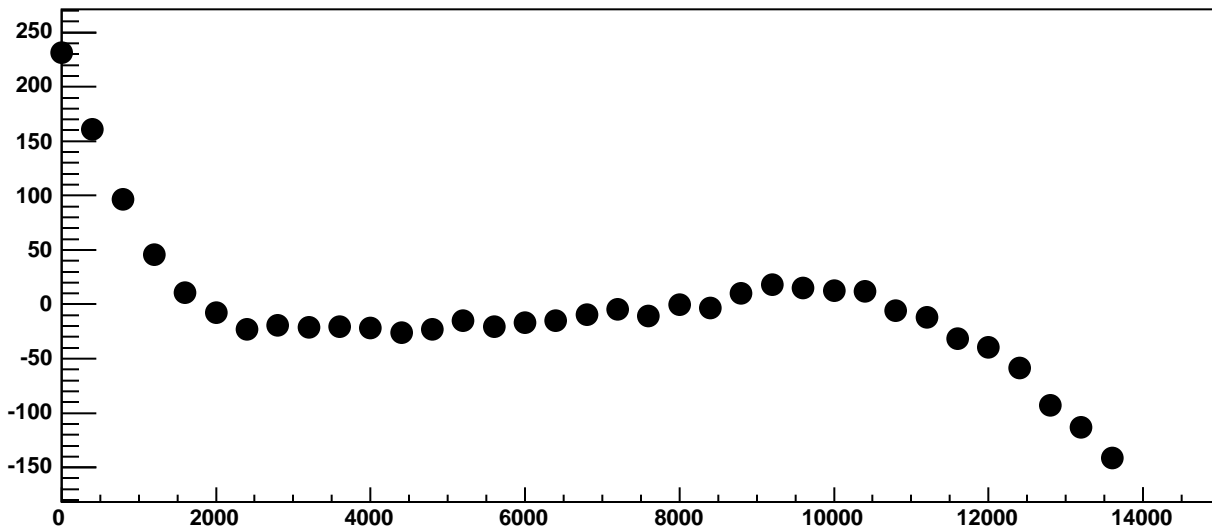
Chip 4, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



Chip 4, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

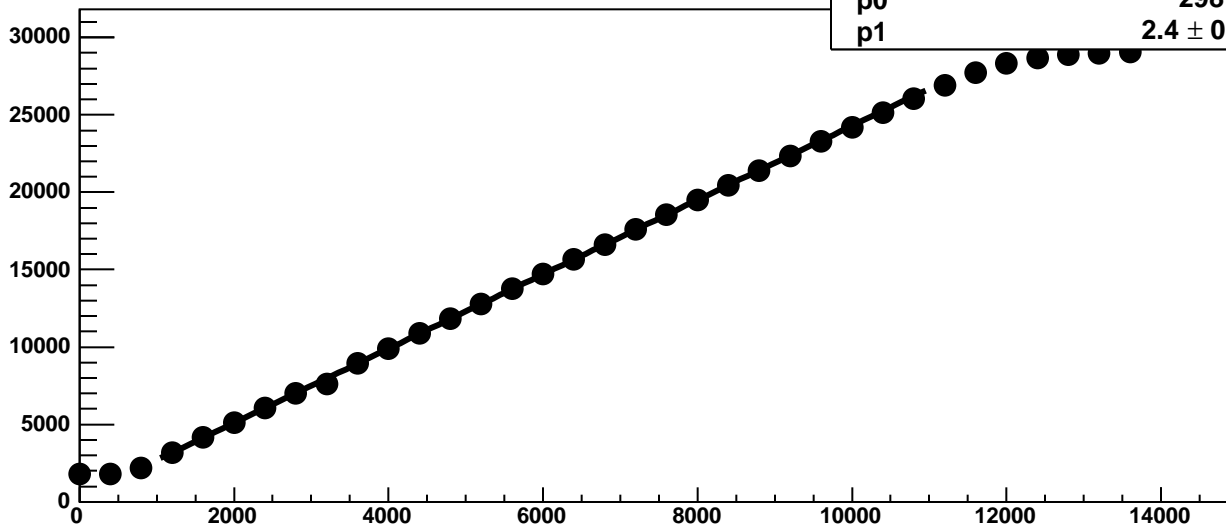


Chip 4, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

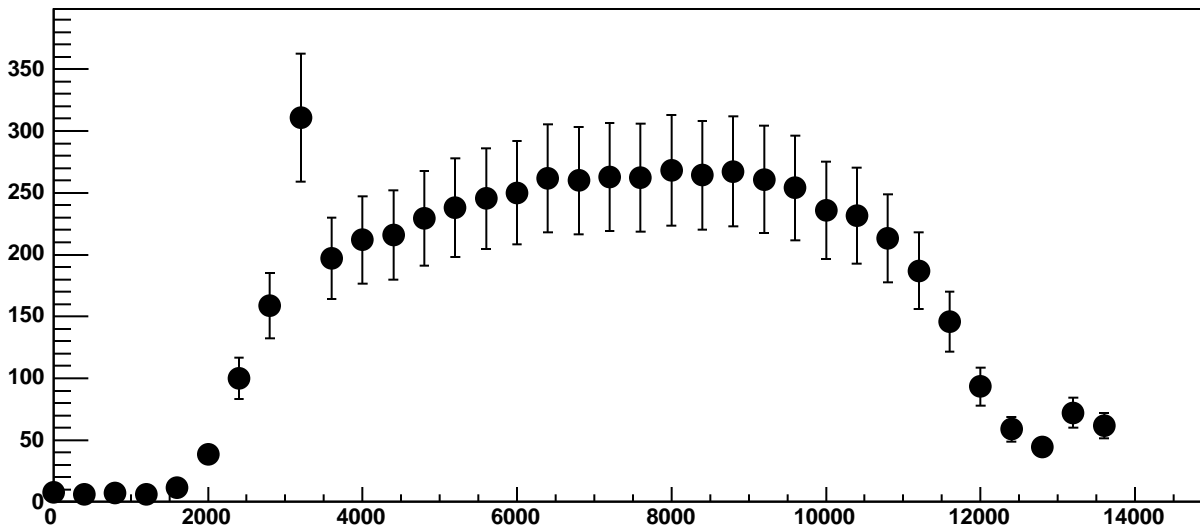


Chip 4, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC

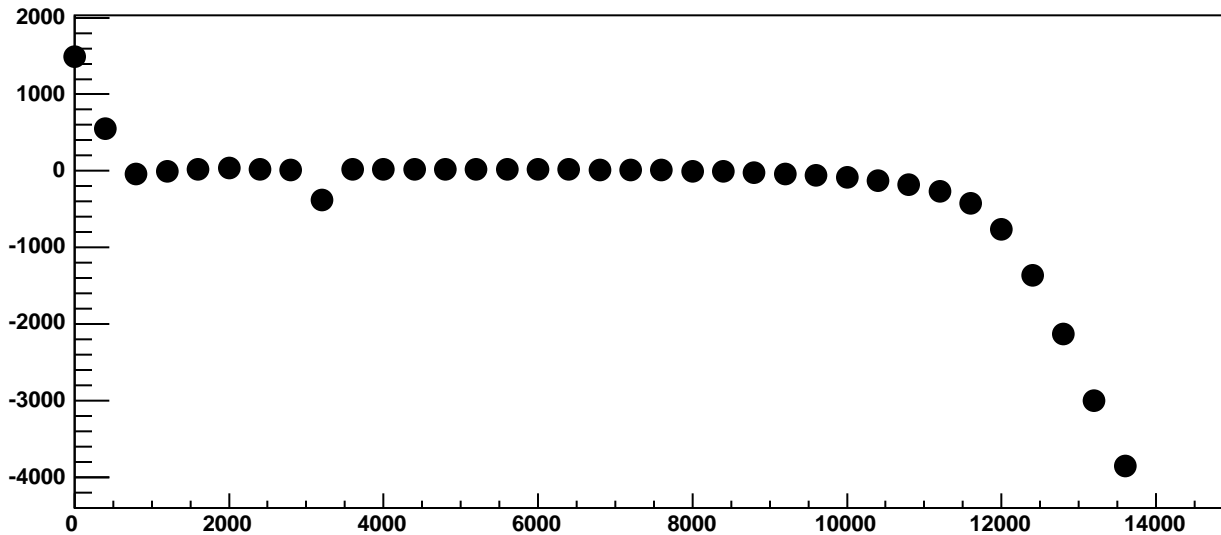
χ^2 / ndf 148.8 / 23
p0 298 ± 2.949
p1 2.4 ± 0.001944



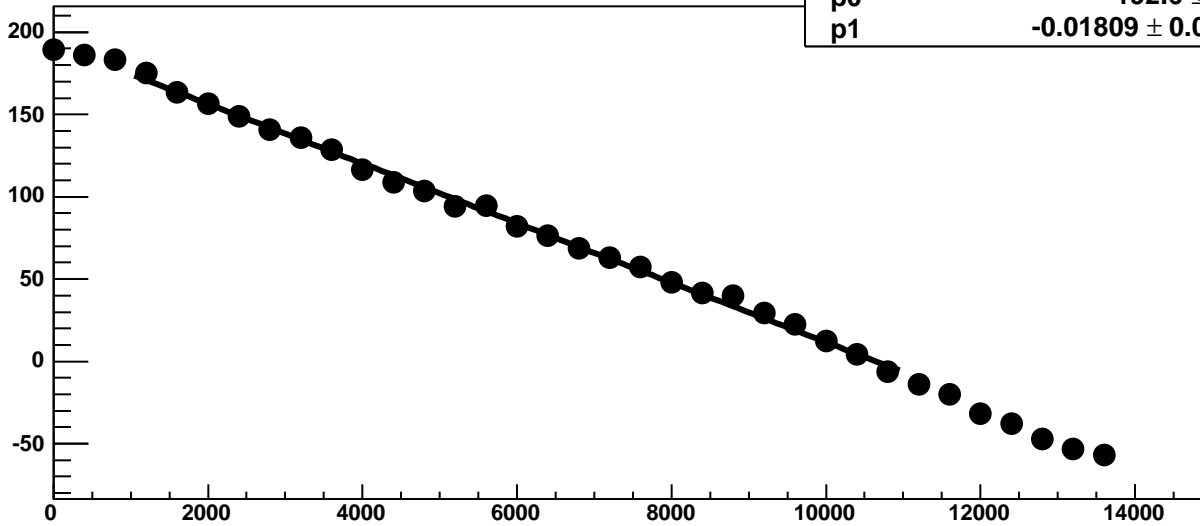
Chip 4, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

31.19 / 23

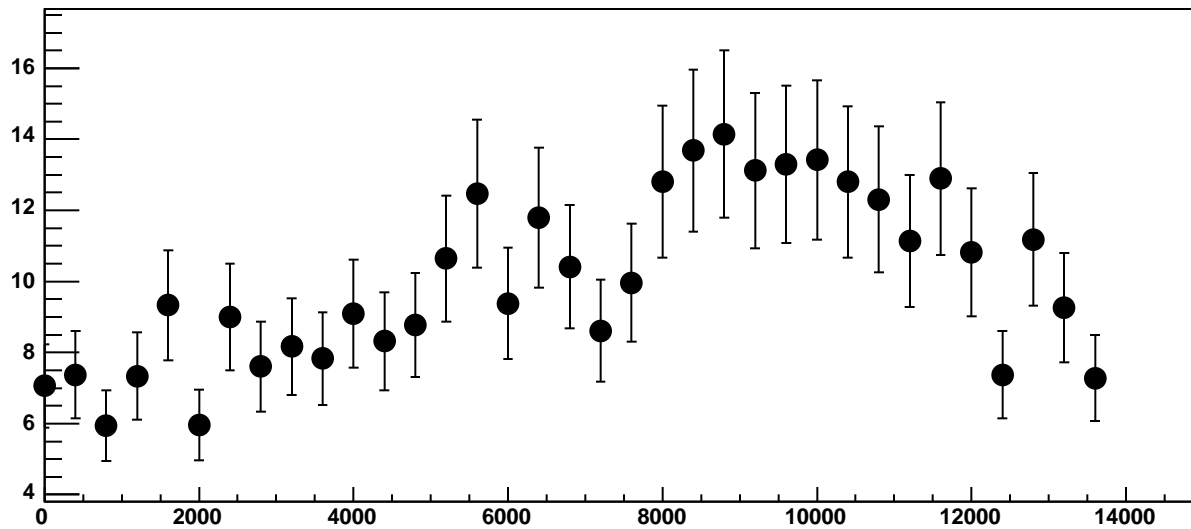
p0

192.6 ± 0.8958

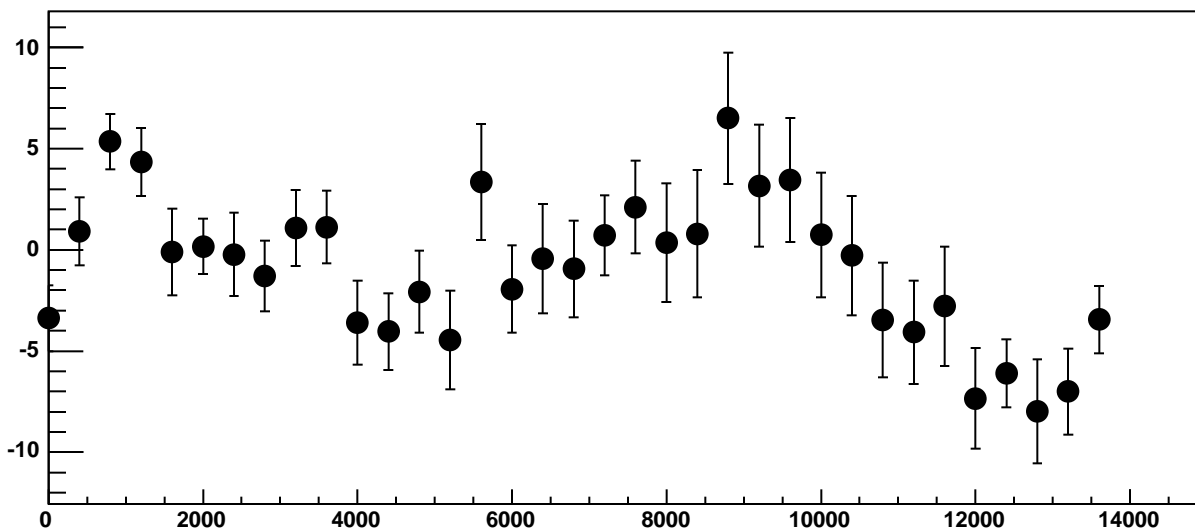
p1

-0.01809 ± 0.0001599

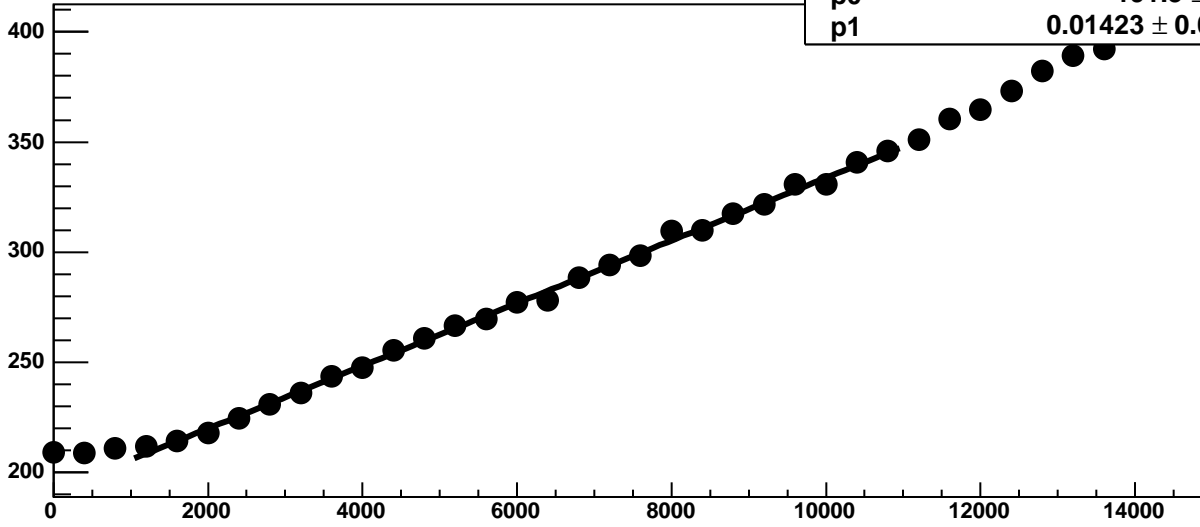
Chip 4, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

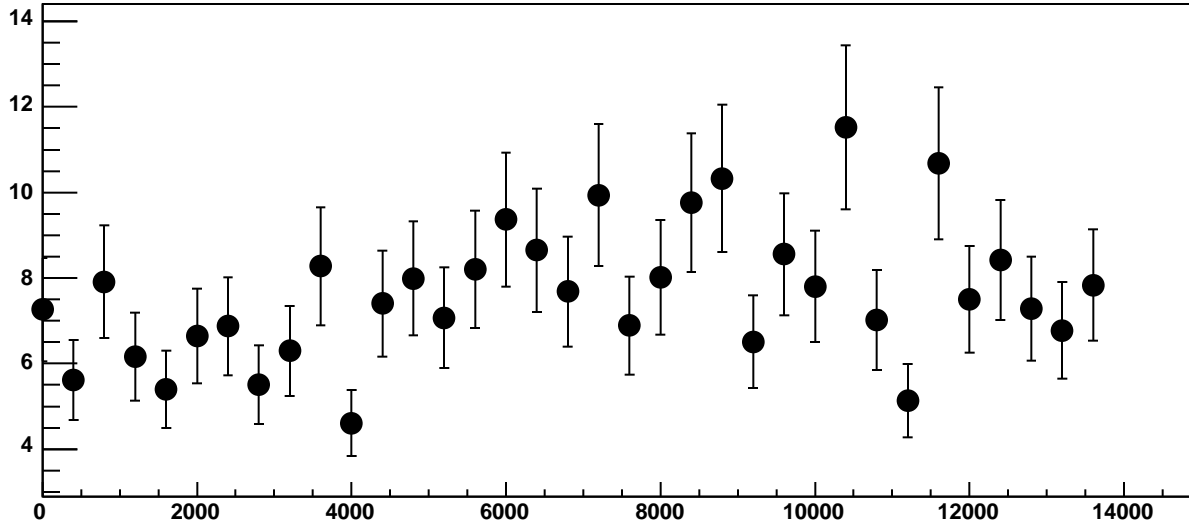


Chip 4, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

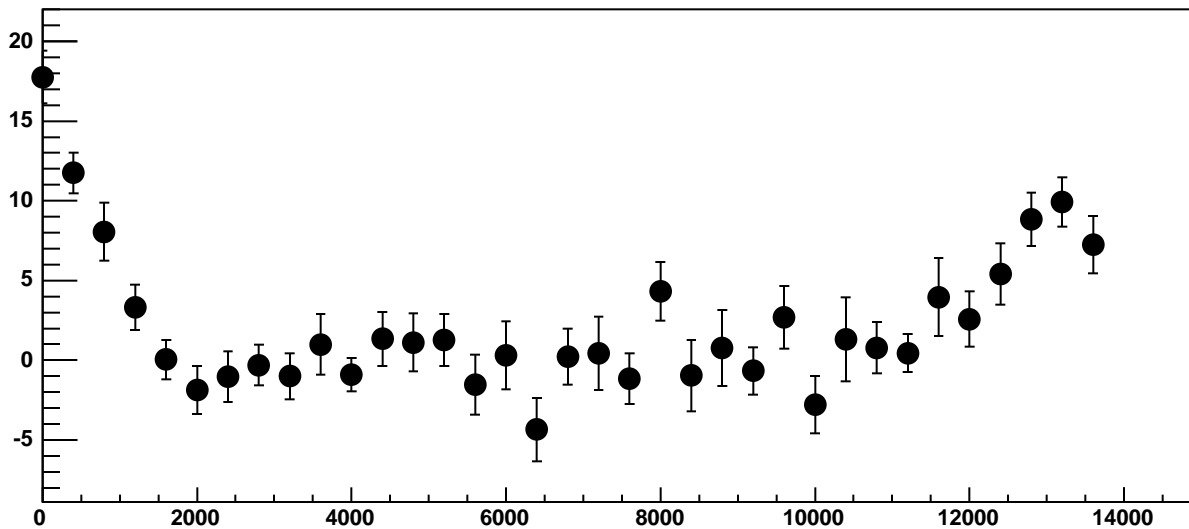


χ^2 / ndf 27.38 / 23
p0 191.3 ± 0.6815
p1 0.01423 ± 0.0001131

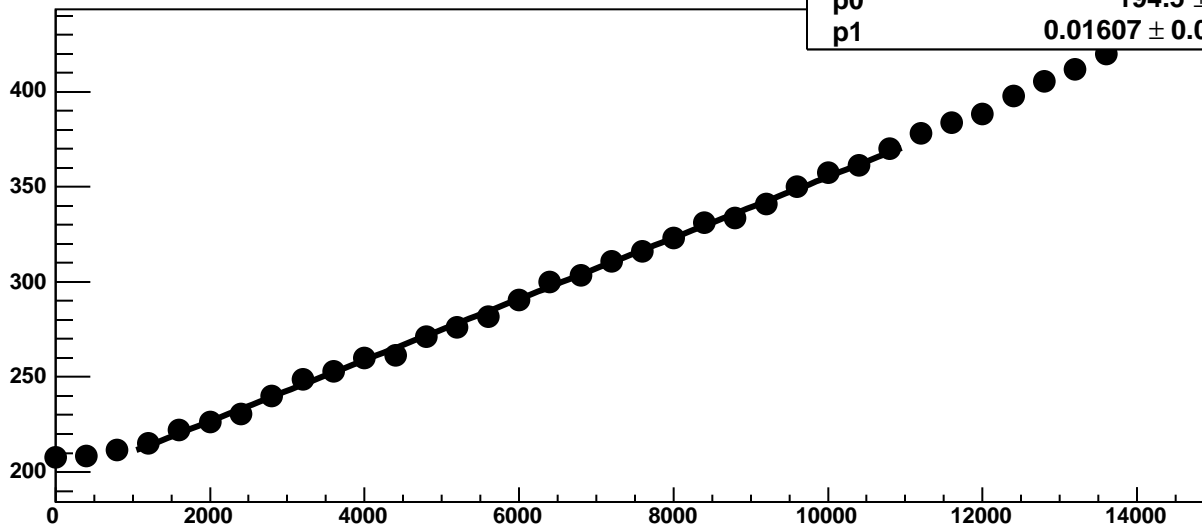
Chip 4, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

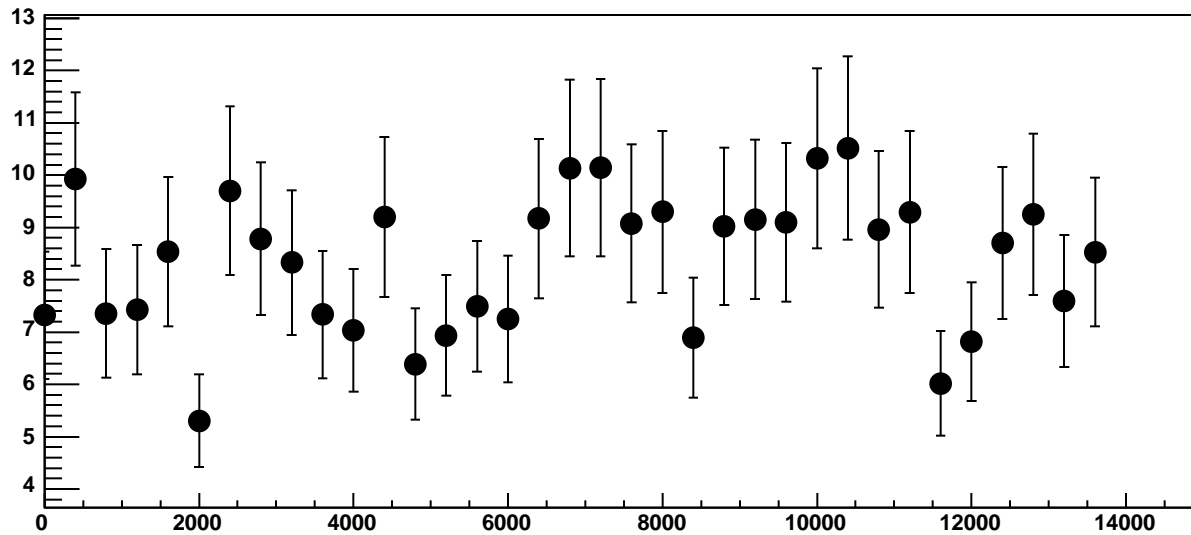


Chip 4, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

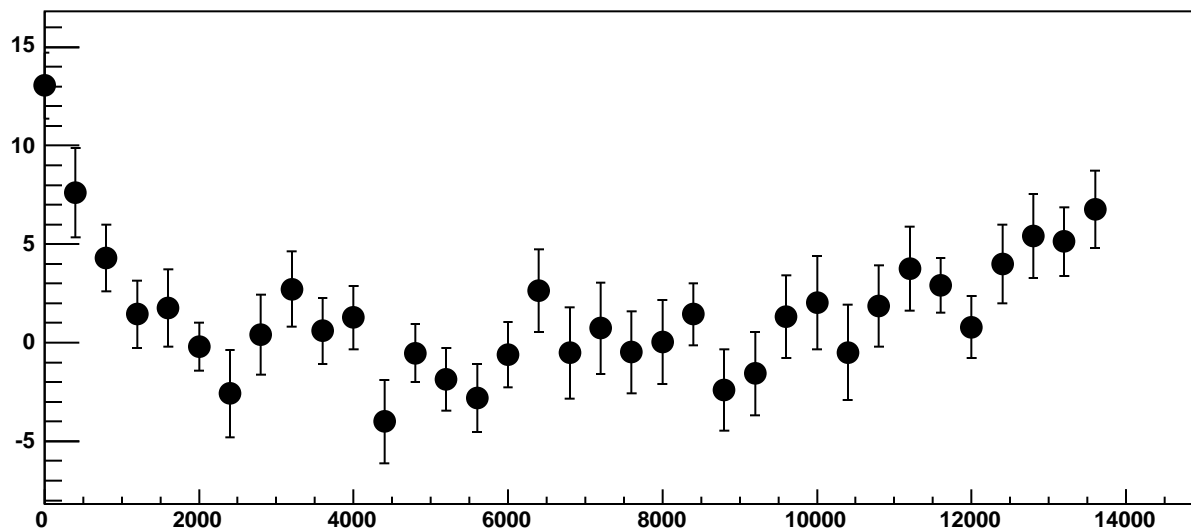


χ^2 / ndf 20.13 / 23
p0 194.5 ± 0.8148
p1 0.01607 ± 0.0001322

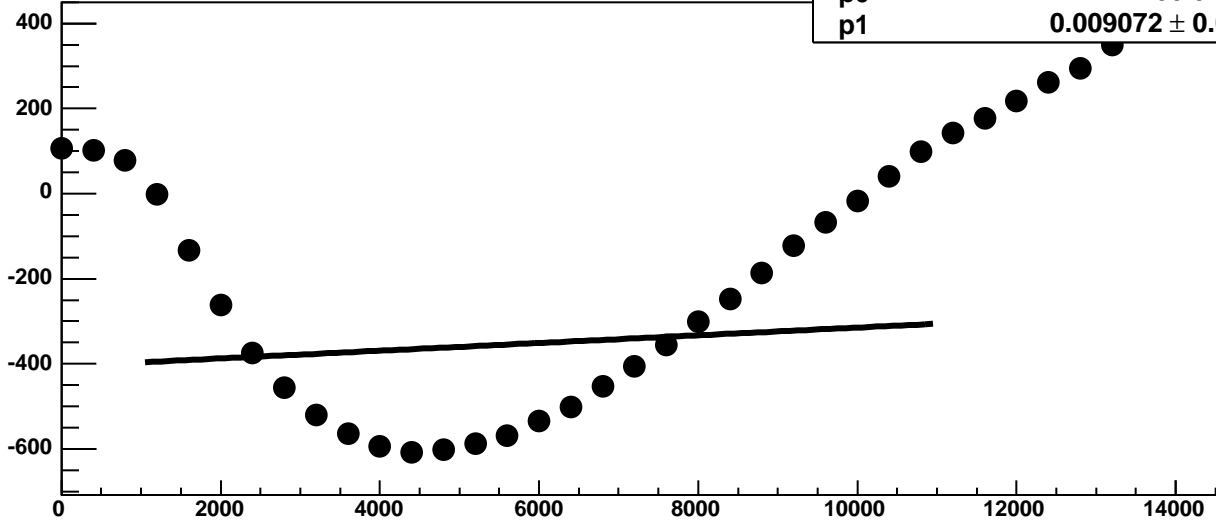
Chip 4, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC

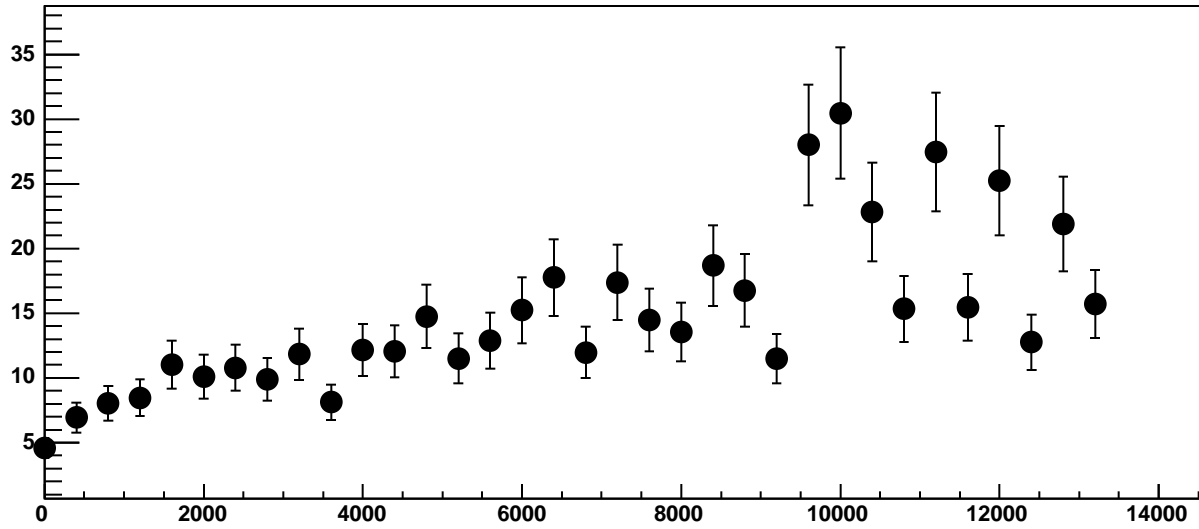


Chip 4, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

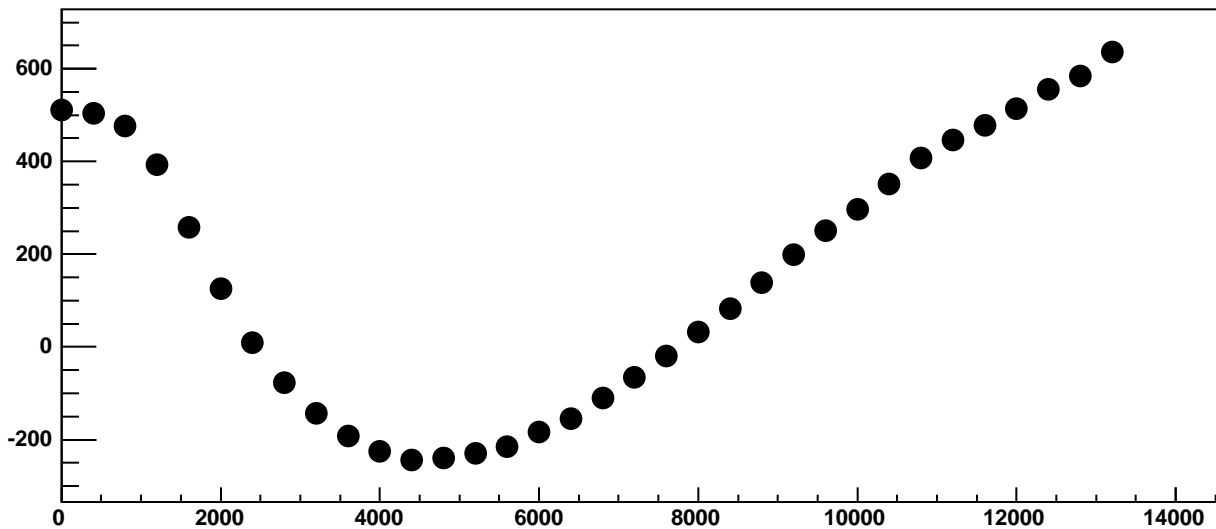


χ^2 / ndf 1.358e+05 / 23
p0 -405.5 ± 1.173
p1 0.009072 ± 0.000214

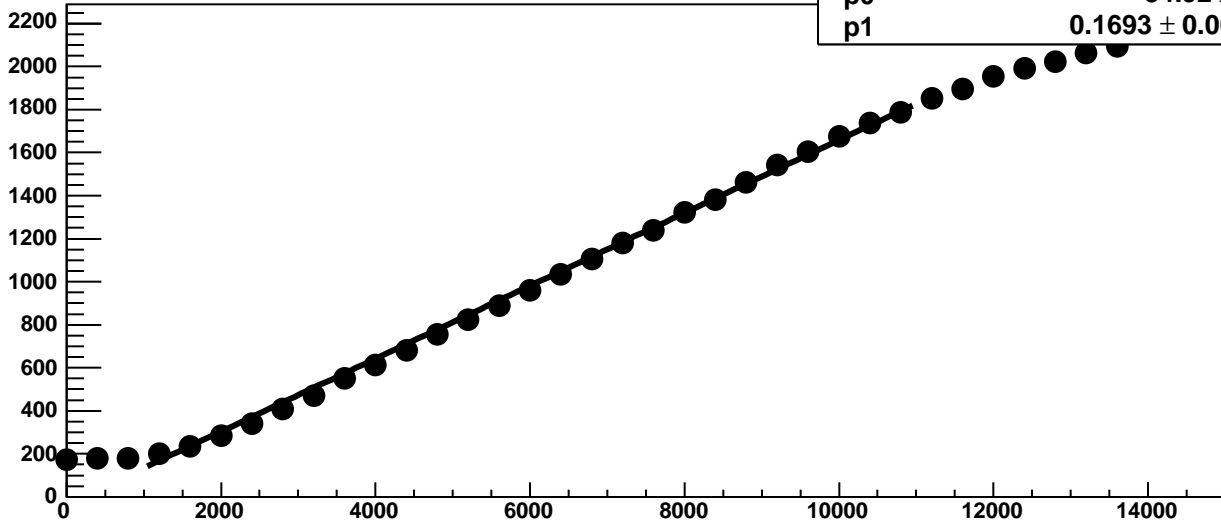
Chip 4, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



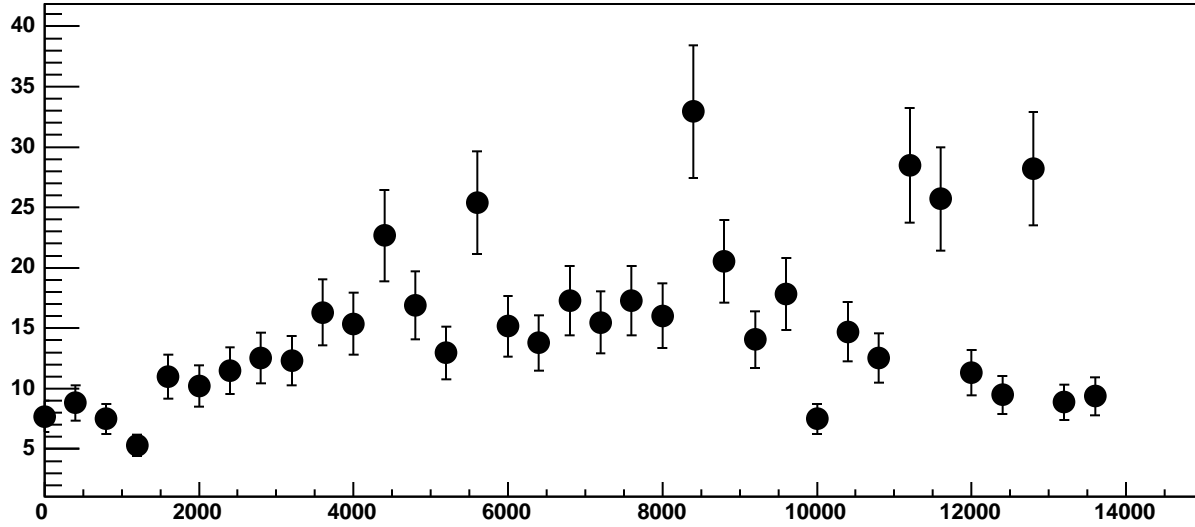
Chip 4, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



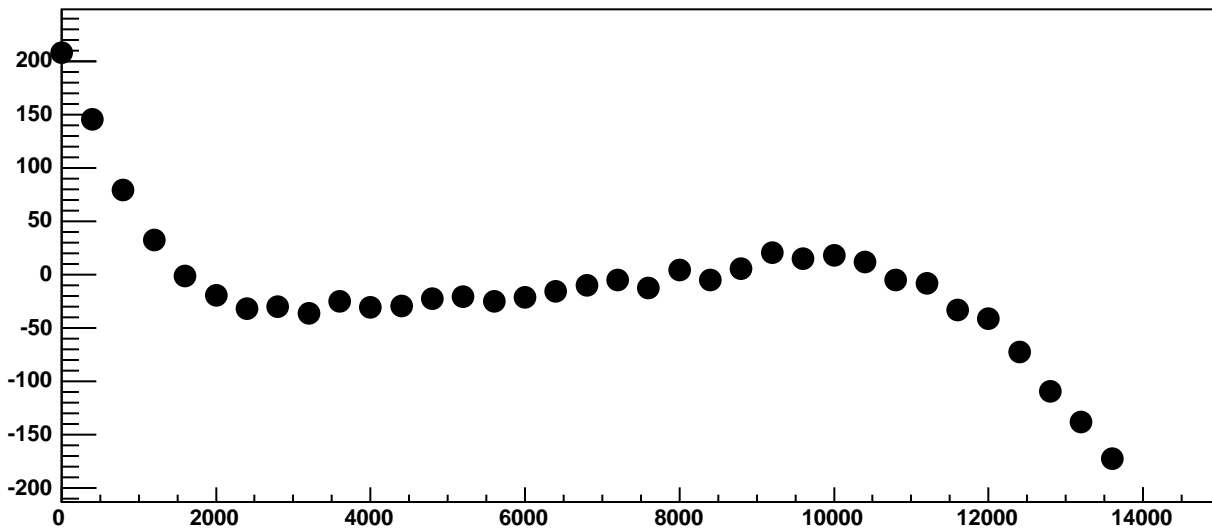
Chip 4, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



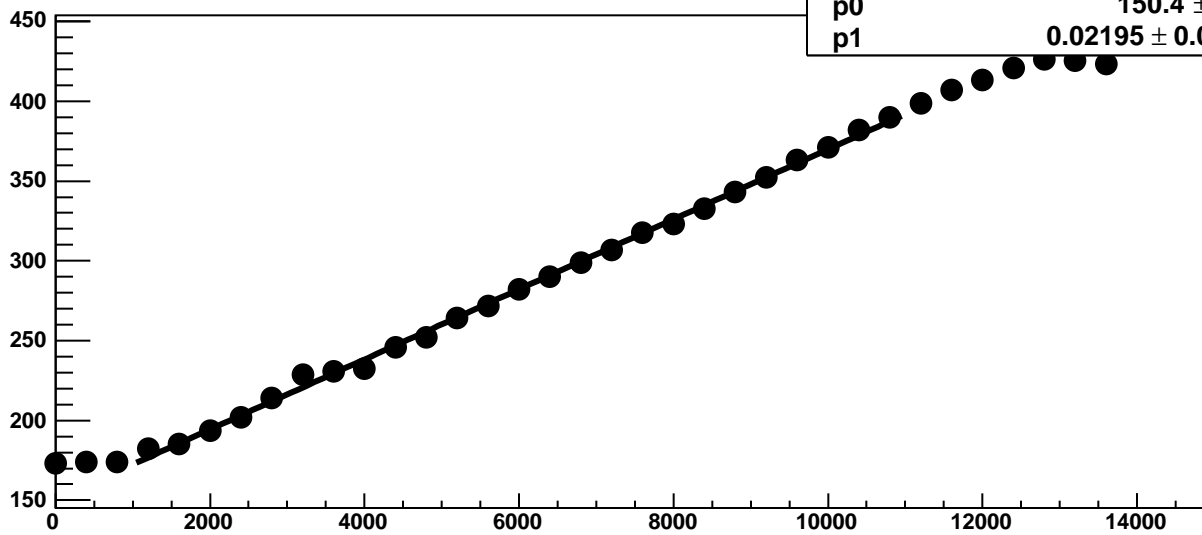
Chip 4, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



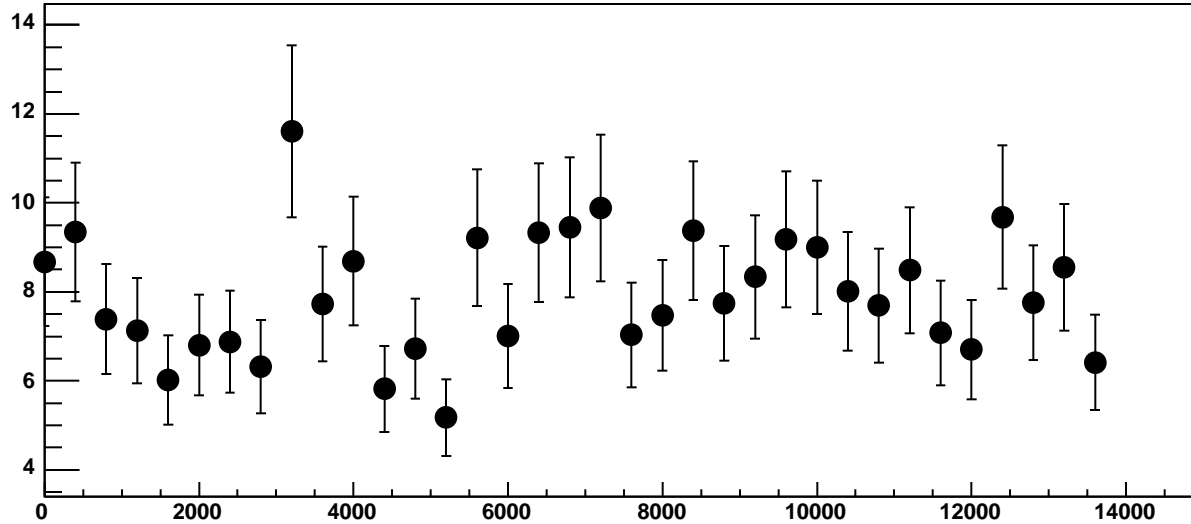
Chip 4, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



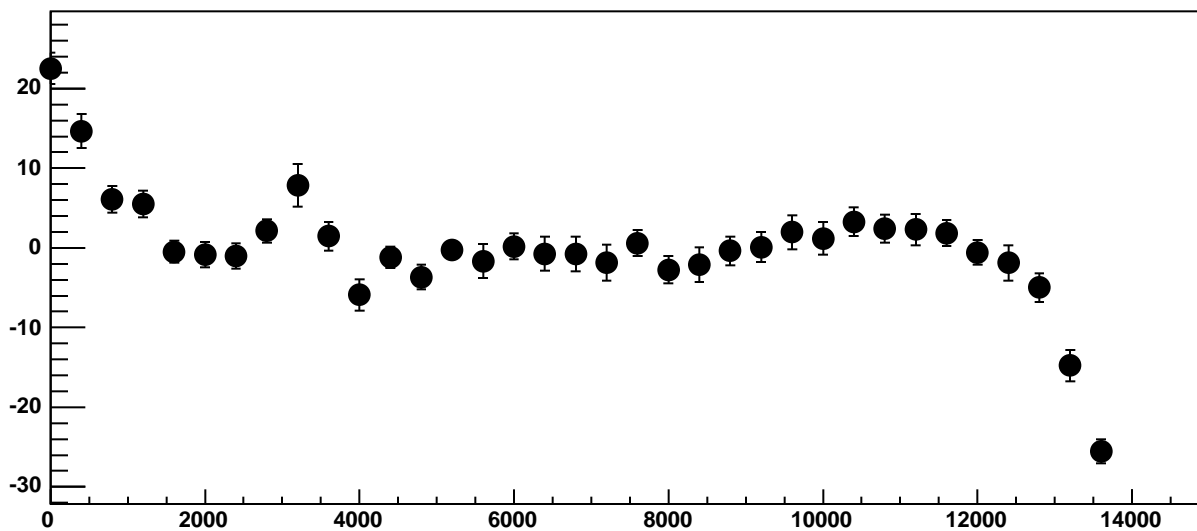
Chip 4, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



Chip 4, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

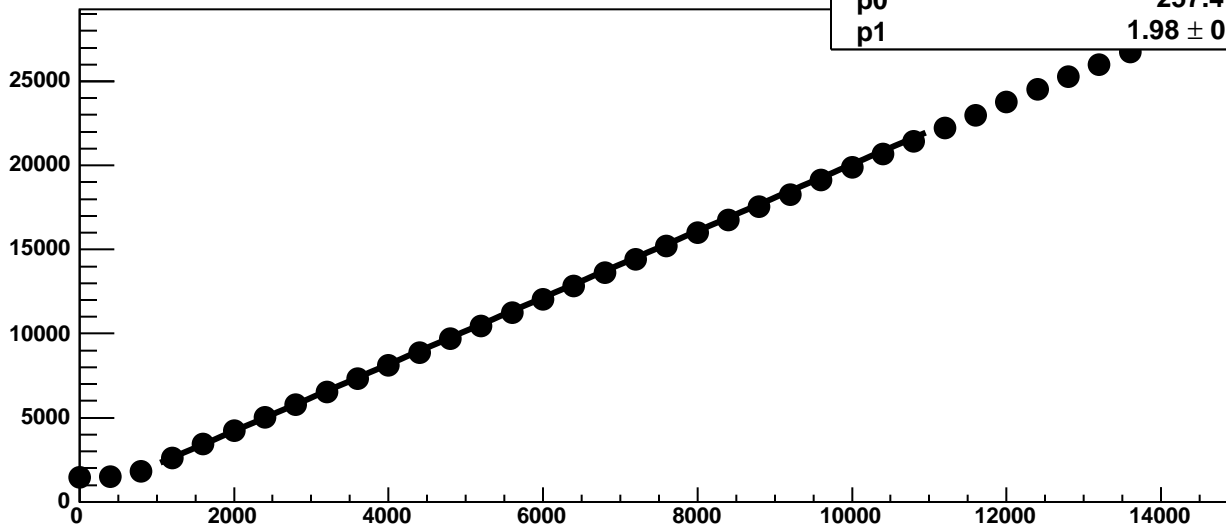


Chip 4, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

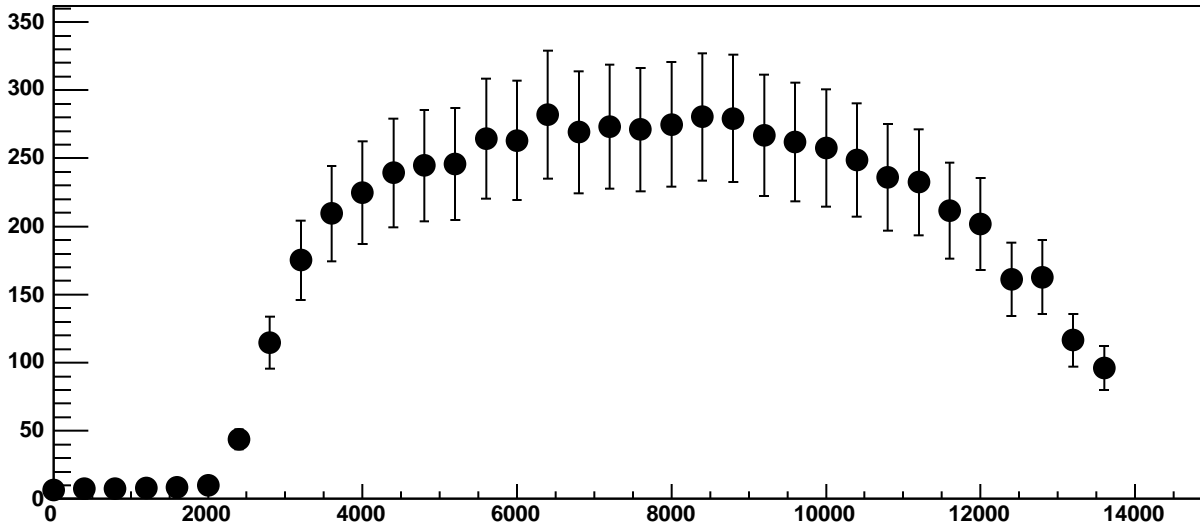


Chip 4, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC

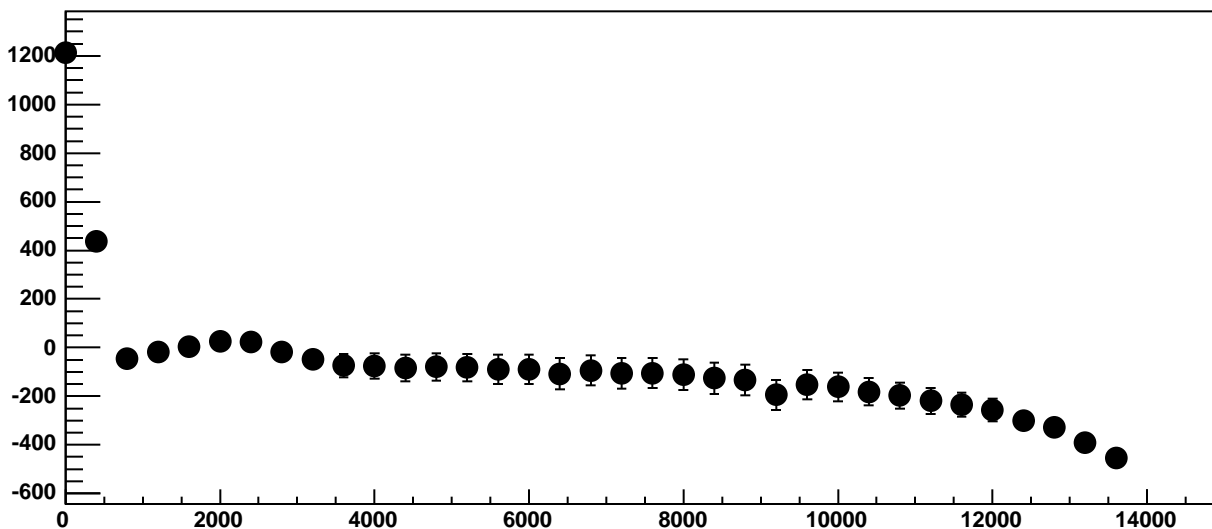
χ^2 / ndf 308 / 23
p0 257.4 ± 3.257
p1 1.98 ± 0.001898



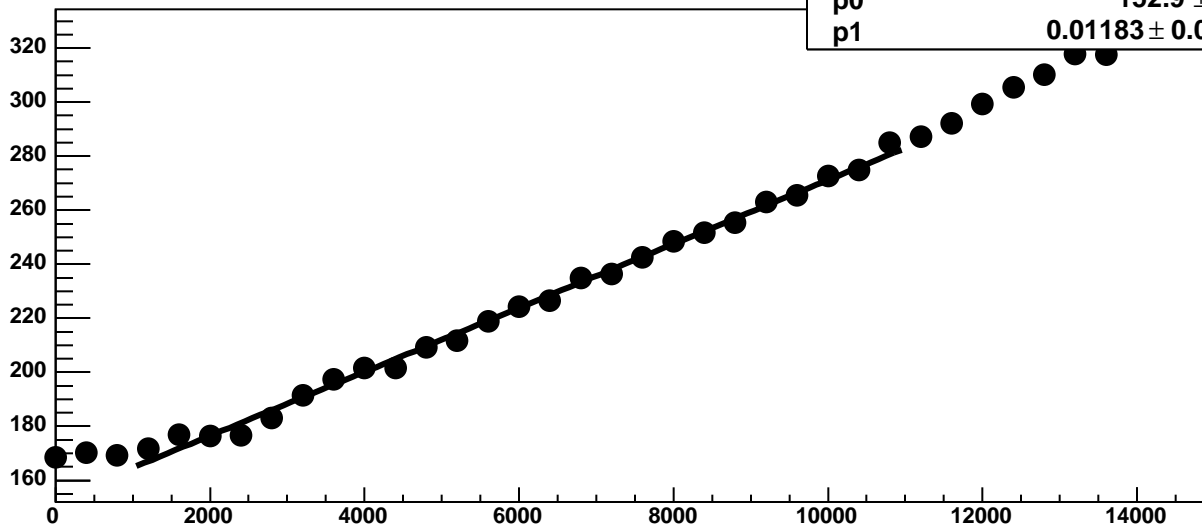
Chip 4, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC

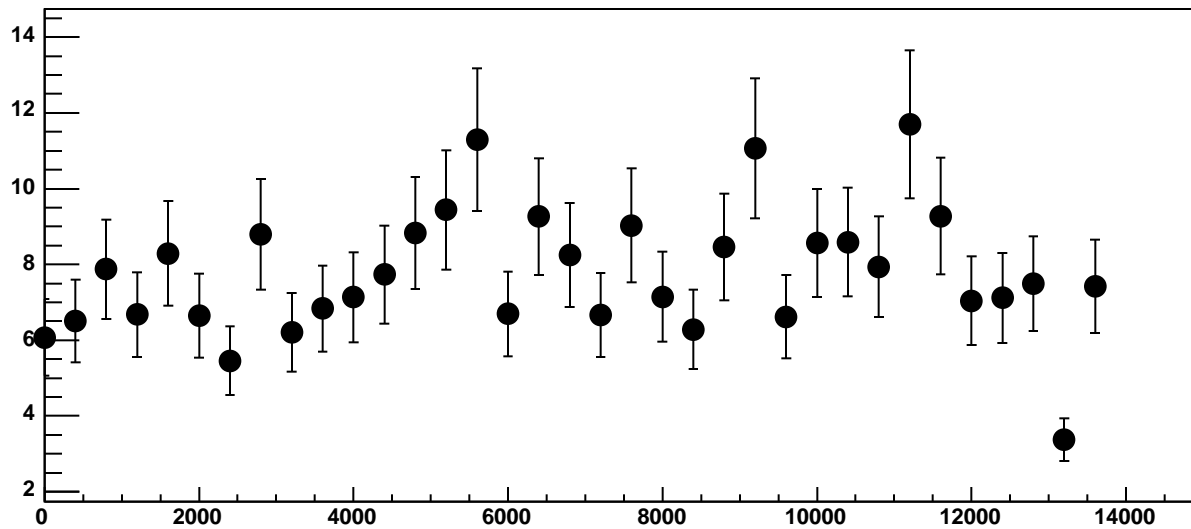


Chip 4, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

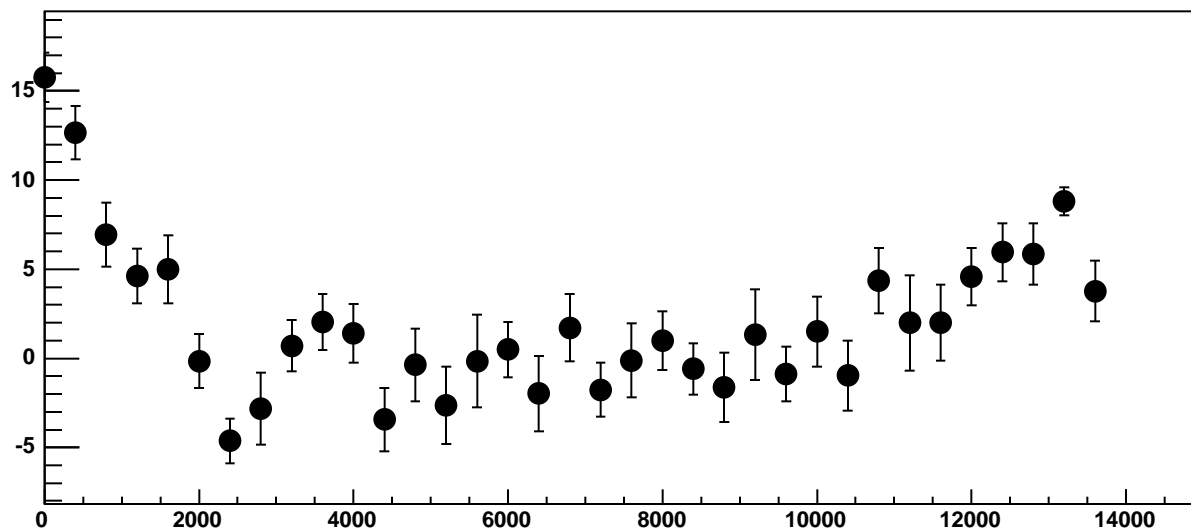


χ^2 / ndf 50.99 / 23
p0 152.9 ± 0.7534
p1 0.01183 ± 0.0001178

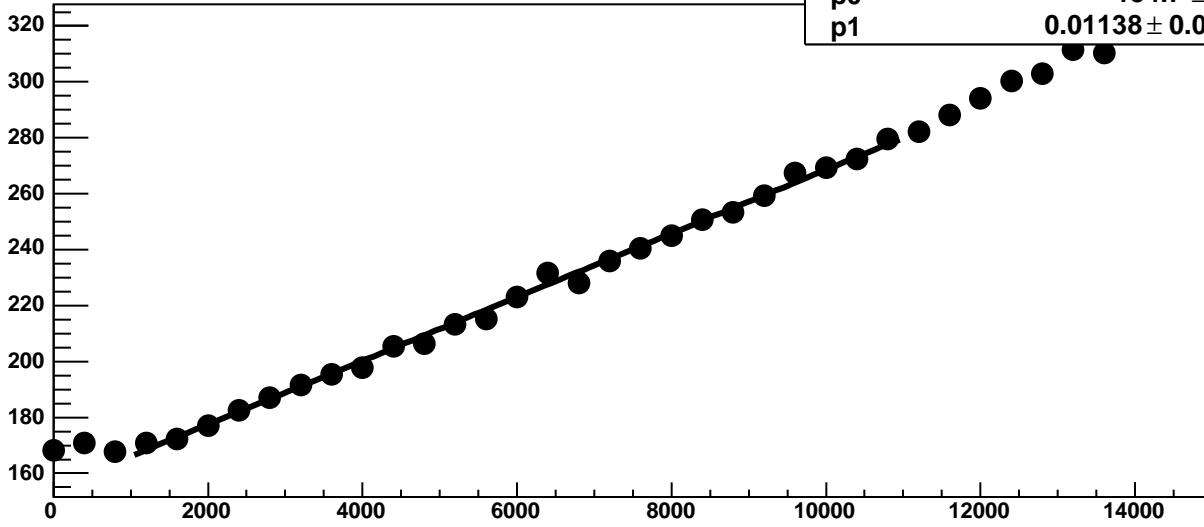
Chip 4, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

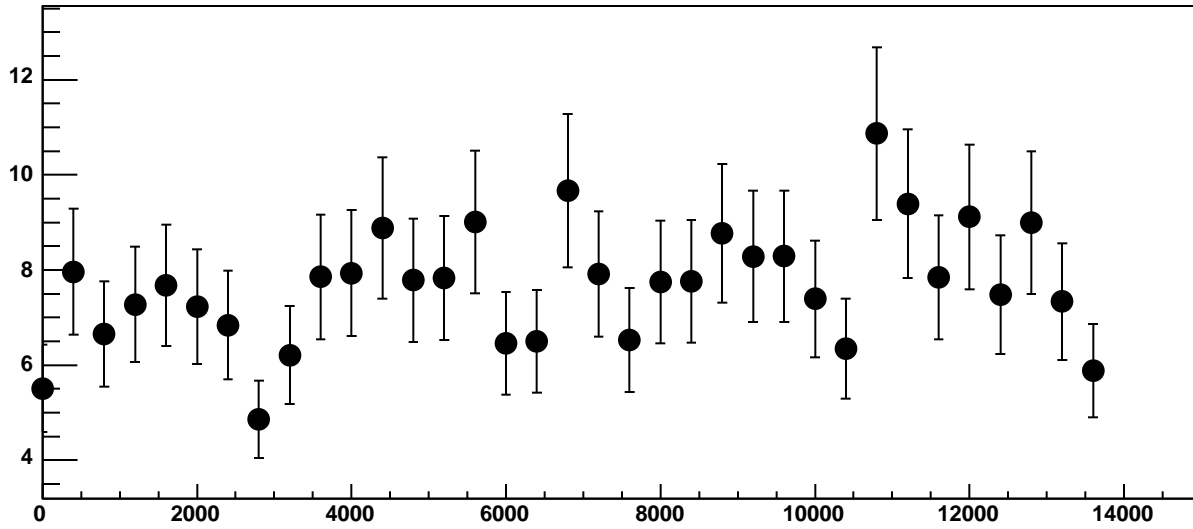


Chip 4, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

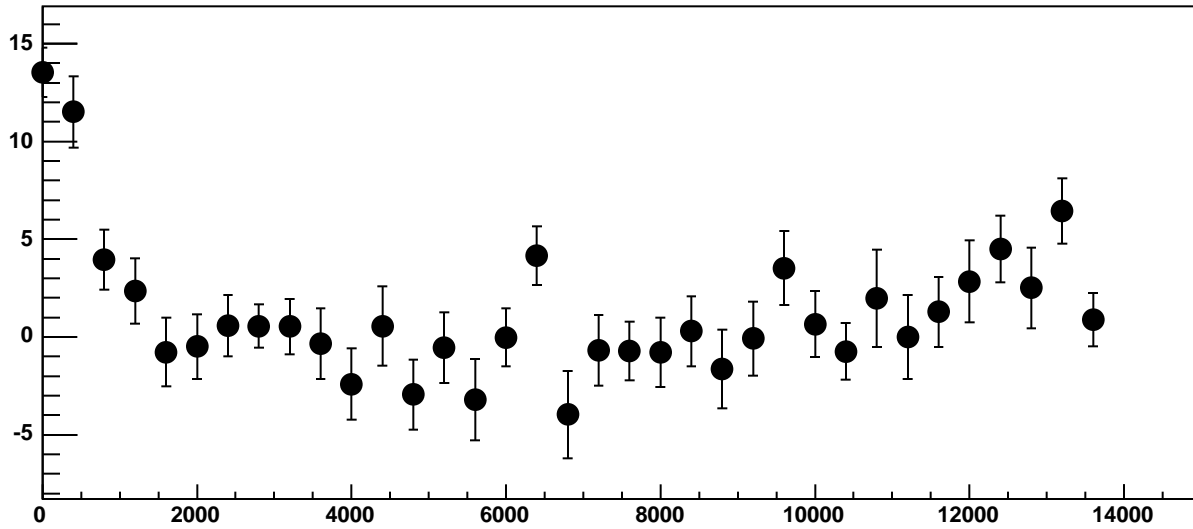


χ^2 / ndf 26.6 / 23
p0 154.7 ± 0.7467
p1 0.01138 ± 0.0001173

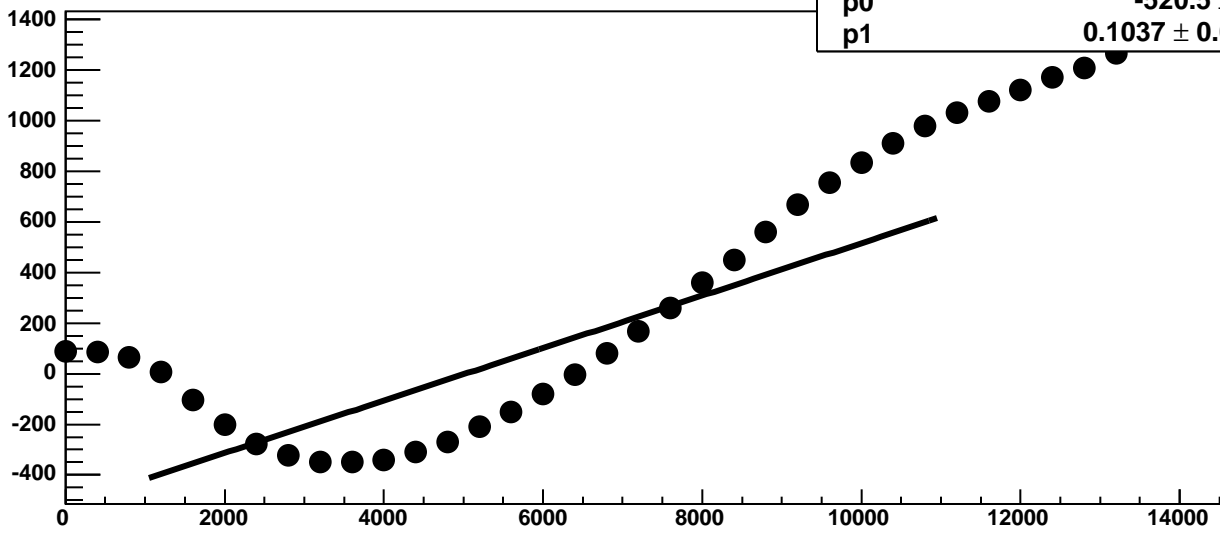
Chip 4, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

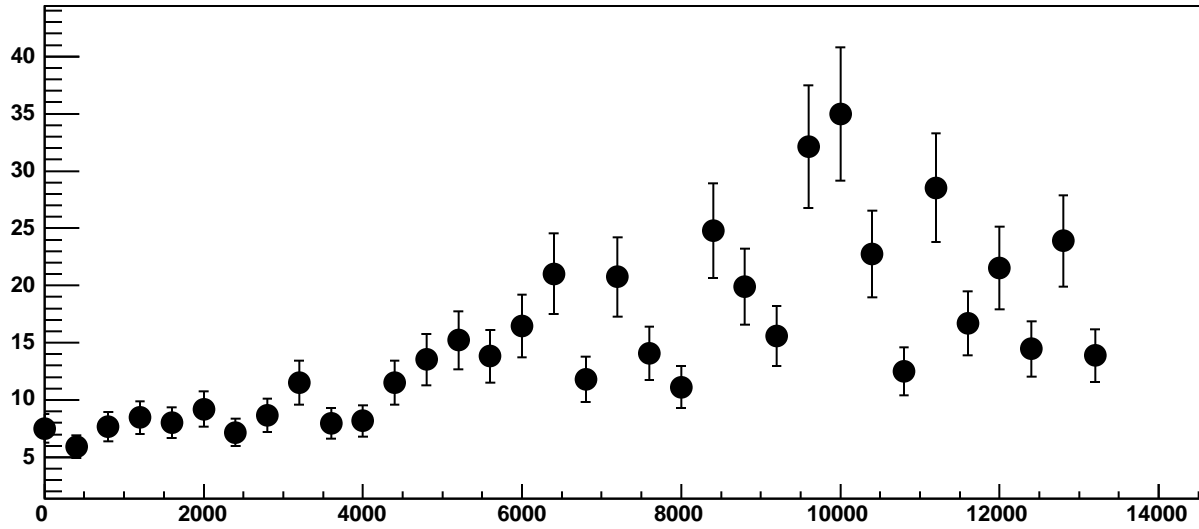


Chip 4, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC

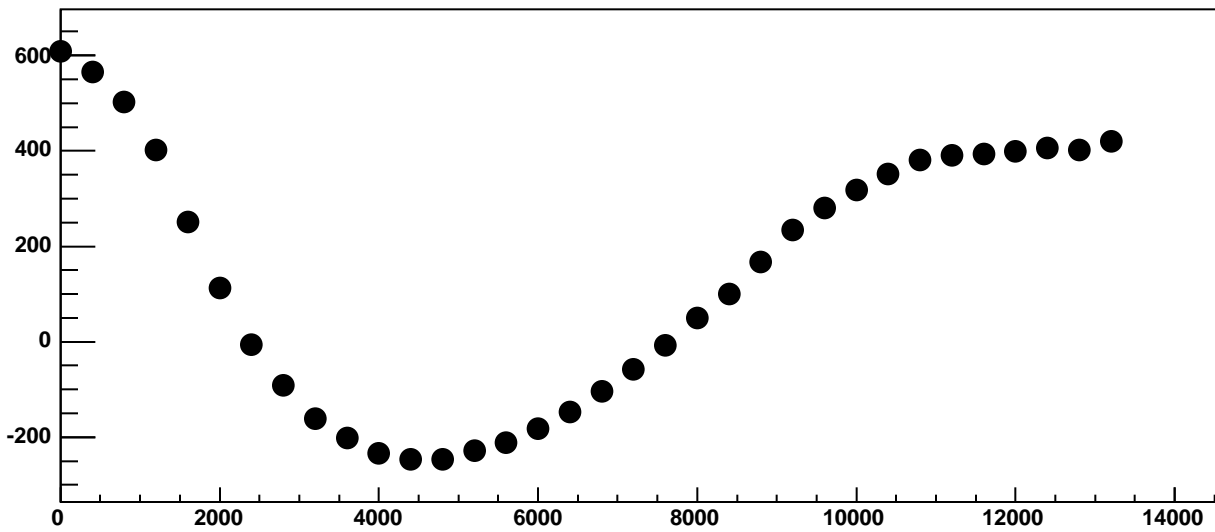


χ^2 / ndf 1.579e+05 / 23
p0 -520.5 ± 1.034
p1 0.1037 ± 0.000204

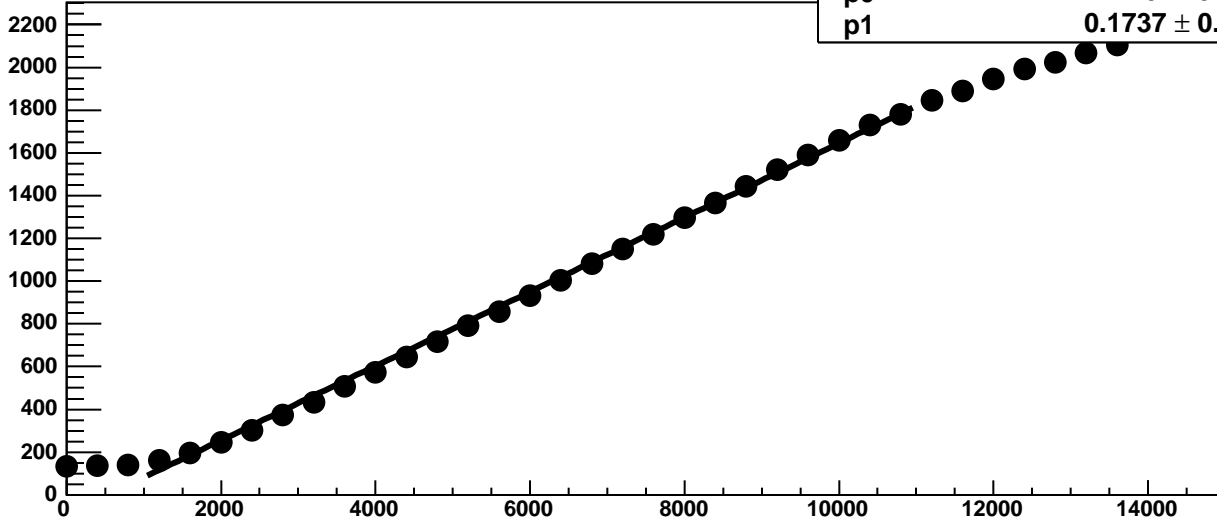
Chip 4, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1392 / 23

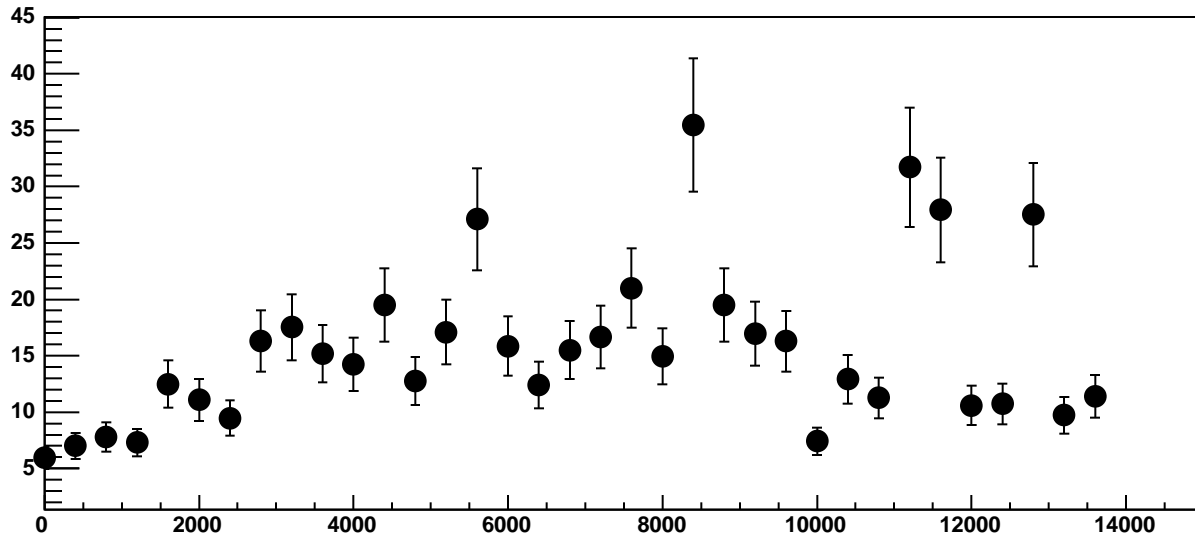
p0

-92.25 ± 1.169

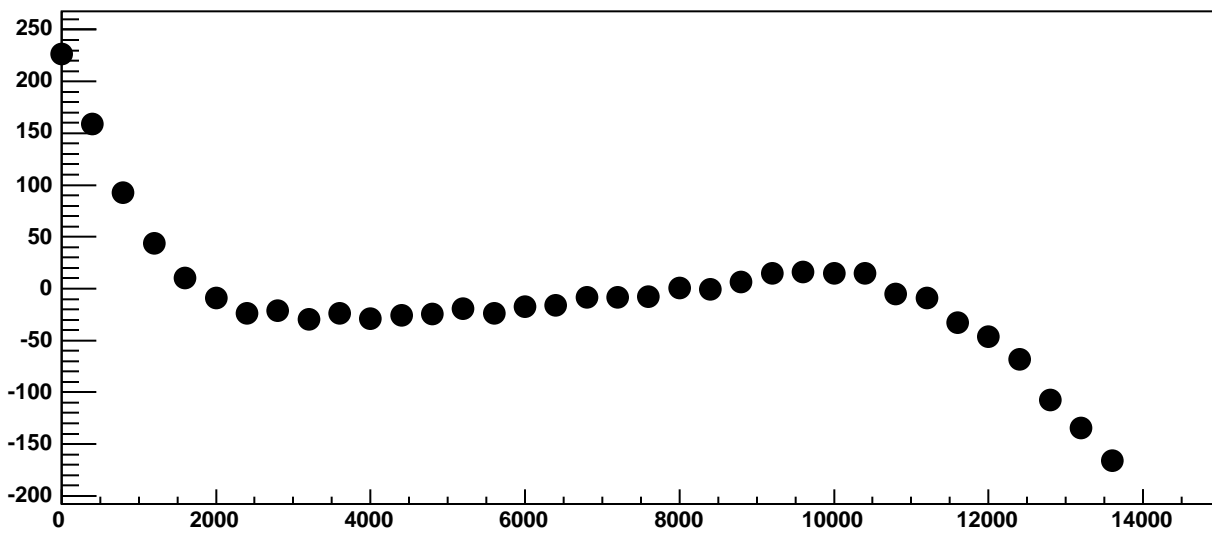
p1

0.1737 ± 0.000177

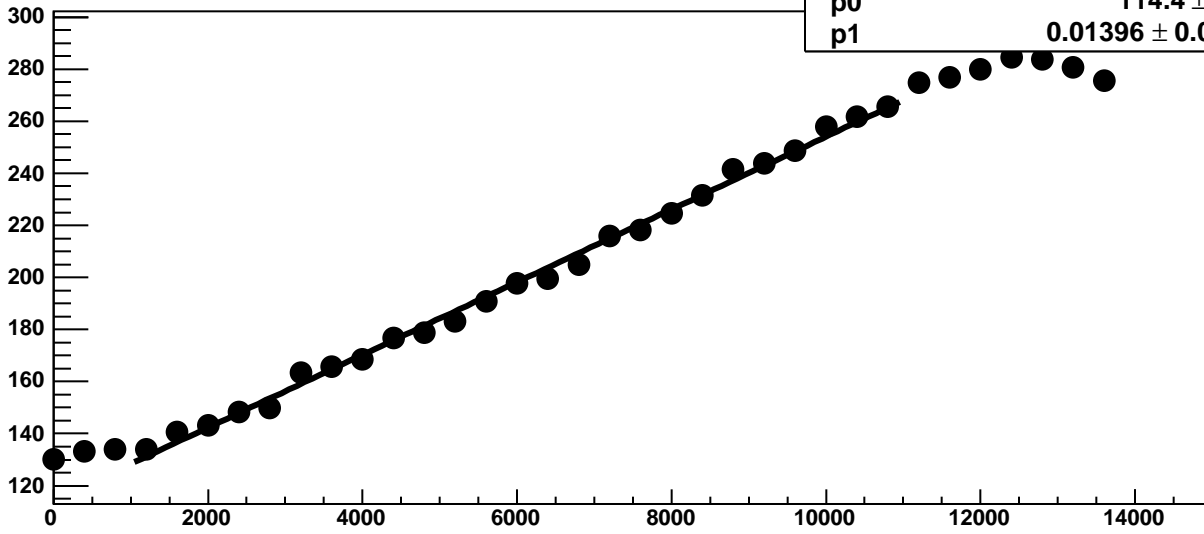
Chip 4, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

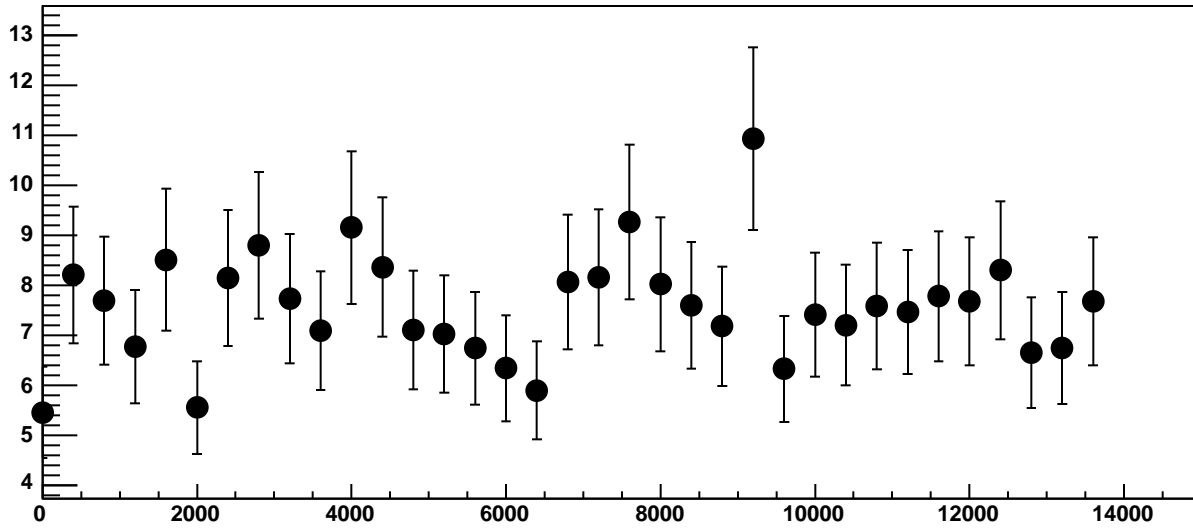


Chip 4, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

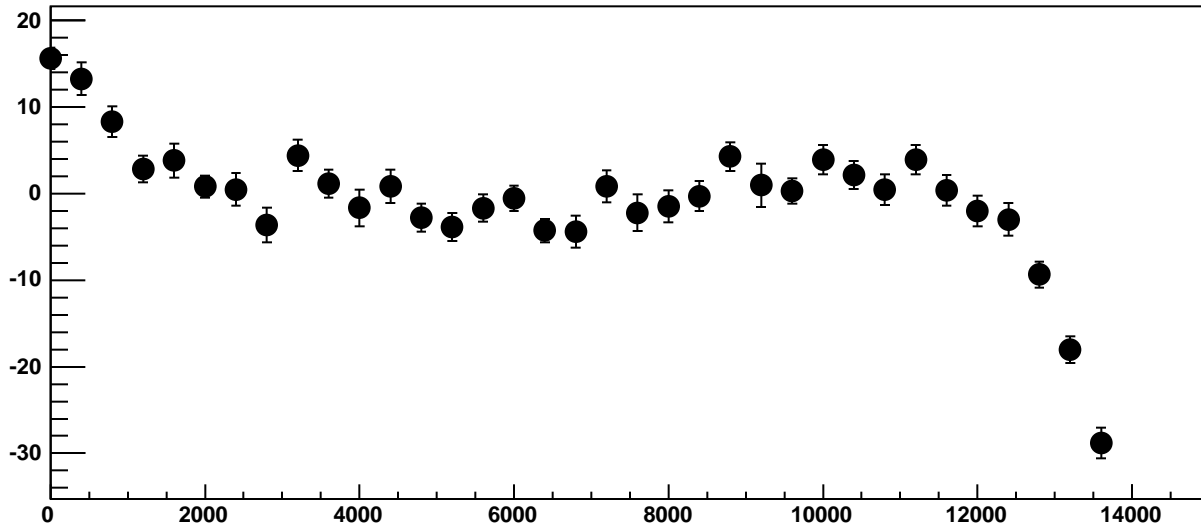


χ^2 / ndf 59.79 / 23
p0 114.4 ± 0.7722
p1 0.01396 ± 0.0001171

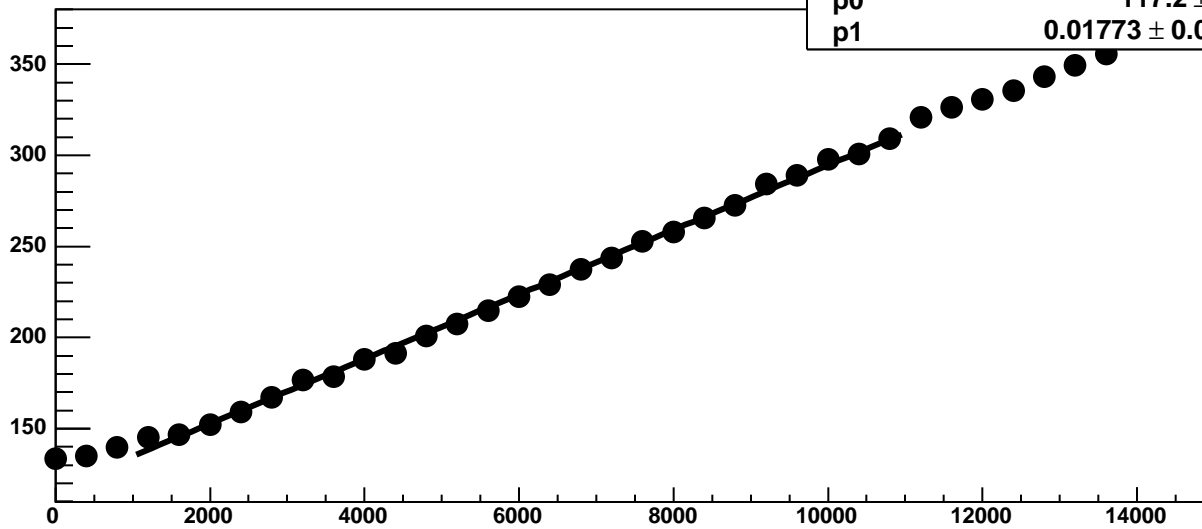
Chip 4, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

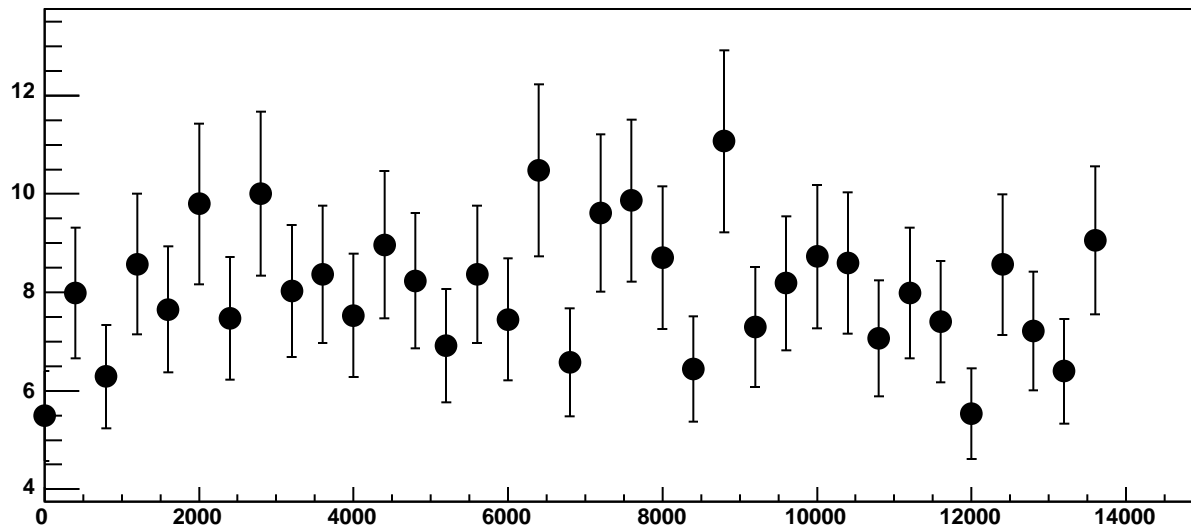


Chip 4, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

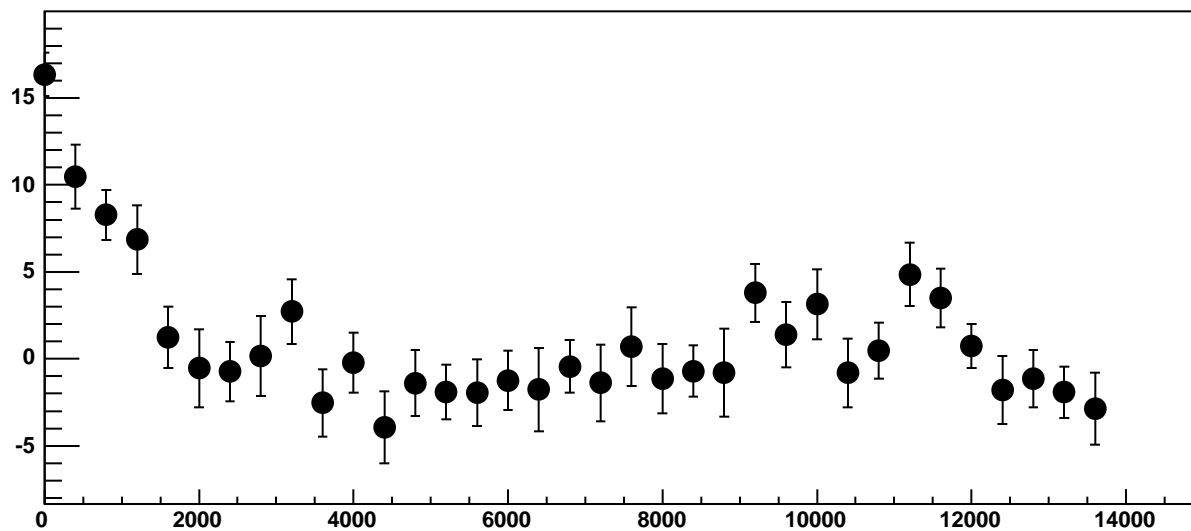


χ^2 / ndf 34.2 / 23
p0 117.2 ± 0.8737
p1 0.01773 ± 0.0001299

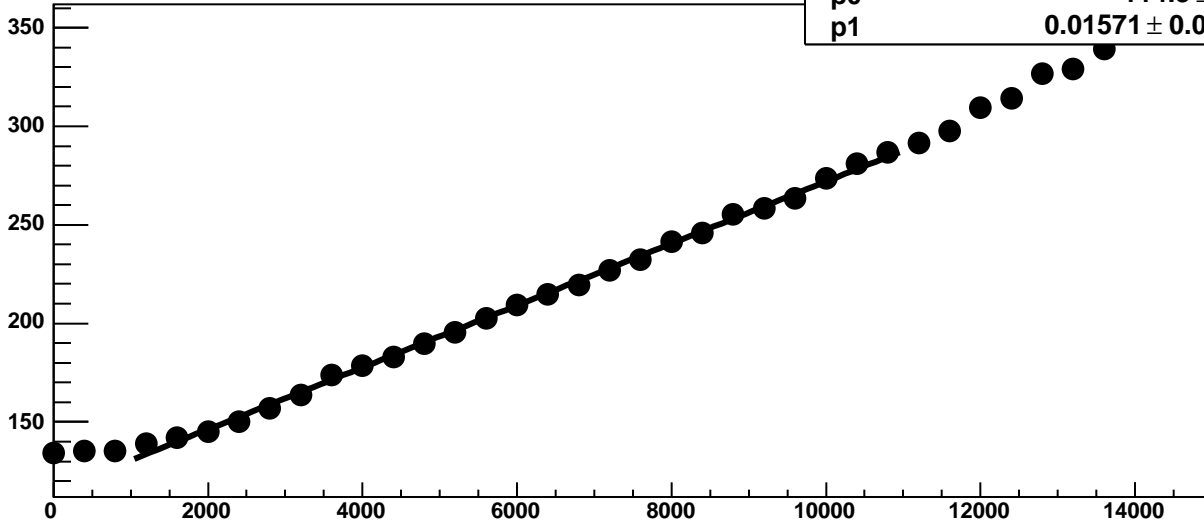
Chip 4, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

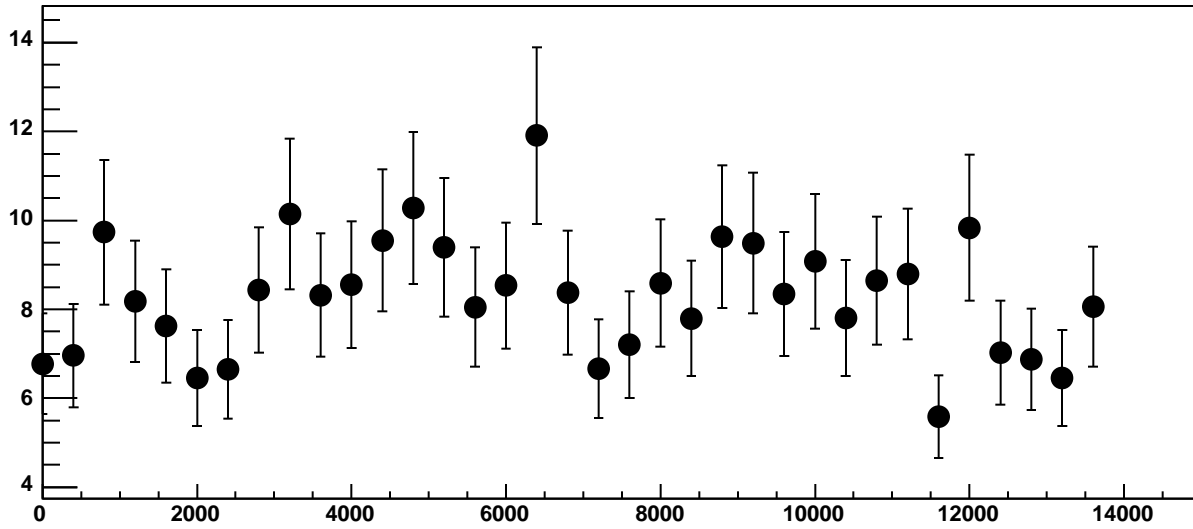


Chip 4, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

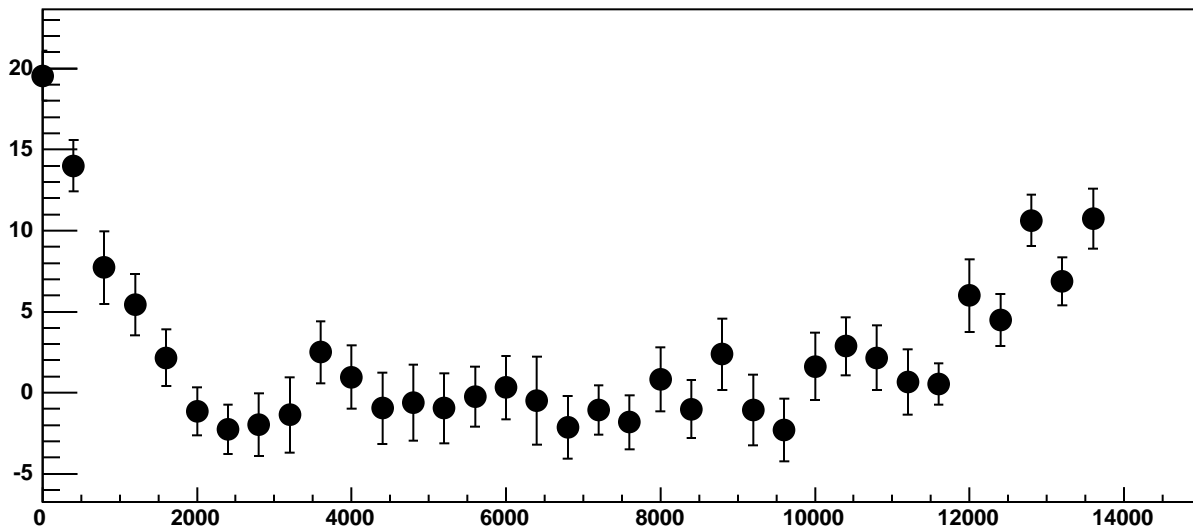


χ^2 / ndf 27.21 / 23
p0 114.8 ± 0.8391
p1 0.01571 ± 0.0001283

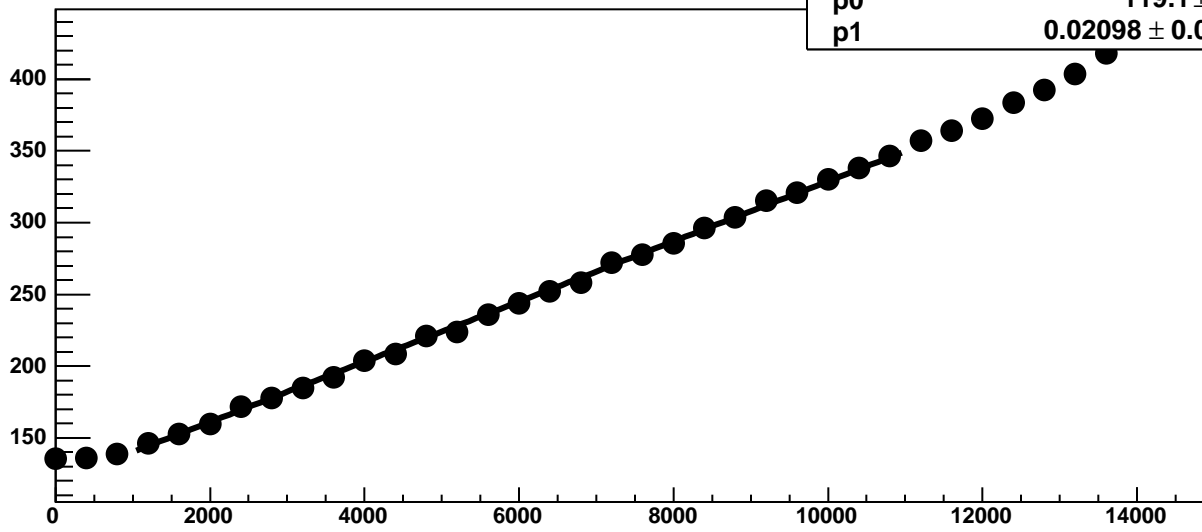
Chip 4, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

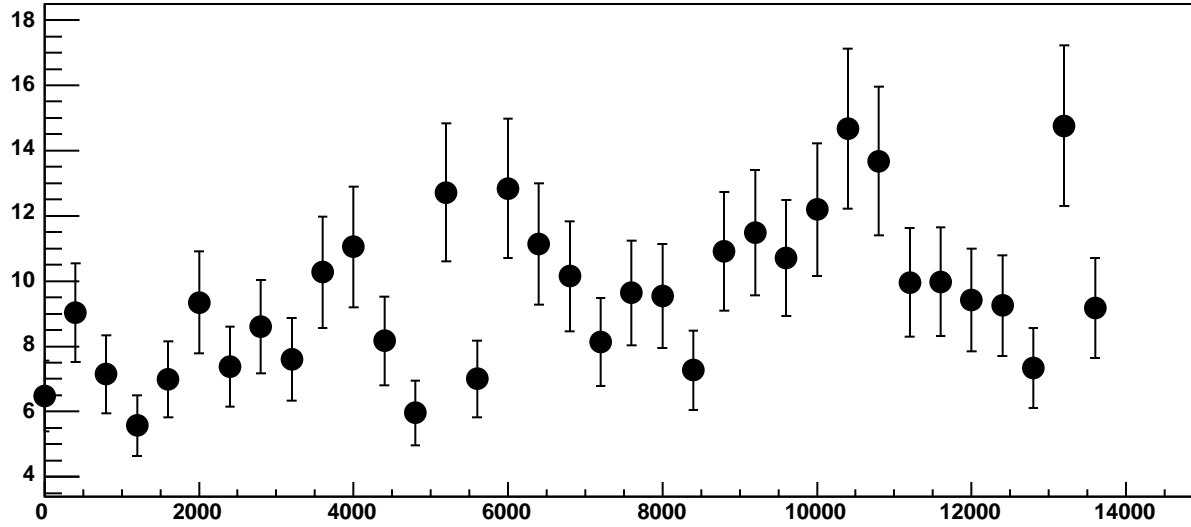


Chip 4, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

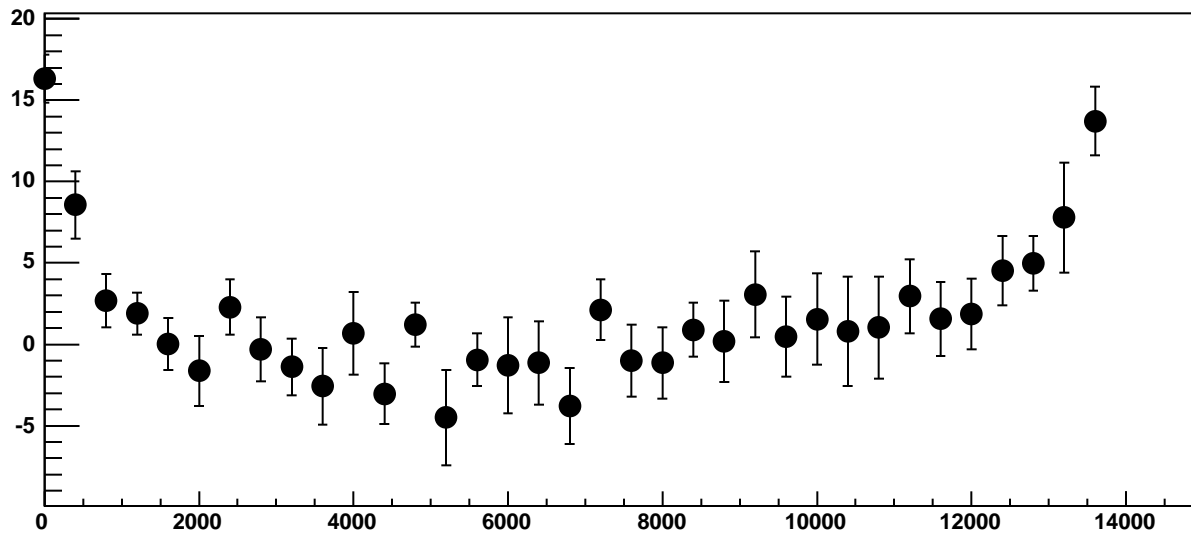


χ^2 / ndf 19.6 / 23
p0 119.1 ± 0.8384
p1 0.02098 ± 0.0001443

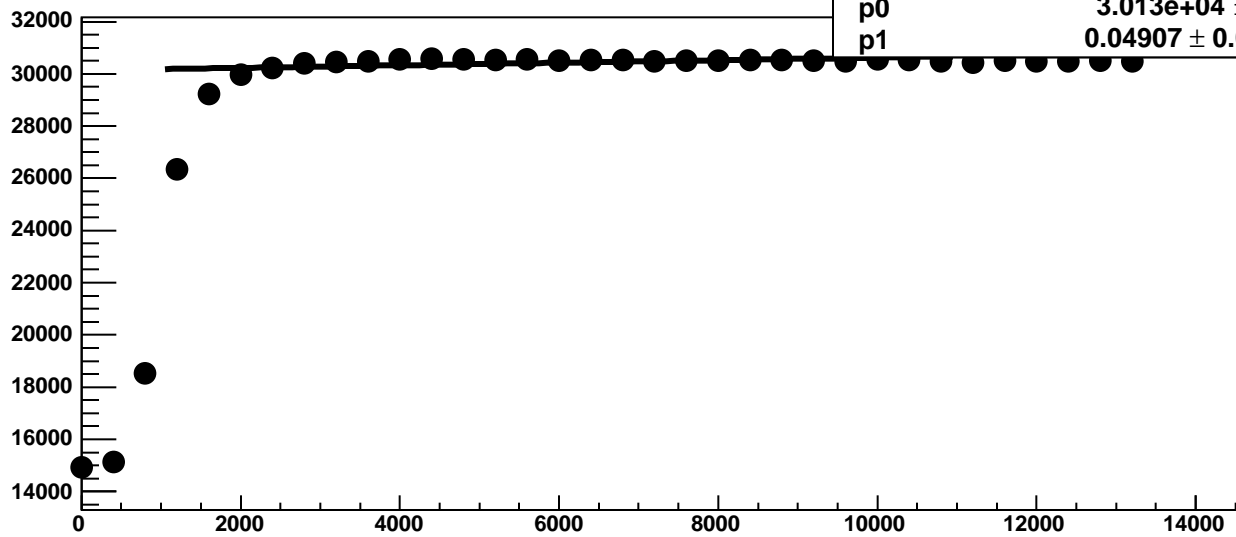
Chip 4, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



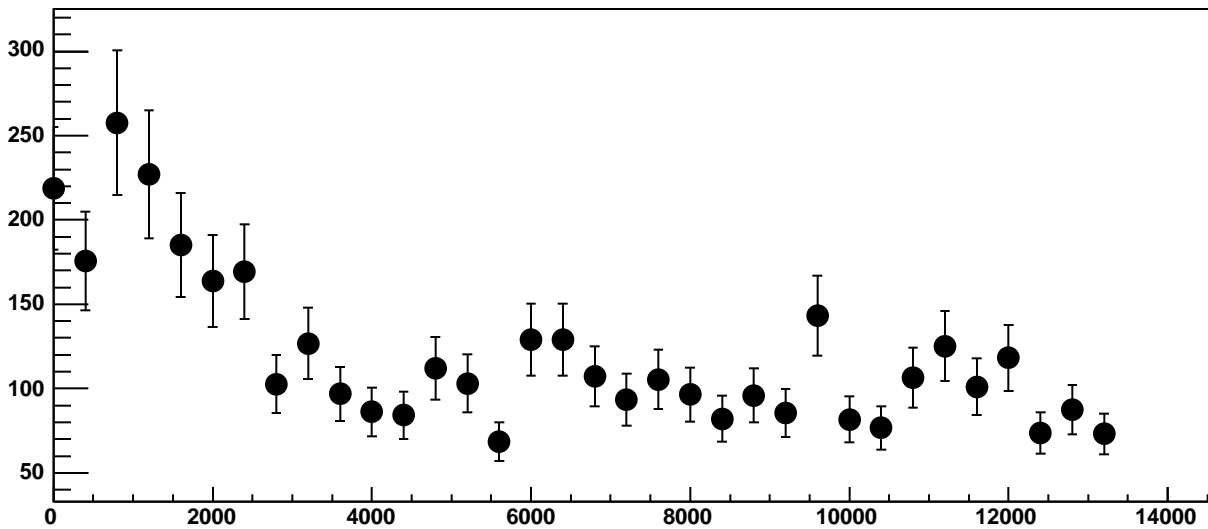
Chip 4, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



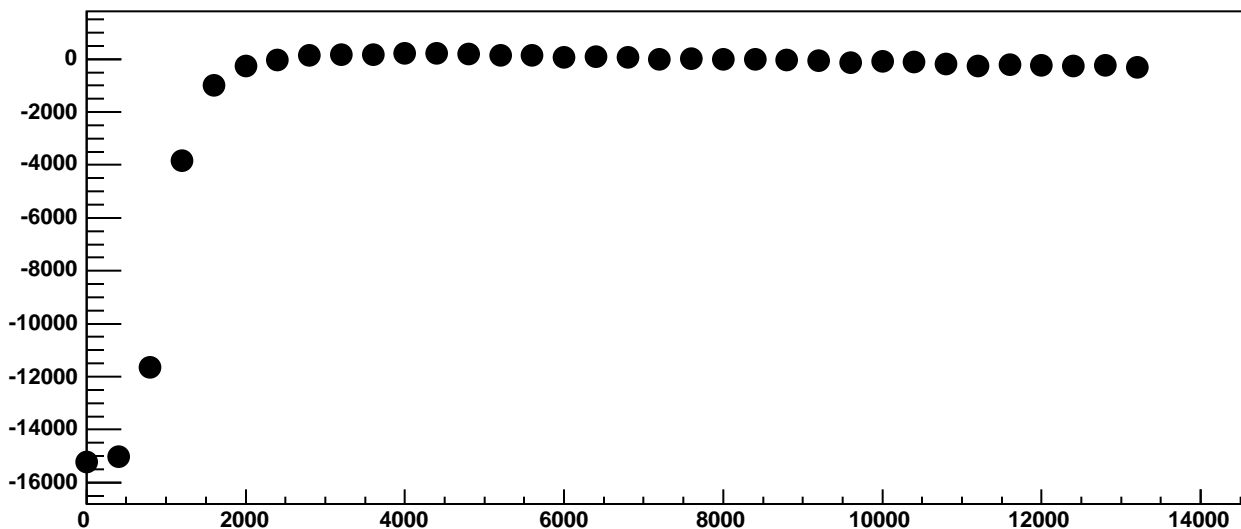
Chip 4, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC



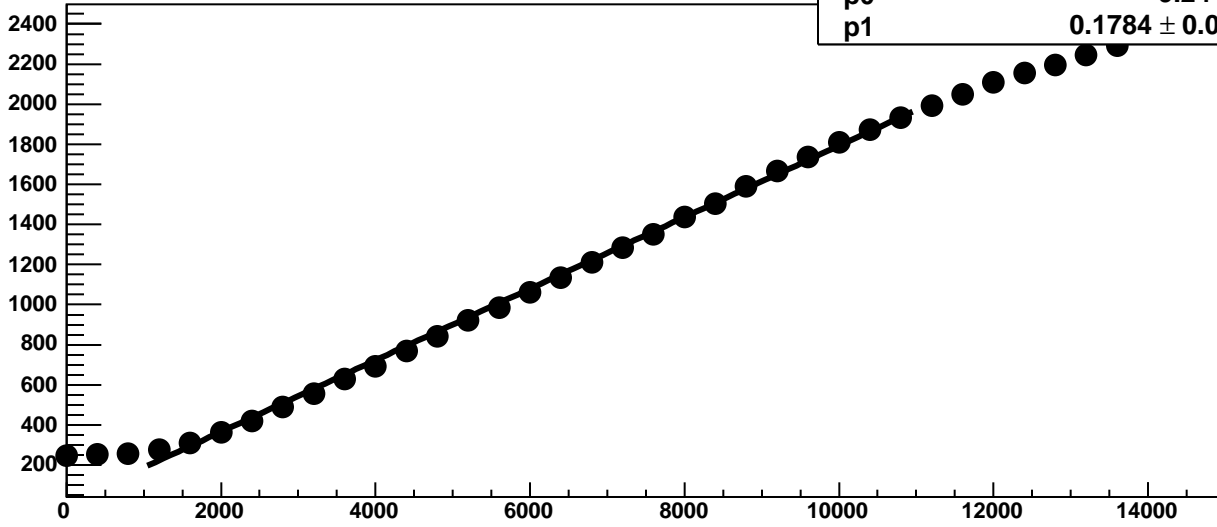
Chip 4, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC

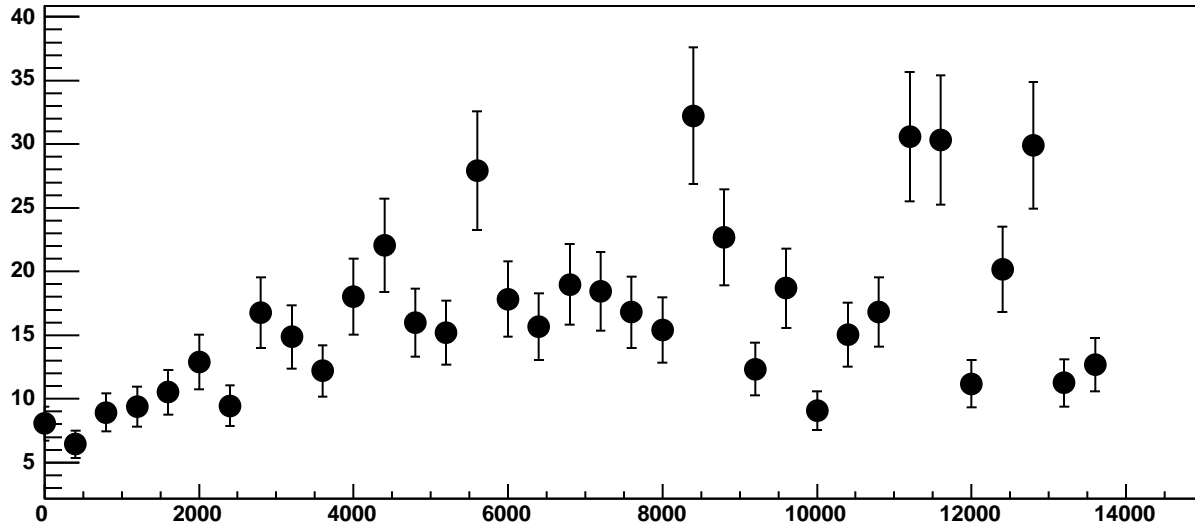


Chip 4, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

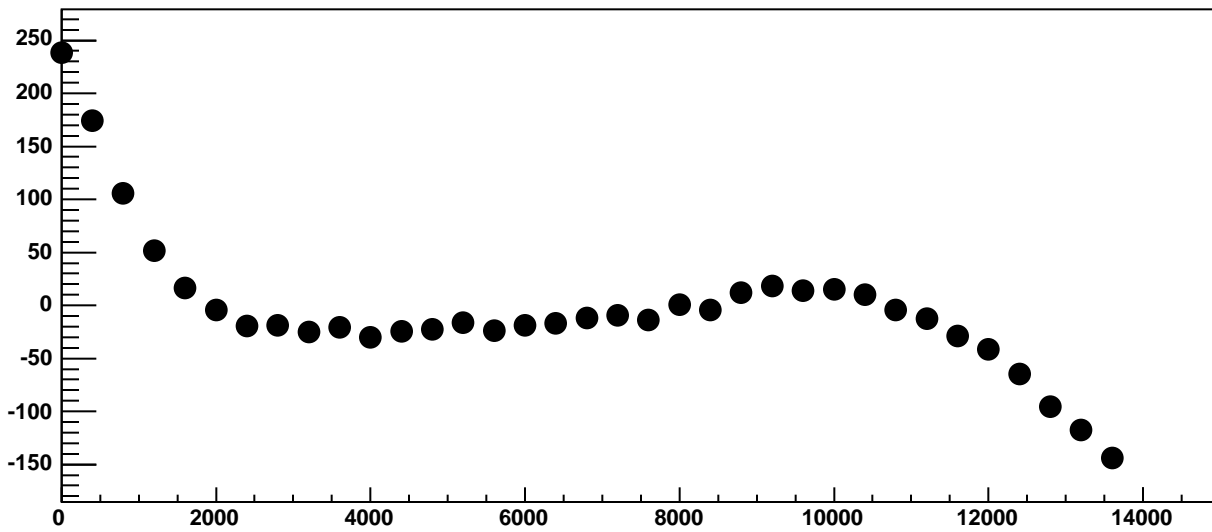


χ^2 / ndf 1177 / 23
p0 9.24 ± 1.276
p1 0.1784 ± 0.0002015

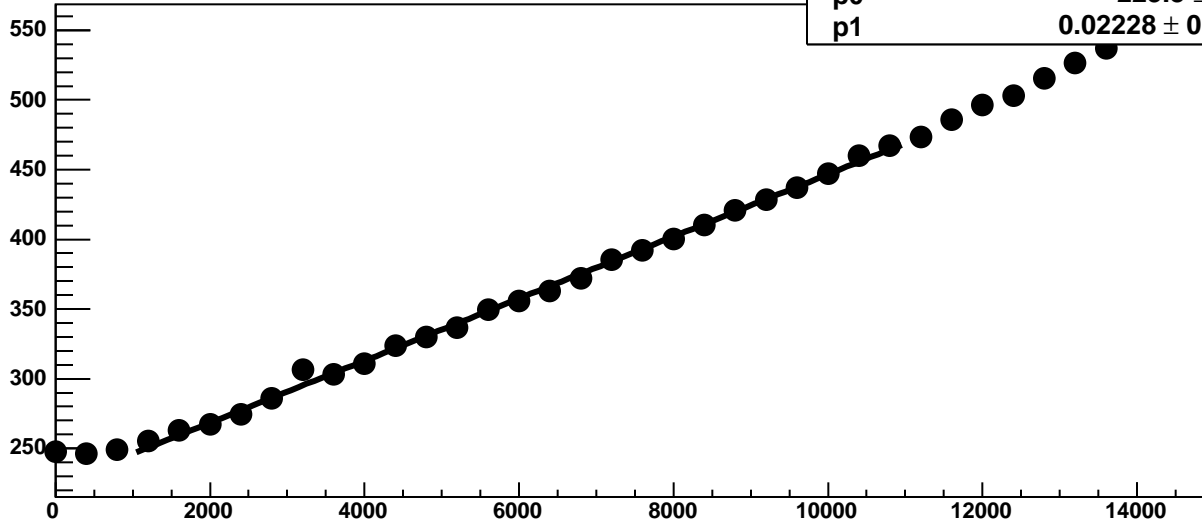
Chip 4, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

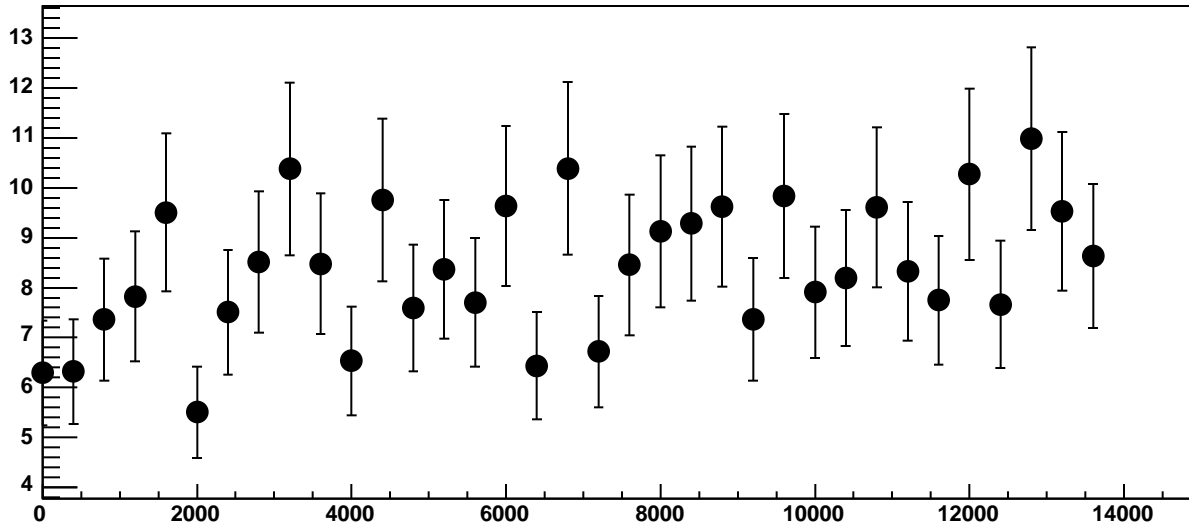


Chip 4, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

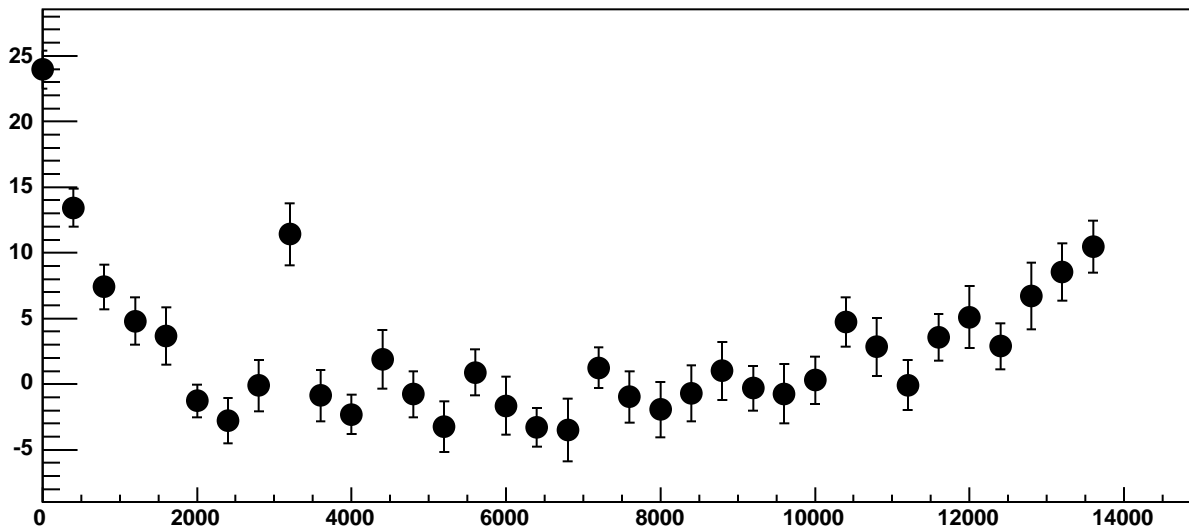


χ^2 / ndf 61.14 / 23
p0 223.8 ± 0.8268
p1 0.02228 ± 0.000129

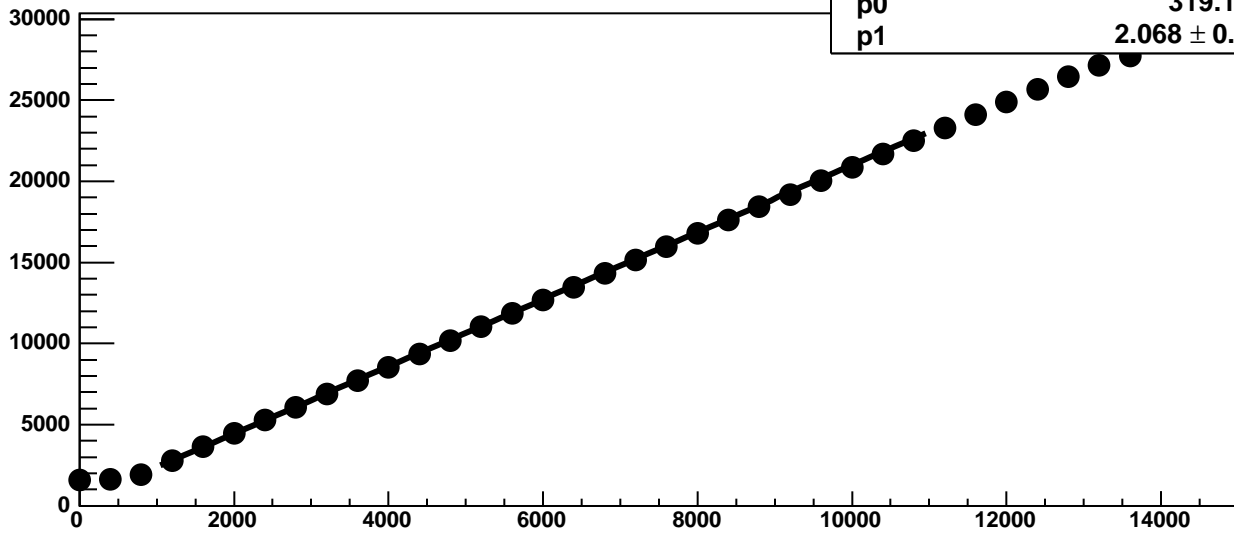
Chip 4, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



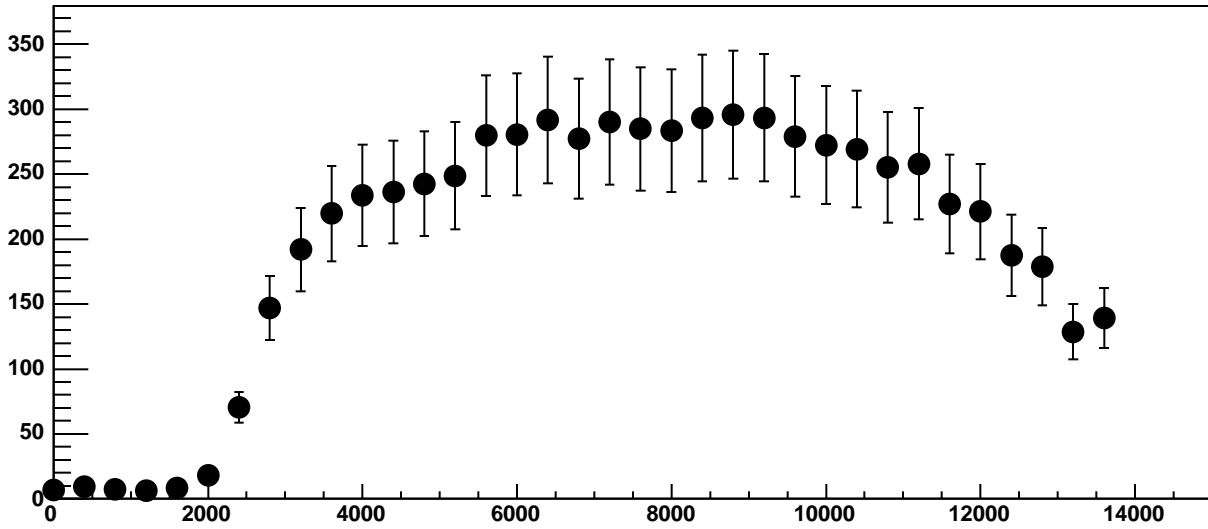
Chip 4, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



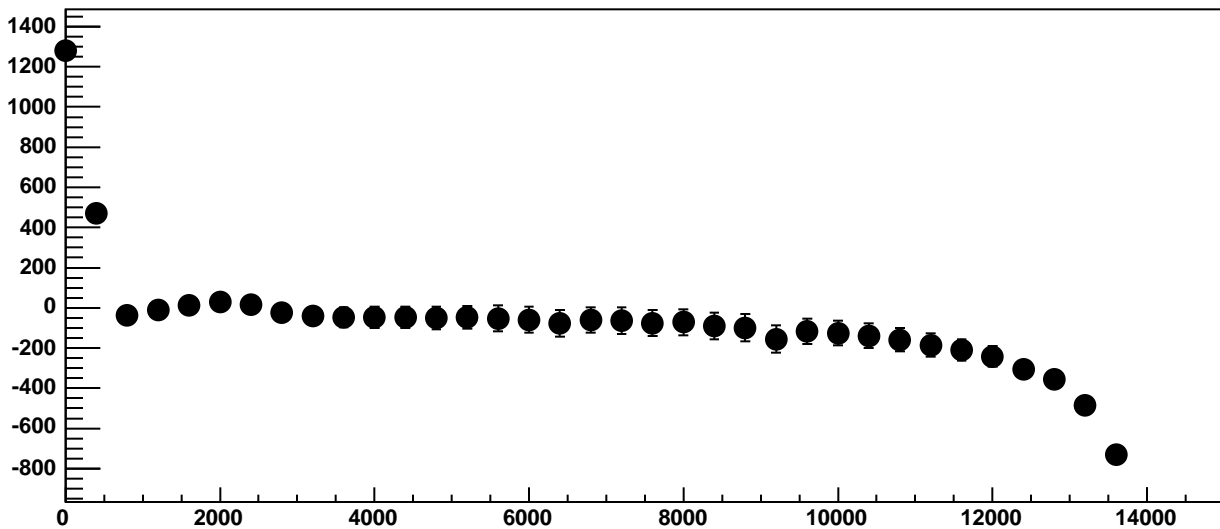
Chip 4, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC



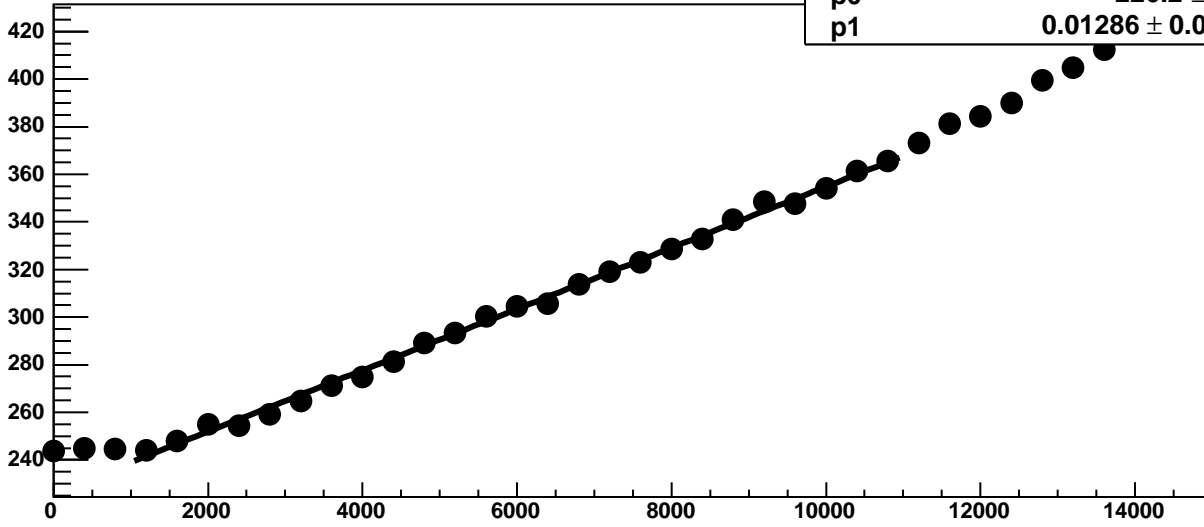
Chip 4, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC

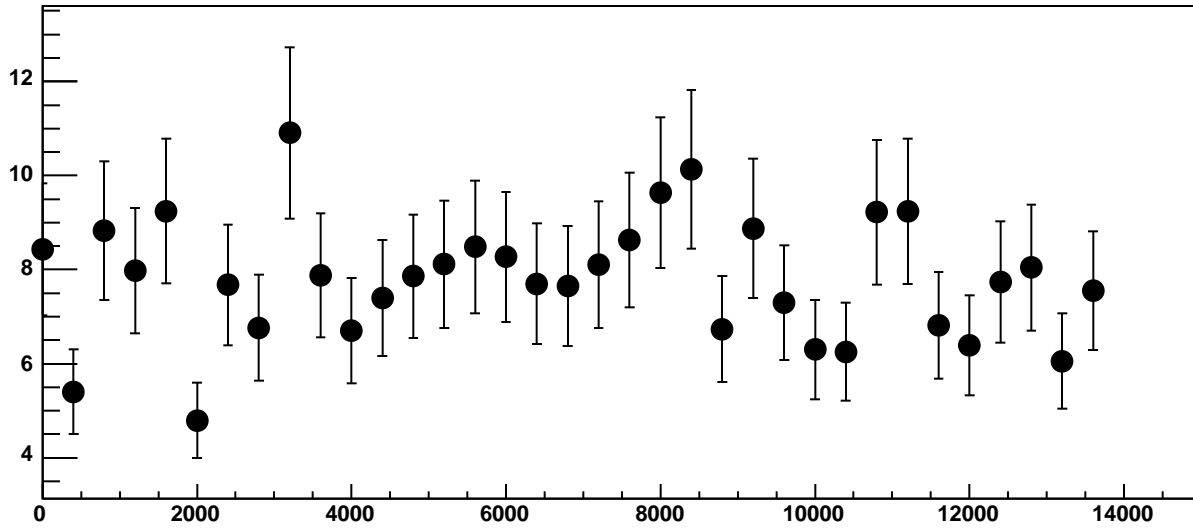


Chip 4, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

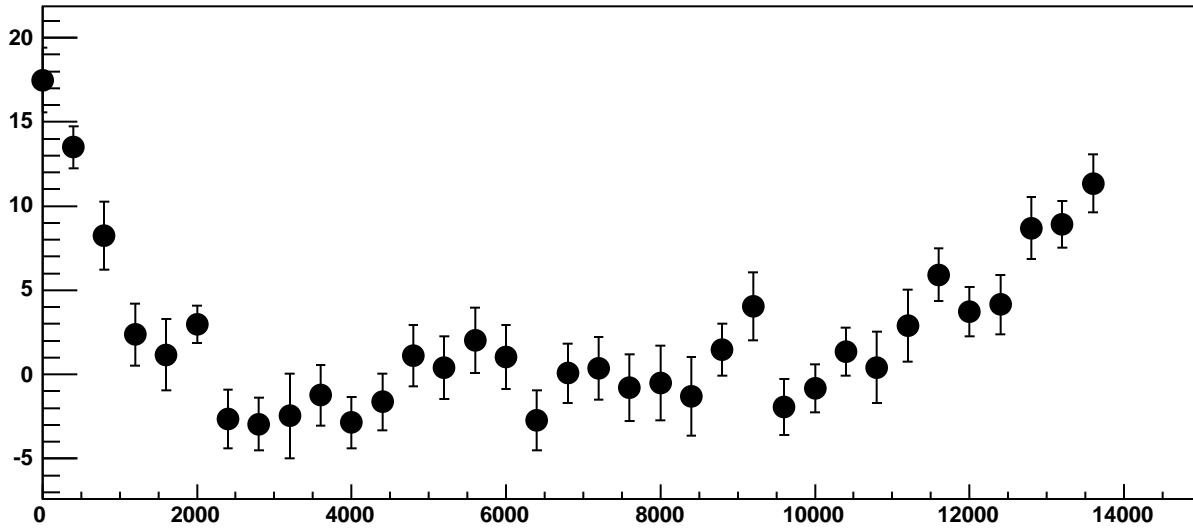


χ^2 / ndf 33.17 / 23
p0 226.2 ± 0.7622
p1 0.01286 ± 0.0001157

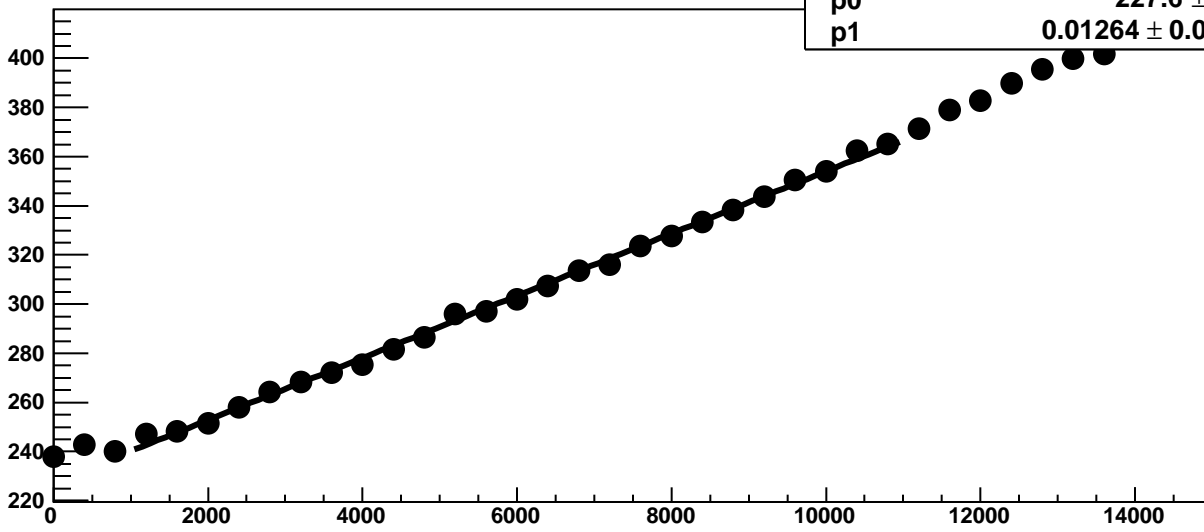
Chip 4, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

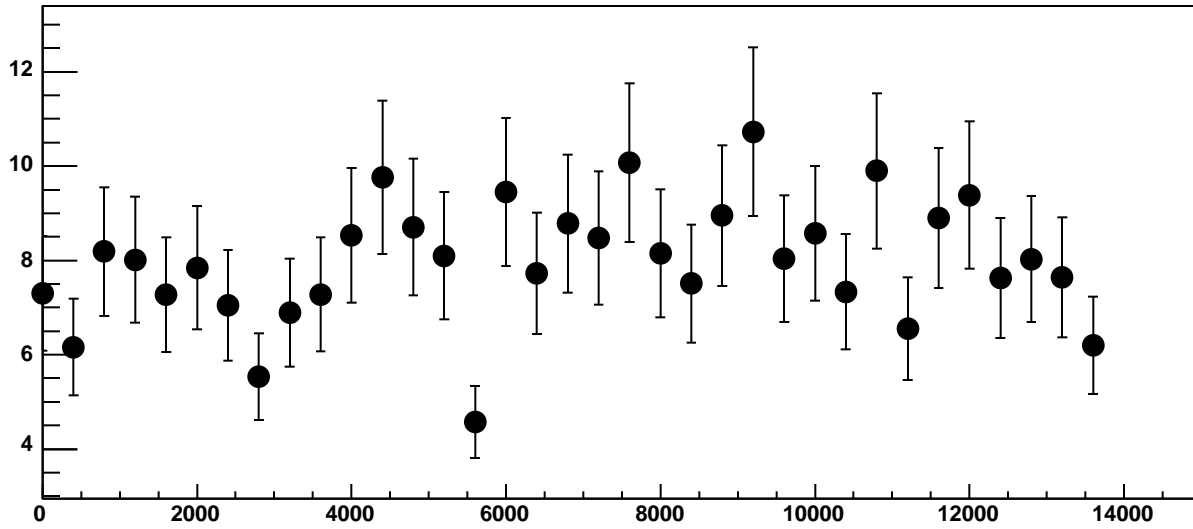


Chip 4, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

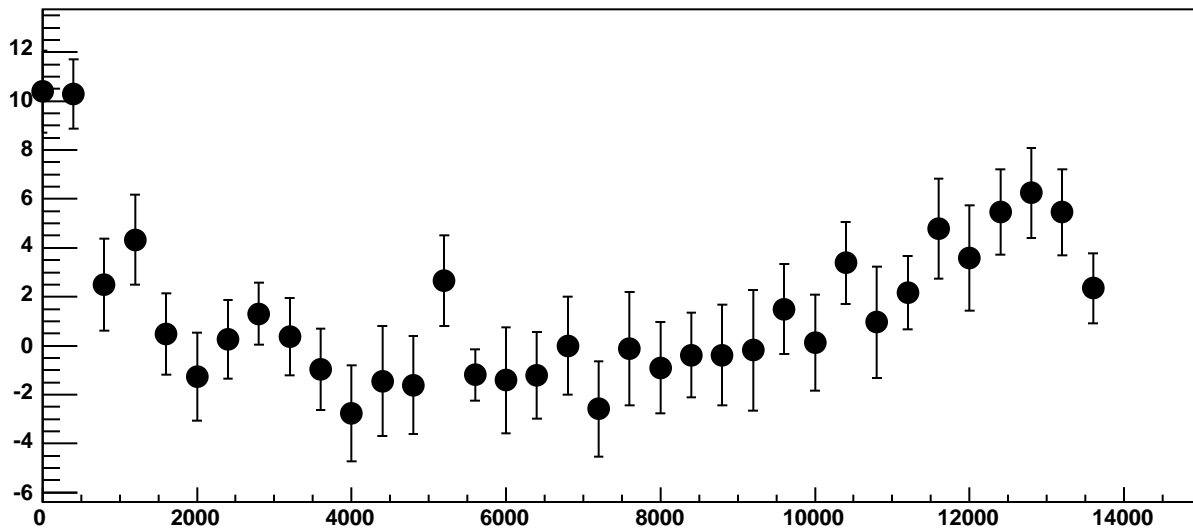


χ^2 / ndf 21.83 / 23
p0 227.6 ± 0.7882
p1 0.01264 ± 0.0001259

Chip 4, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

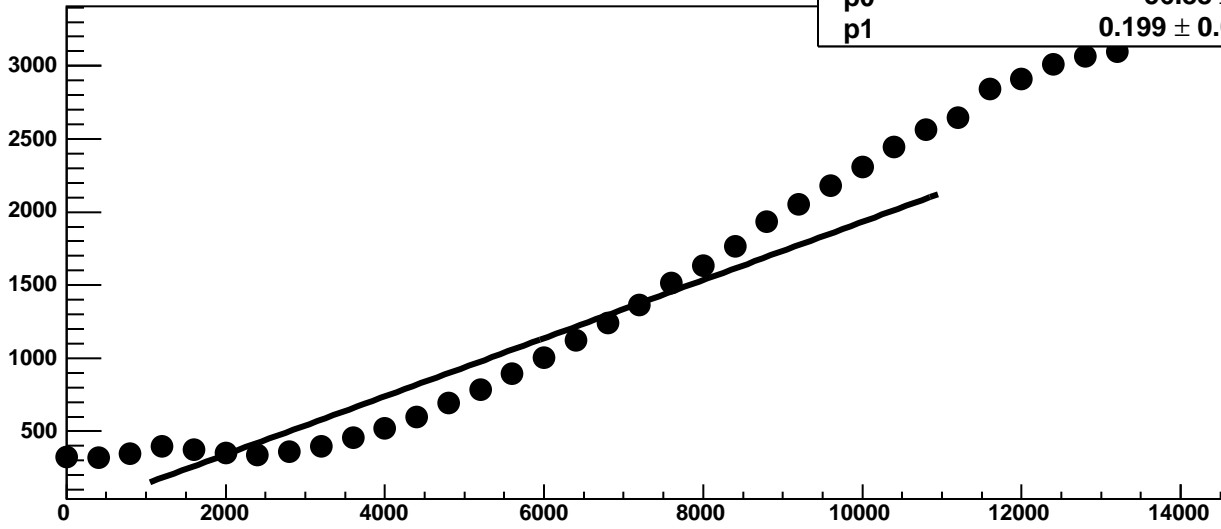


Chip 4, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

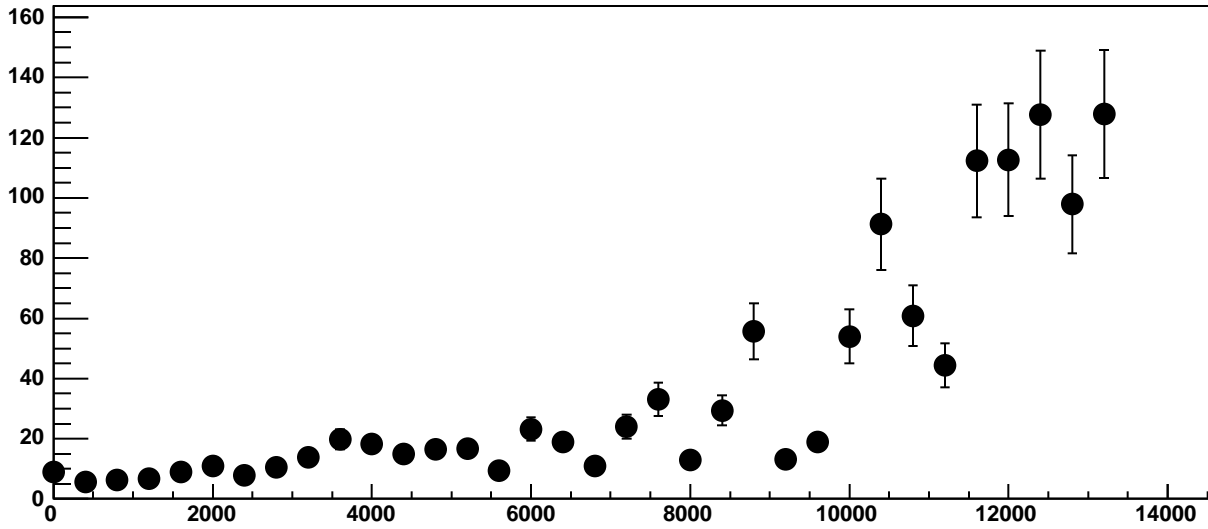


Chip 4, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC

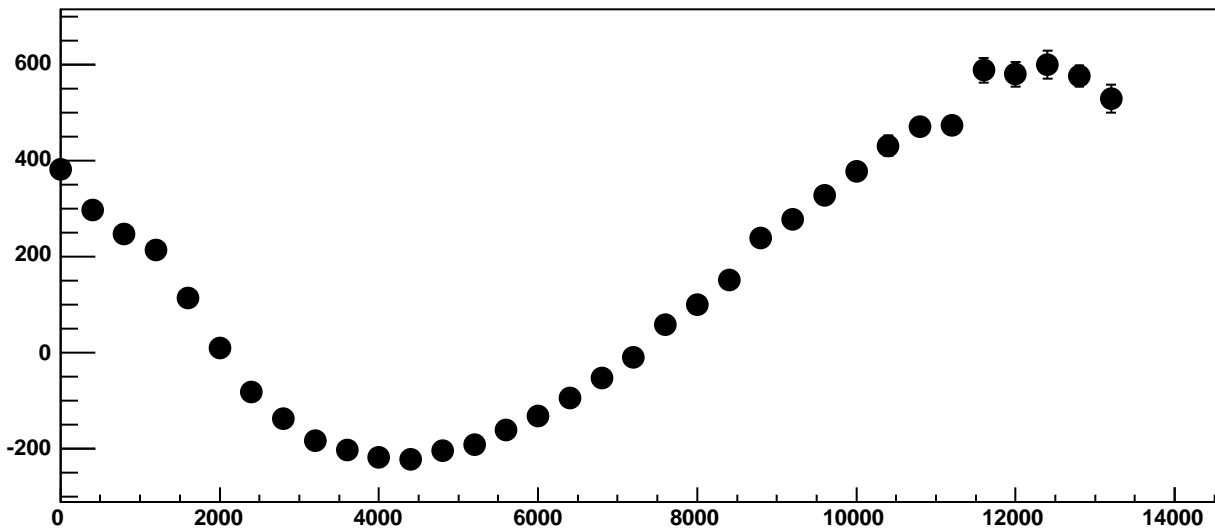
χ^2 / ndf 7.092e+04 / 23
p0 -56.88 ± 1.158
p1 0.199 ± 0.000243



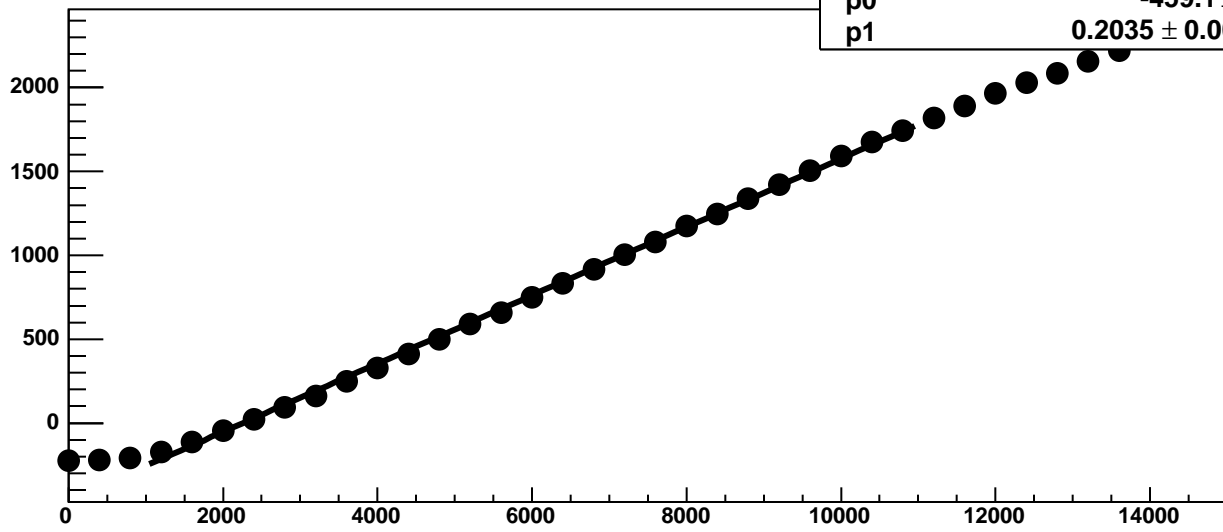
Chip 4, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC

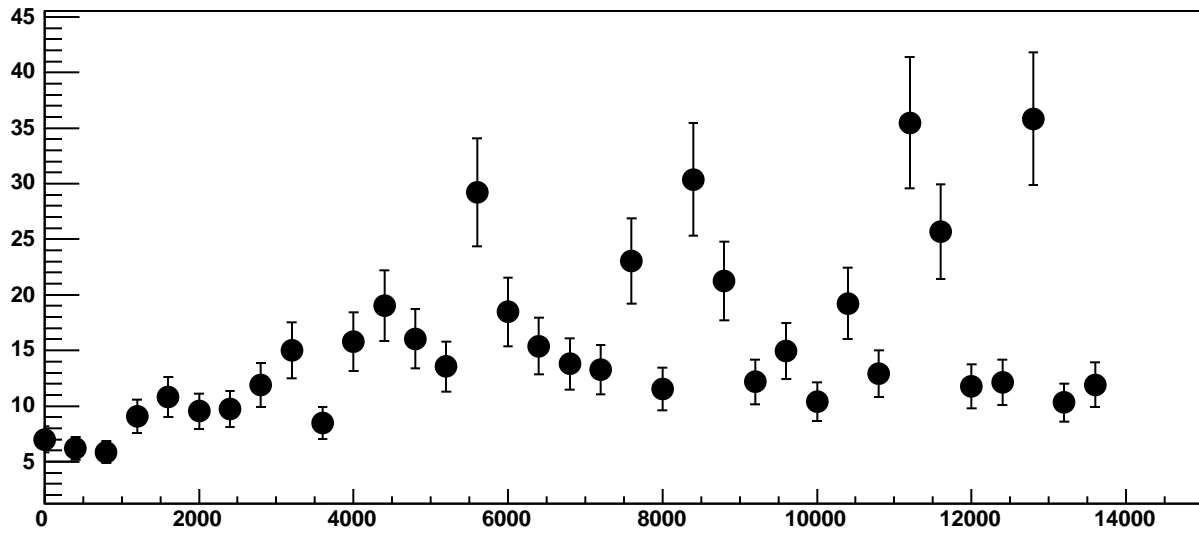


Chip 5, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

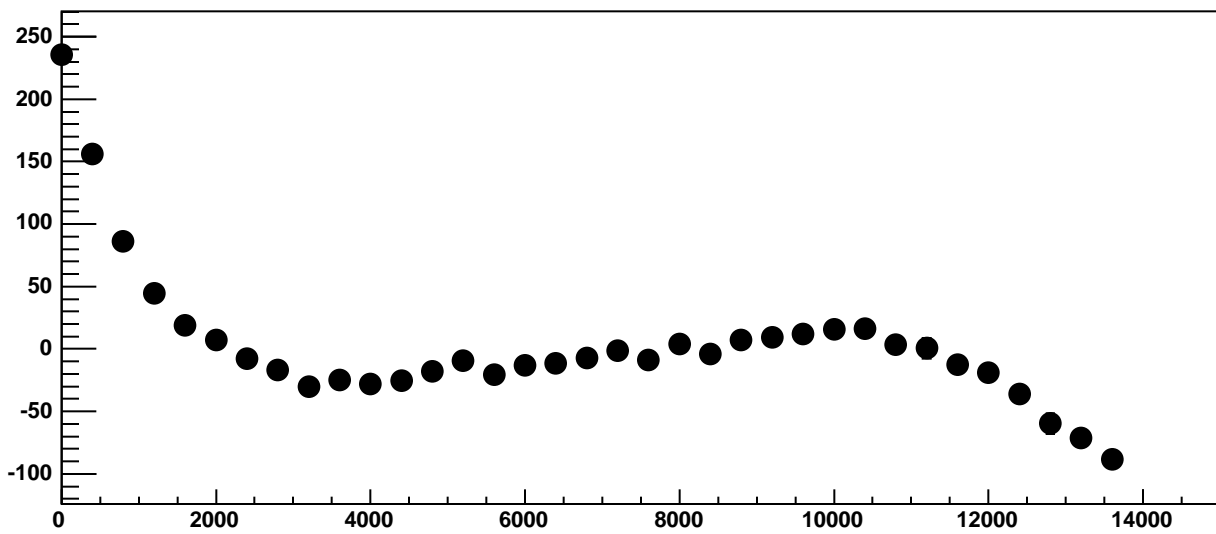


χ^2 / ndf 1066 / 23
p0 -459.1 ± 1.168
p1 0.2035 ± 0.0001914

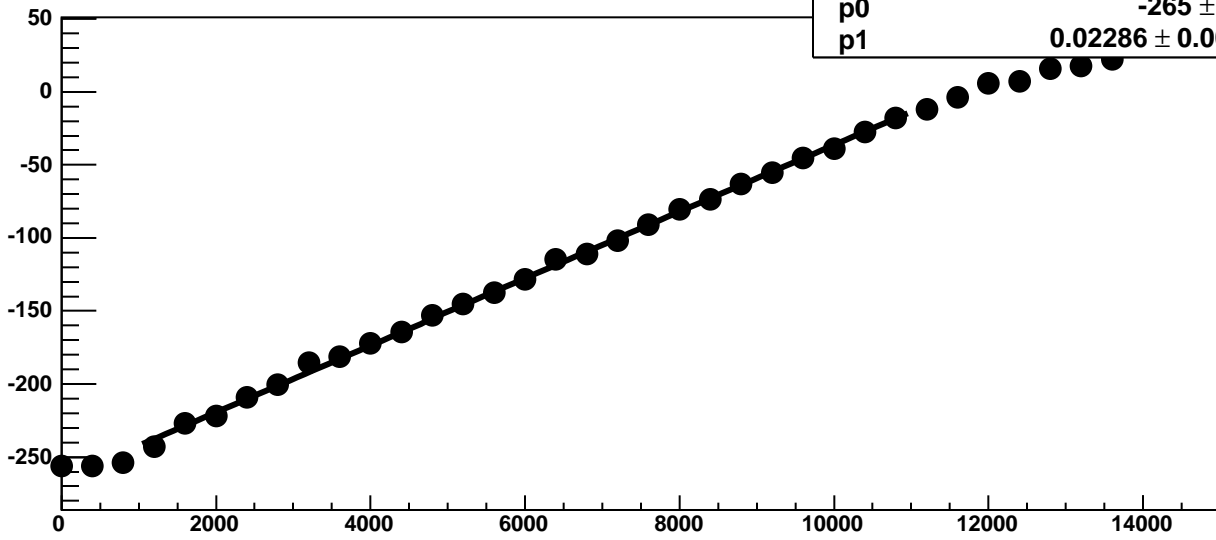
Chip 5, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



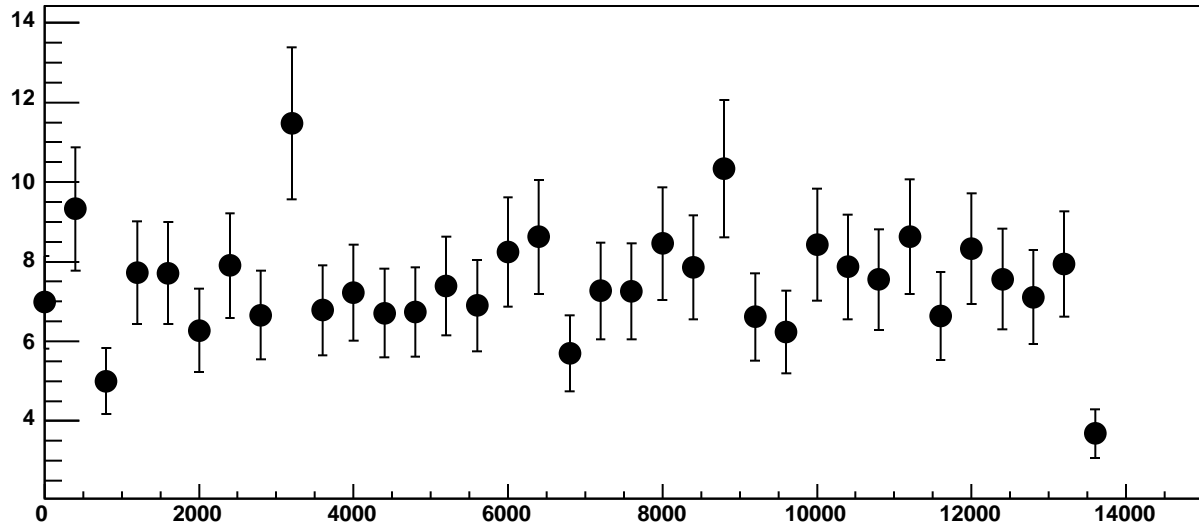
Chip 5, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC



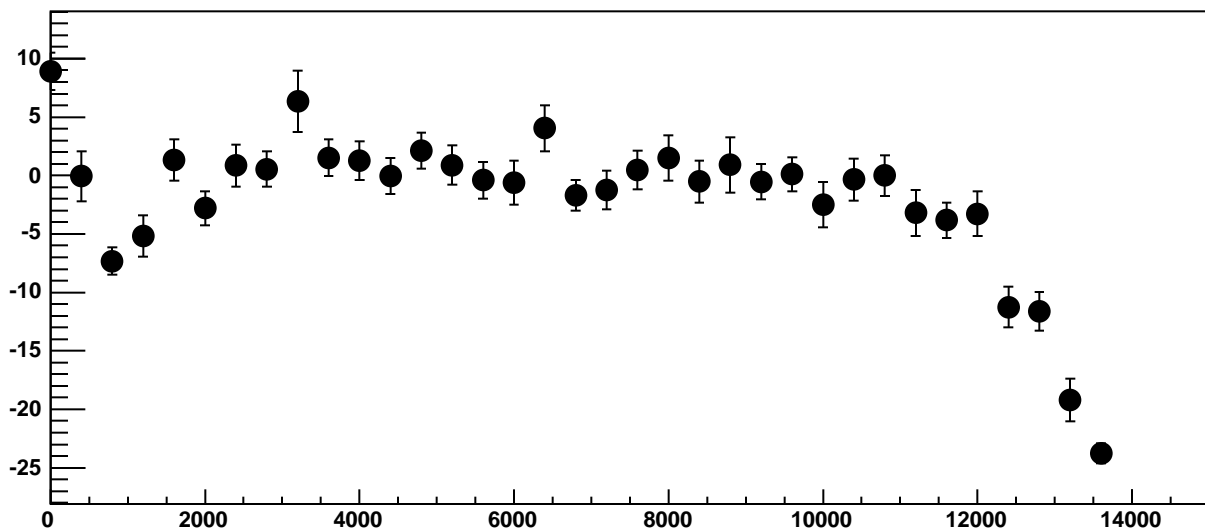
Chip 5, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC



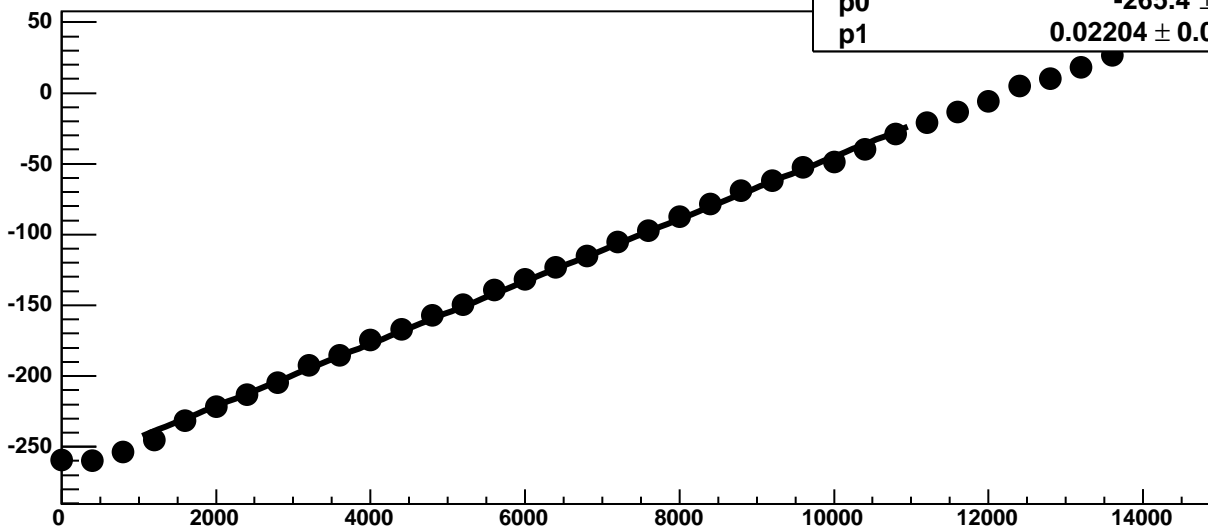
Chip 5, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

51 / 23

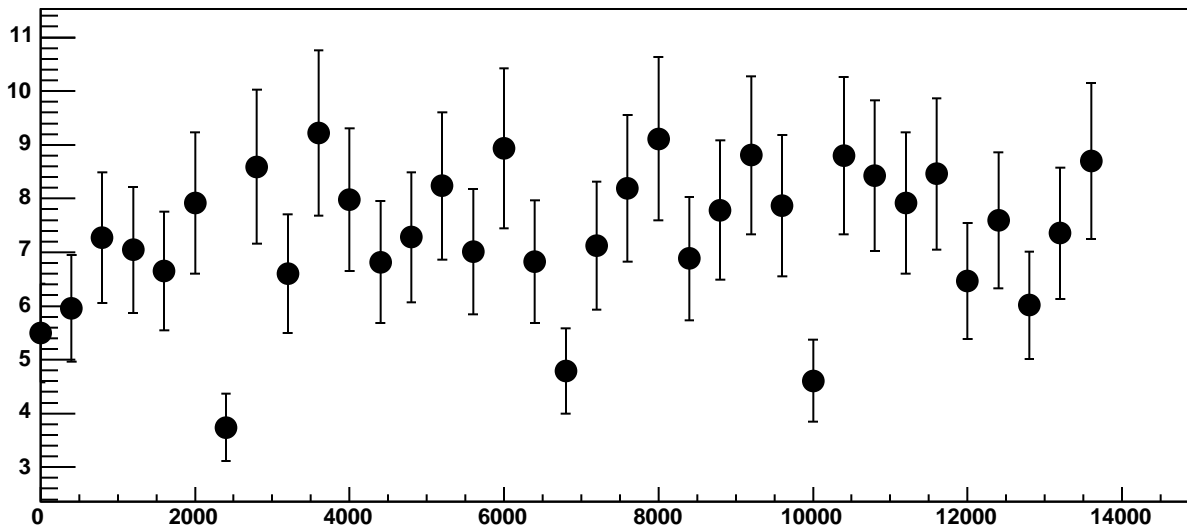
p0

-265.4 ± 0.6863

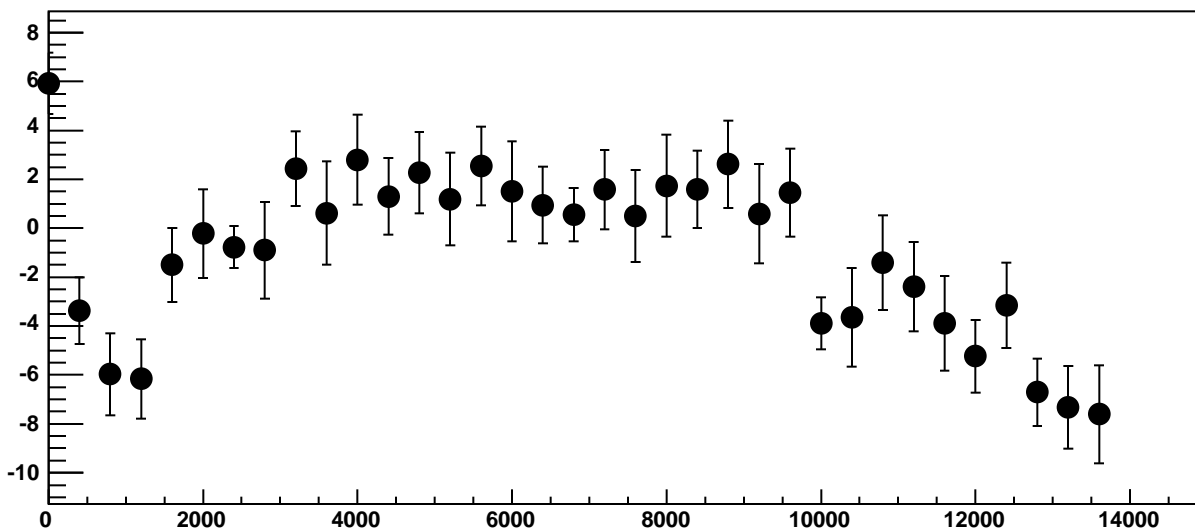
p1

0.02204 ± 0.0001063

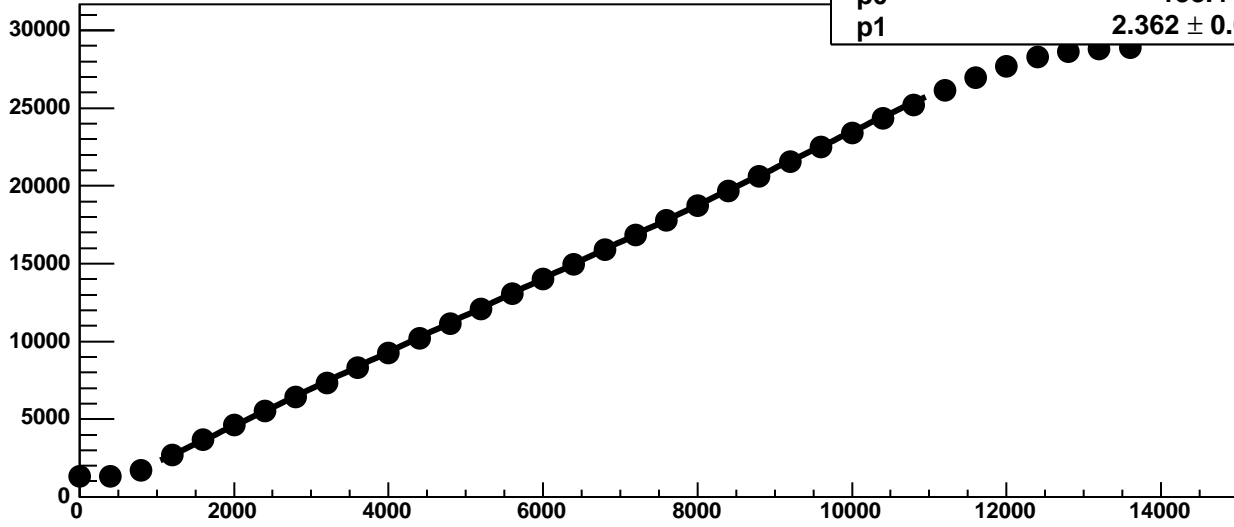
Chip 5, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



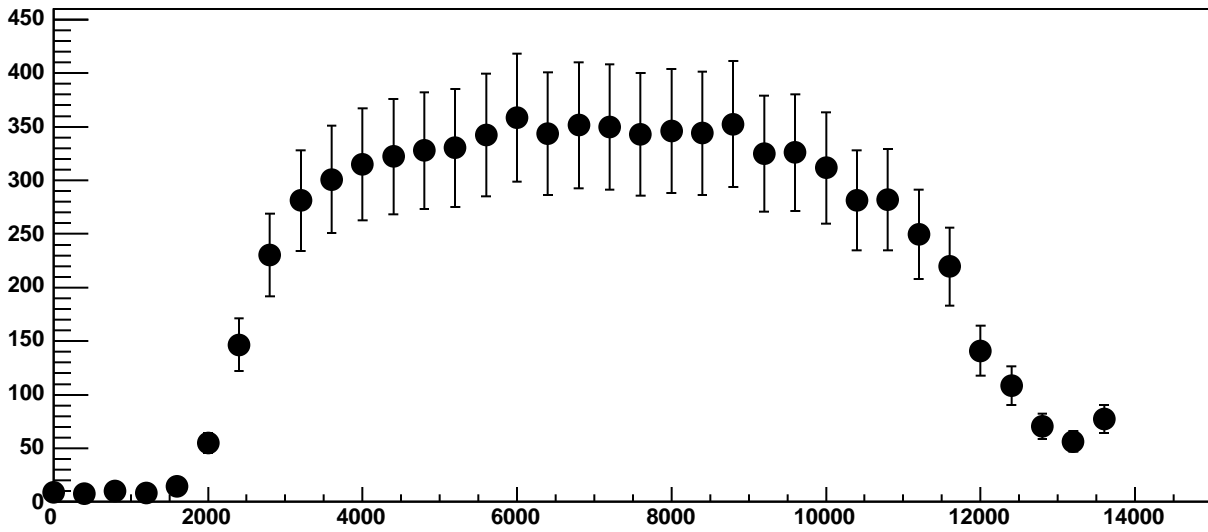
Chip 5, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



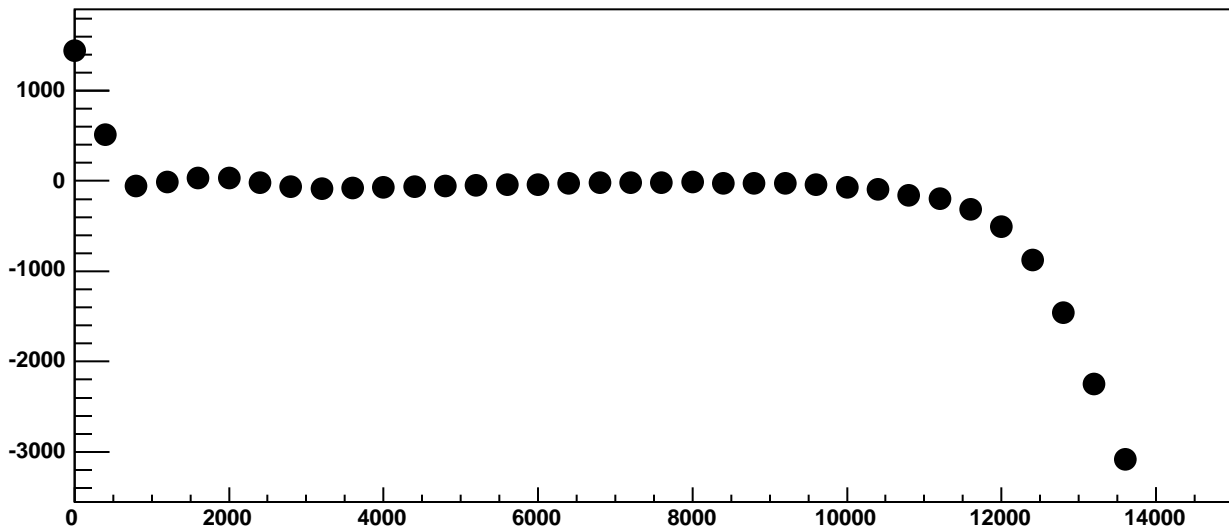
Chip 5, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC



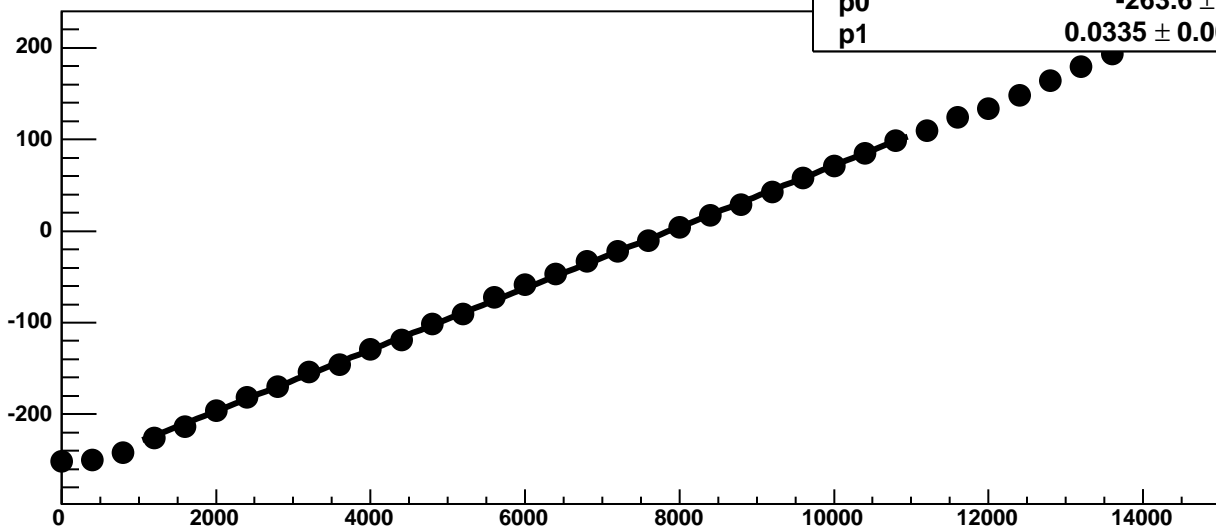
Chip 5, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC

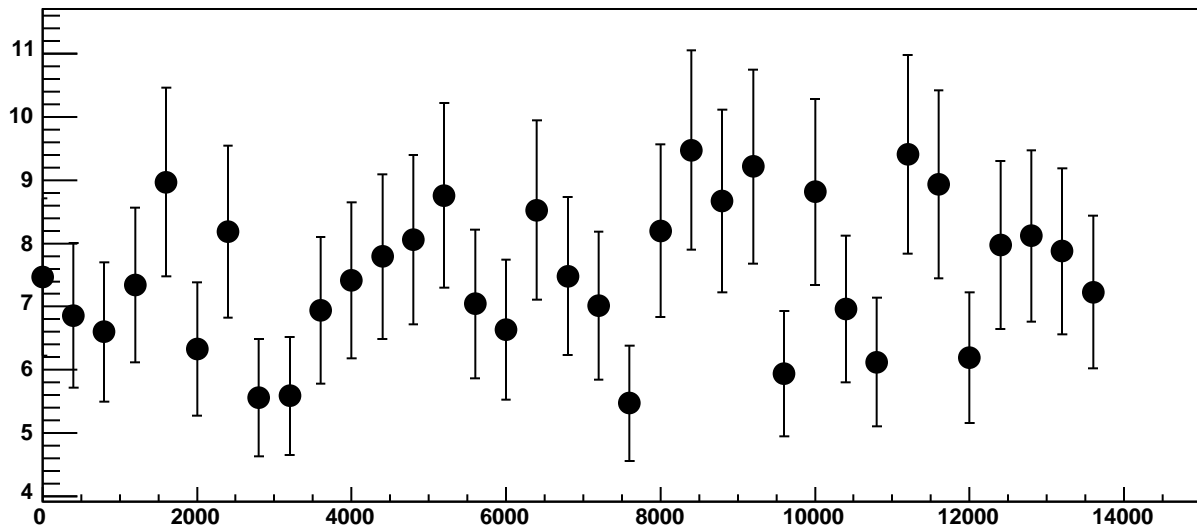


Chip 5, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

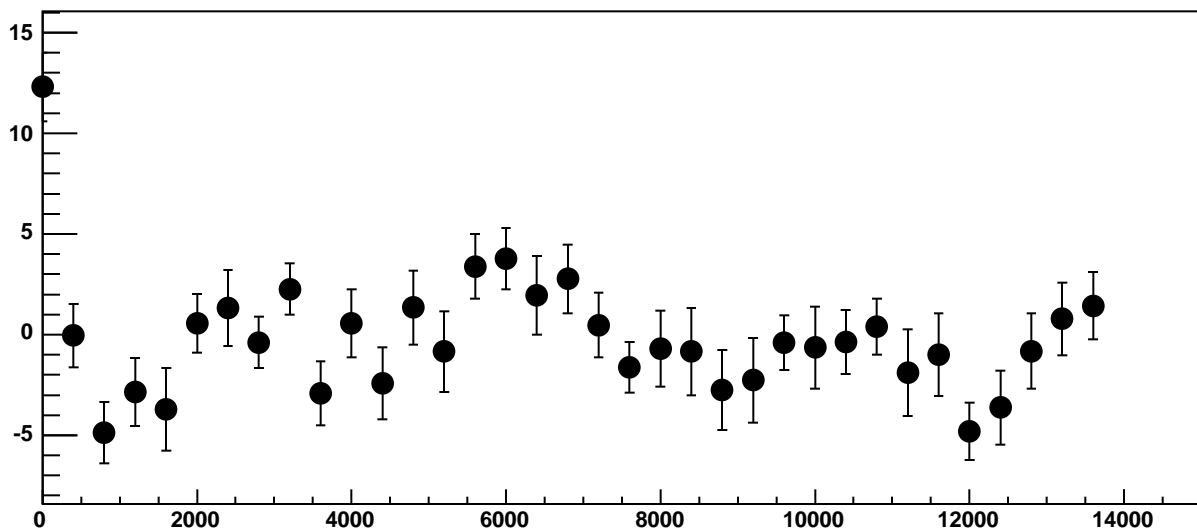


χ^2 / ndf 35.55 / 23
p0 -263.6 ± 0.7418
p1 0.0335 ± 0.0001123

Chip 5, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

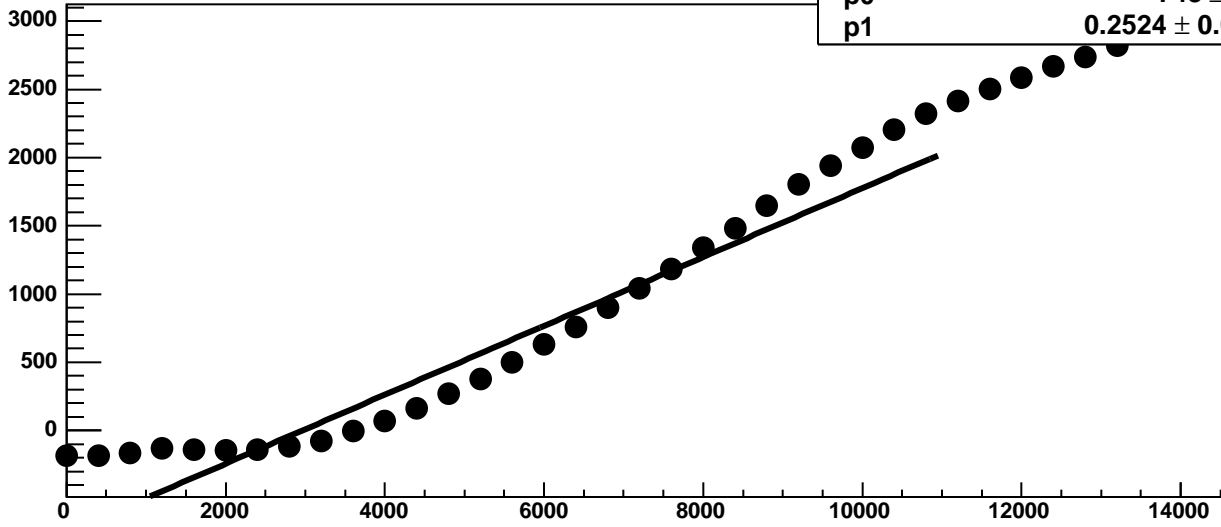


Chip 5, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

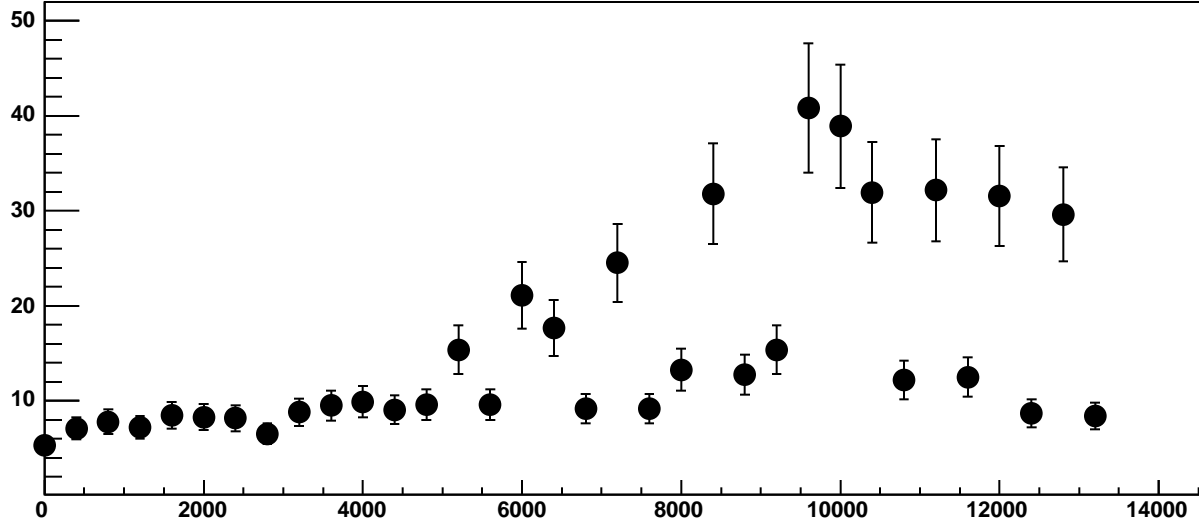


Chip 5, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

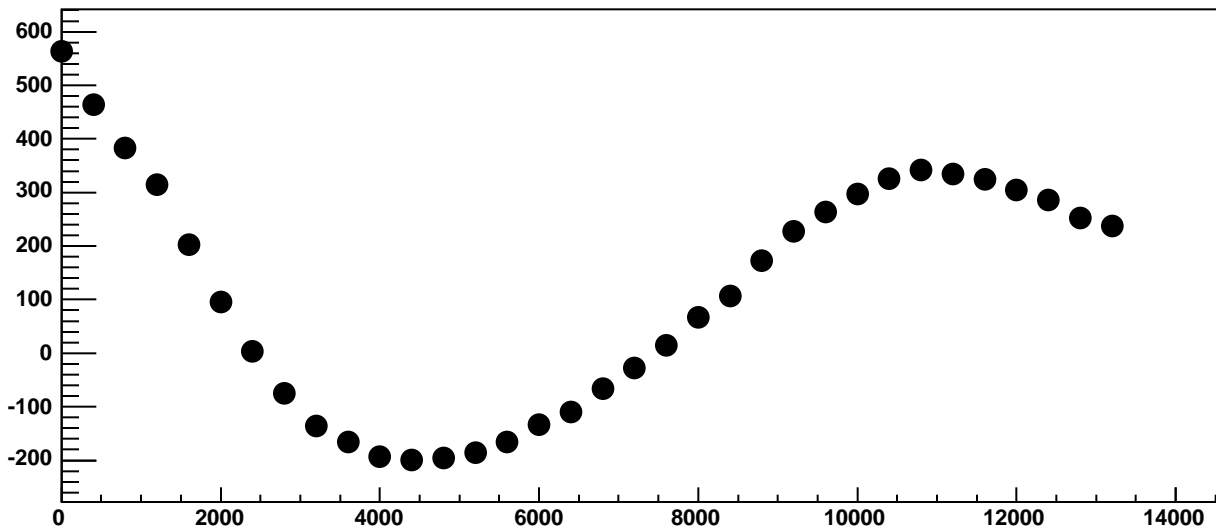
χ^2 / ndf 1.244e+05 / 23
p0 -748 ± 0.9781
p1 0.2524 ± 0.000192



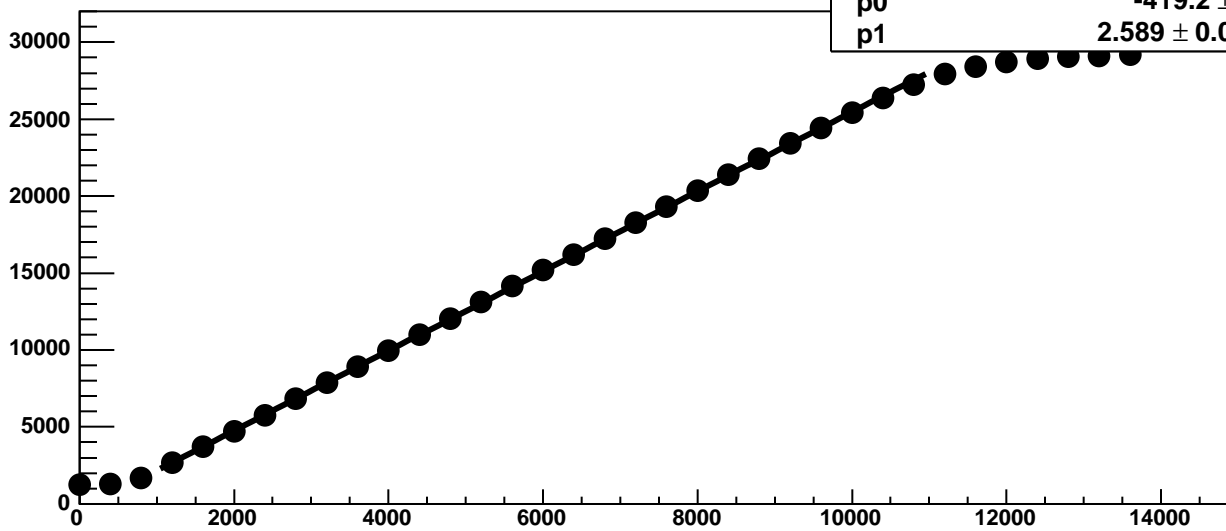
Chip 5, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



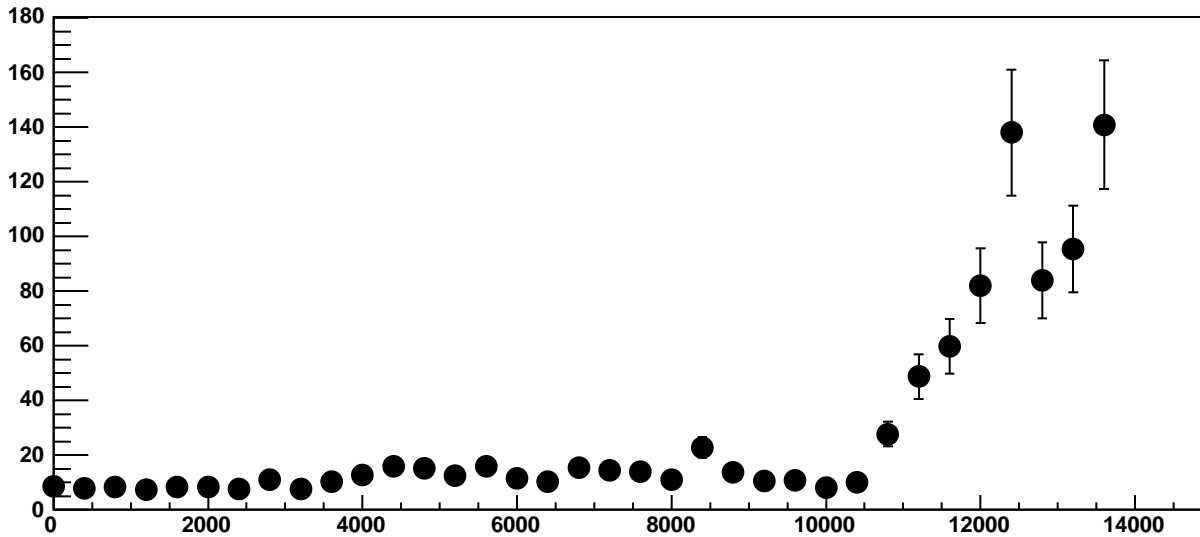
Chip 5, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



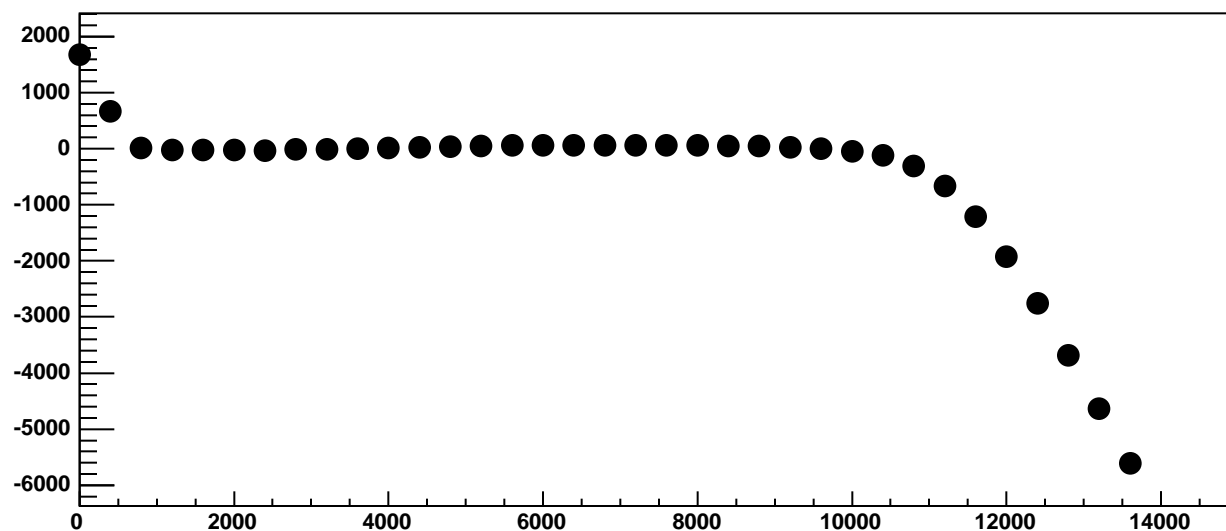
Chip 5, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



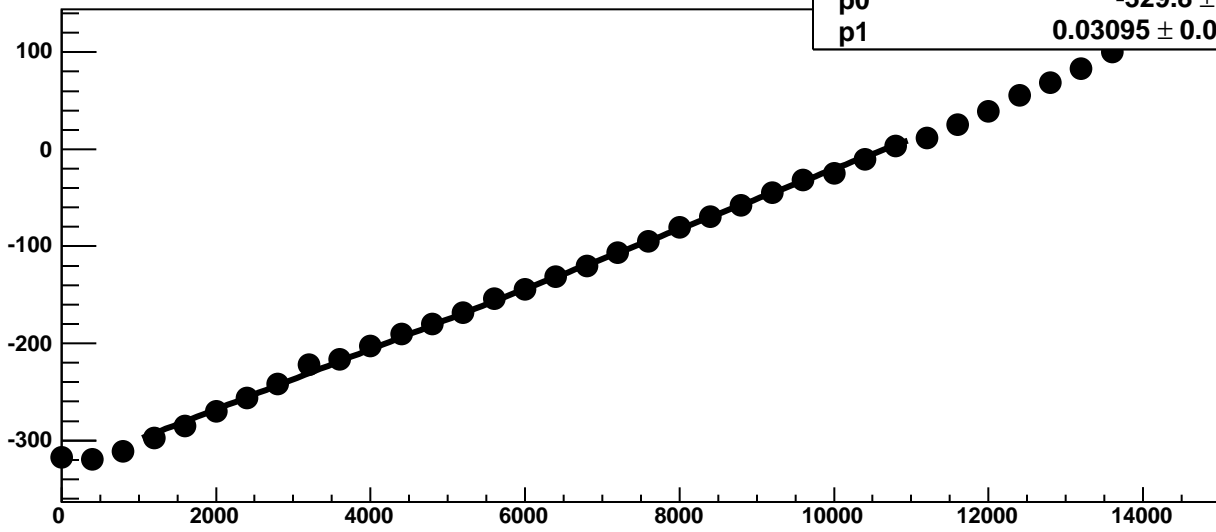
Chip 5, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC

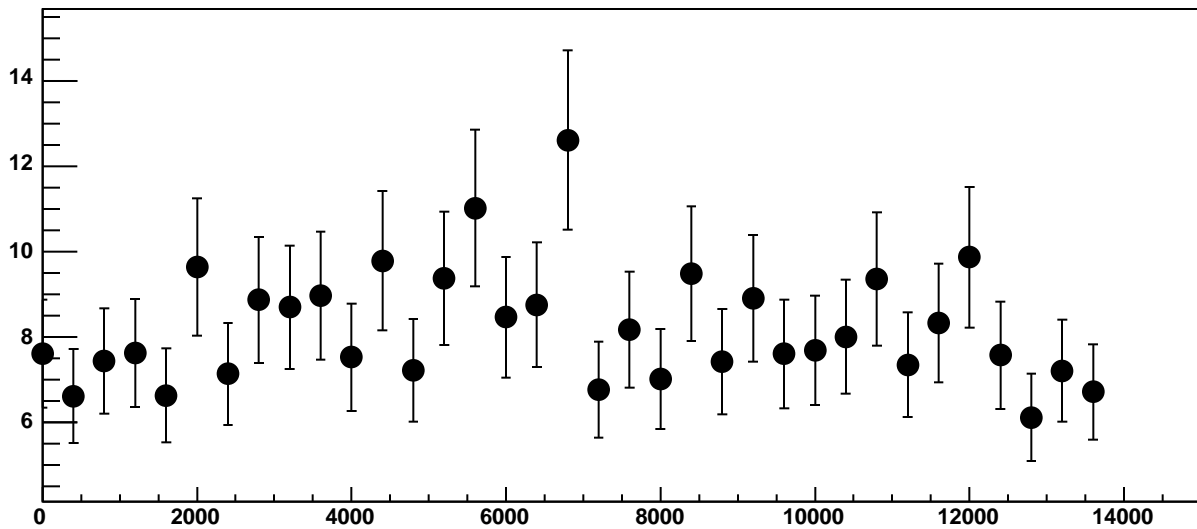


Chip 5, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

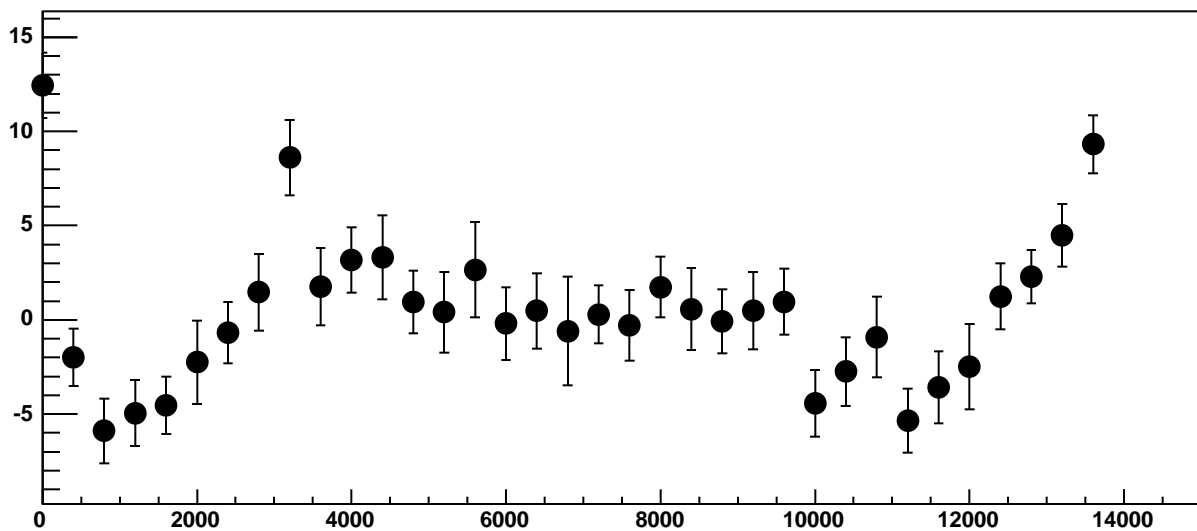


χ^2 / ndf 55.61 / 23
p0 -329.8 ± 0.8452
p1 0.03095 ± 0.0001271

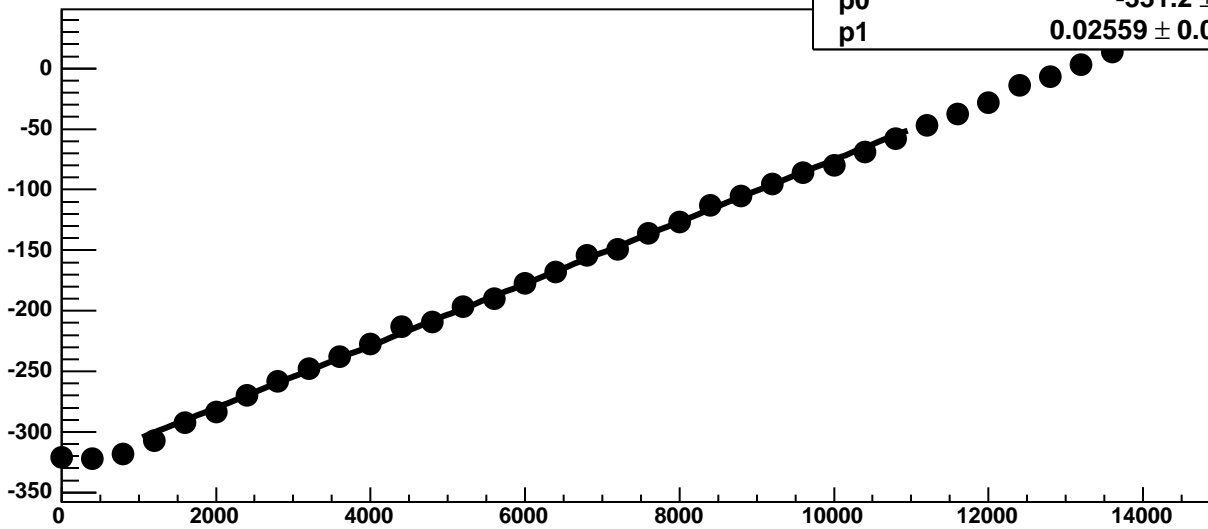
Chip 5, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

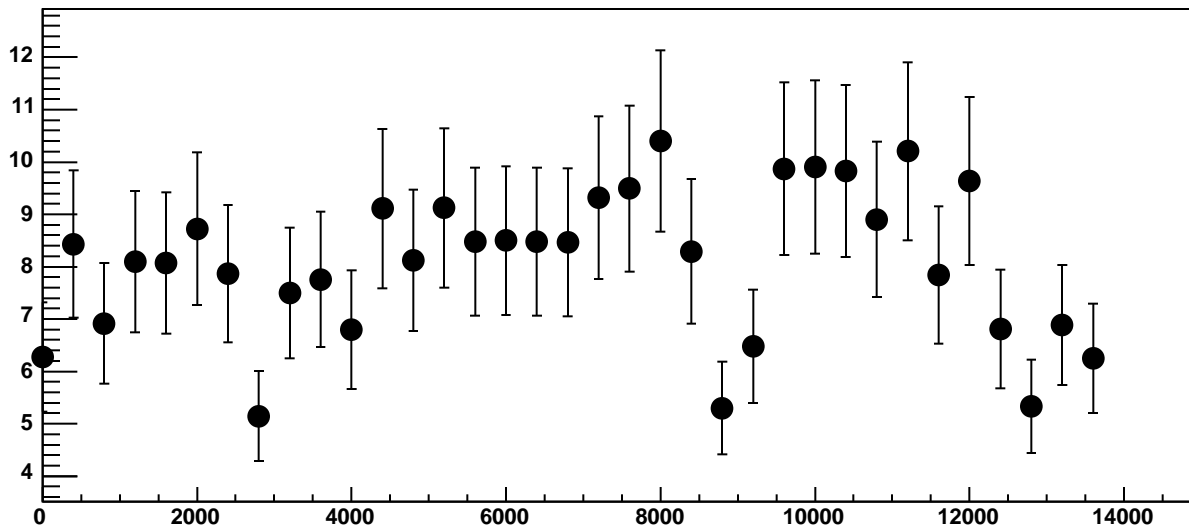


Chip 5, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

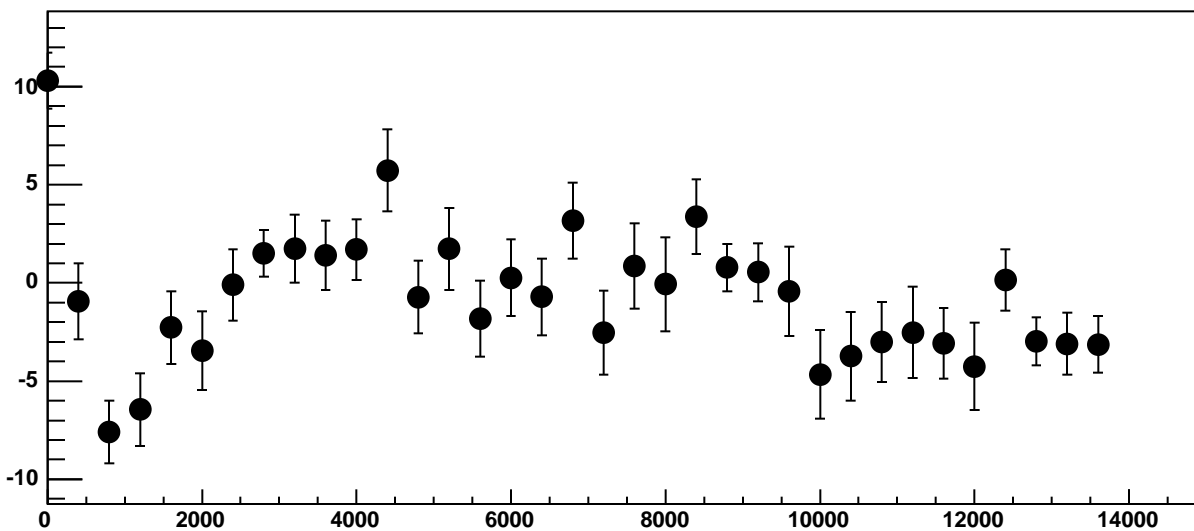


χ^2 / ndf 47.46 / 23
p0 -331.2 ± 0.8127
p1 0.02559 ± 0.0001262

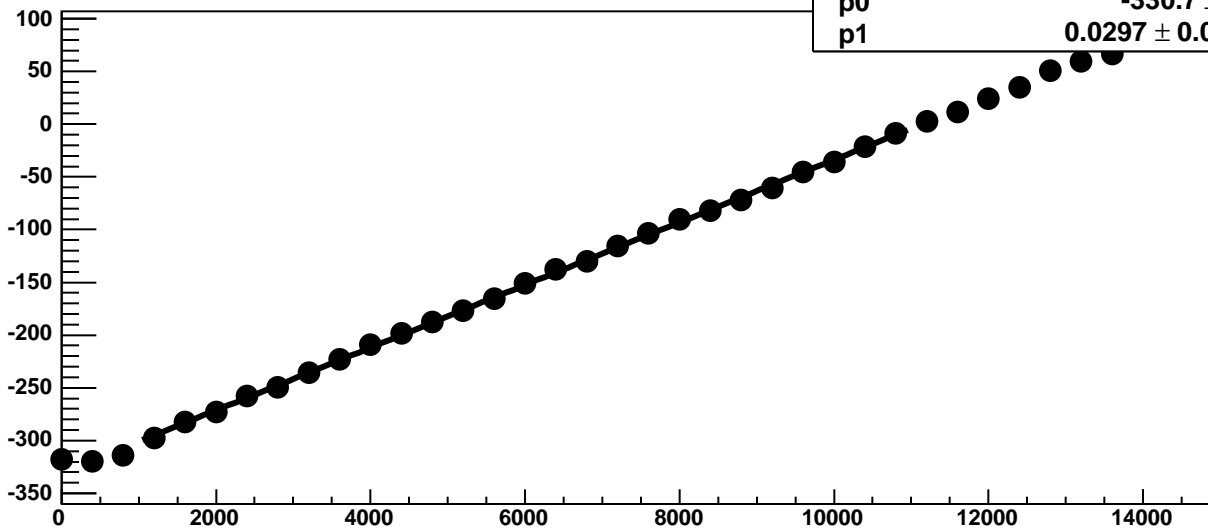
Chip 5, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

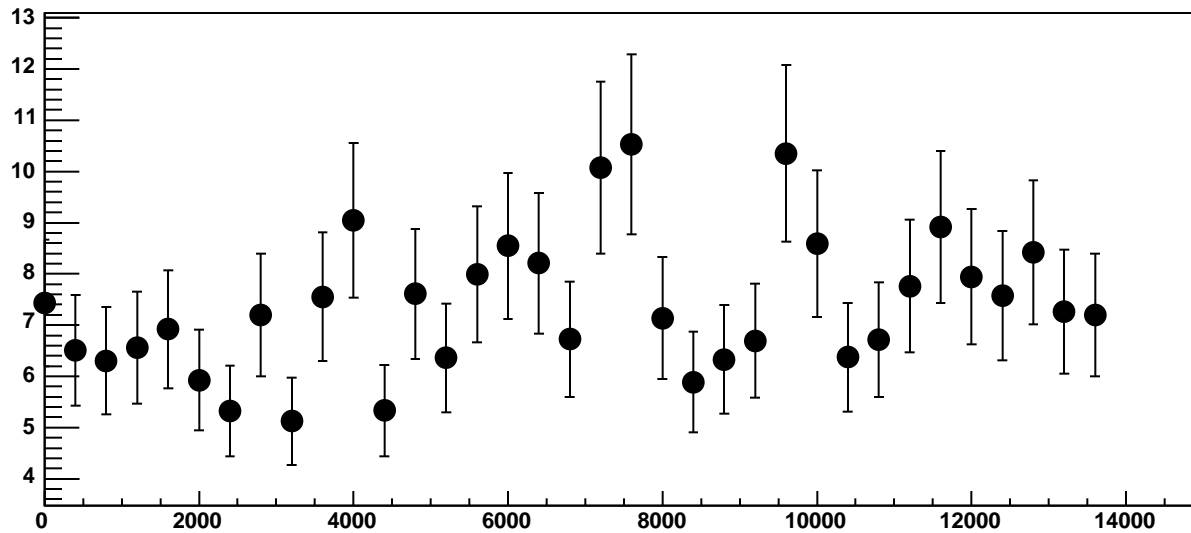


Chip 5, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC

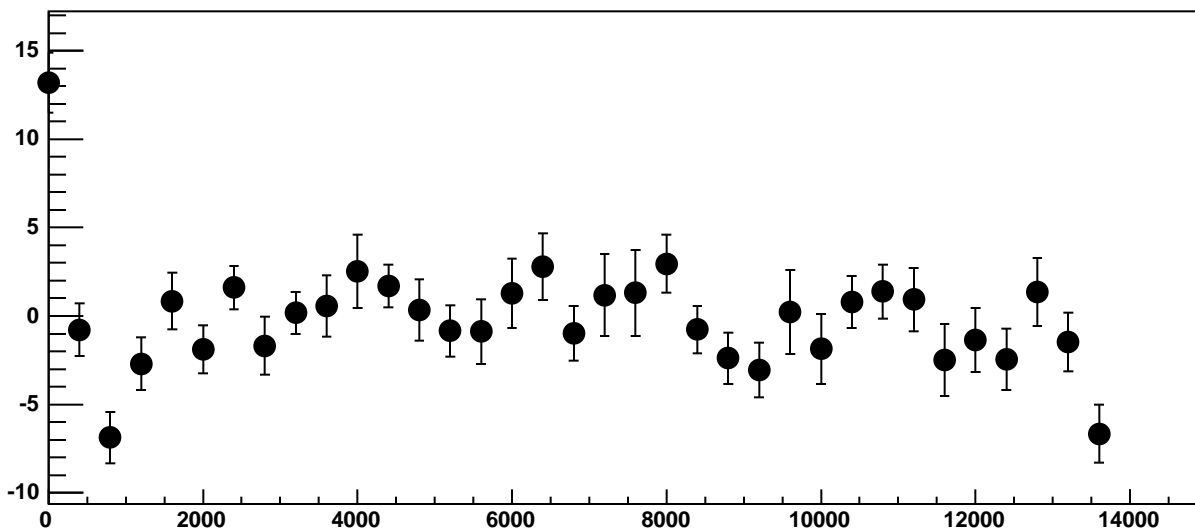


χ^2 / ndf 28.12 / 23
p0 -330.7 ± 0.6821
p1 0.0297 ± 0.0001068

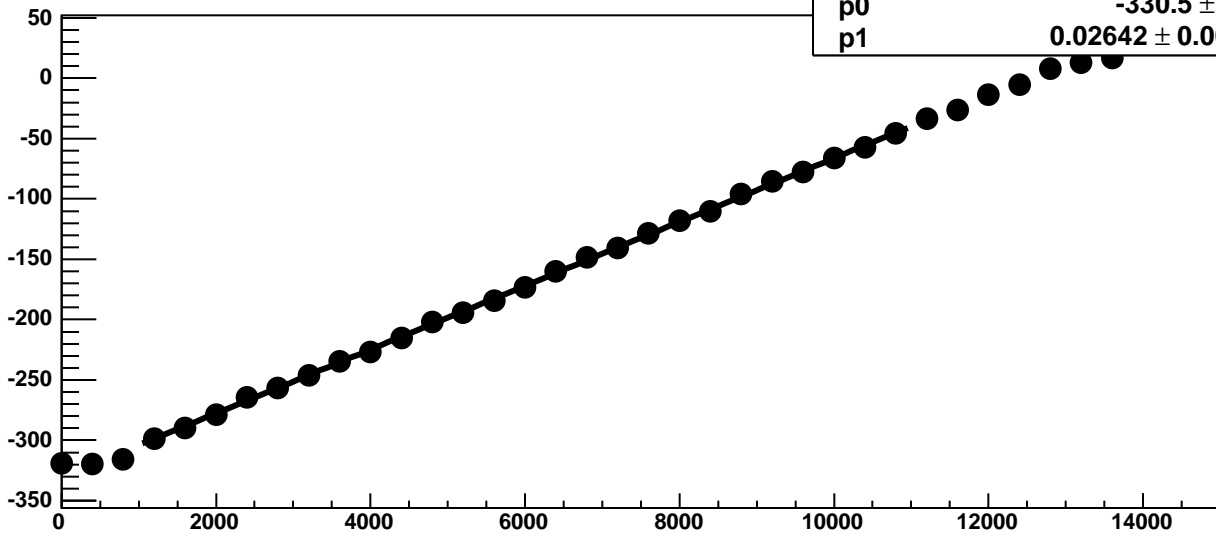
Chip 5, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



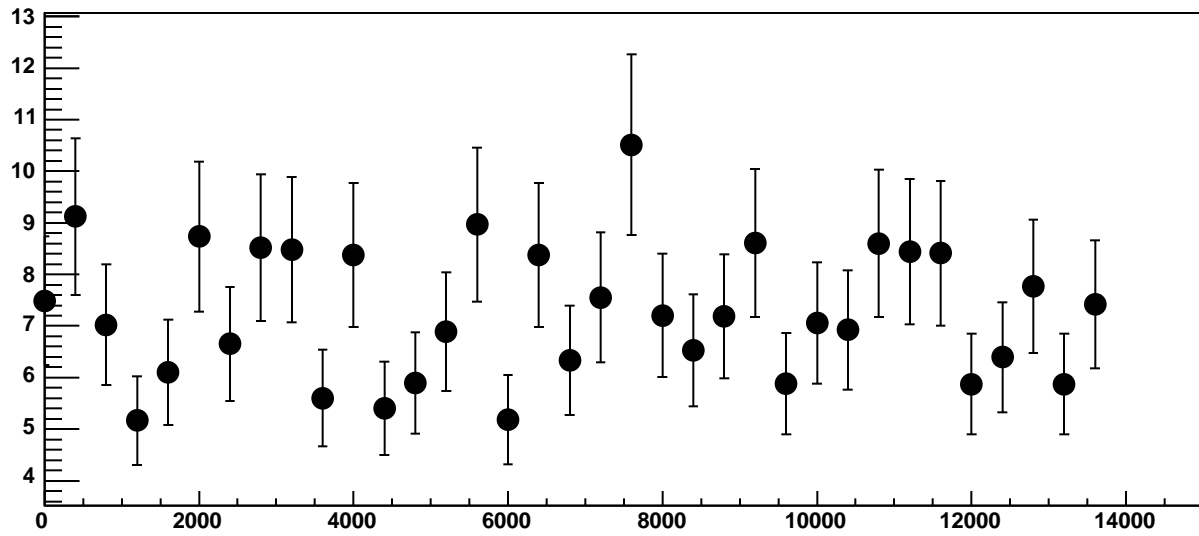
Chip 5, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC



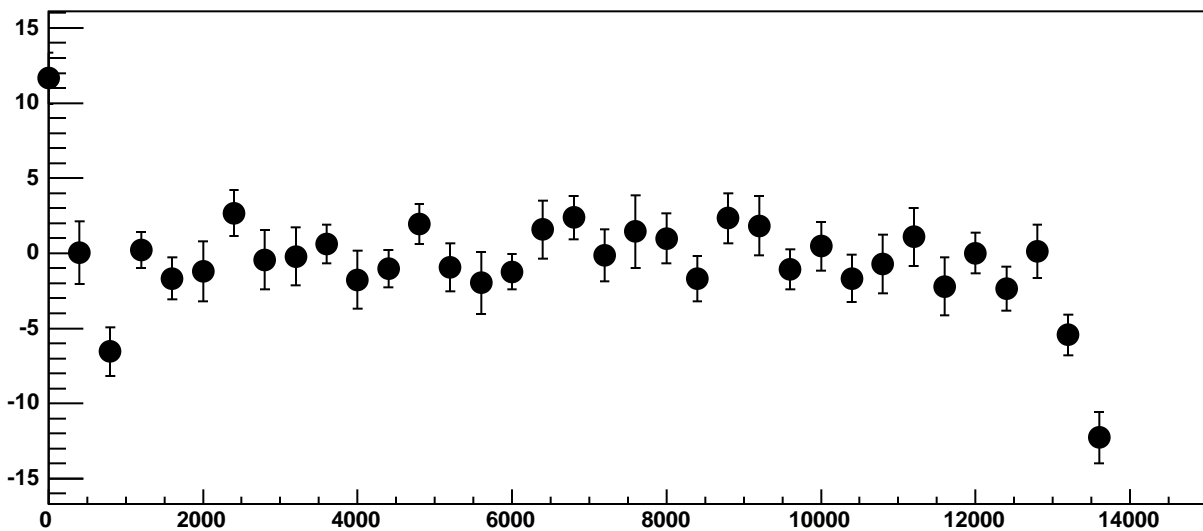
Chip 5, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



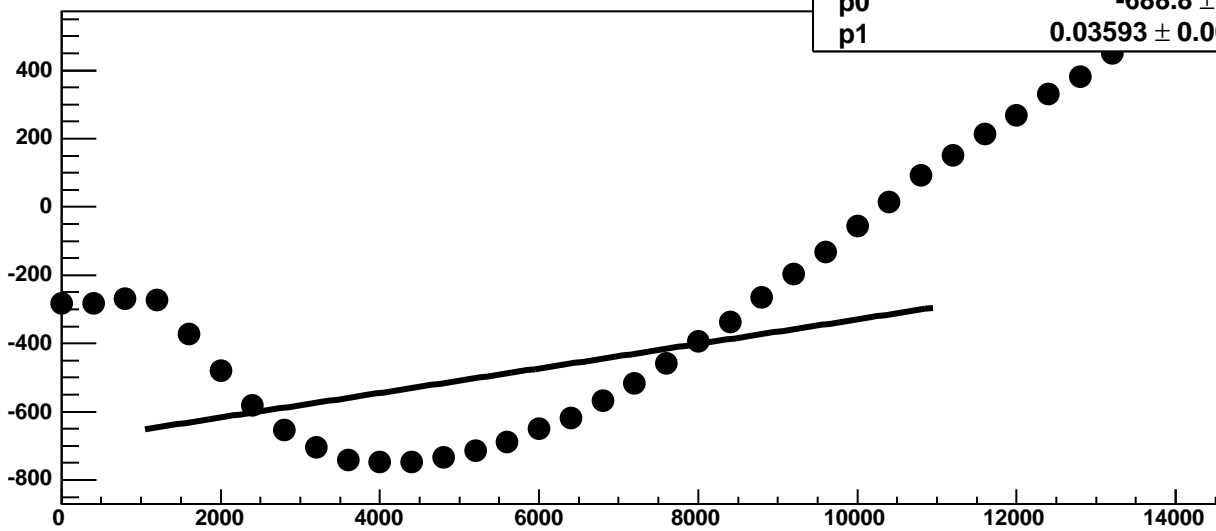
Chip 5, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

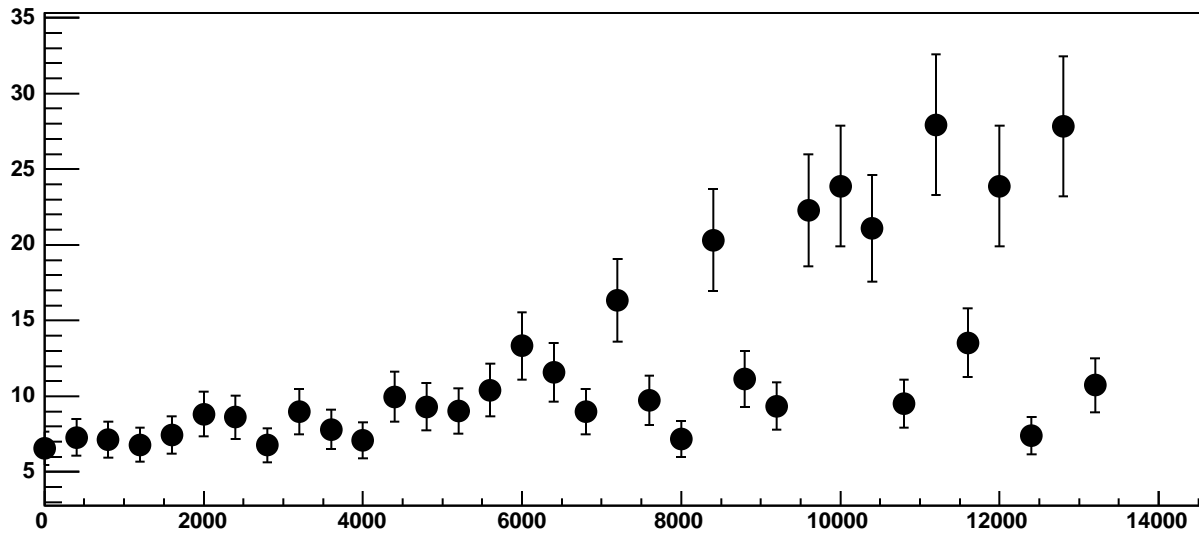


Chip 5, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

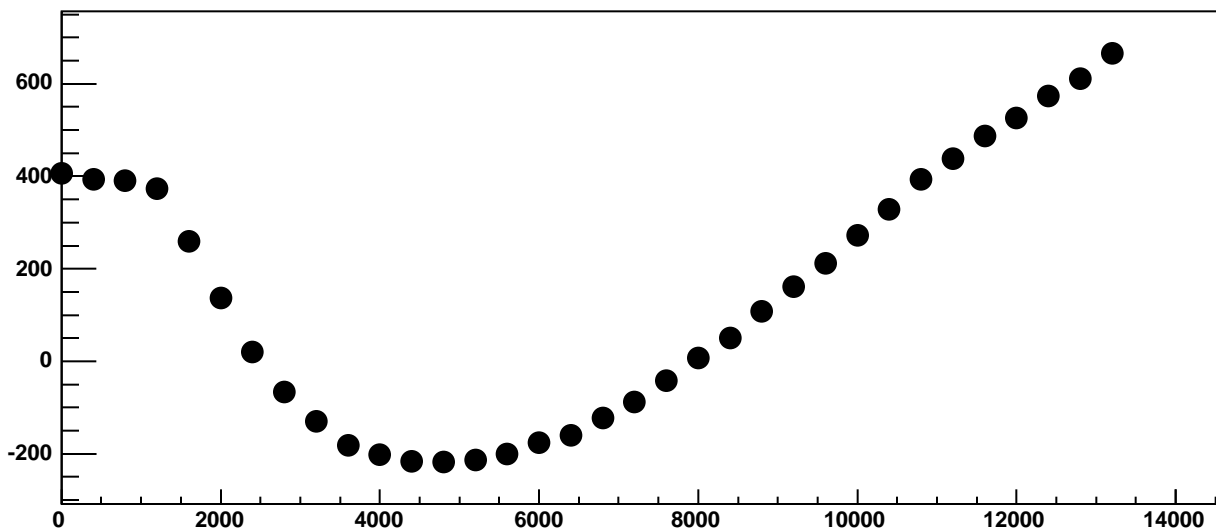


χ^2 / ndf 2.136e+05 / 23
p0 -688.8 ± 0.8905
p1 0.03593 ± 0.0001573

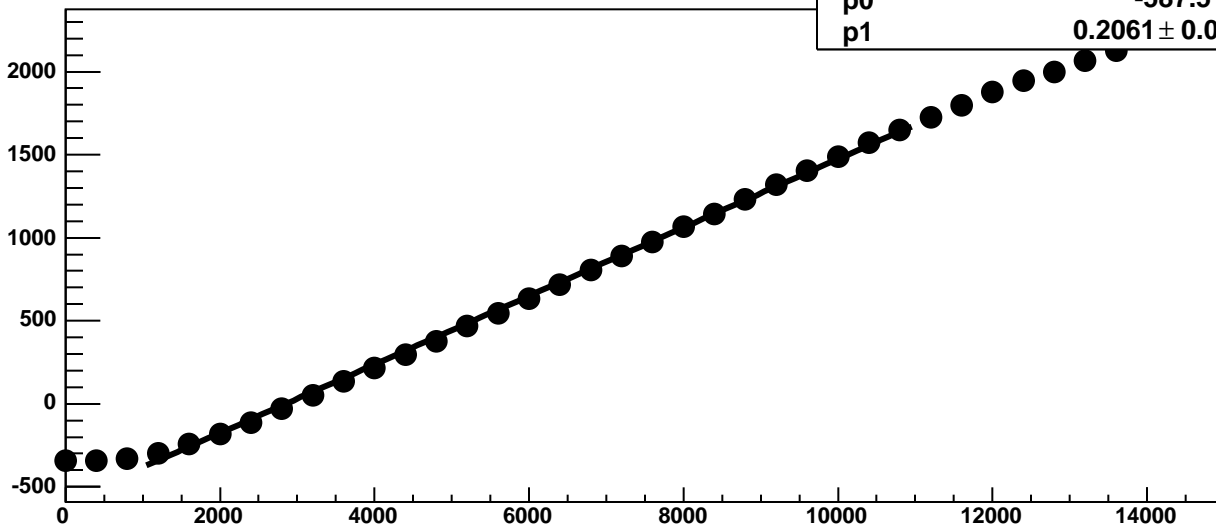
Chip 5, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



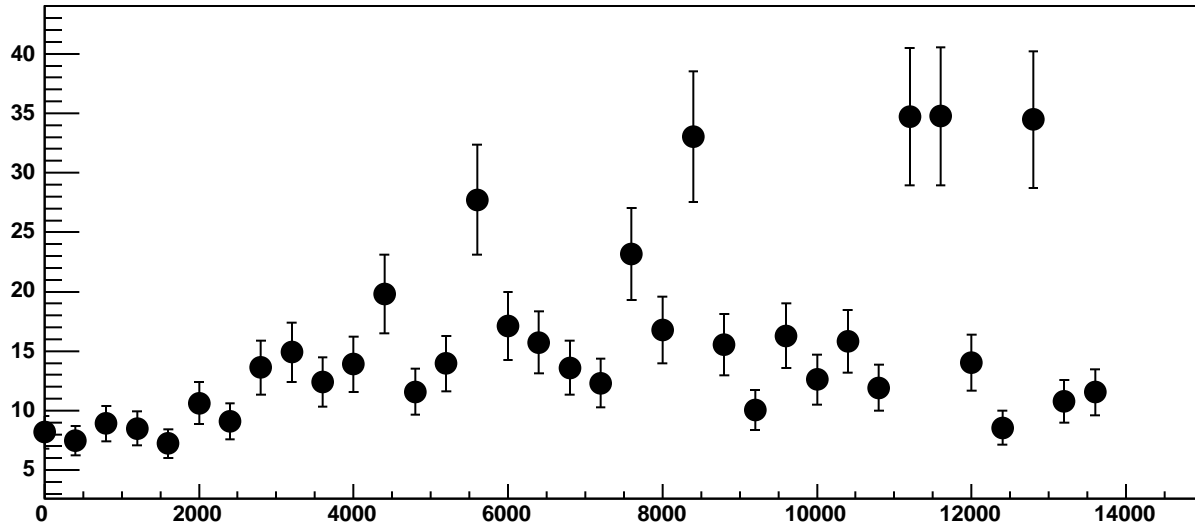
Chip 5, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



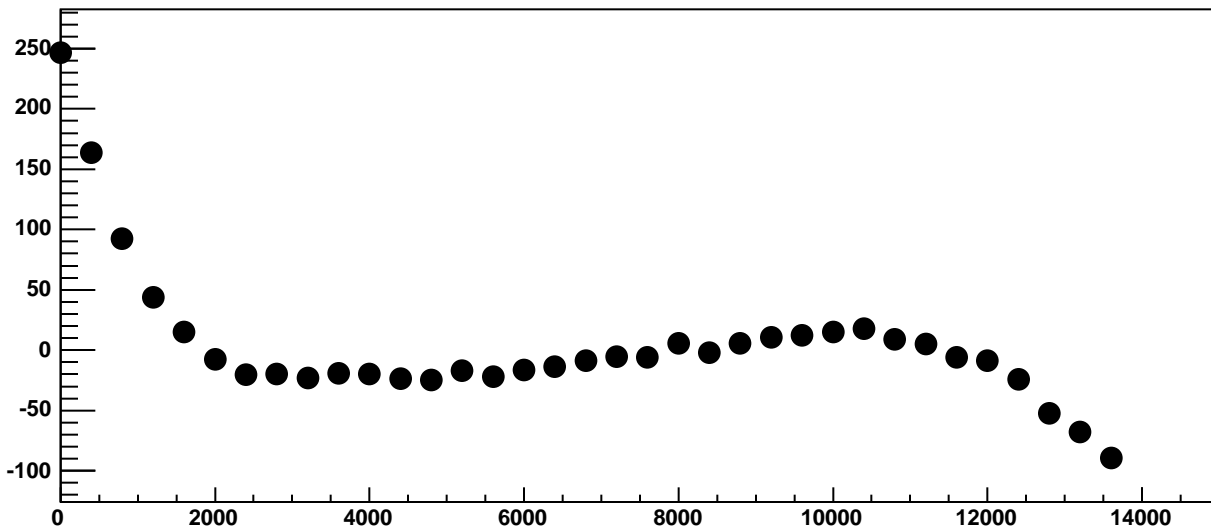
Chip 5, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC



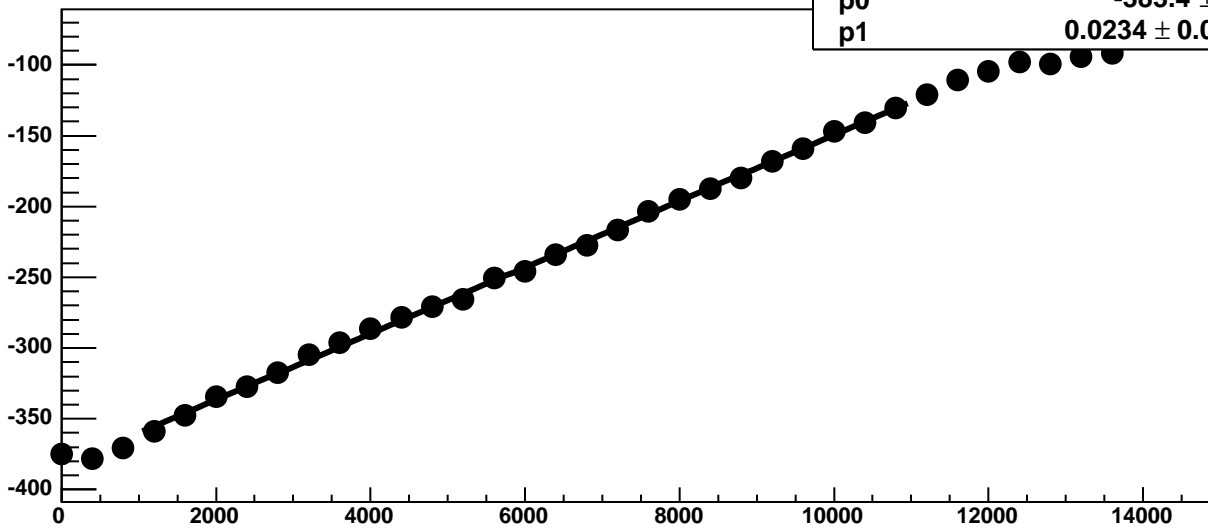
Chip 5, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

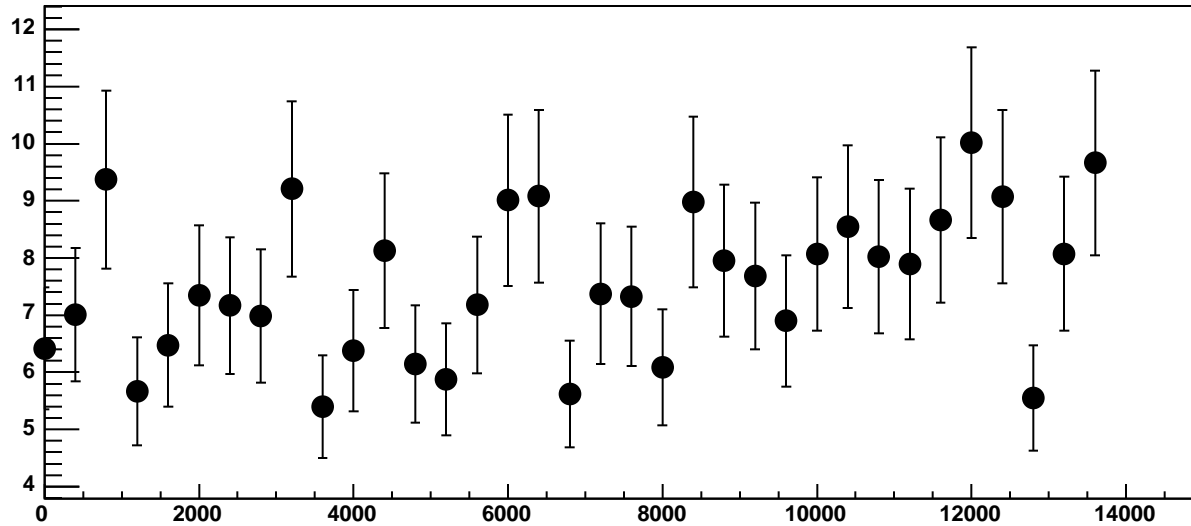


Chip 5, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

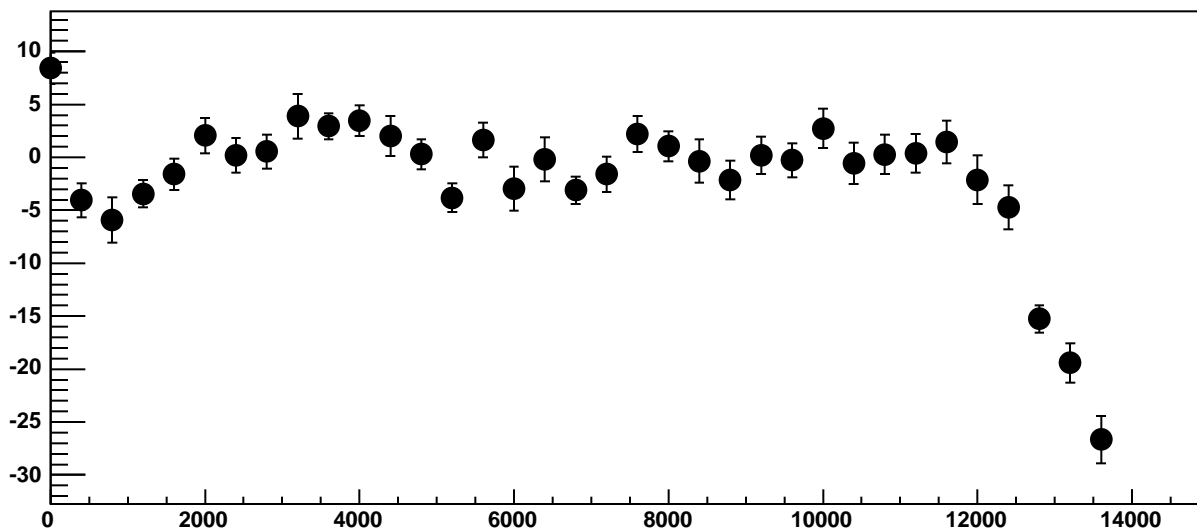


χ^2 / ndf 49.21 / 23
p0 -383.4 ± 0.7182
p1 0.0234 ± 0.0001136

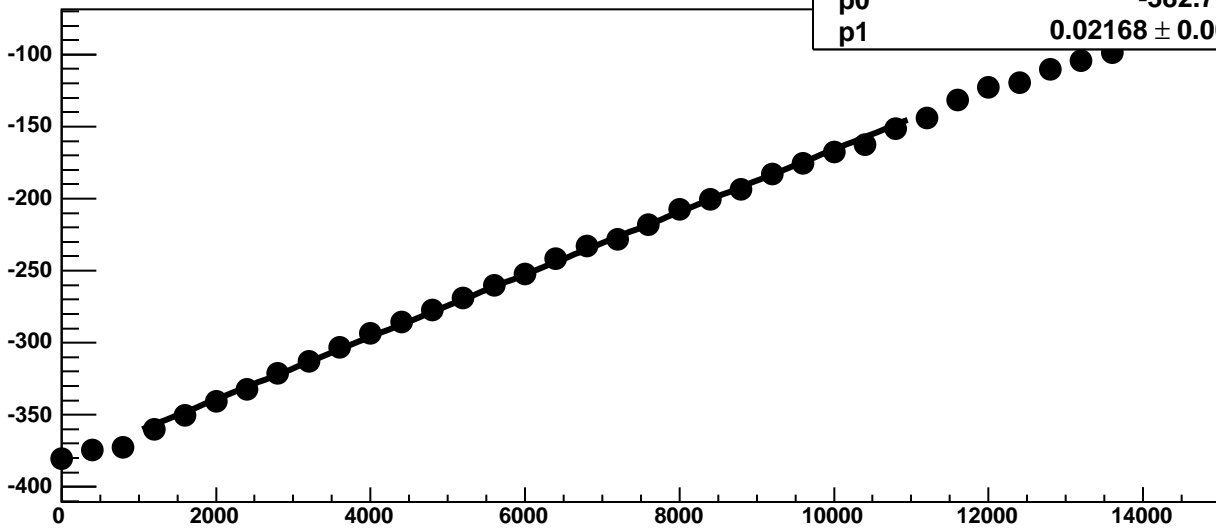
Chip 5, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

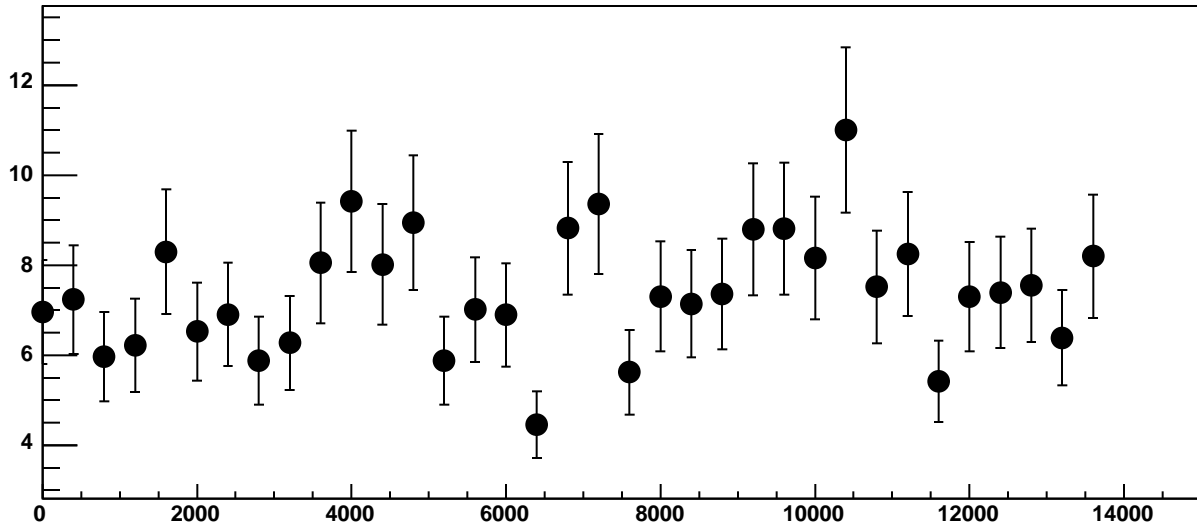


Chip 5, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

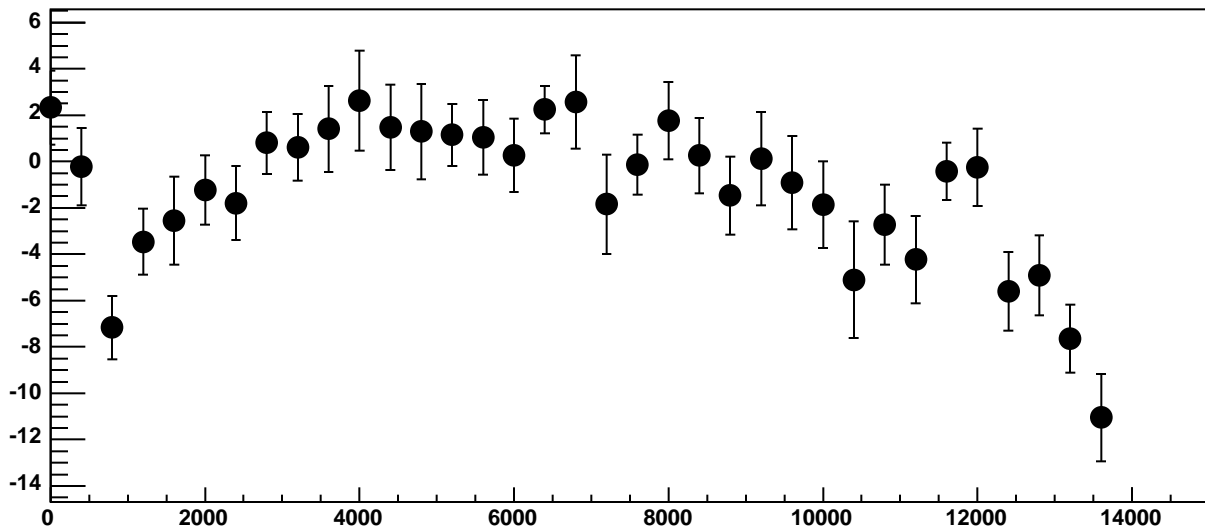


χ^2 / ndf 31.25 / 23
p0 -382.7 ± 0.75
p1 0.02168 ± 0.0001186

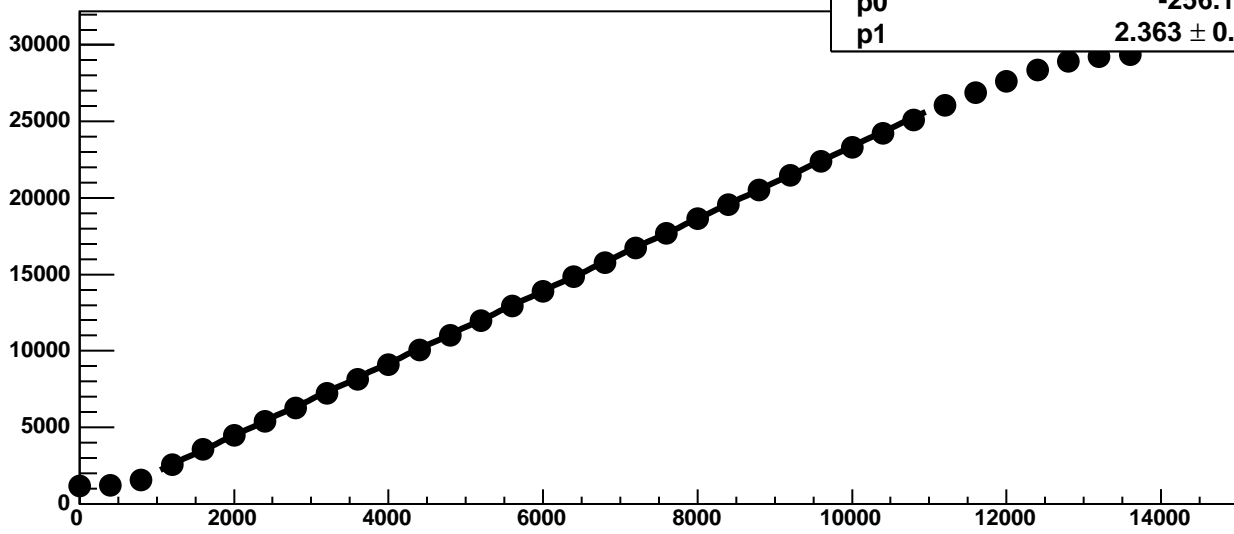
Chip 5, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



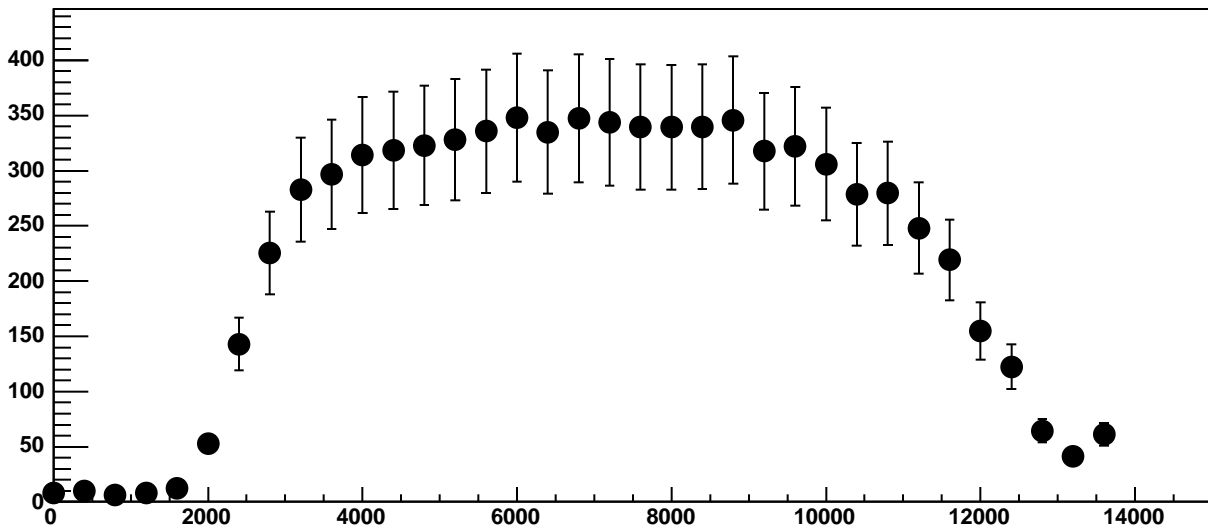
Chip 5, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



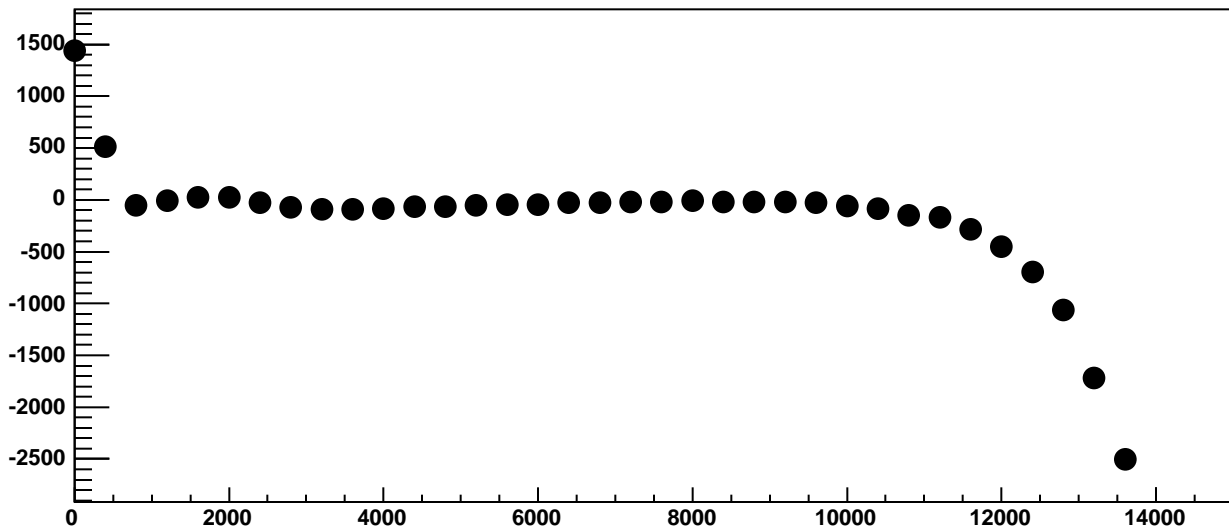
Chip 5, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC



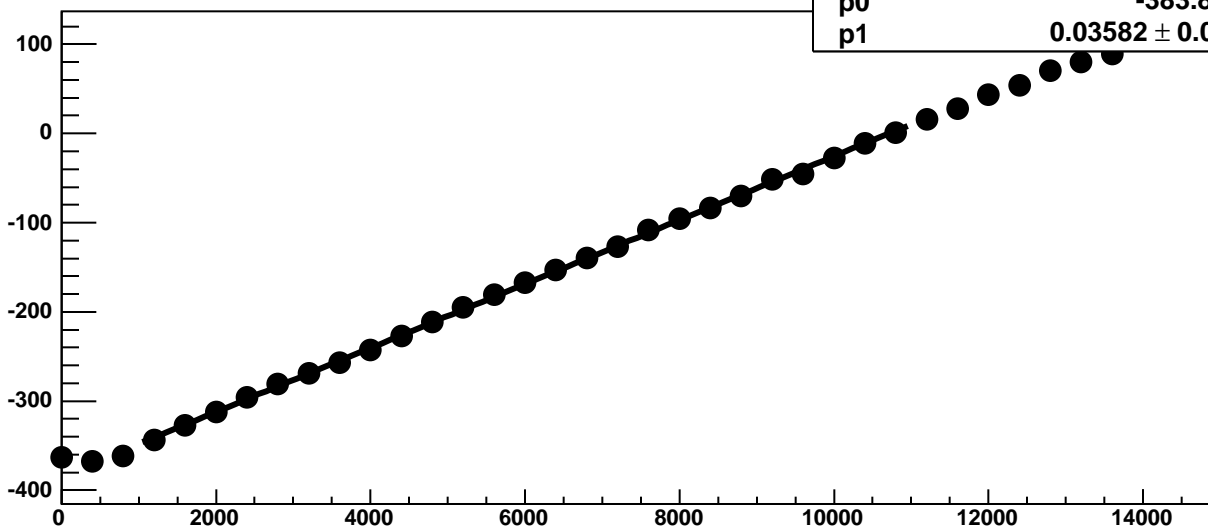
Chip 5, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC

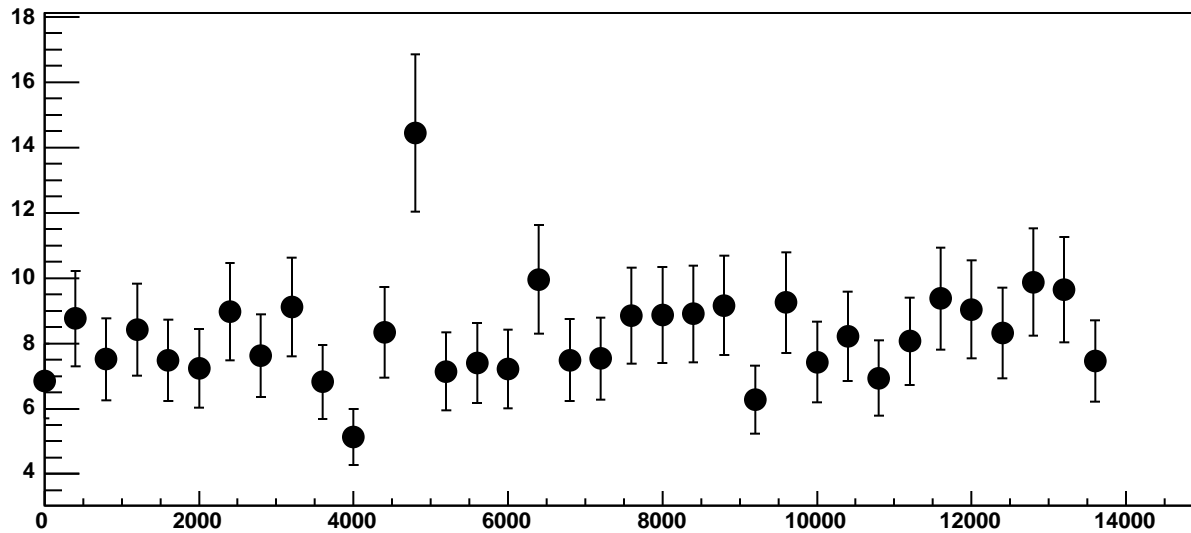


Chip 5, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

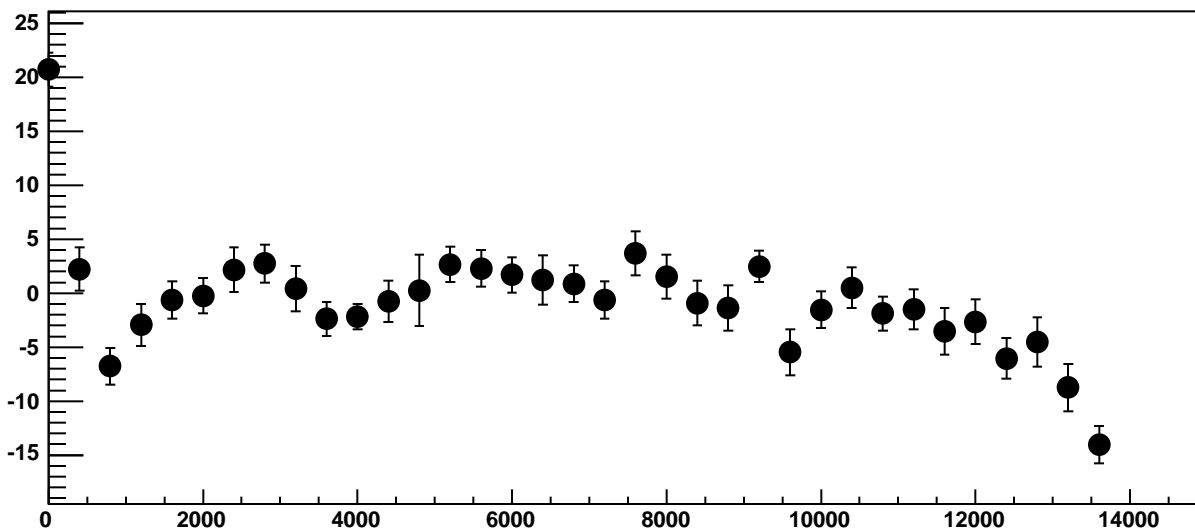


χ^2 / ndf 34.62 / 23
p0 -383.8 ± 0.811
p1 0.03582 ± 0.0001227

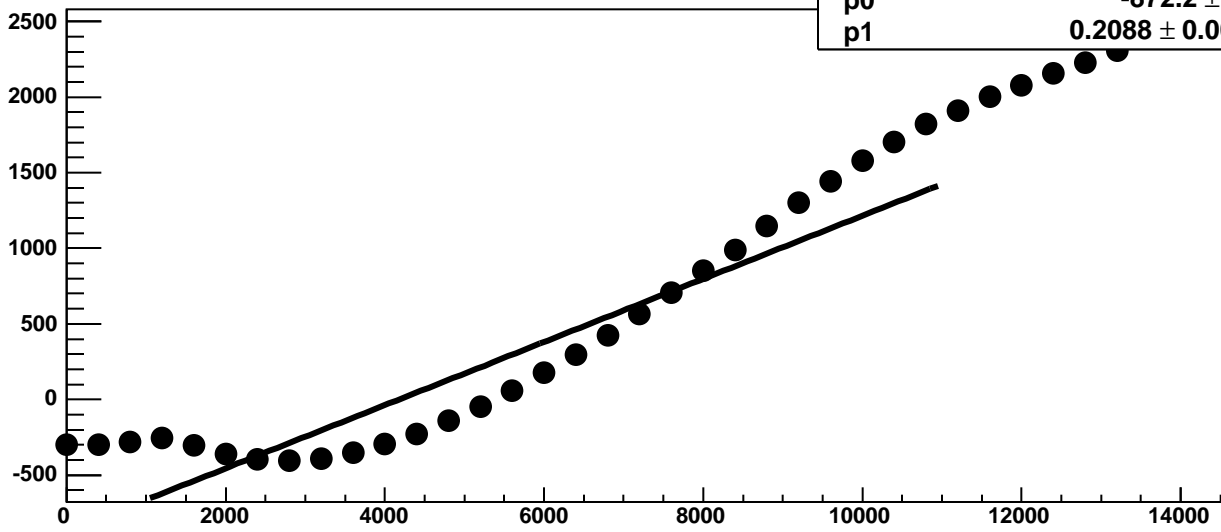
Chip 5, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



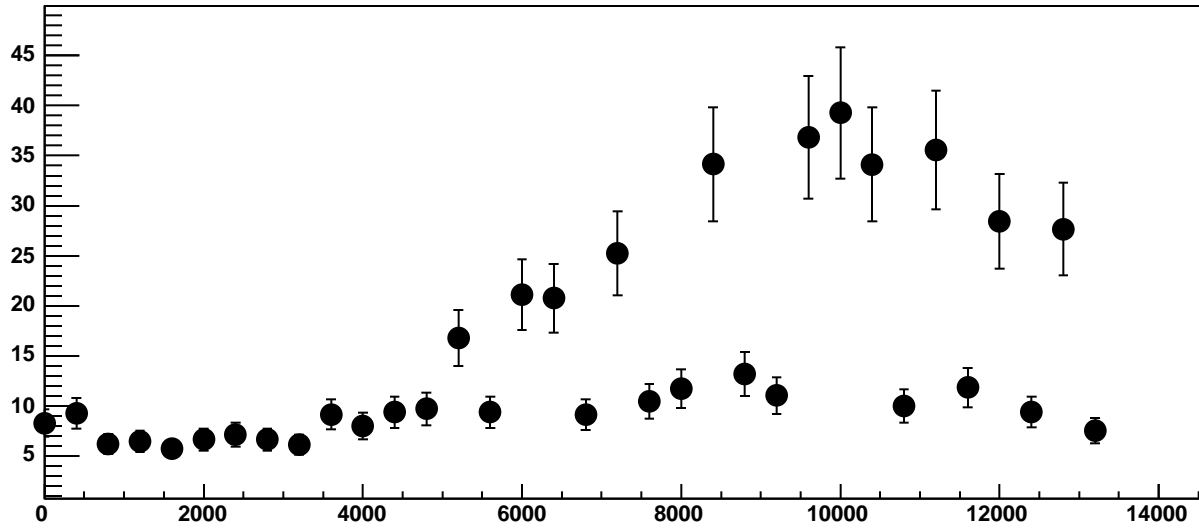
Chip 5, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



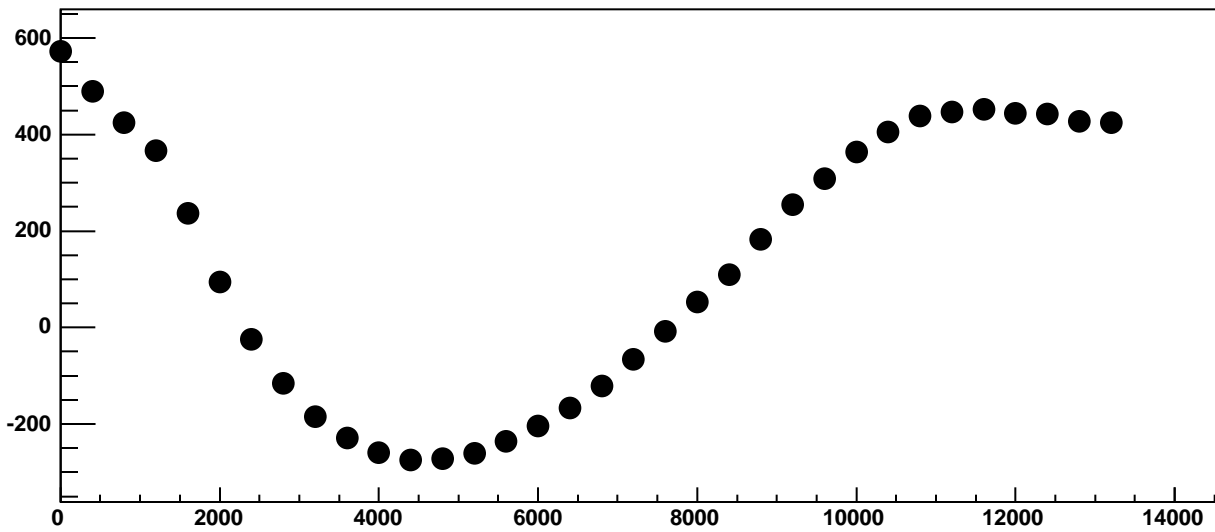
Chip 5, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



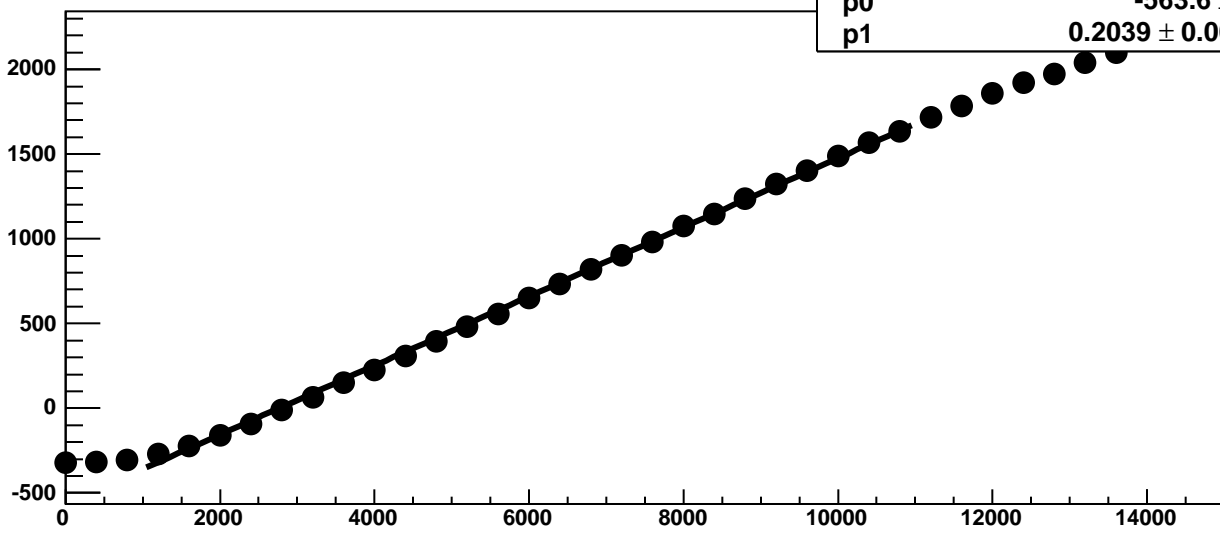
Chip 5, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



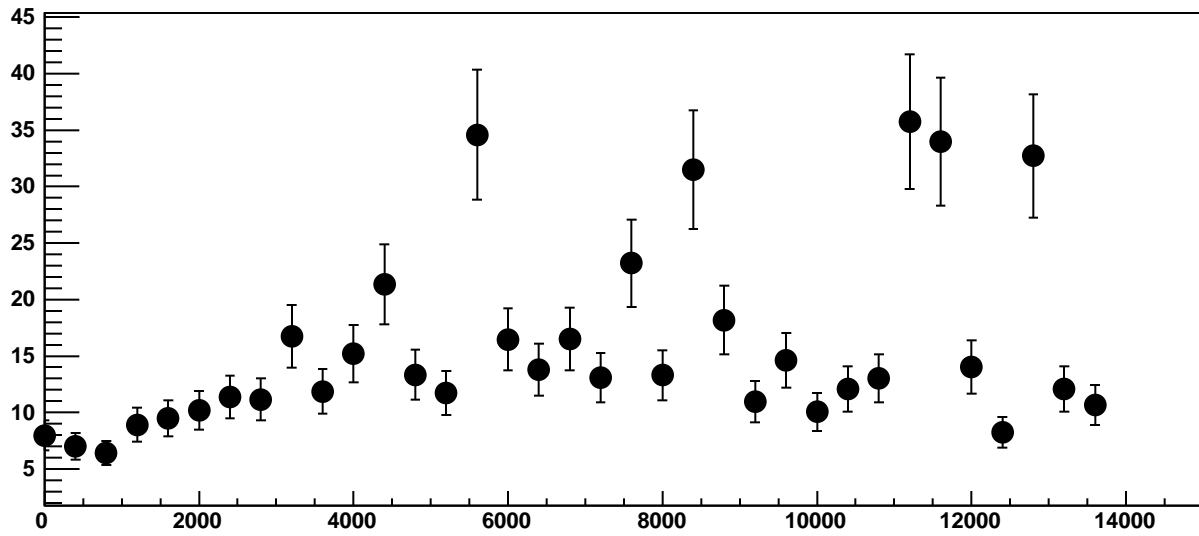
Chip 5, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



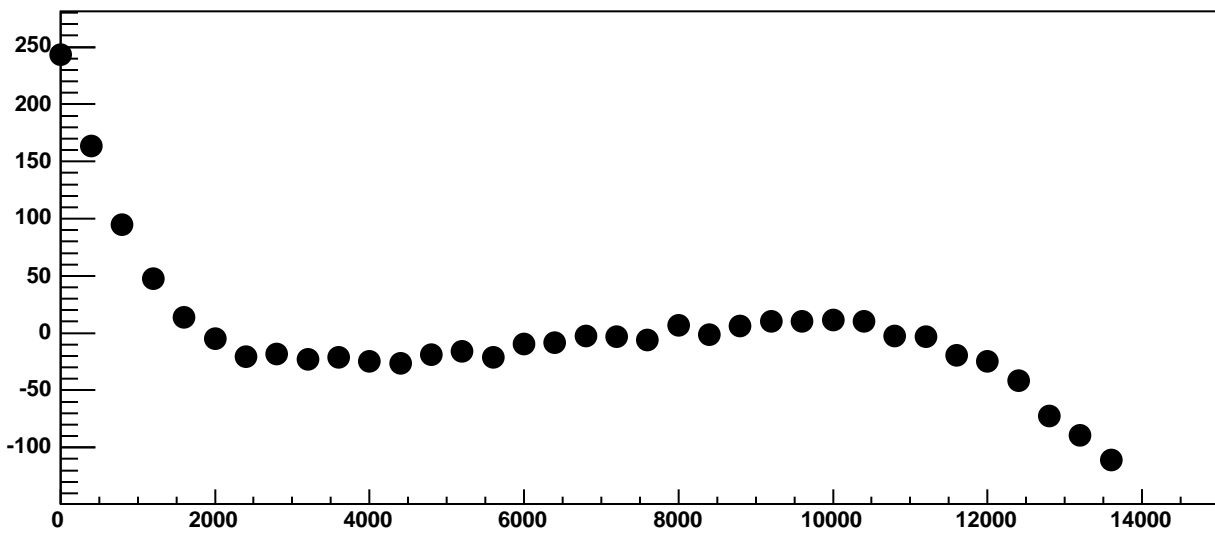
Chip 5, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



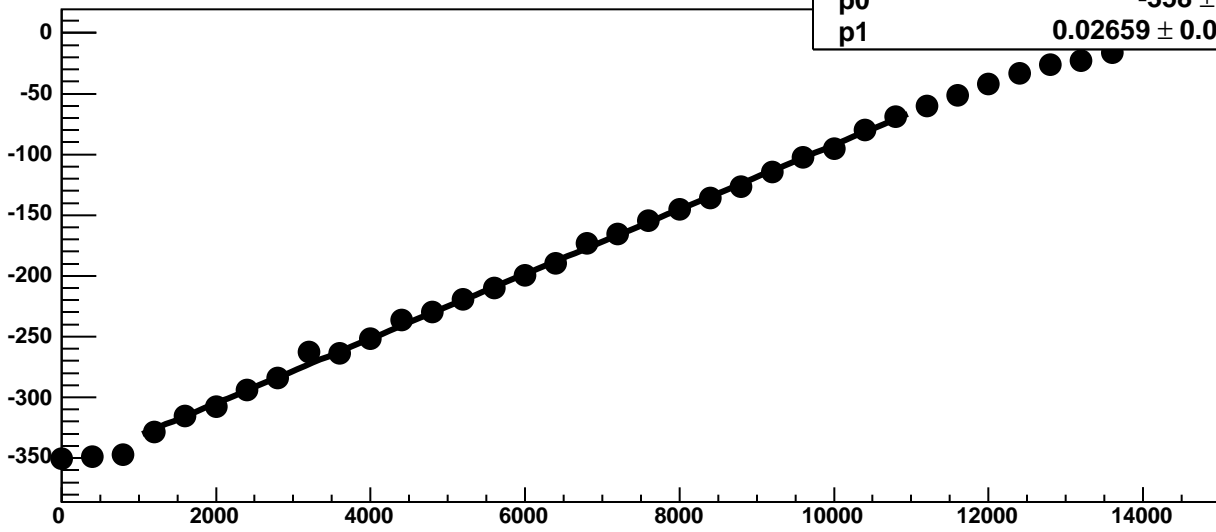
Chip 5, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



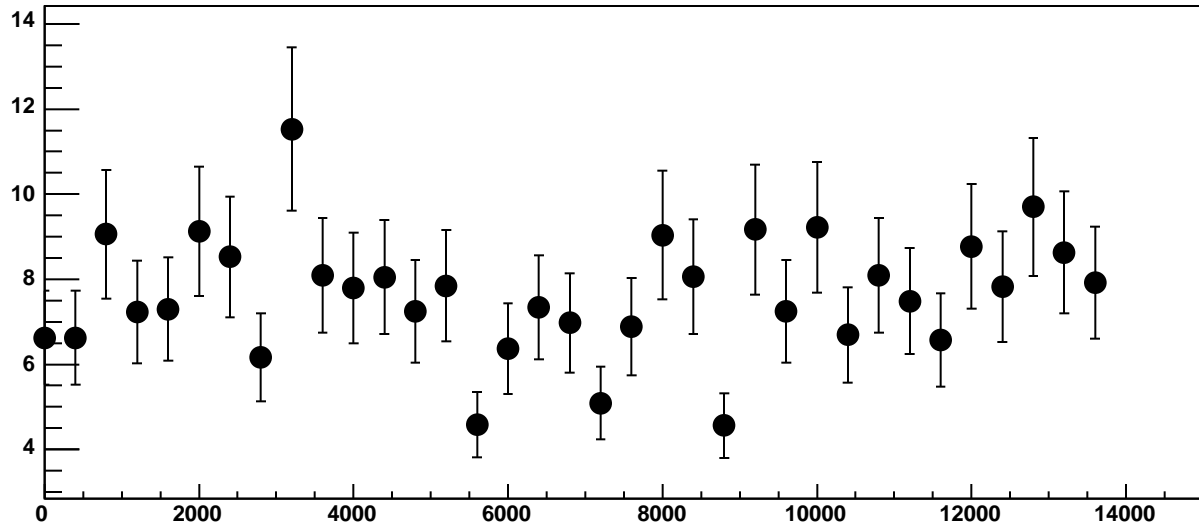
Chip 5, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



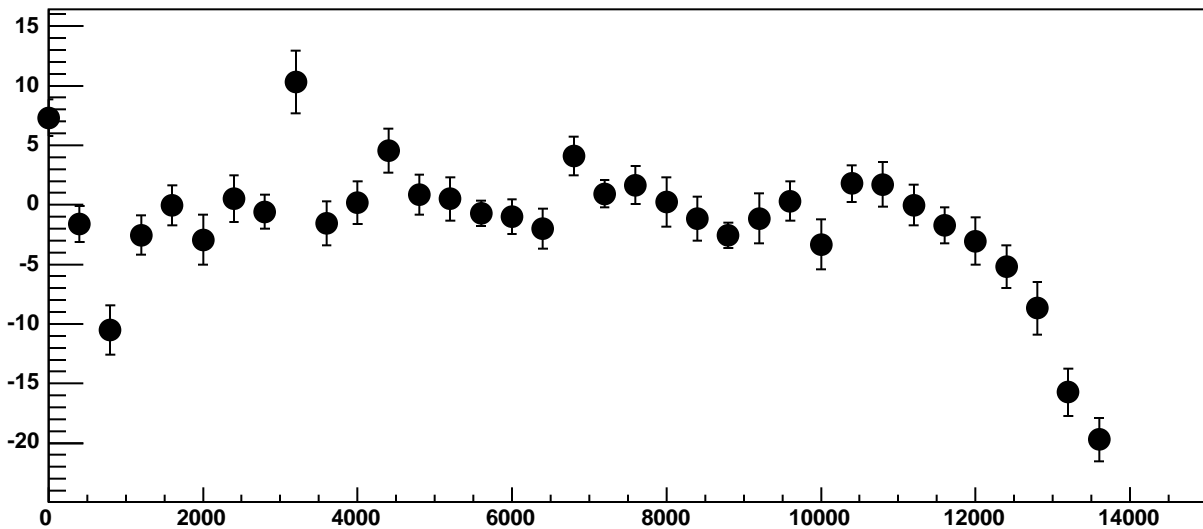
Chip 5, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC



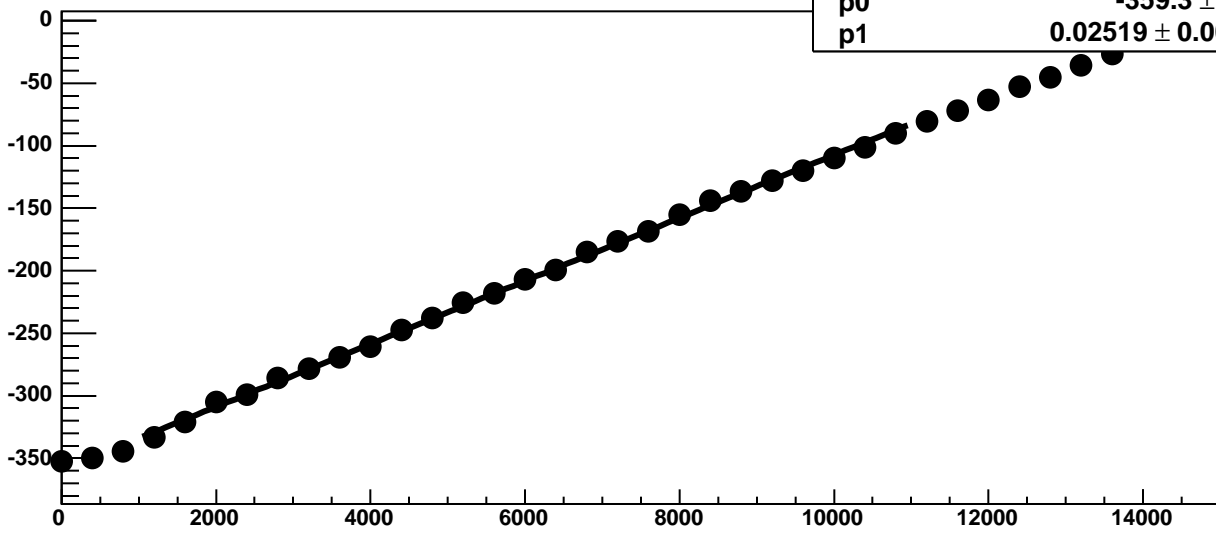
Chip 5, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC

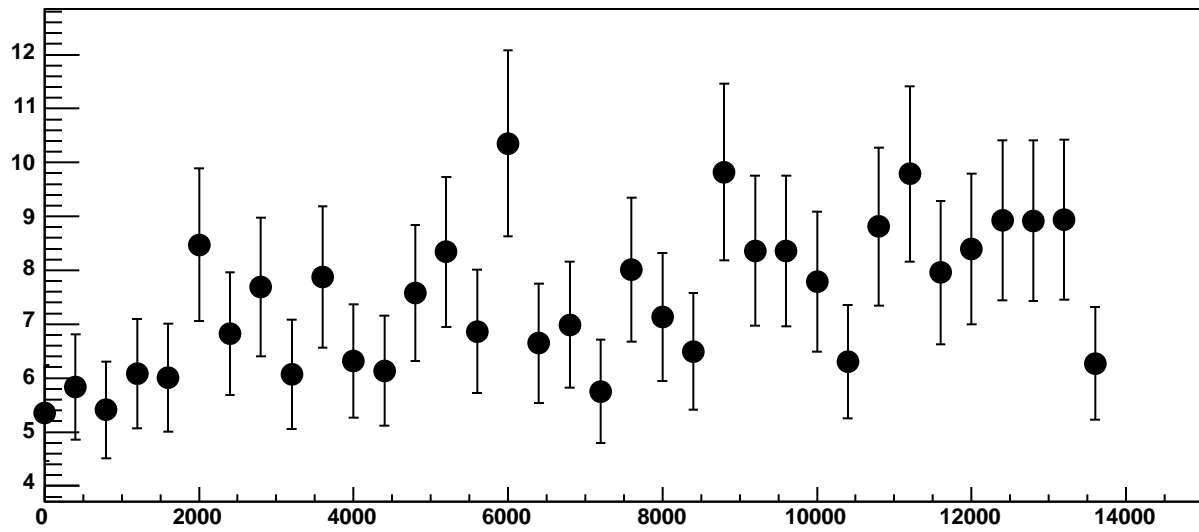


Chip 5, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

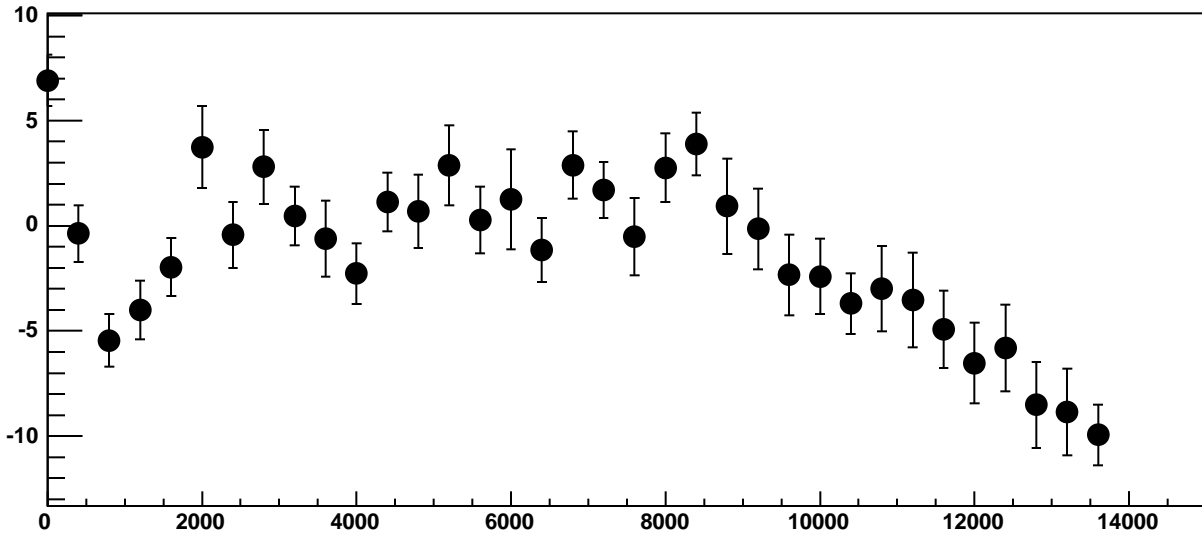


χ^2 / ndf 49.98 / 23
p0 -359.3 ± 0.7273
p1 0.02519 ± 0.0001136

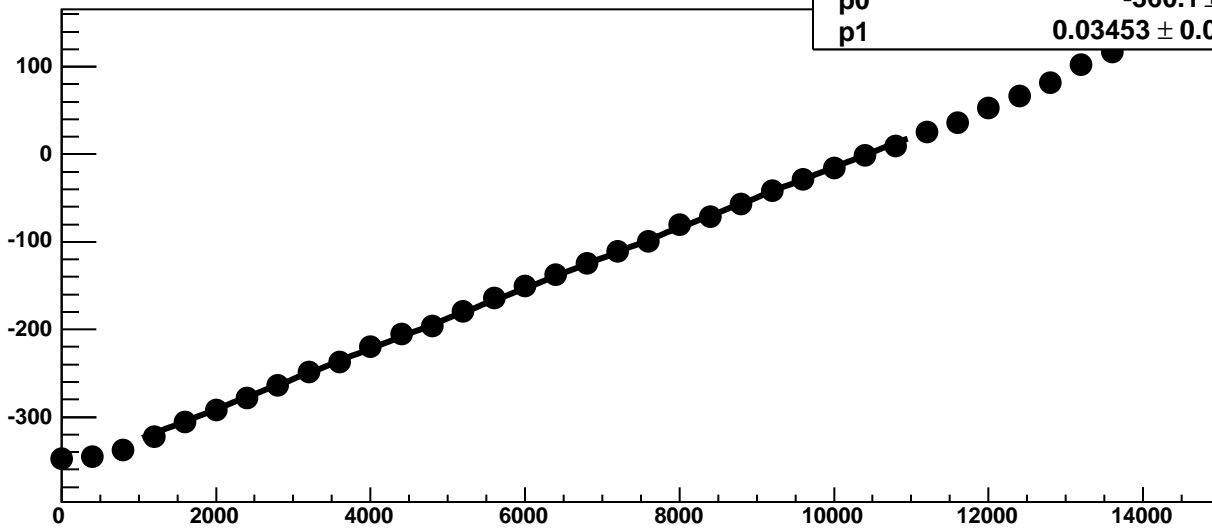
Chip 5, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

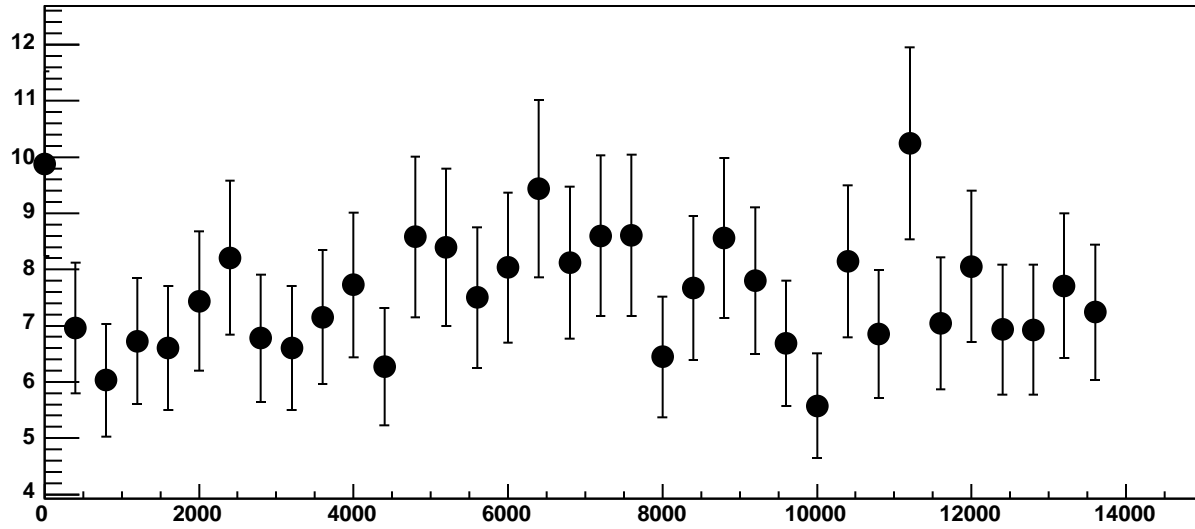


Chip 5, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

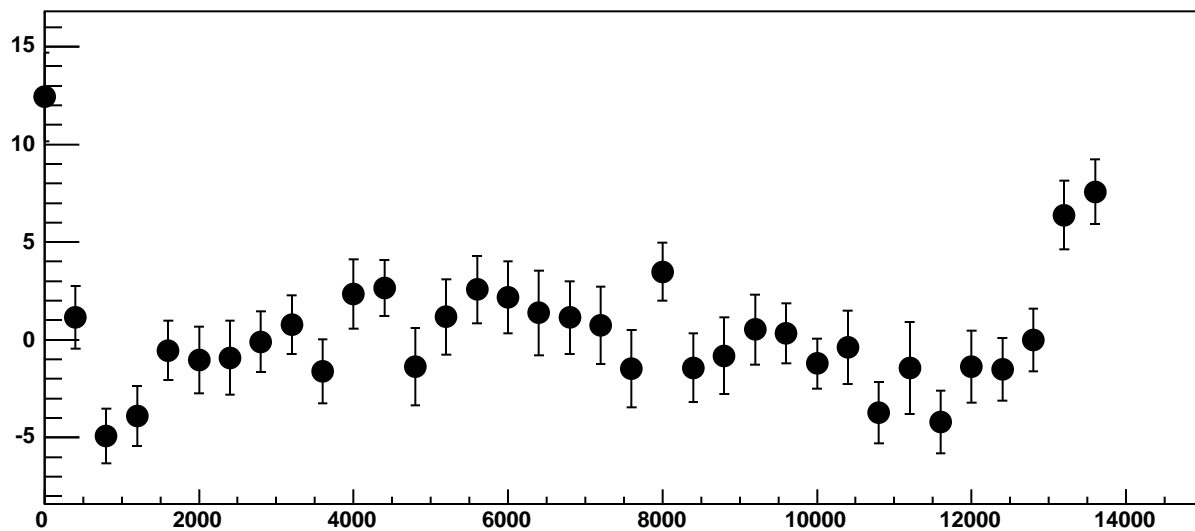


χ^2 / ndf 32.62 / 23
p0 -360.1 ± 0.7471
p1 0.03453 ± 0.0001115

Chip 5, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC

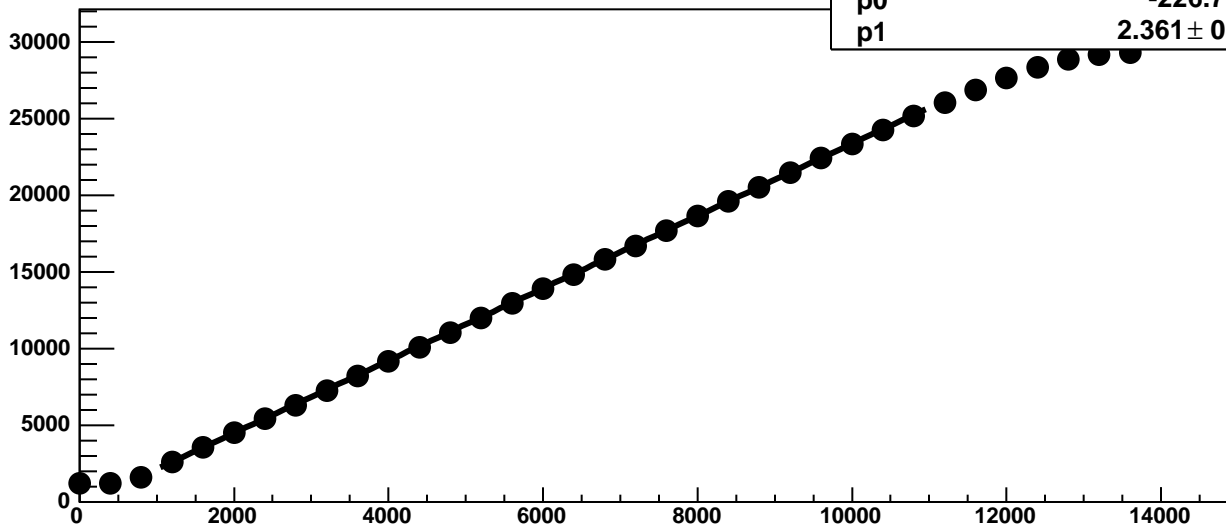


Chip 5, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

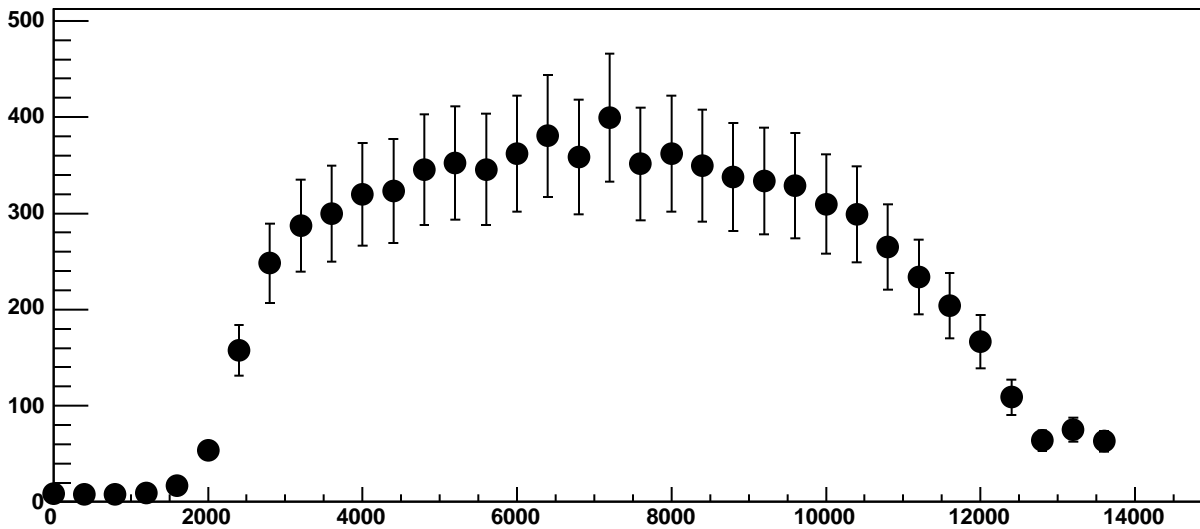


Chip 5, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC

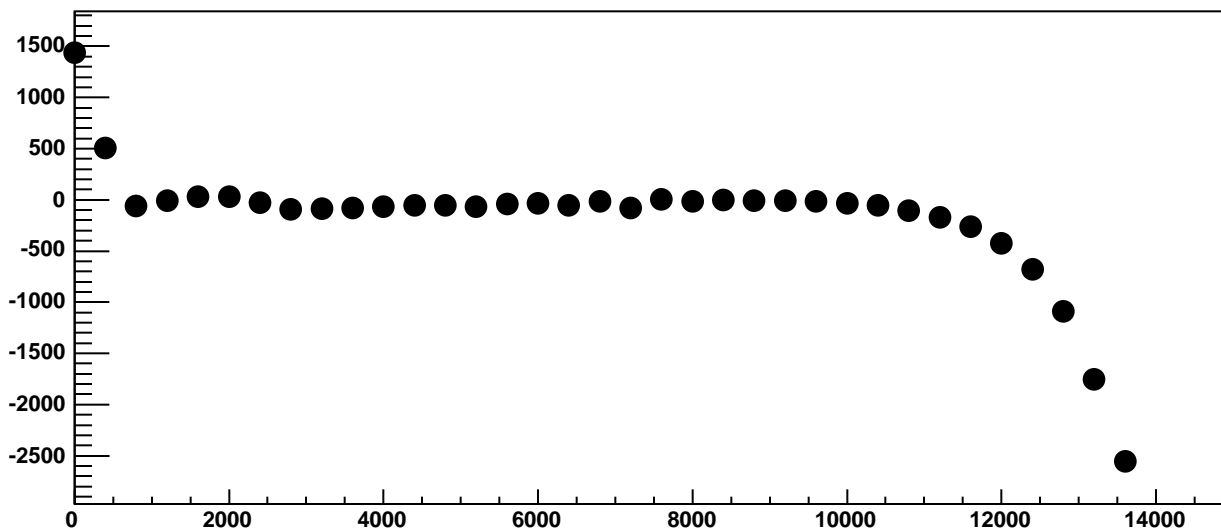
χ^2 / ndf 96.98 / 23
p0 -226.7 ± 4.008
p1 2.361 ± 0.002595



Chip 5, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC

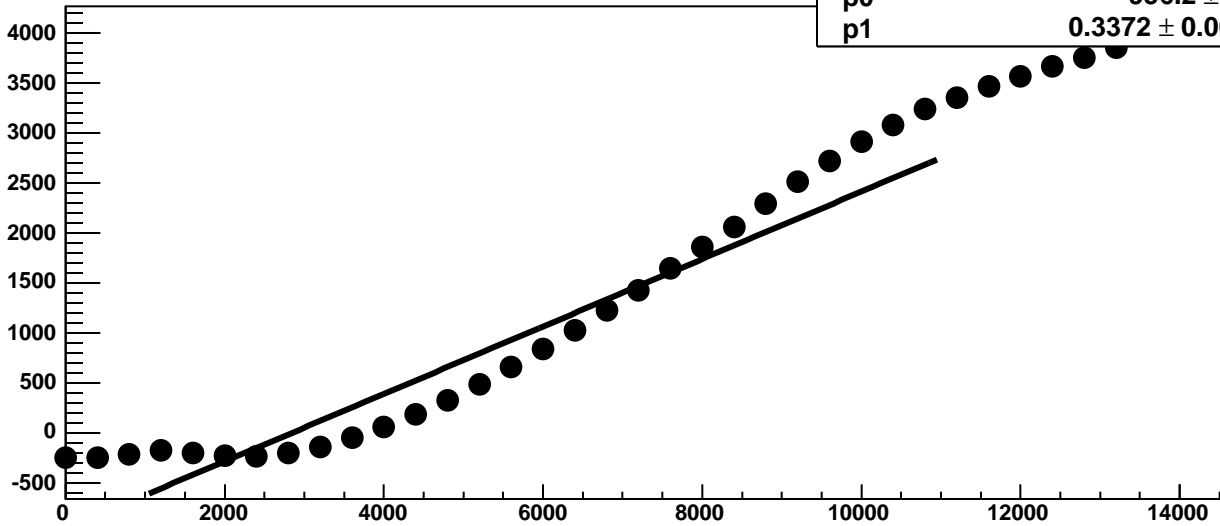


Chip 5, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC

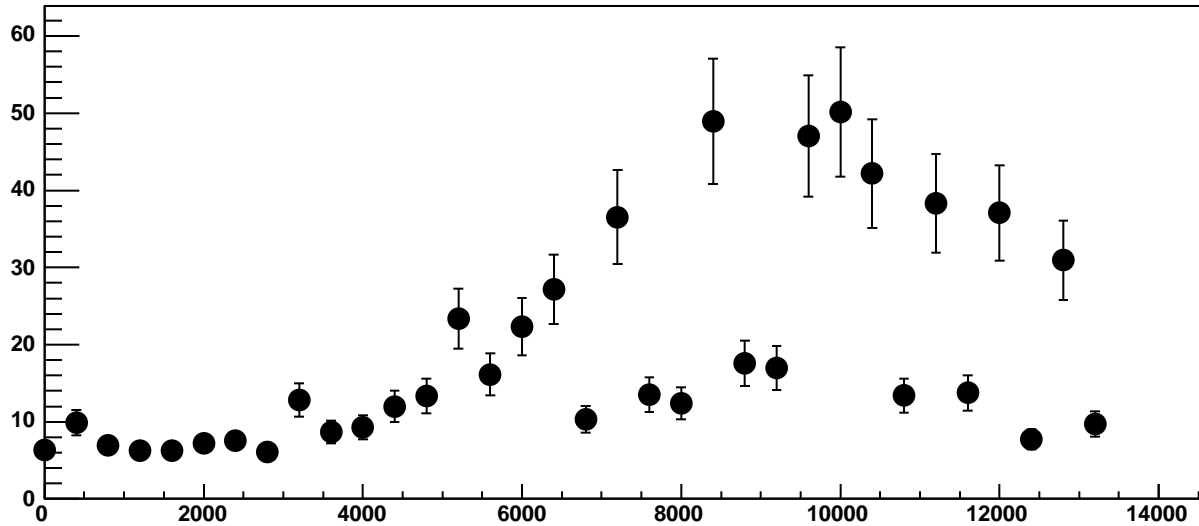


Chip 5, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

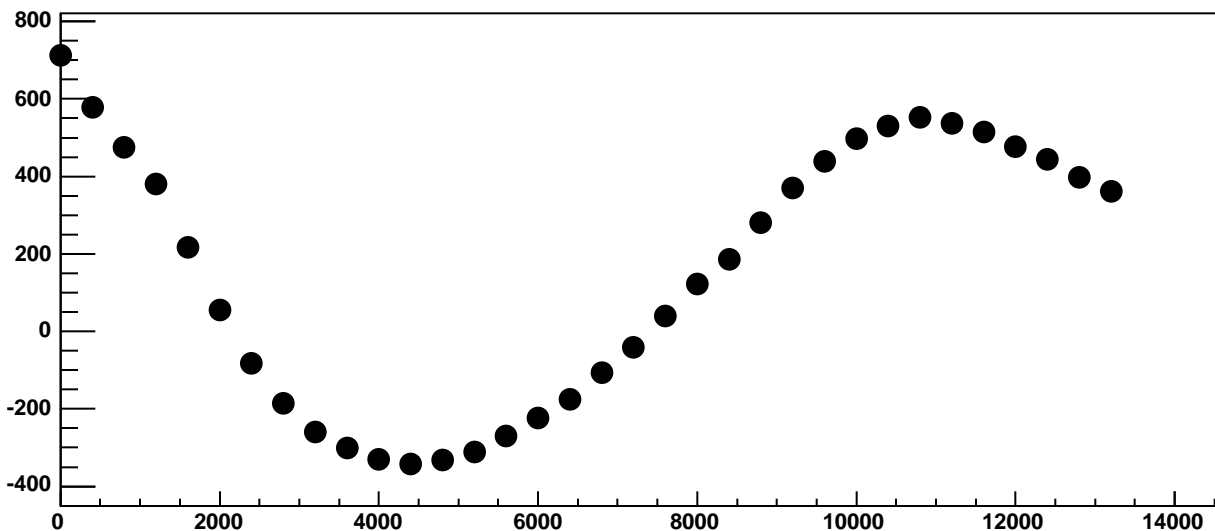
χ^2 / ndf 2.639e+05 / 23
p0 -956.2 ± 0.9077
p1 0.3372 ± 0.0002036



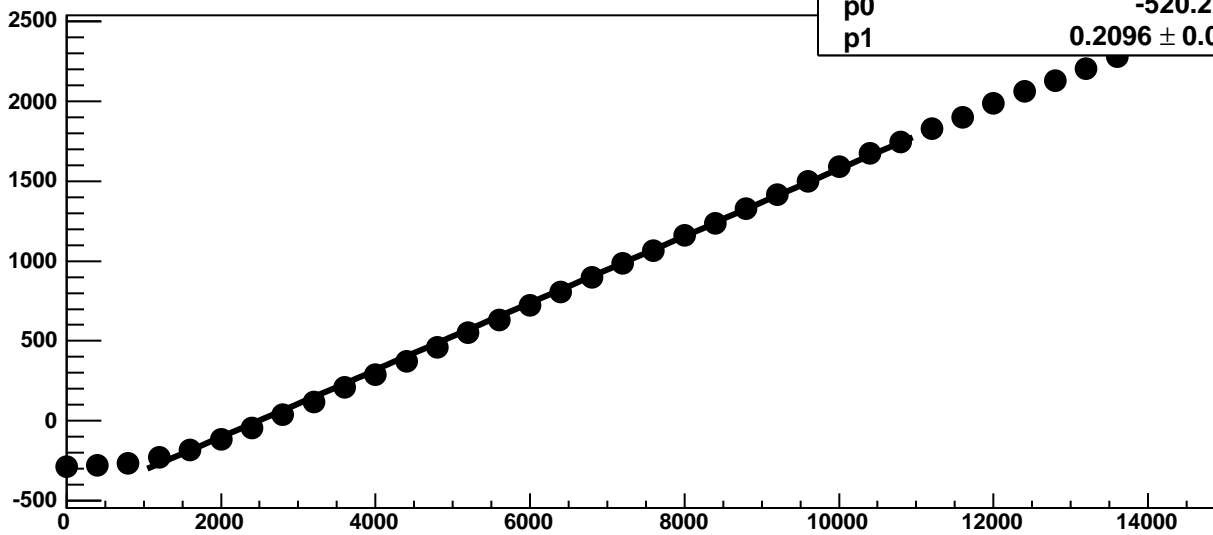
Chip 5, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



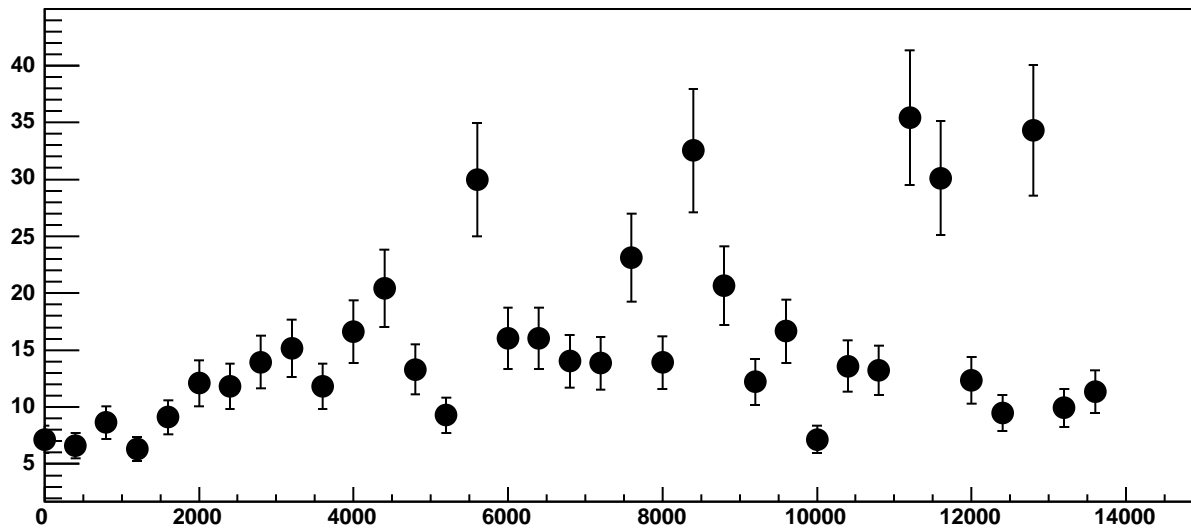
Chip 5, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



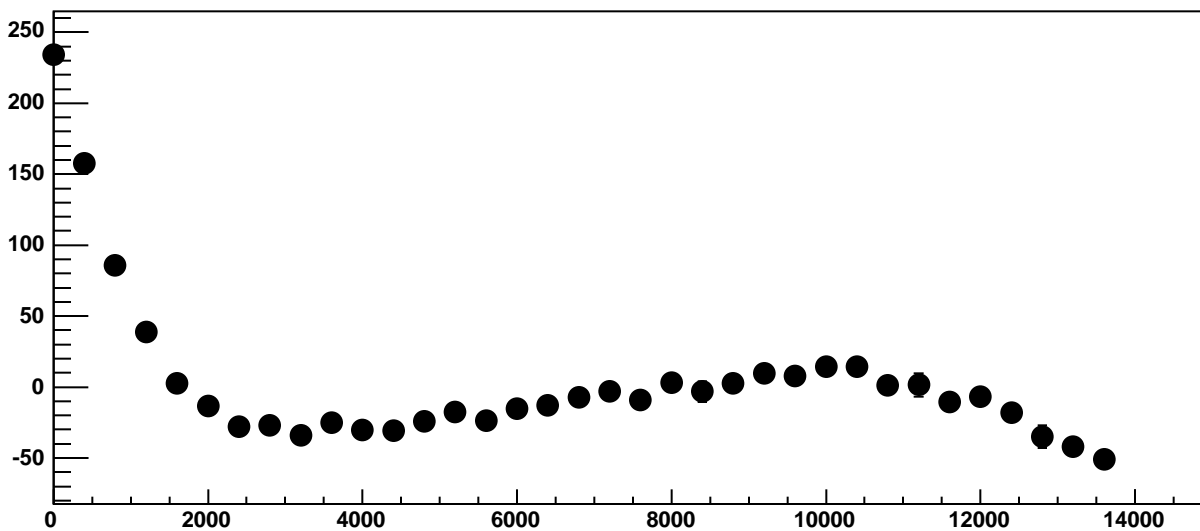
Chip 5, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



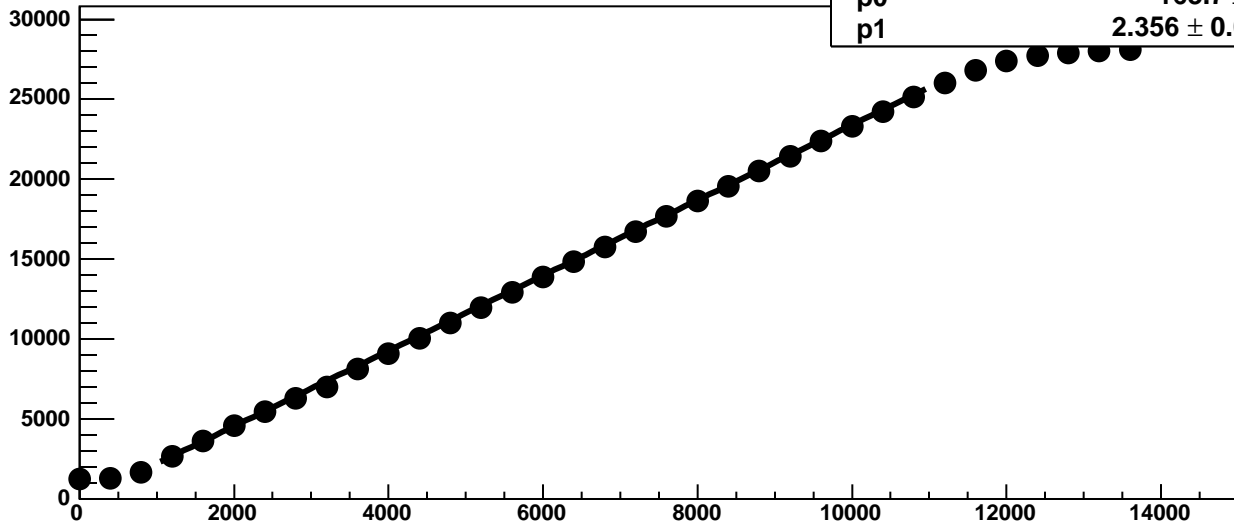
Chip 5, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



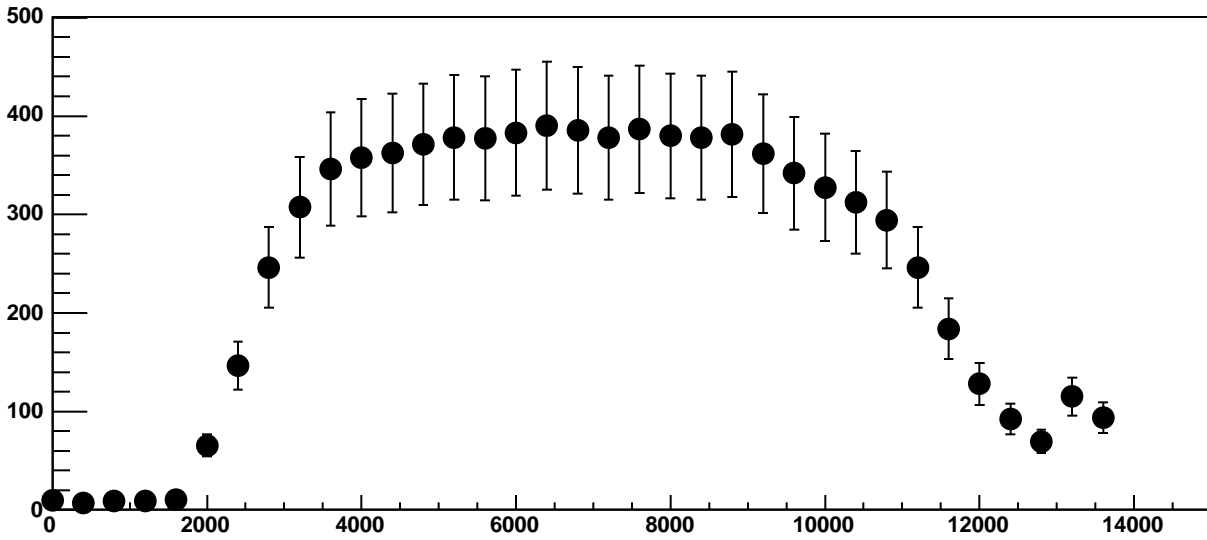
Chip 5, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



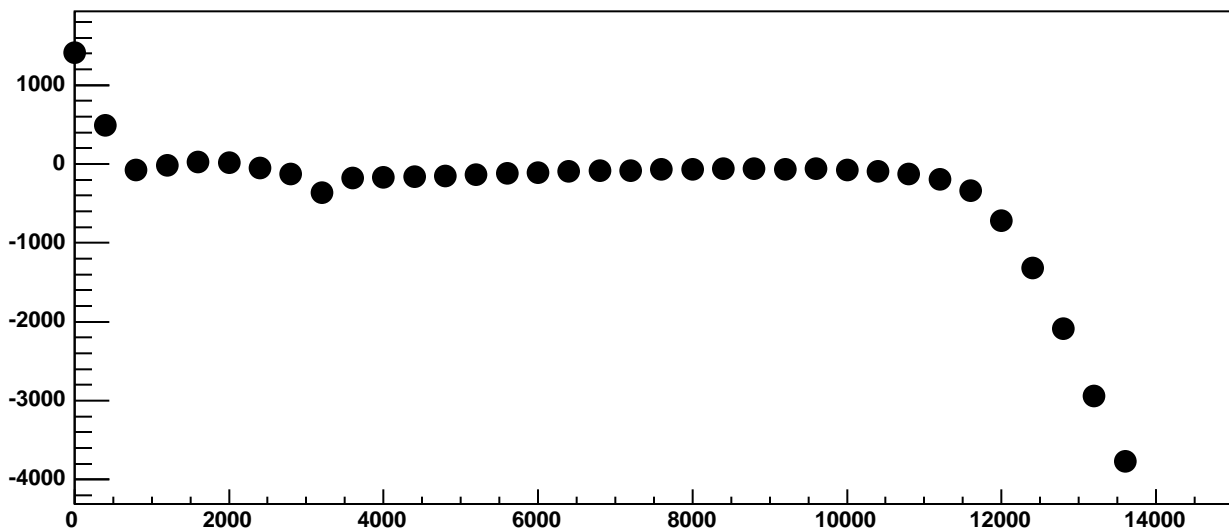
Chip 5, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC



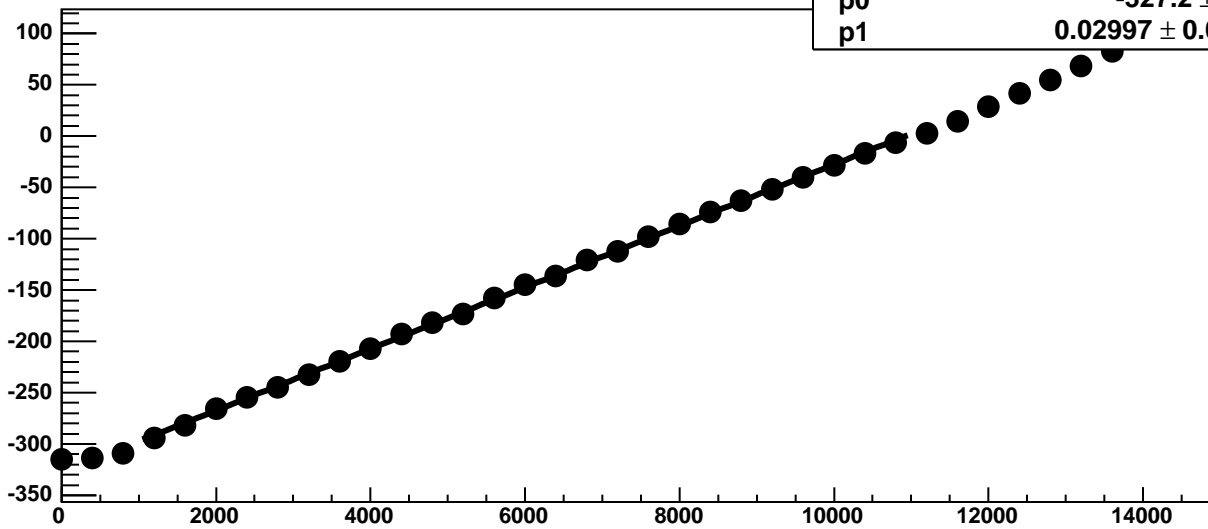
Chip 5, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

19.86 / 23

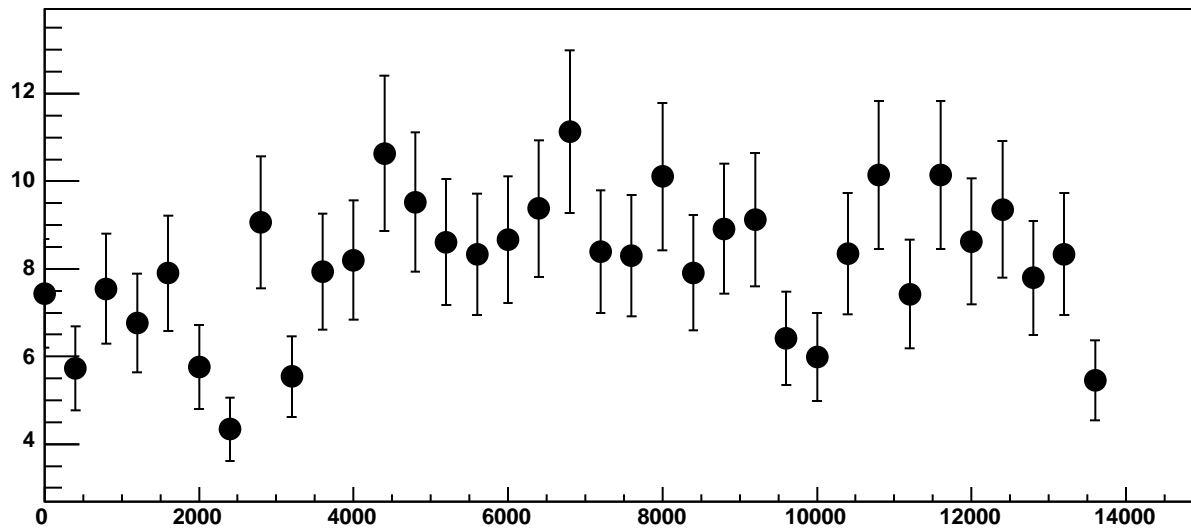
p0

-327.2 ± 0.7086

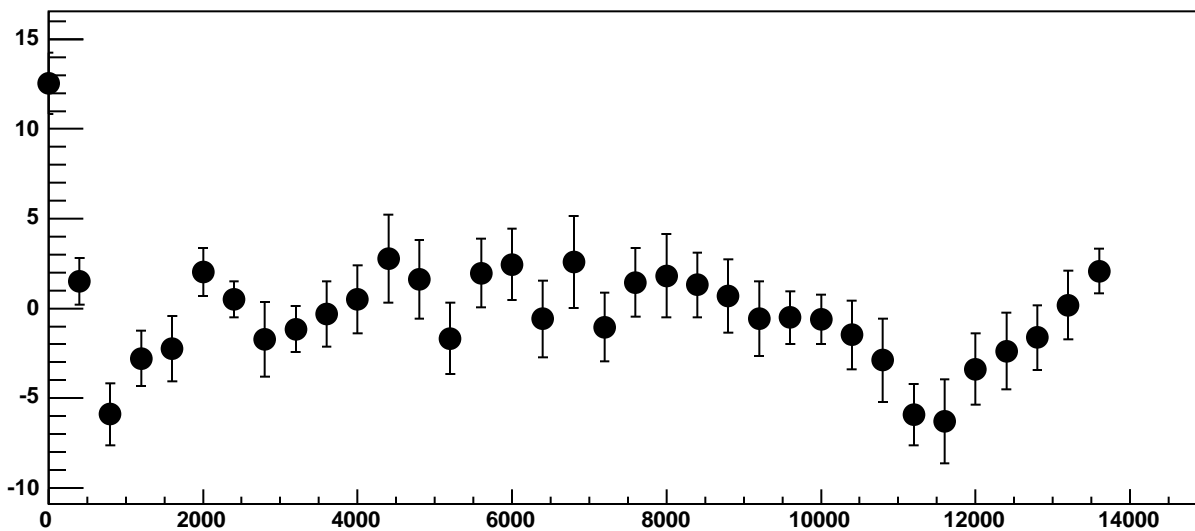
p1

0.02997 ± 0.0001131

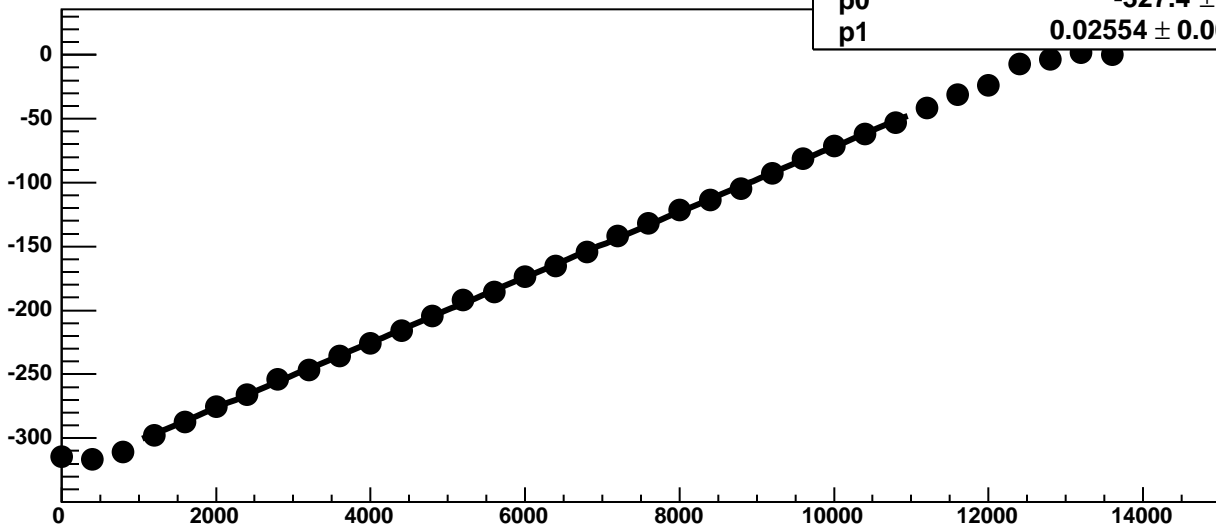
Chip 5, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



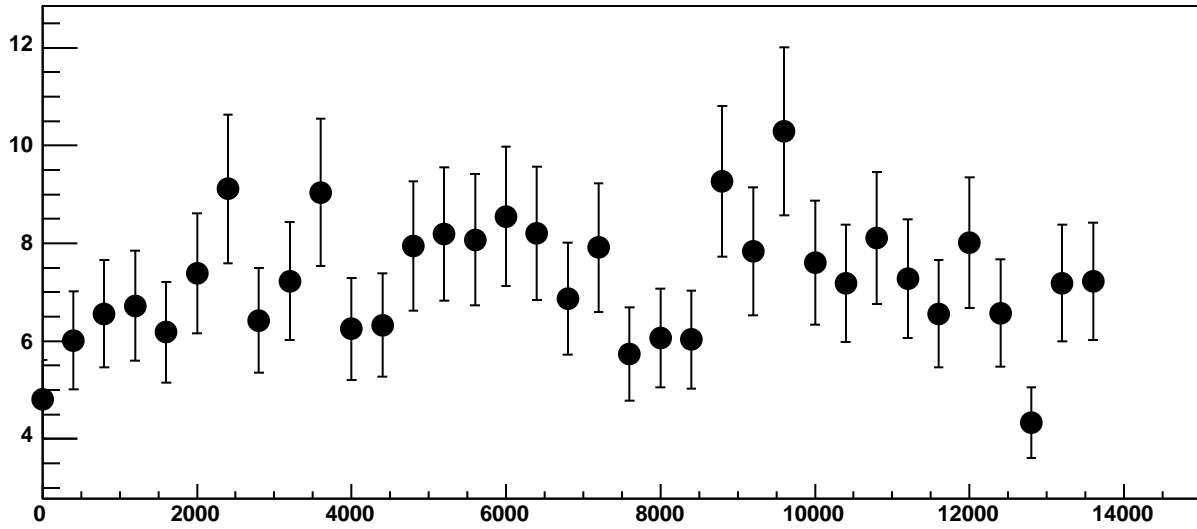
Chip 5, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



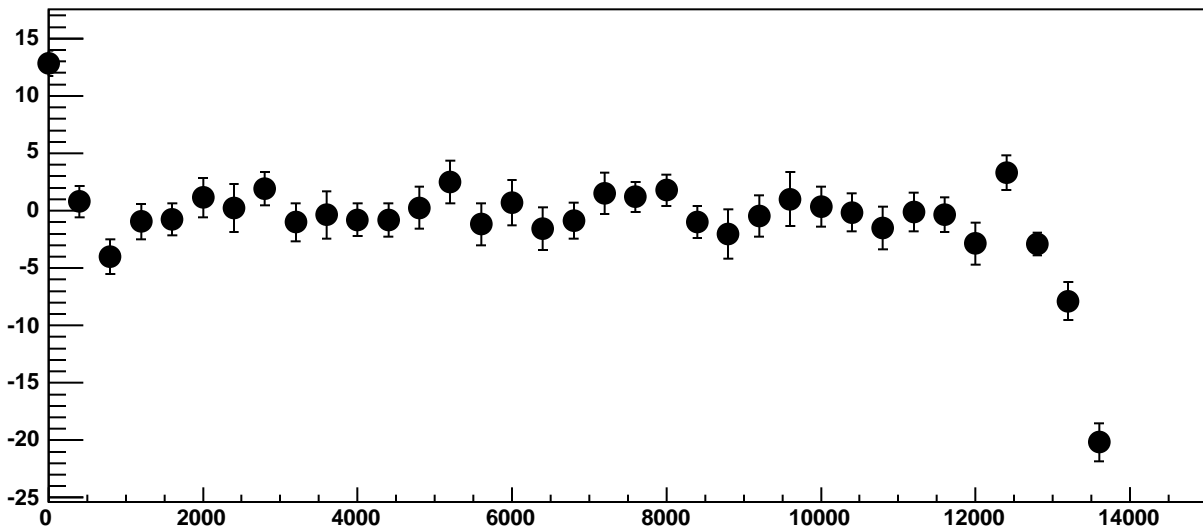
Chip 5, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



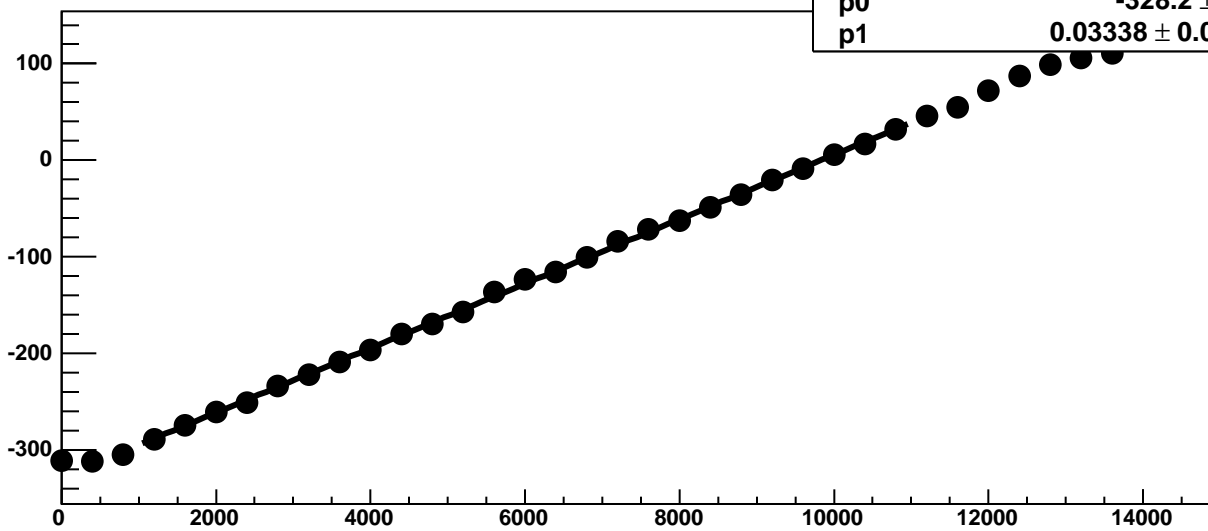
Chip 5, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



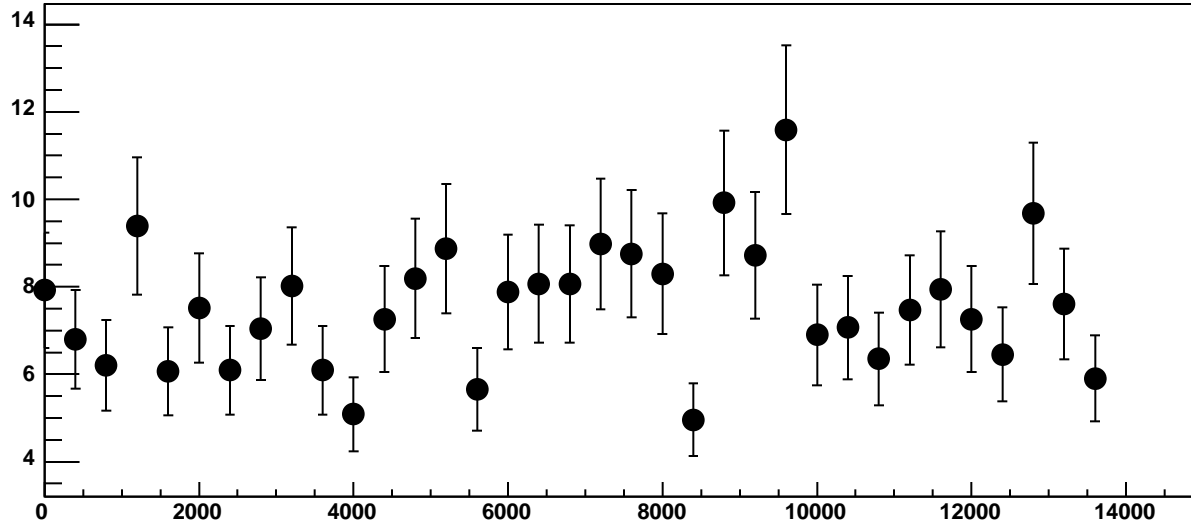
Chip 5, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



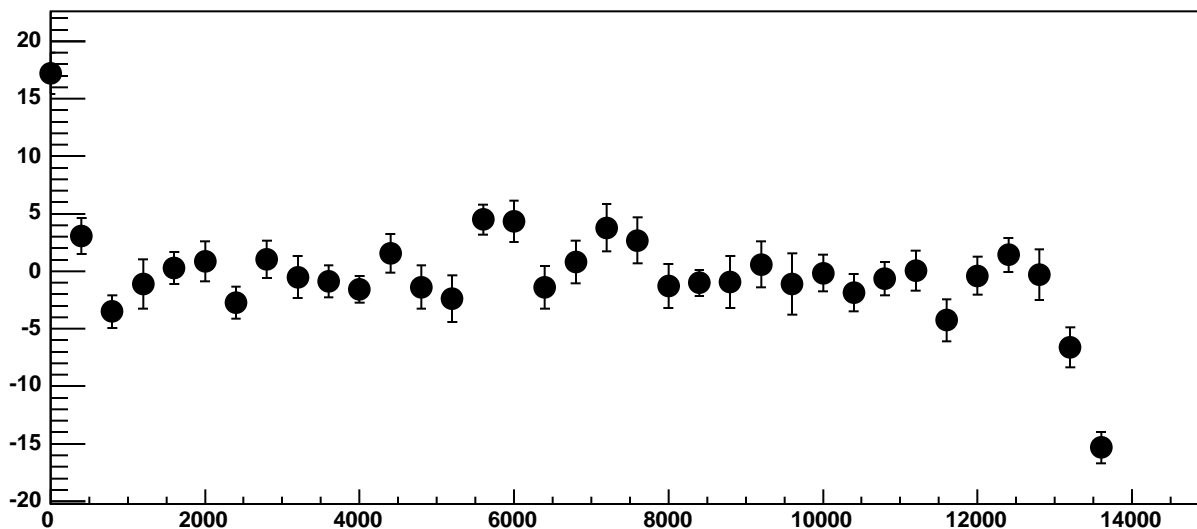
Chip 5, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC



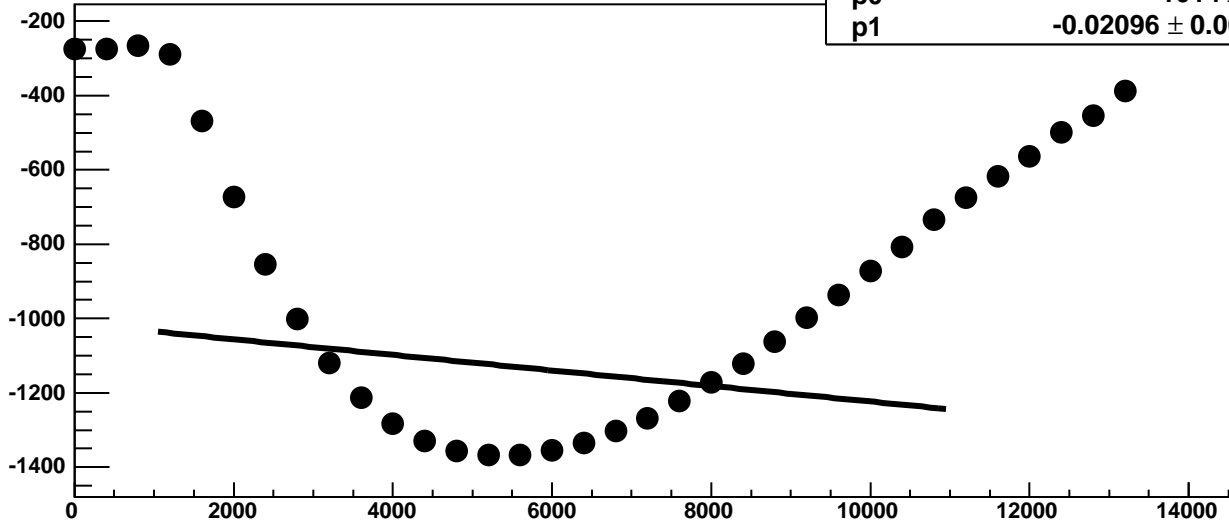
Chip 5, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



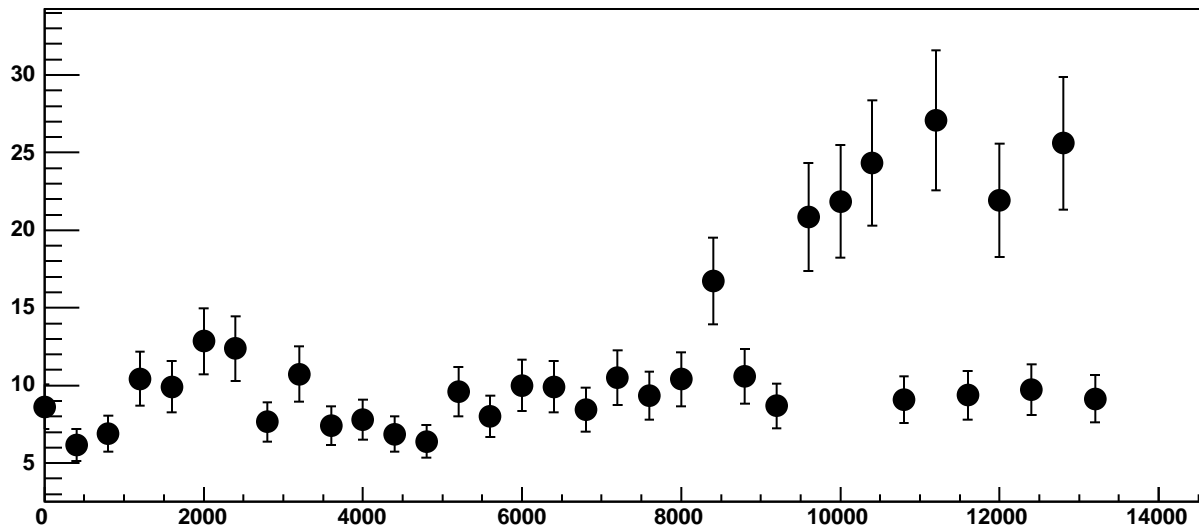
Chip 5, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC



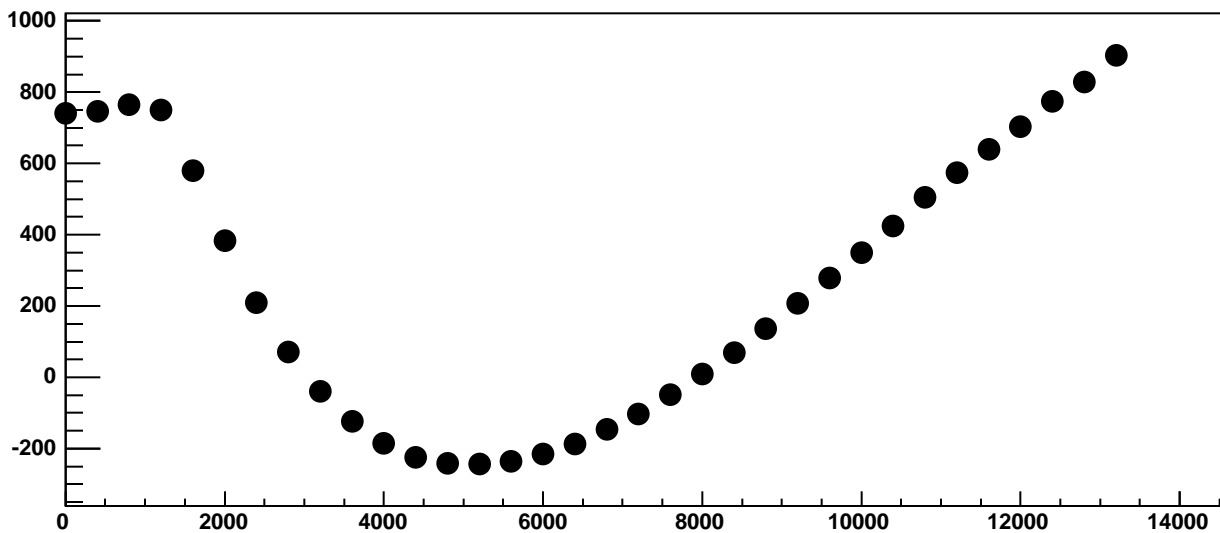
Chip 5, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



Chip 5, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC

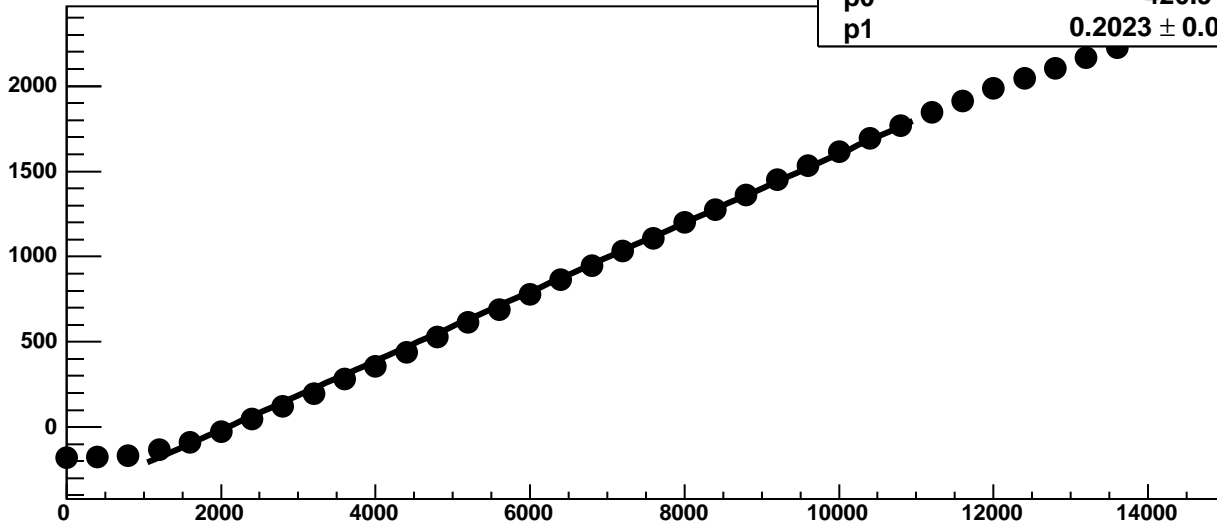


Chip 5, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

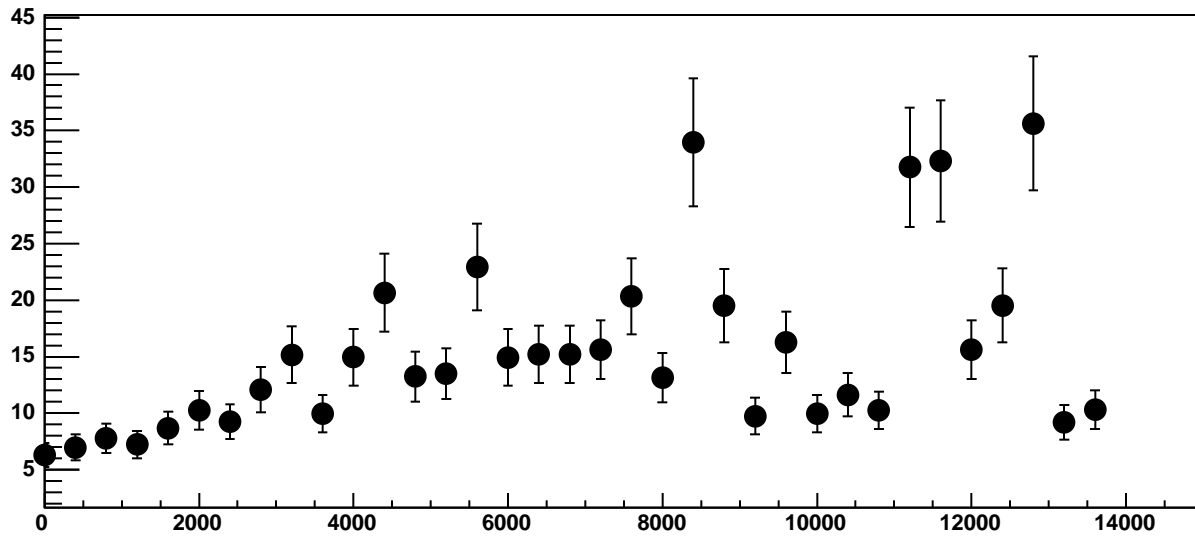


Chip 5, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

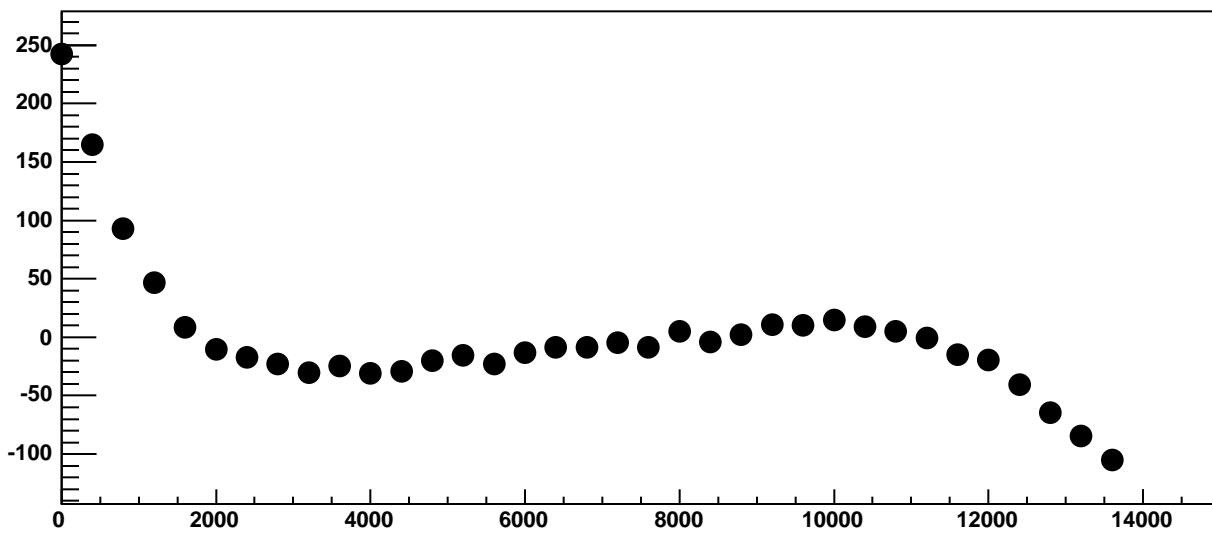
χ^2 / ndf 1492 / 23
p0 -420.9 ± 1.056
p1 0.2023 ± 0.0001673



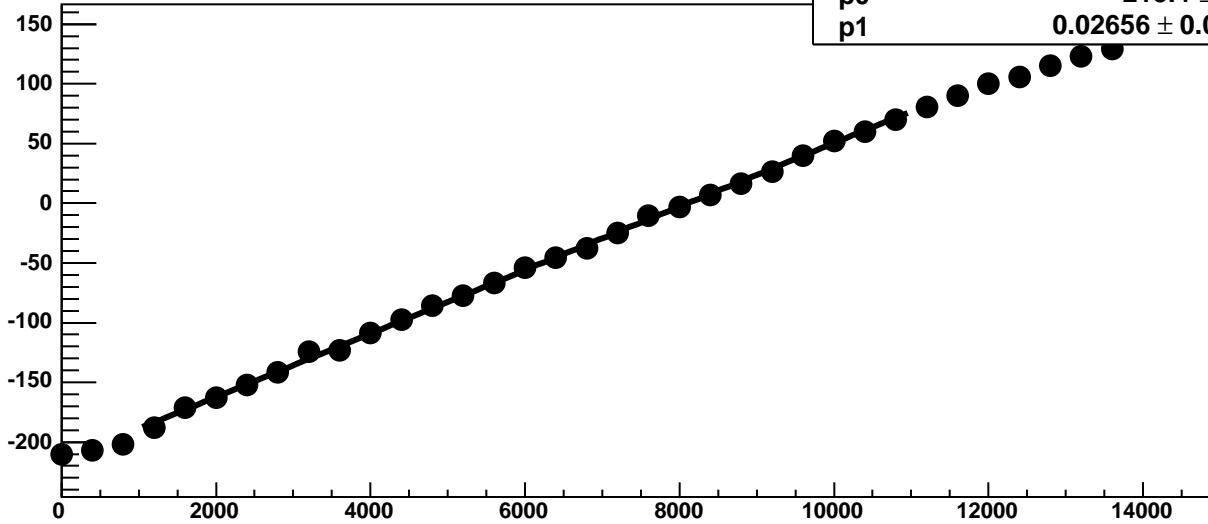
Chip 5, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

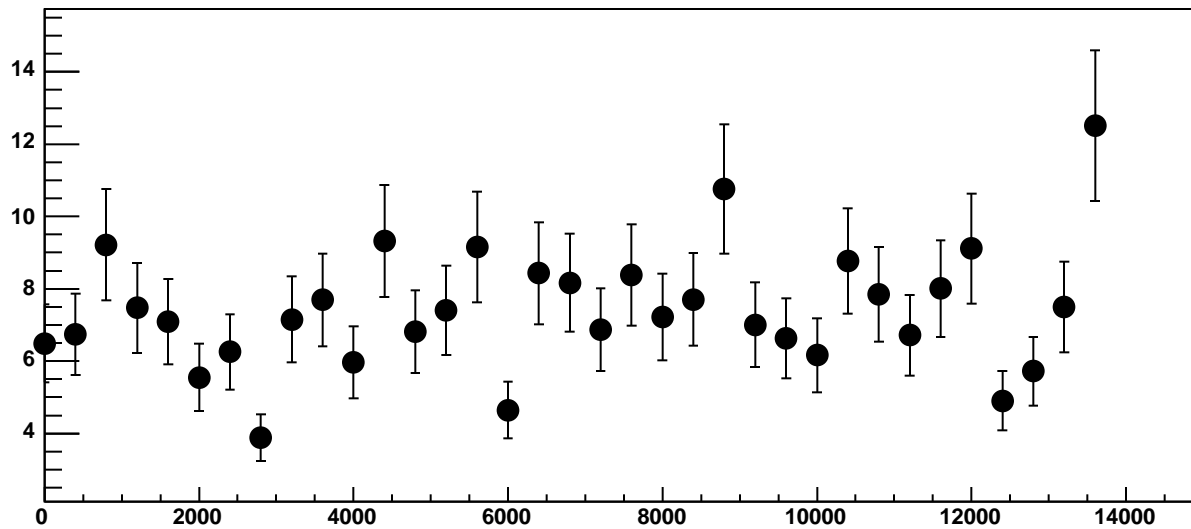


Chip 5, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

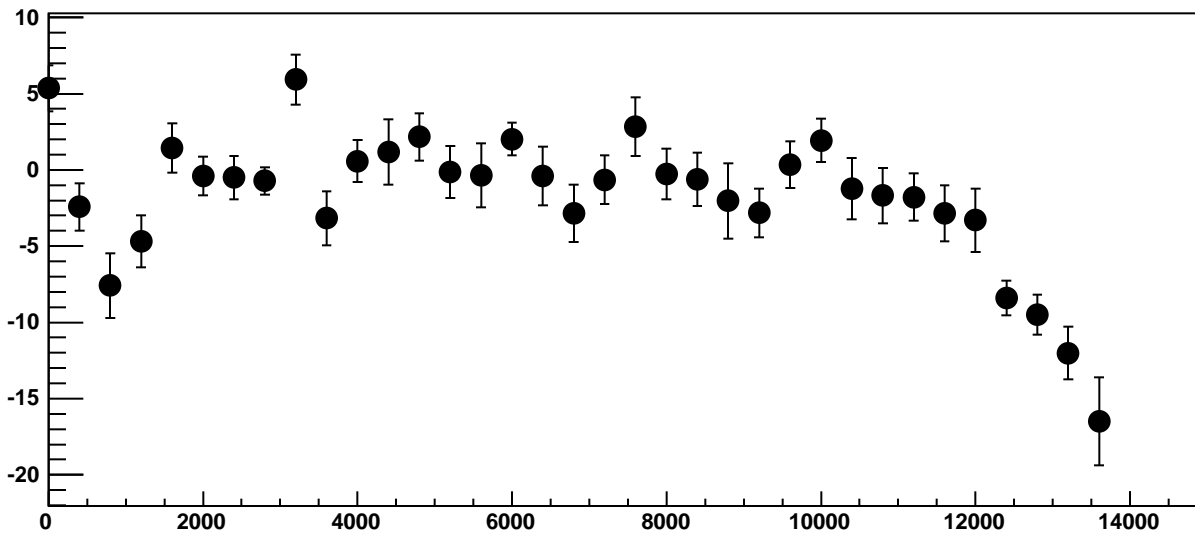


χ^2 / ndf 43.18 / 23
p0 -215.4 ± 0.6722
p1 0.02656 ± 0.0001081

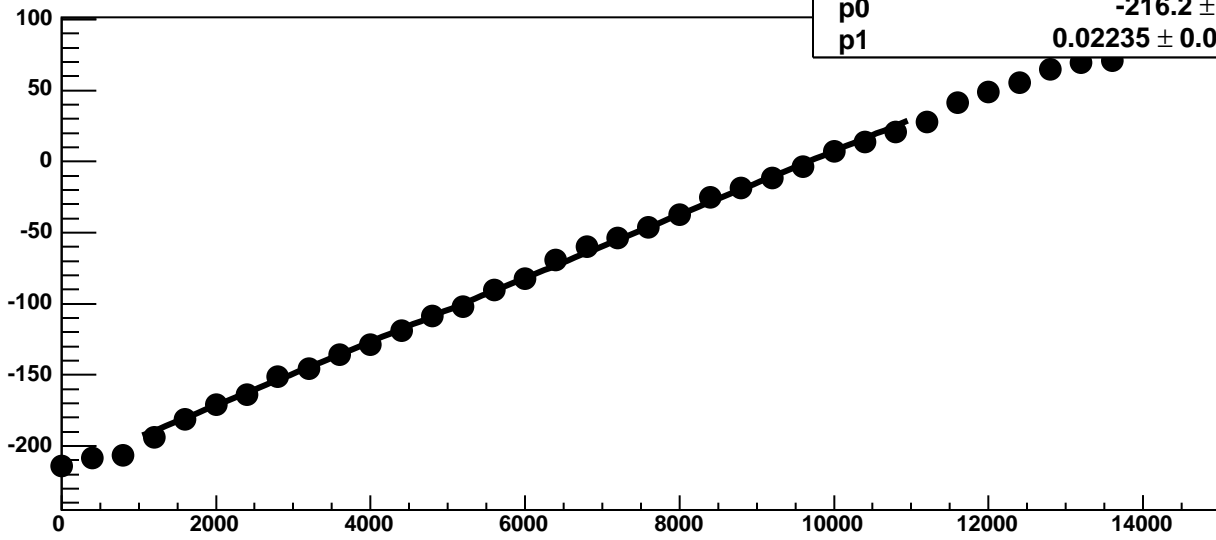
Chip 5, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

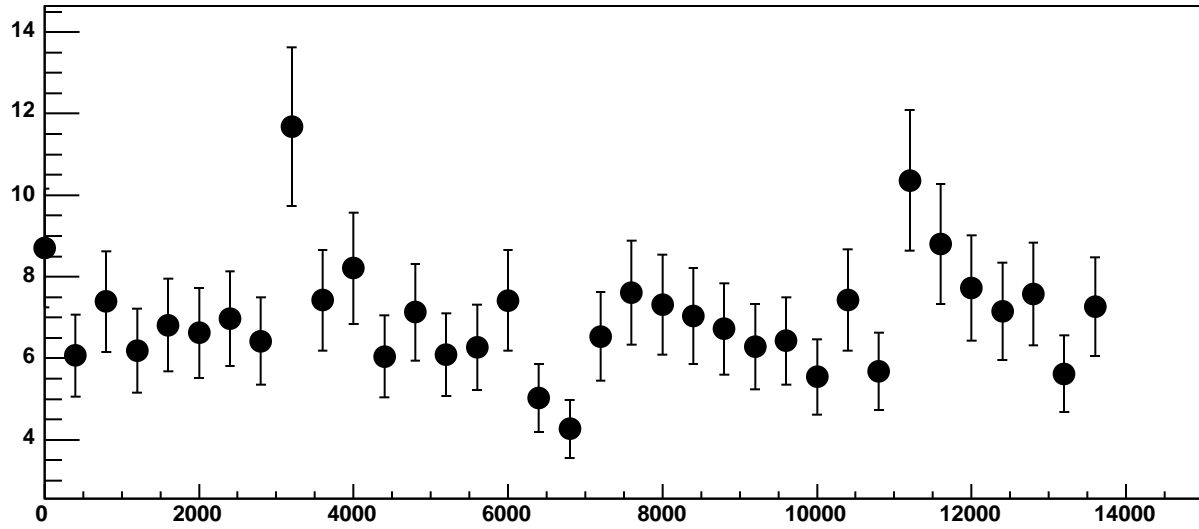


Chip 5, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

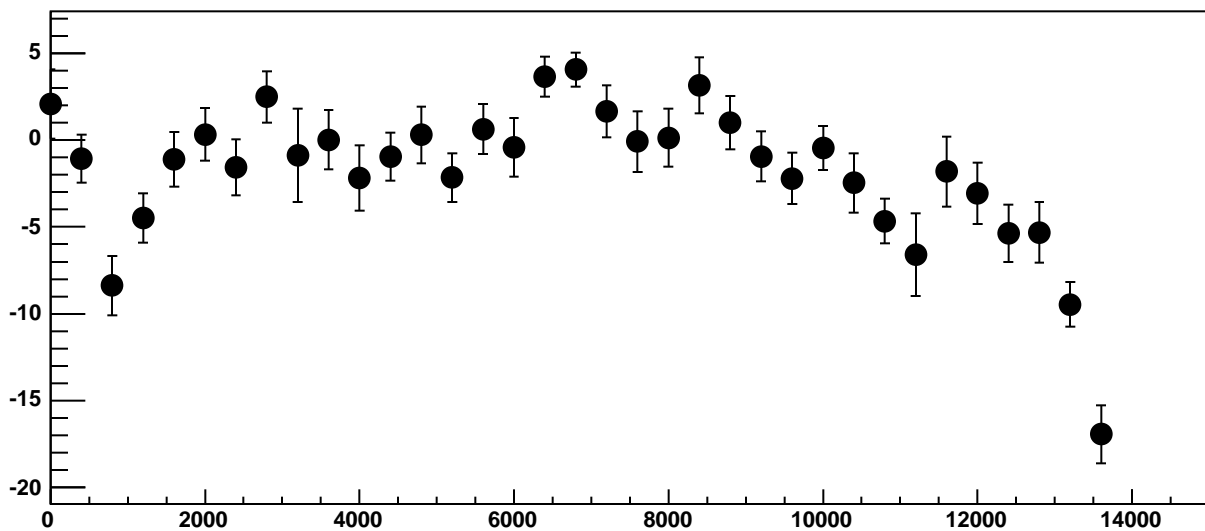


χ^2 / ndf 69.35 / 23
p0 -216.2 ± 0.7125
p1 0.02235 ± 0.0001041

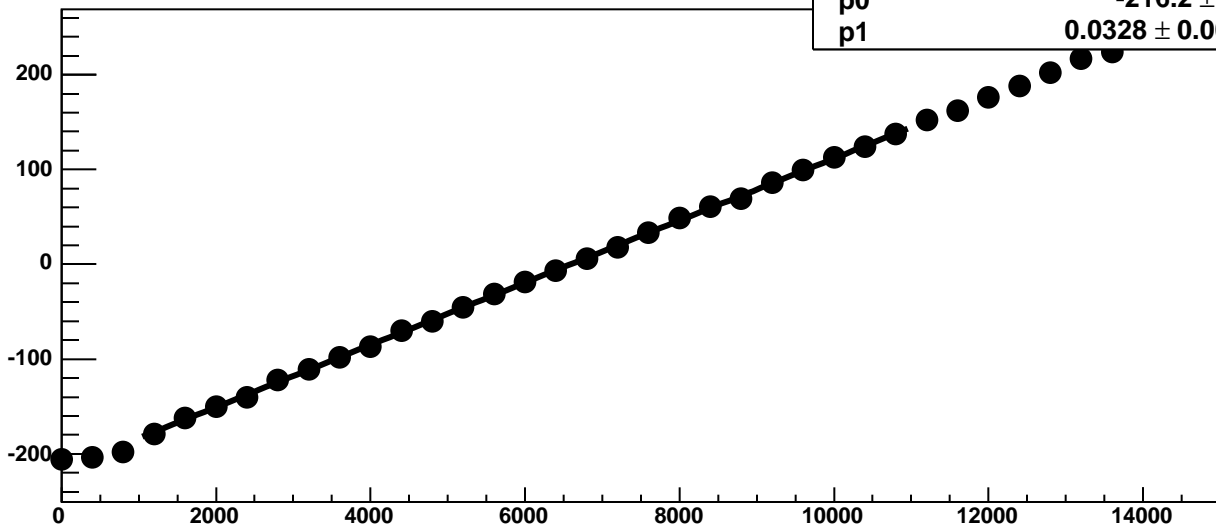
Chip 5, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

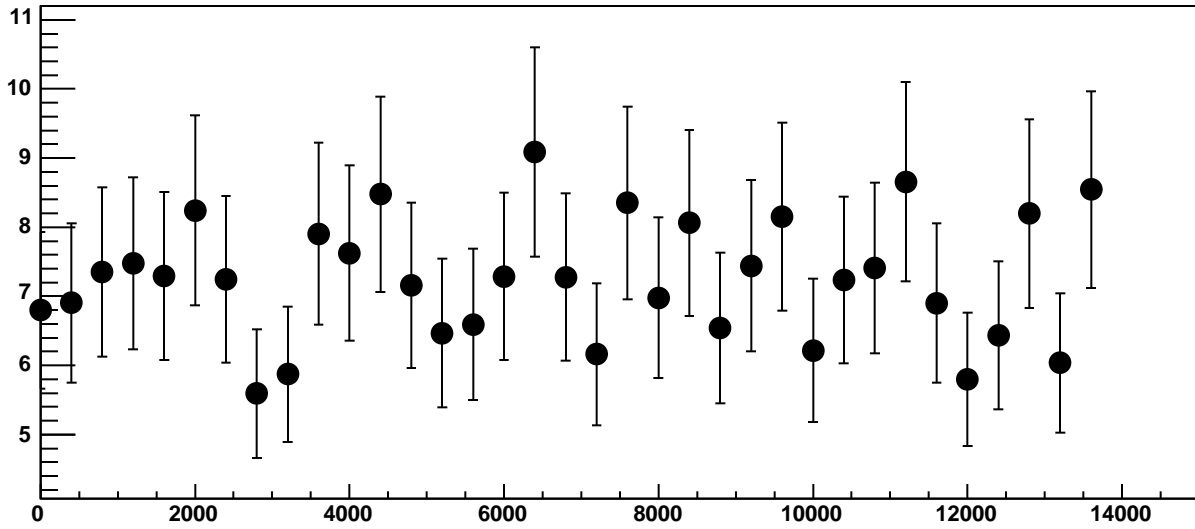


Chip 5, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

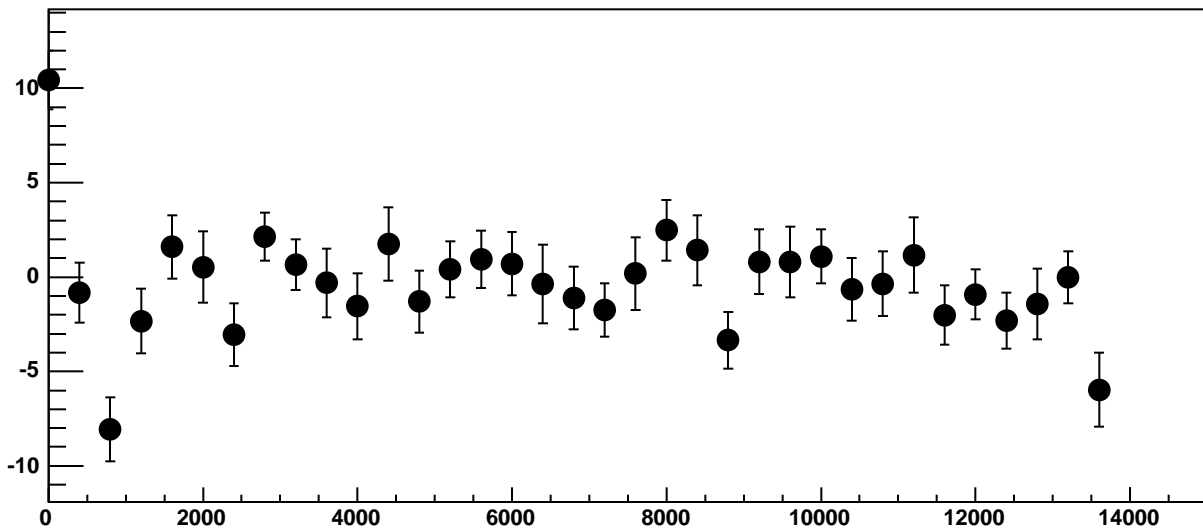


χ^2 / ndf 23.28 / 23
p0 -216.2 ± 0.7514
p1 0.0328 ± 0.0001135

Chip 5, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC

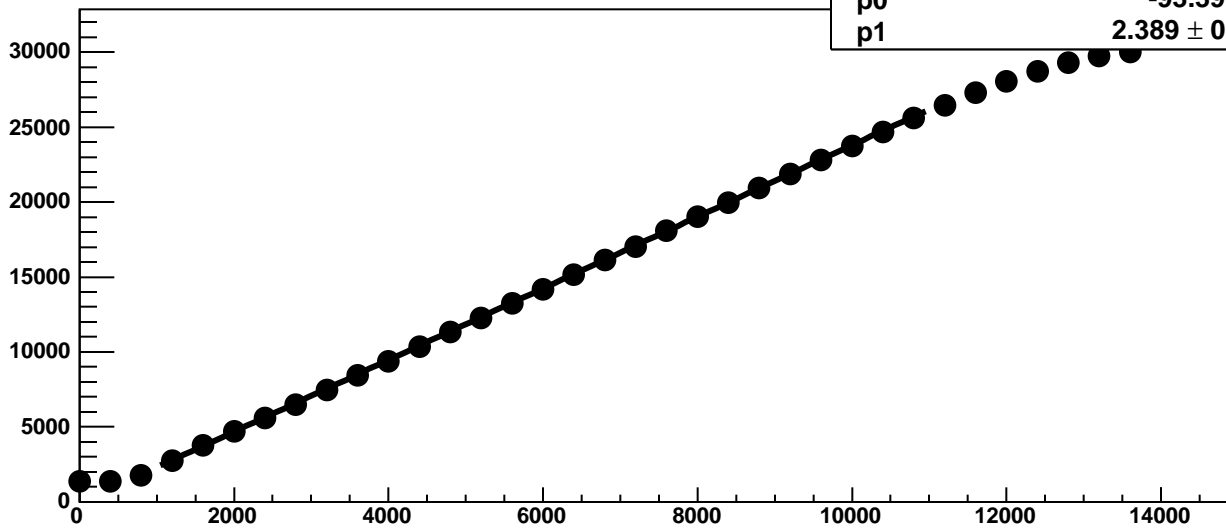


Chip 5, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

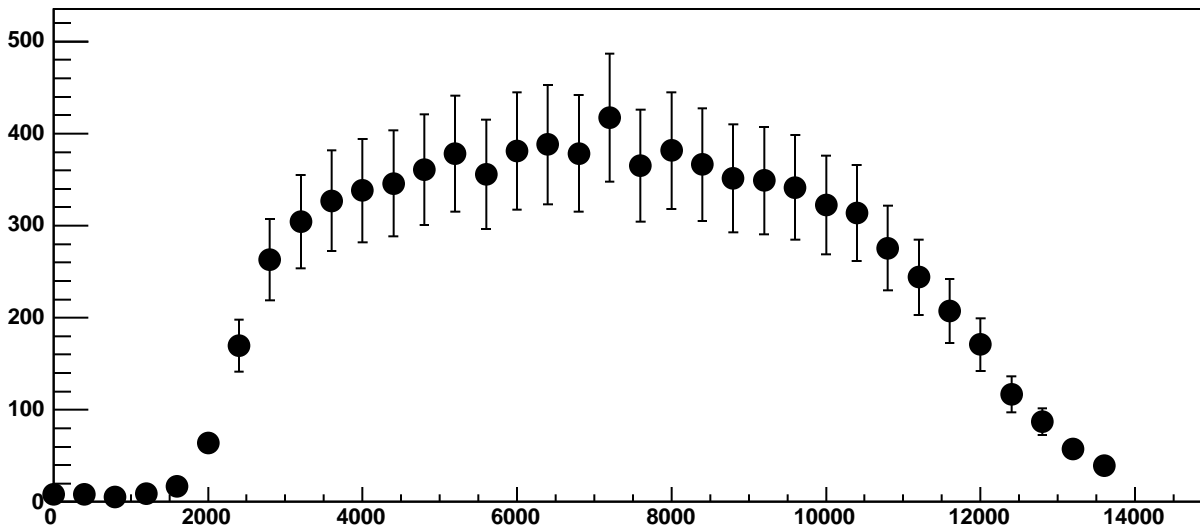


Chip 5, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC

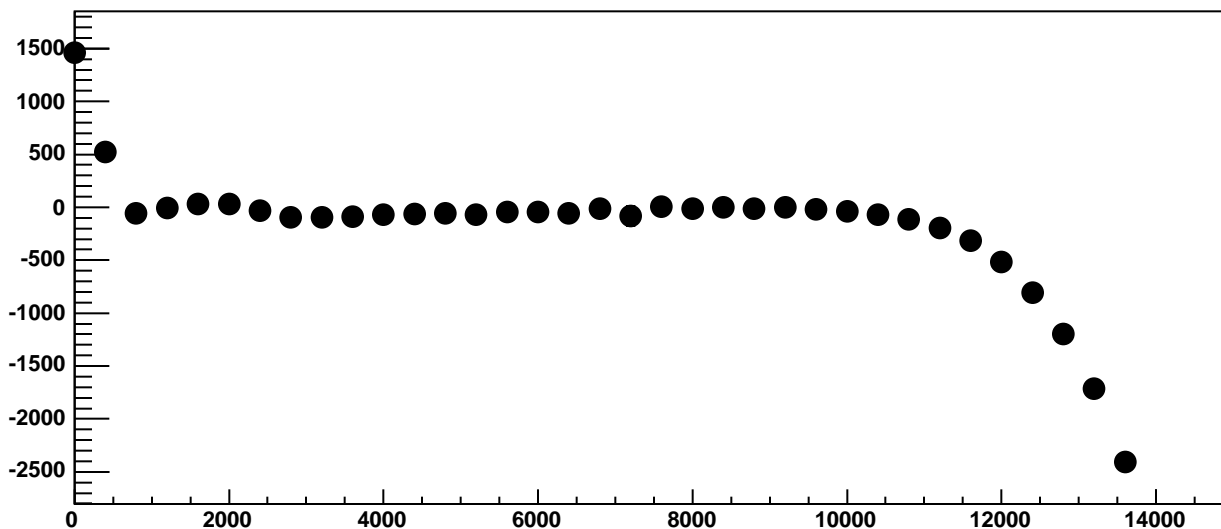
χ^2 / ndf 102.3 / 23
p0 -93.39 ± 4.107
p1 2.389 ± 0.002709



Chip 5, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC

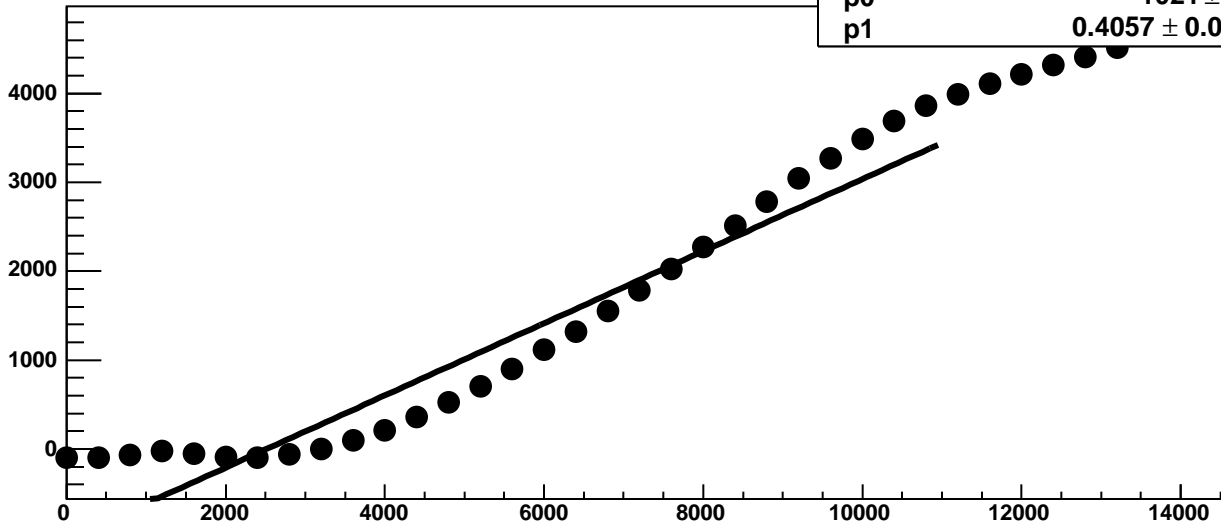


Chip 5, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC

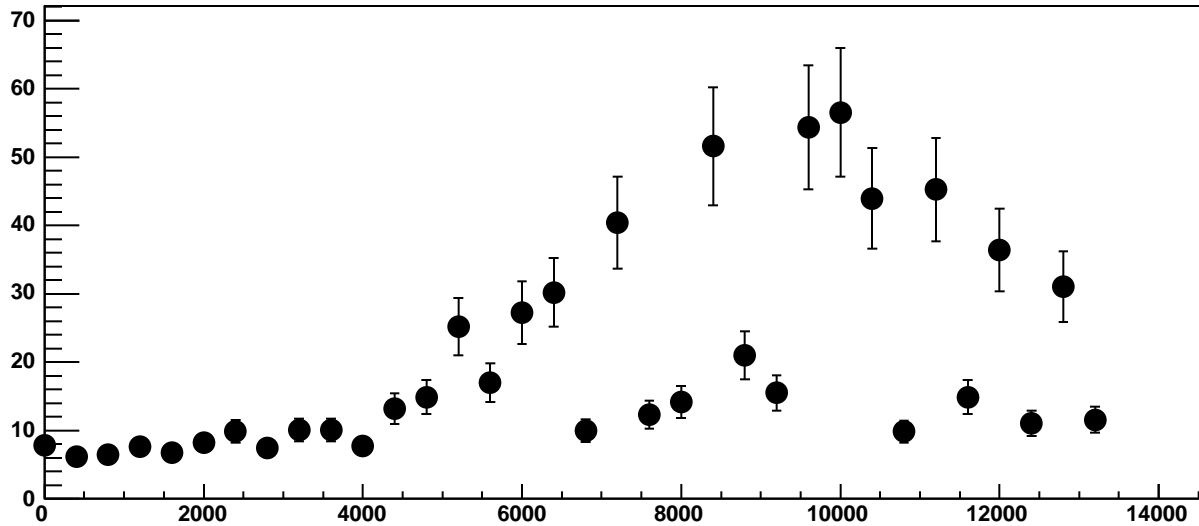


Chip 5, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

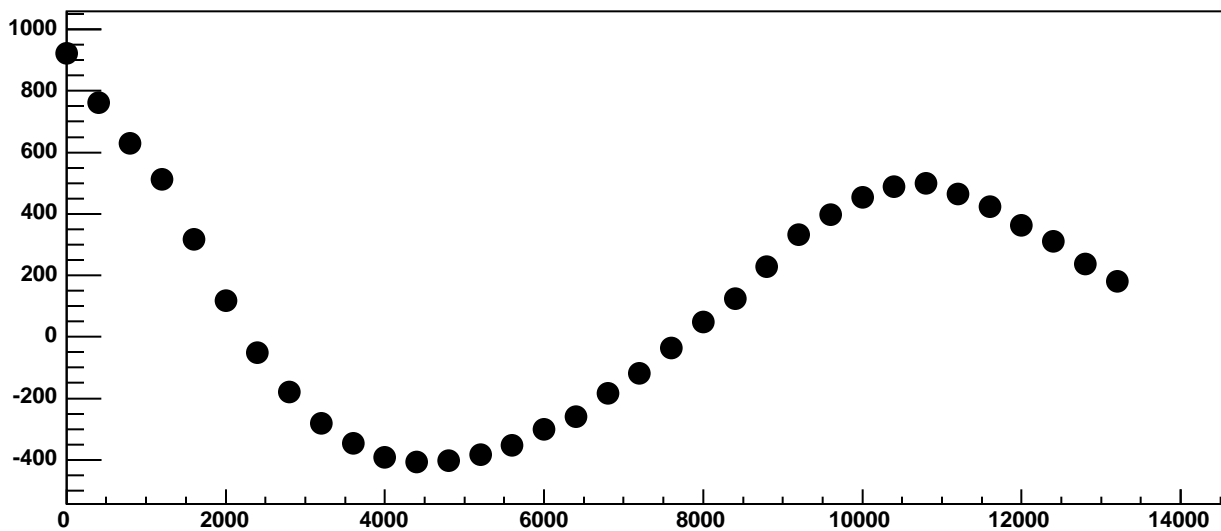
χ^2 / ndf 3.476e+05 / 23
p0 -1021 ± 0.9849
p1 0.4057 ± 0.0001981



Chip 5, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC

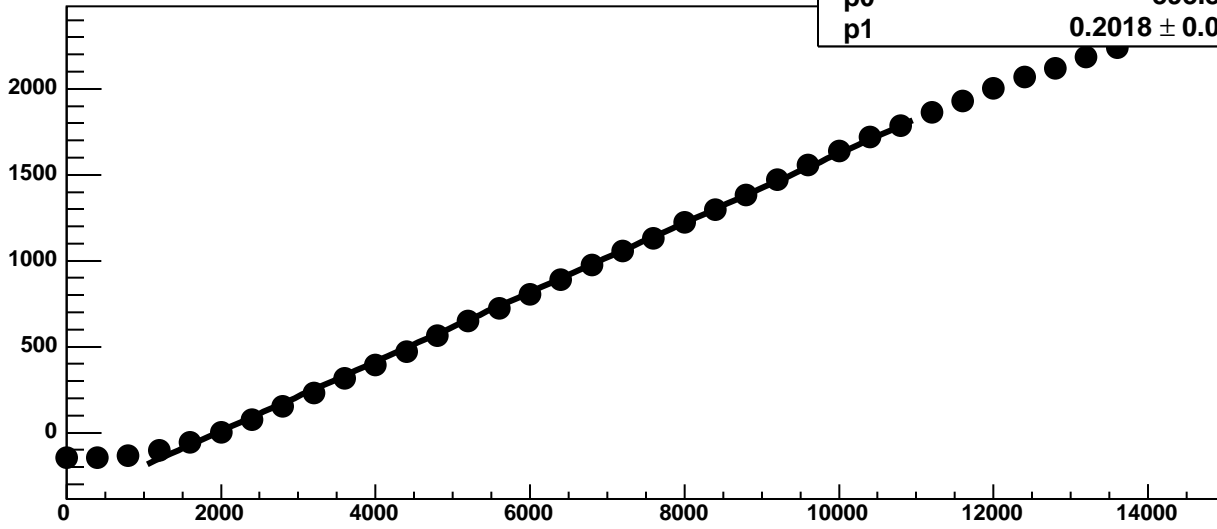


Chip 5, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

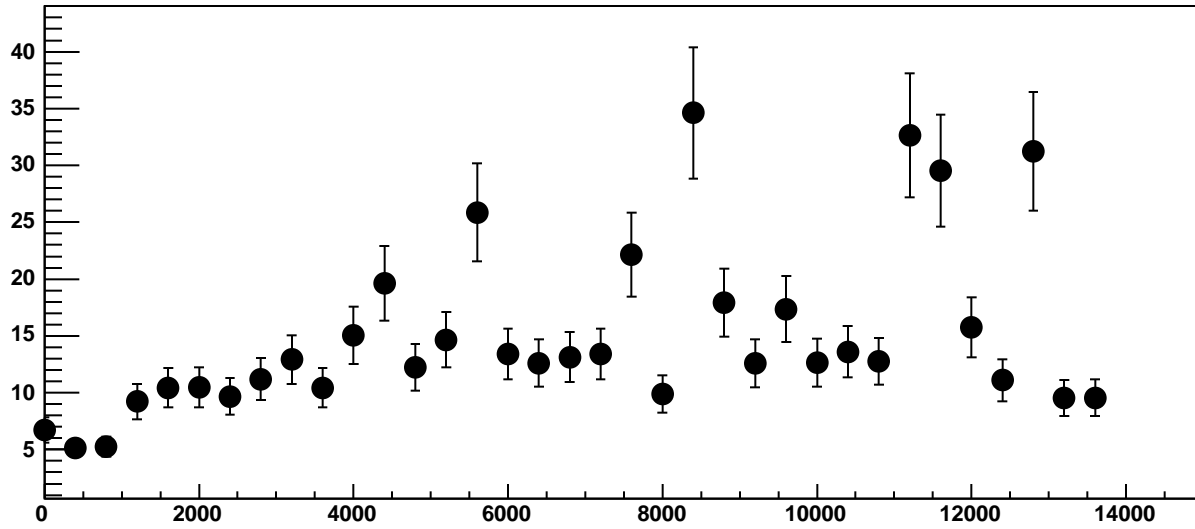


Chip 5, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

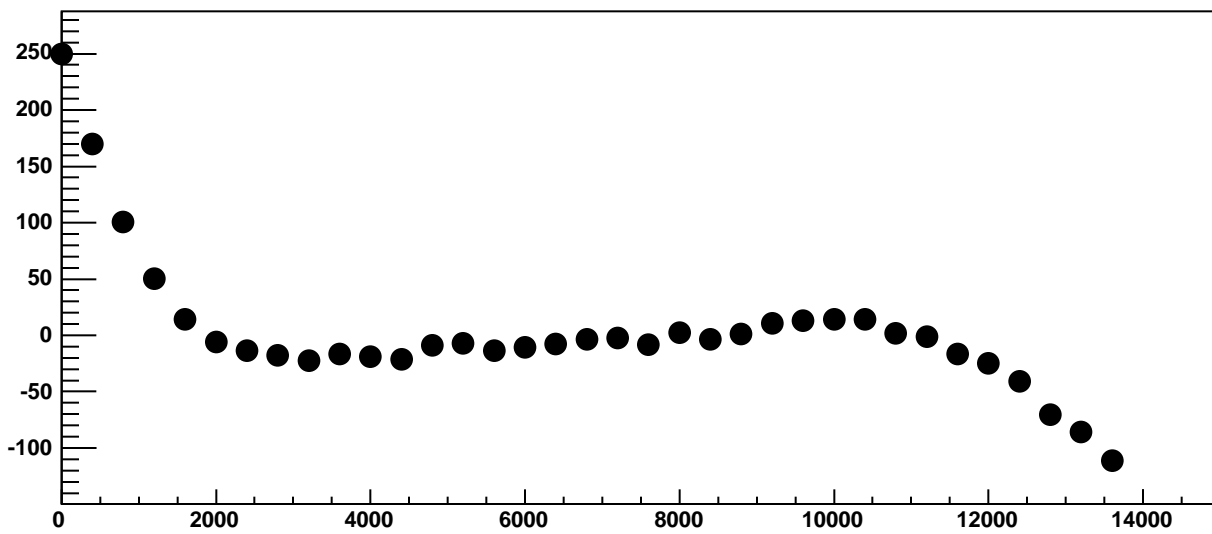
χ^2 / ndf 968.6 / 23
p0 -393.8 ± 1.18
p1 0.2018 ± 0.0001917



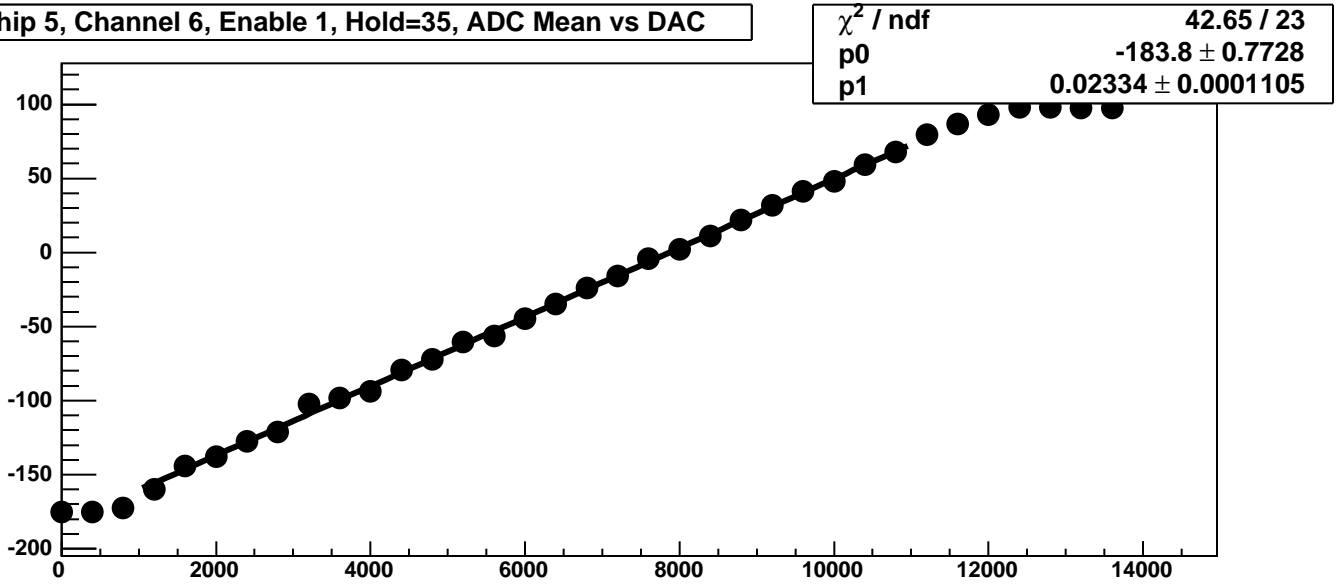
Chip 5, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



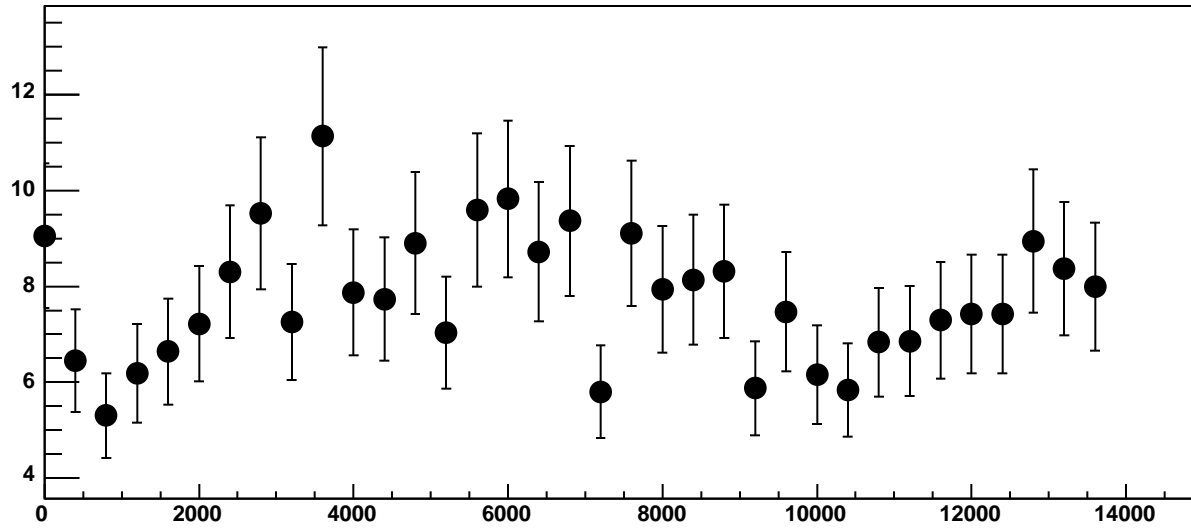
Chip 5, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



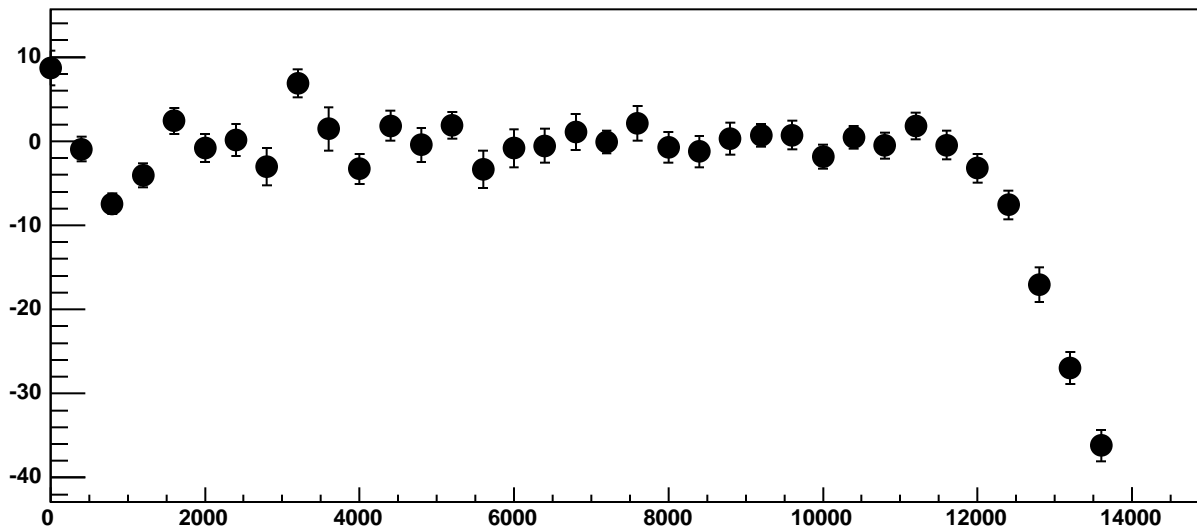
Chip 5, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



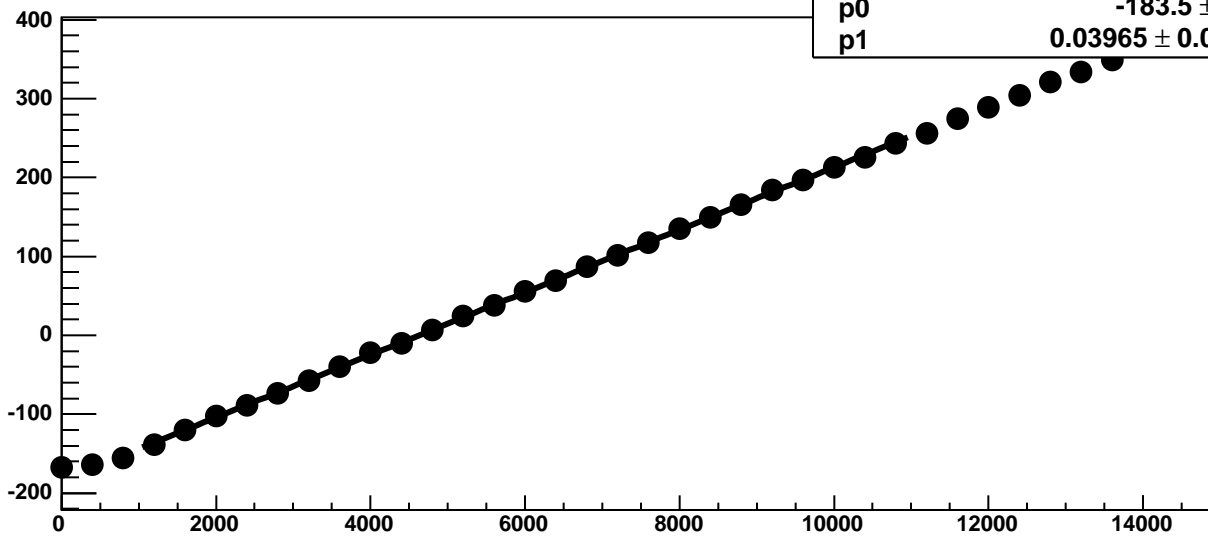
Chip 5, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

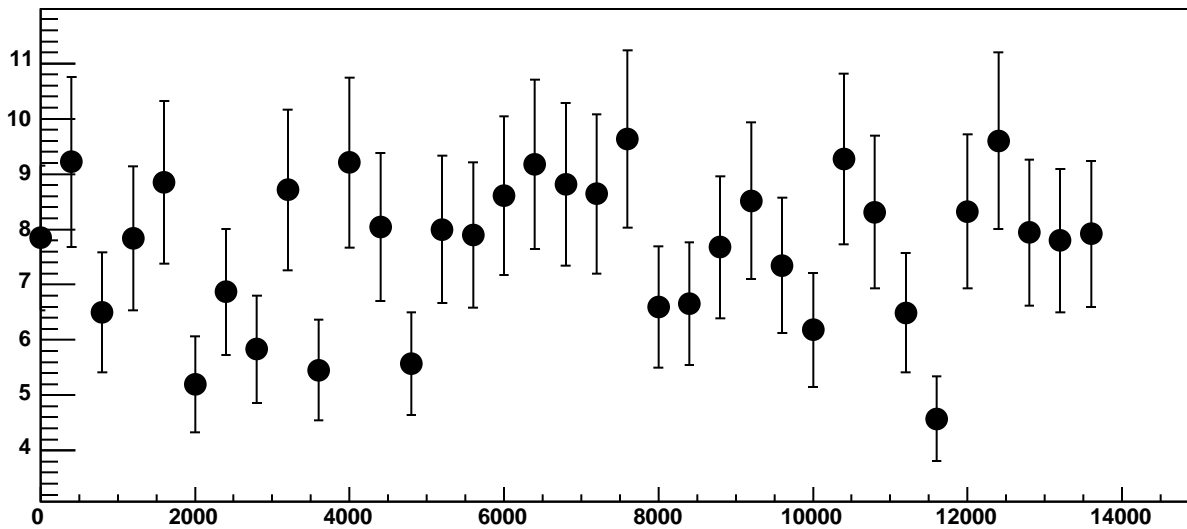


Chip 5, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC

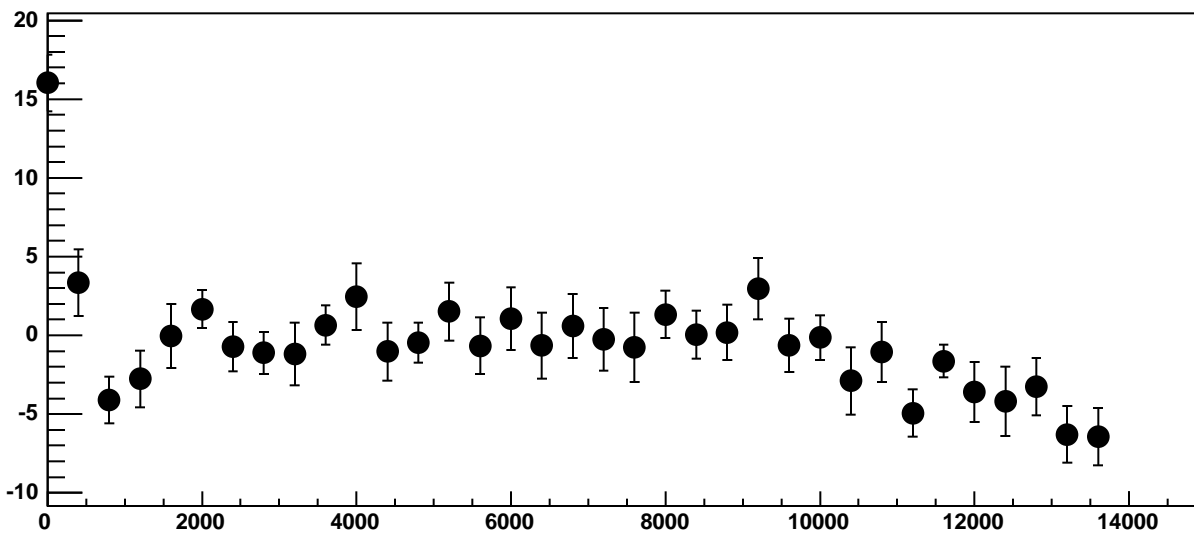


χ^2 / ndf 14.44 / 23
p0 -183.5 ± 0.7375
p1 0.03965 ± 0.0001153

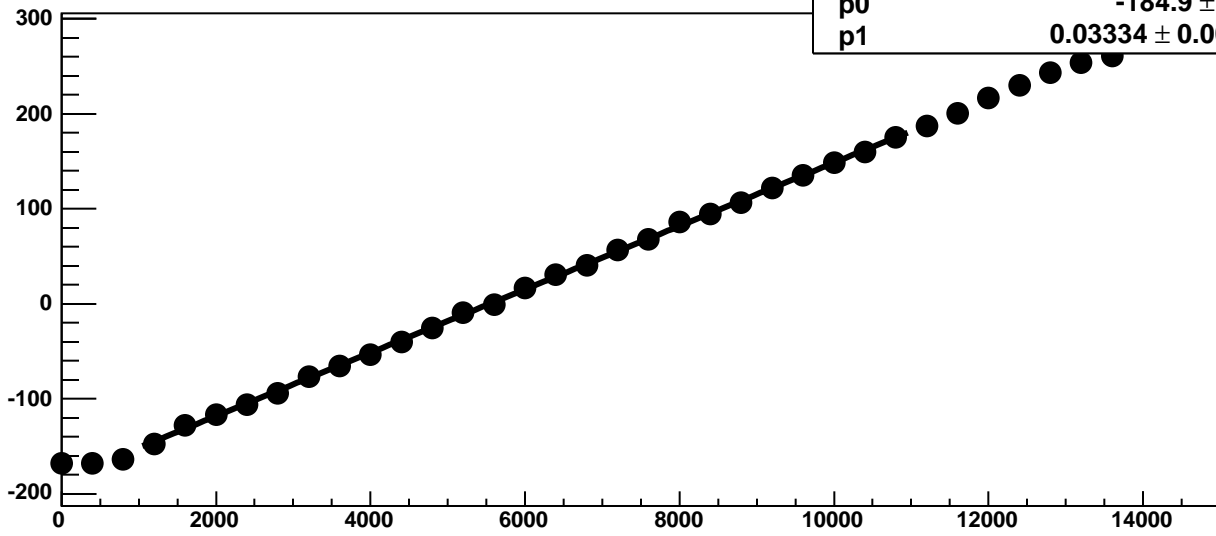
Chip 5, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC

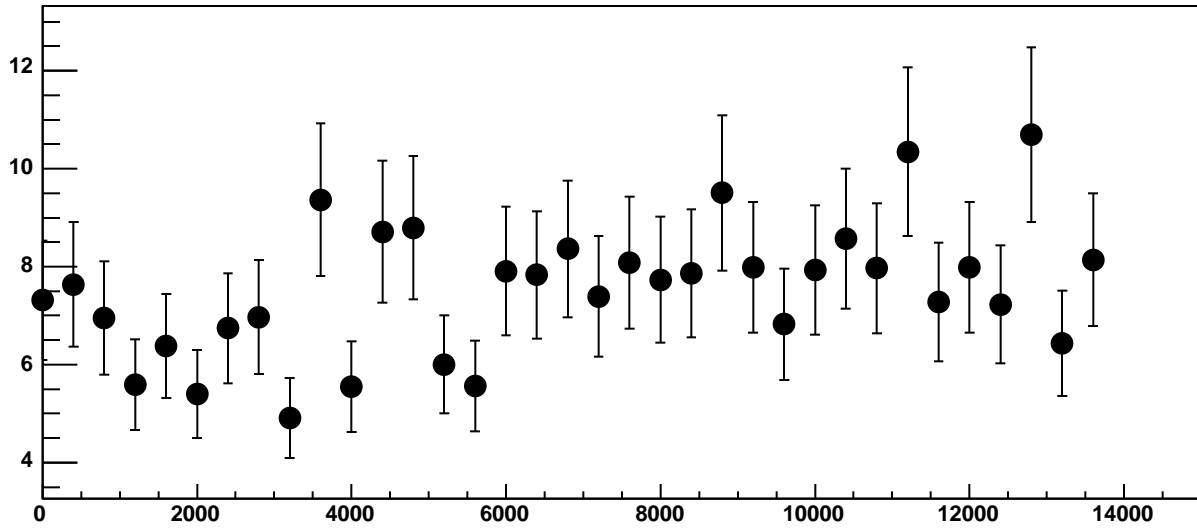


Chip 5, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

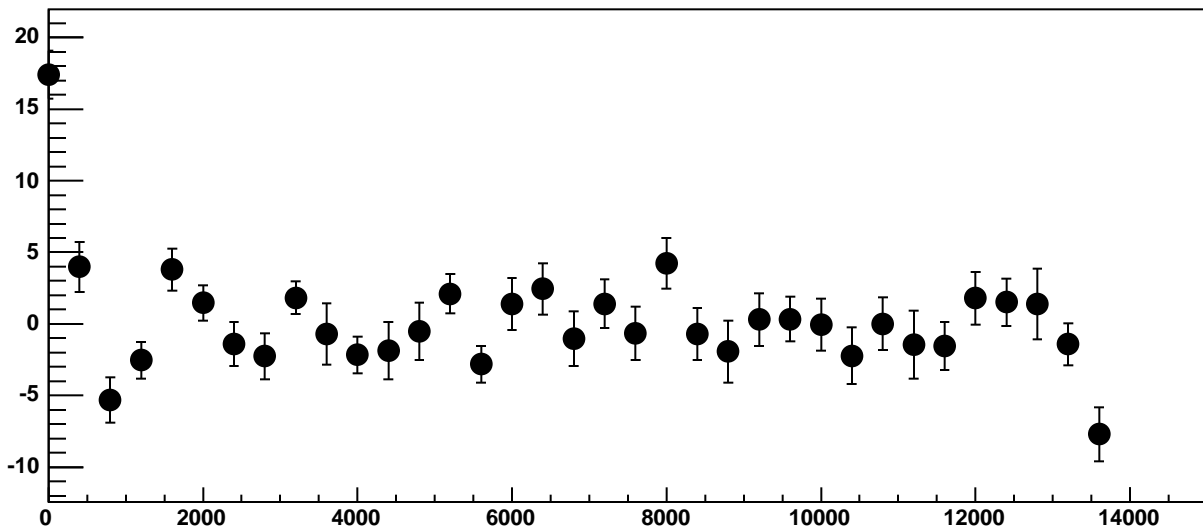


χ^2 / ndf 40.14 / 23
p0 -184.9 ± 0.6748
p1 0.03334 ± 0.0001108

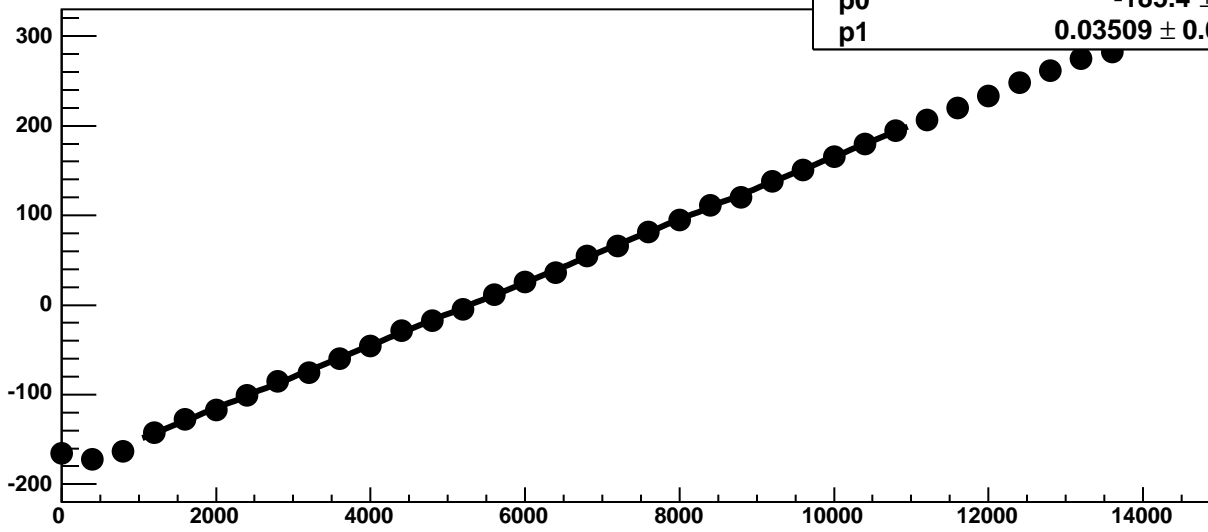
Chip 5, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

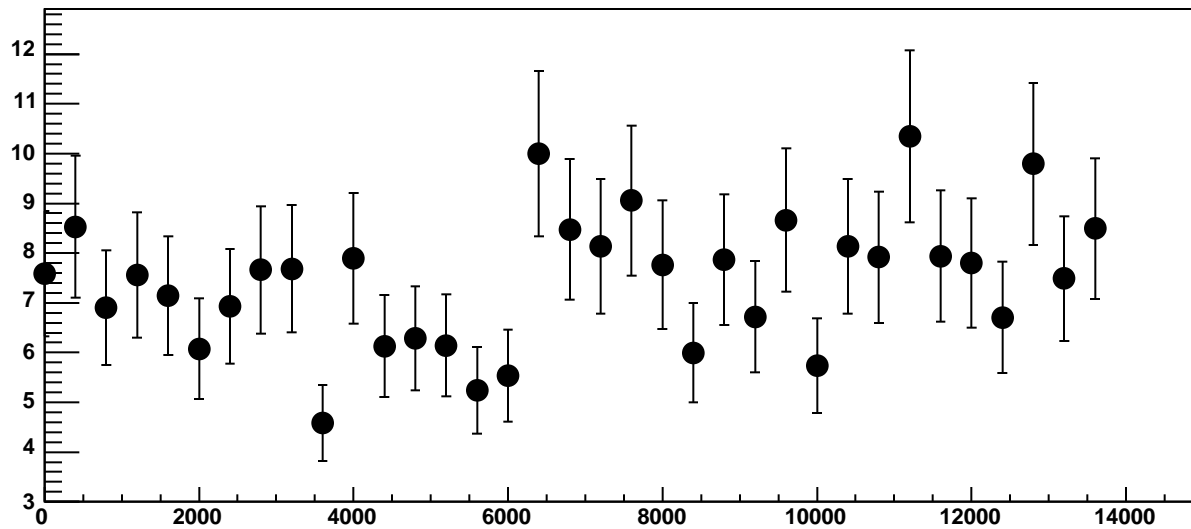


Chip 5, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

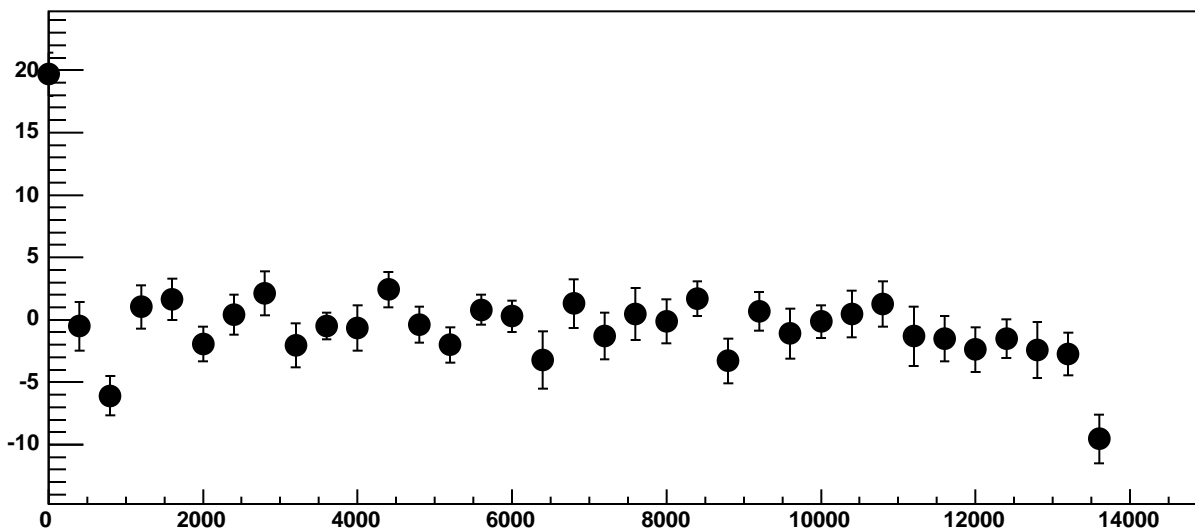


χ^2 / ndf 20.9 / 23
p0 -185.4 ± 0.7173
p1 0.03509 ± 0.0001121

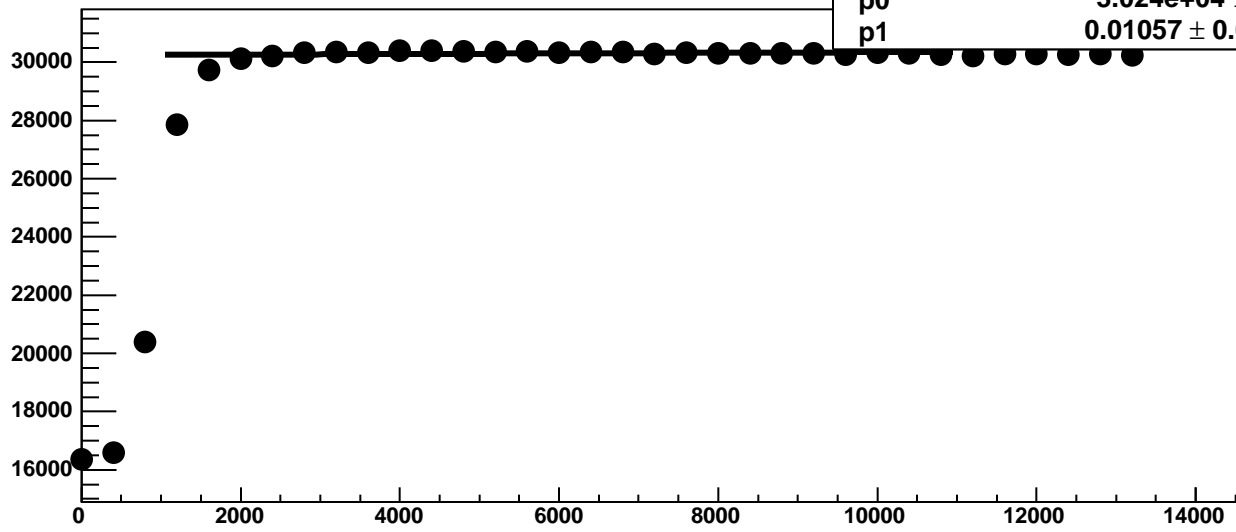
Chip 5, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



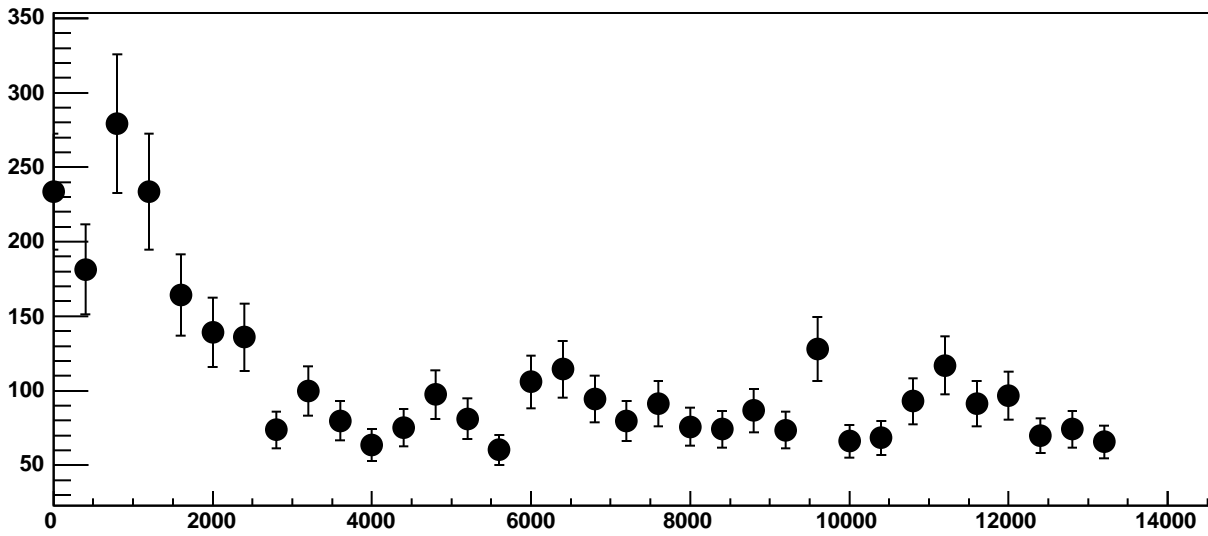
Chip 5, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



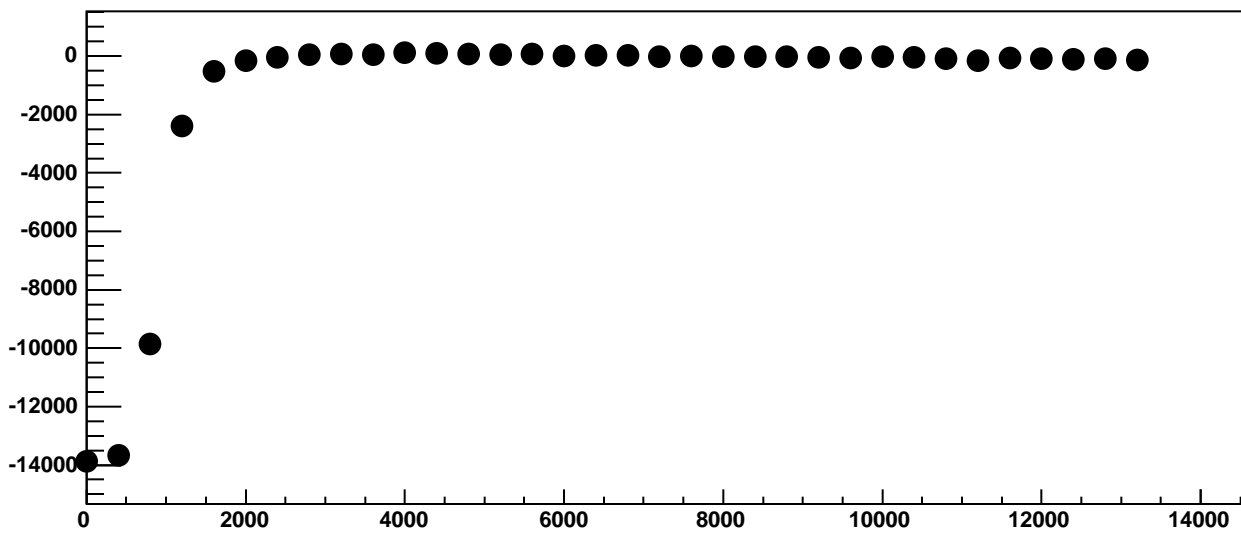
Chip 5, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC



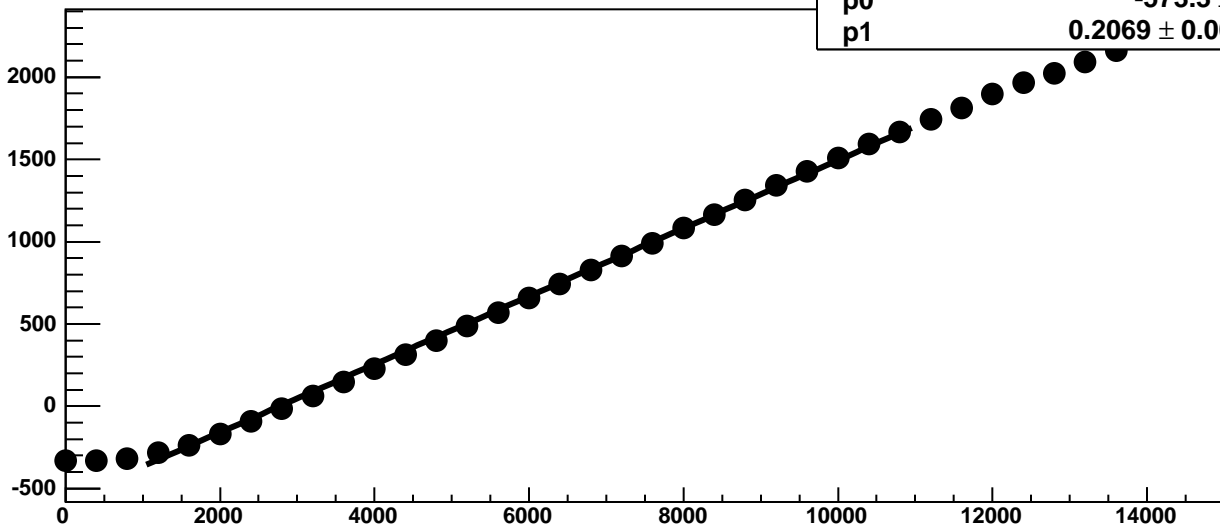
Chip 5, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC



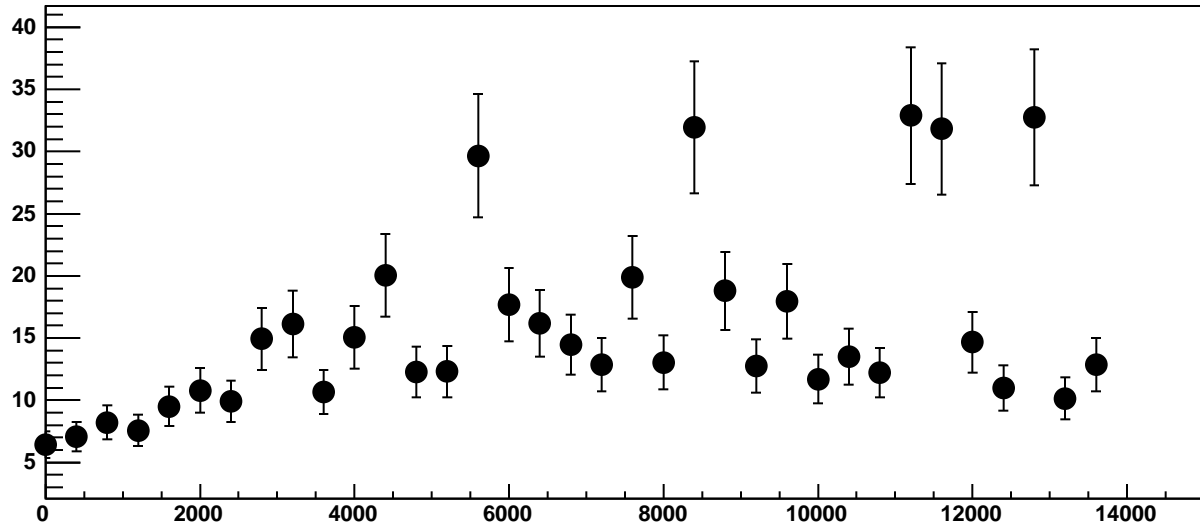
Chip 5, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC



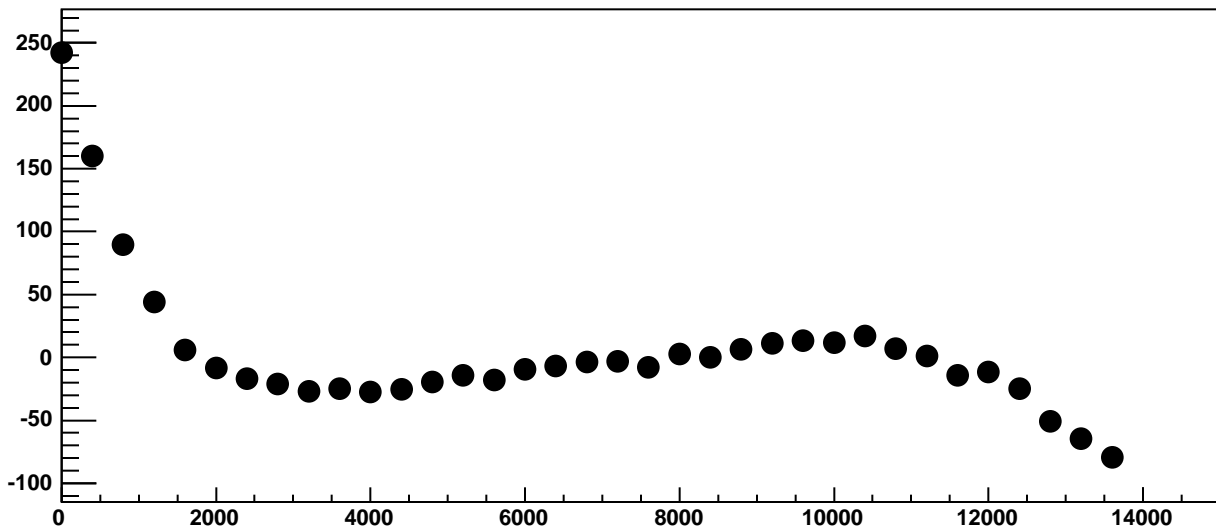
Chip 5, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



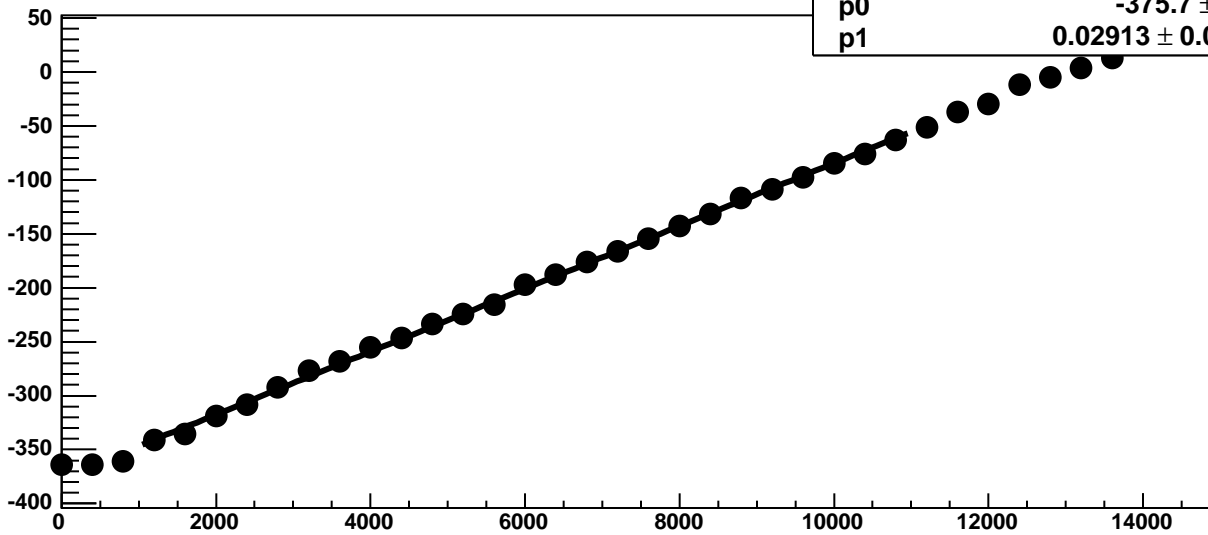
Chip 5, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

63 / 23

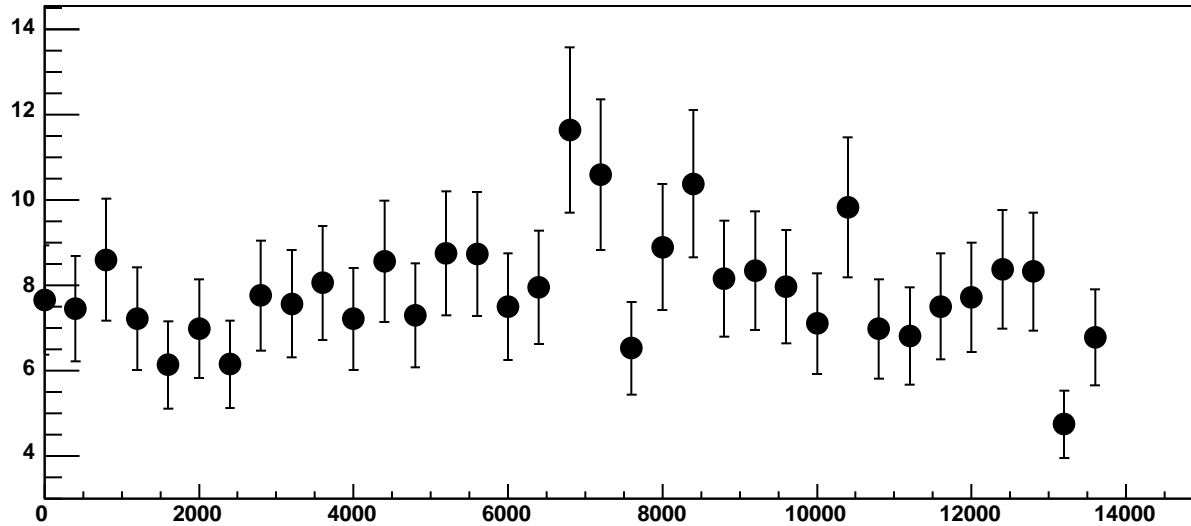
p0

-375.7 ± 0.7568

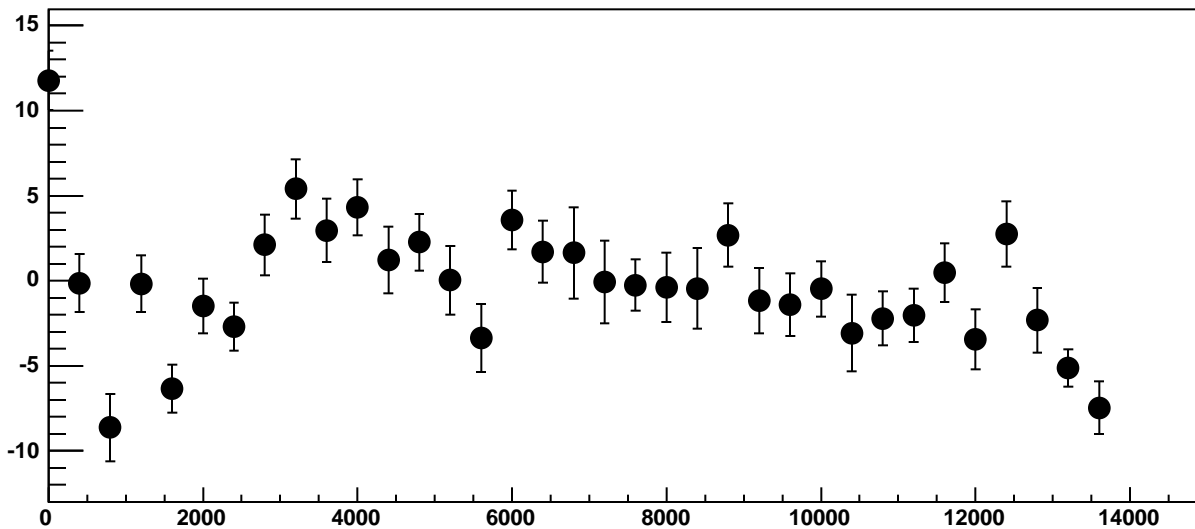
p1

0.02913 ± 0.0001182

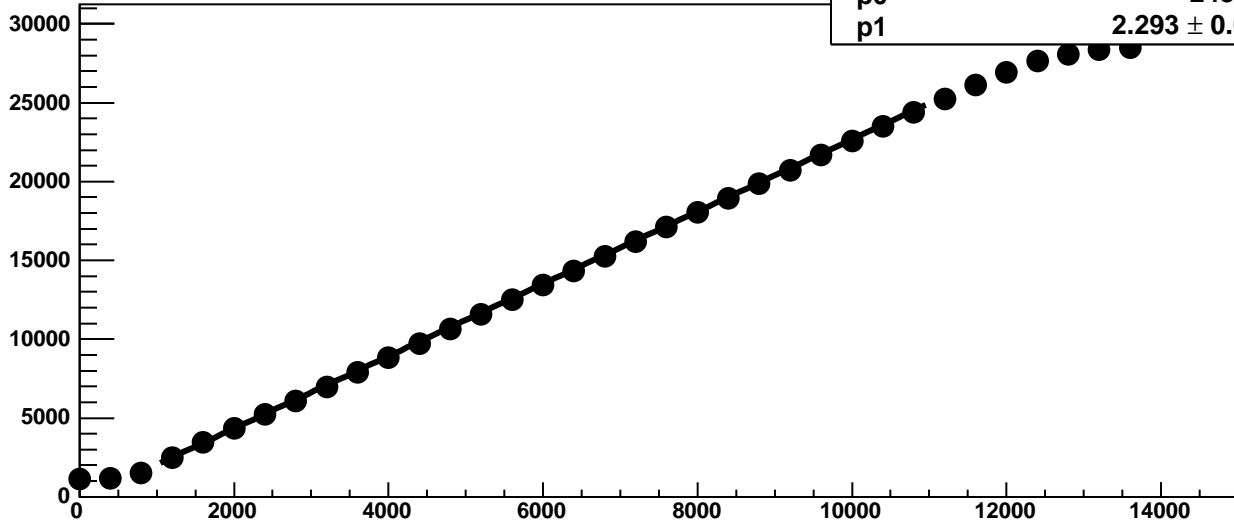
Chip 5, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



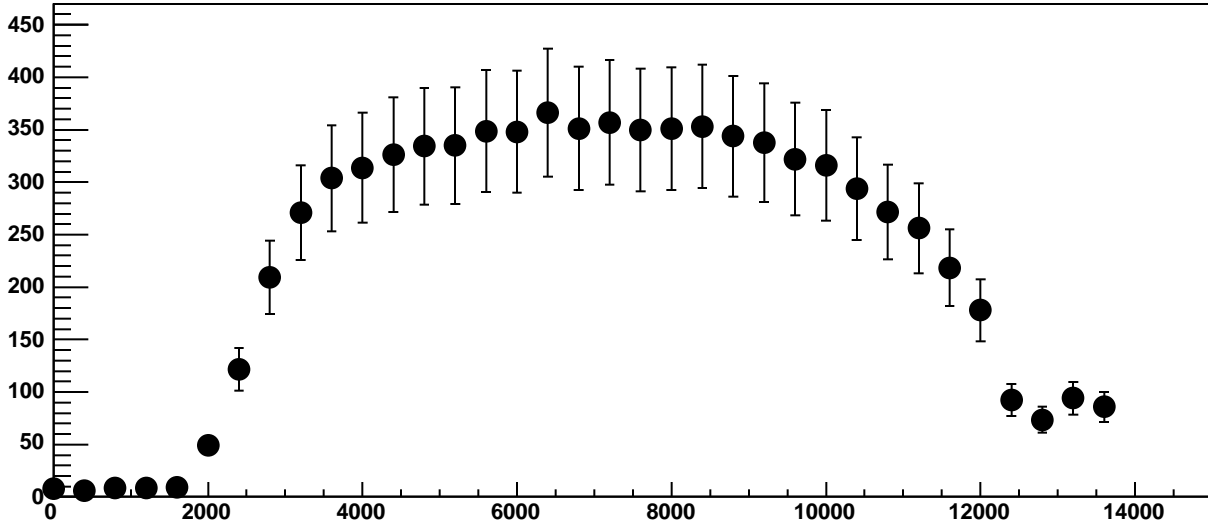
Chip 5, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



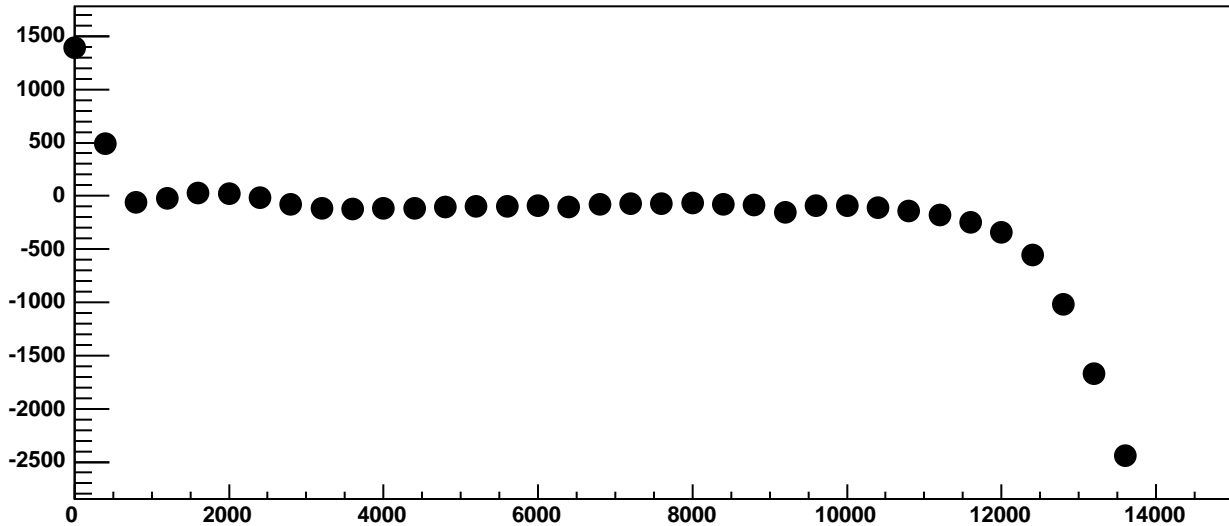
Chip 5, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC



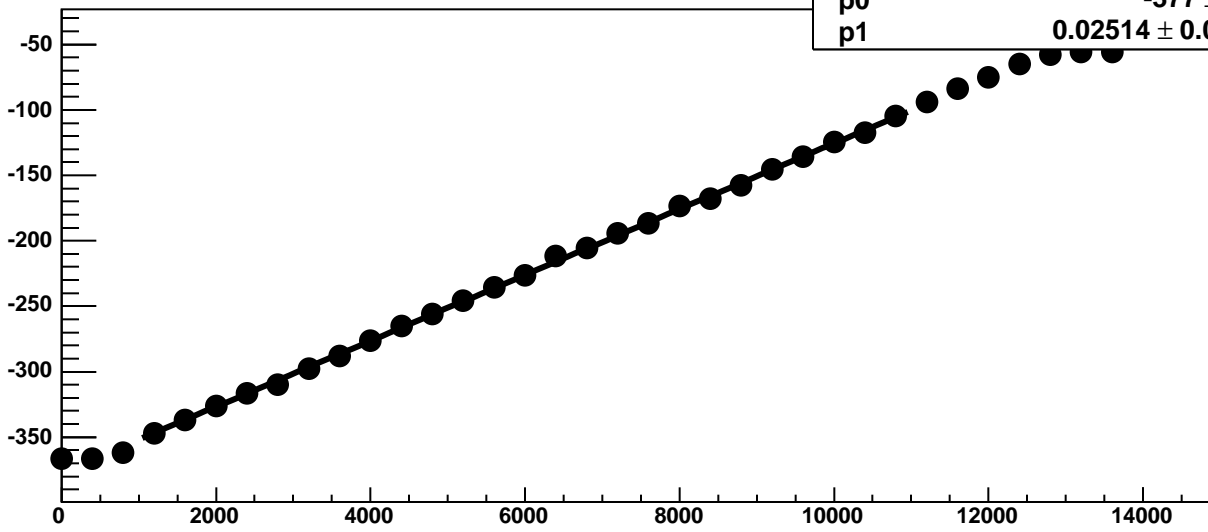
Chip 5, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC

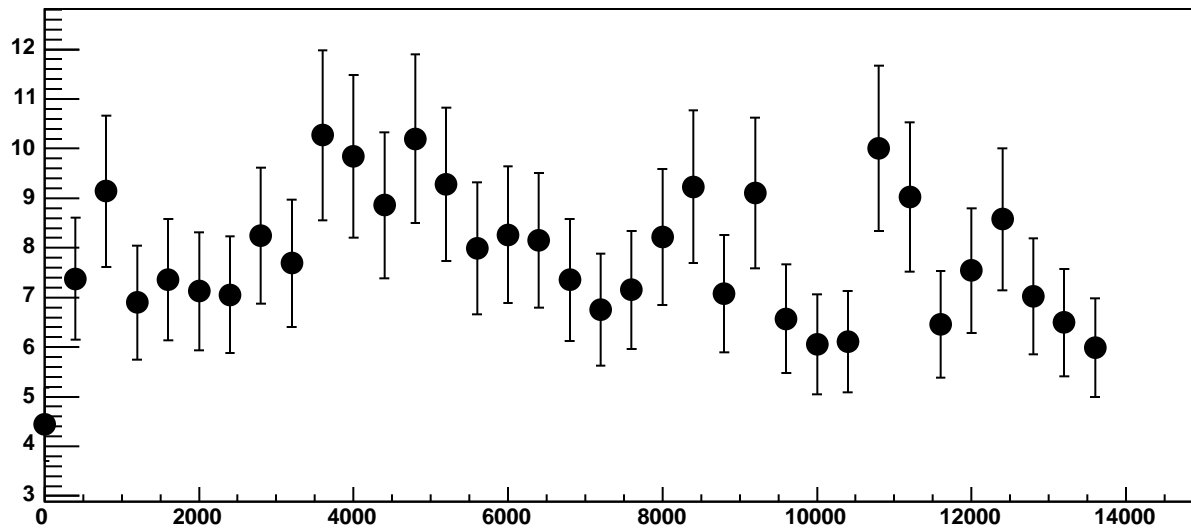


Chip 5, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

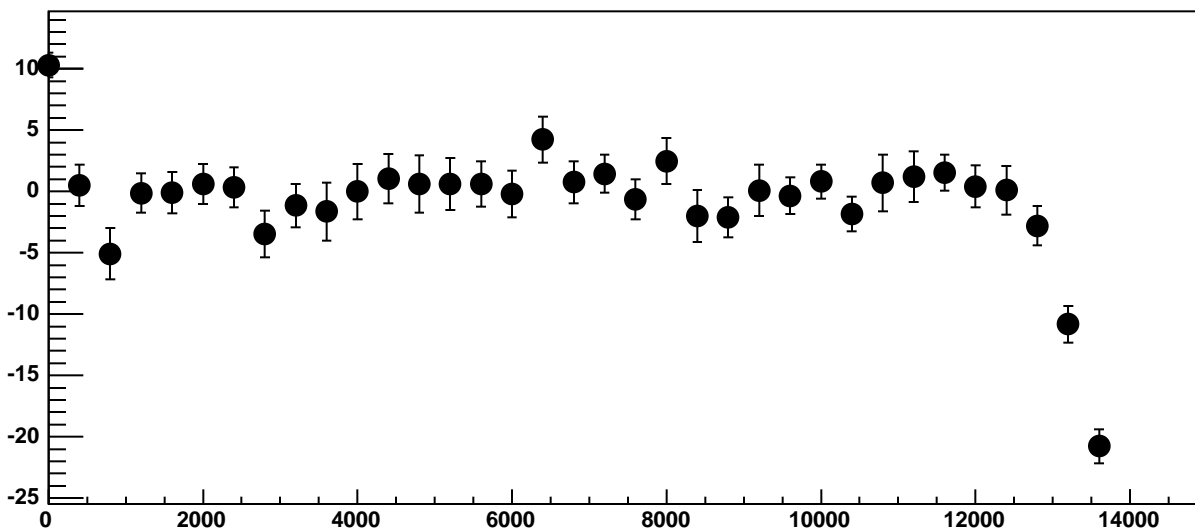


χ^2 / ndf 17.79 / 23
p0 -377 ± 0.8061
p1 0.02514 ± 0.0001172

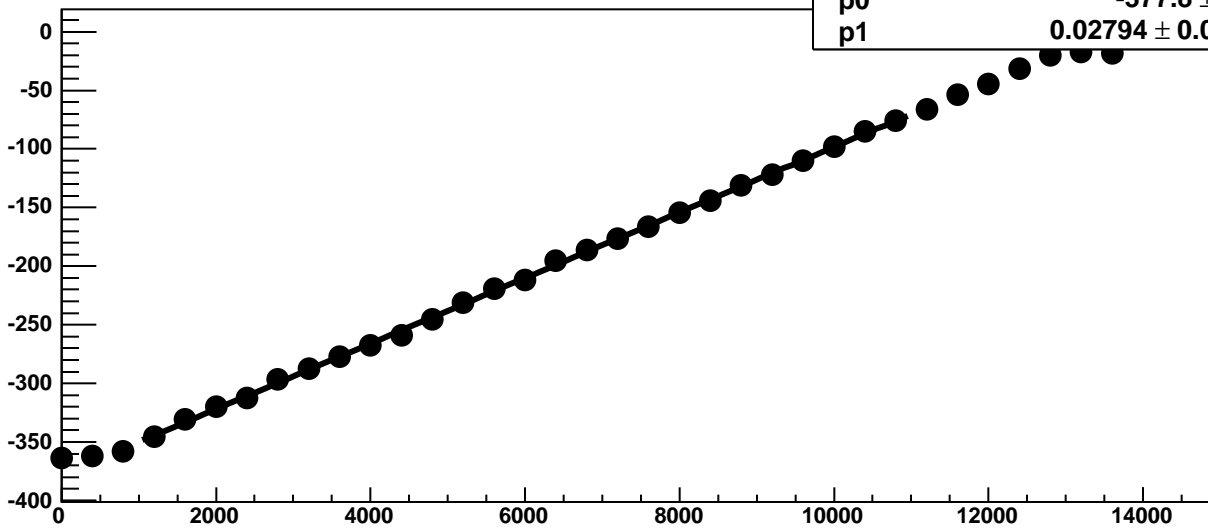
Chip 5, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

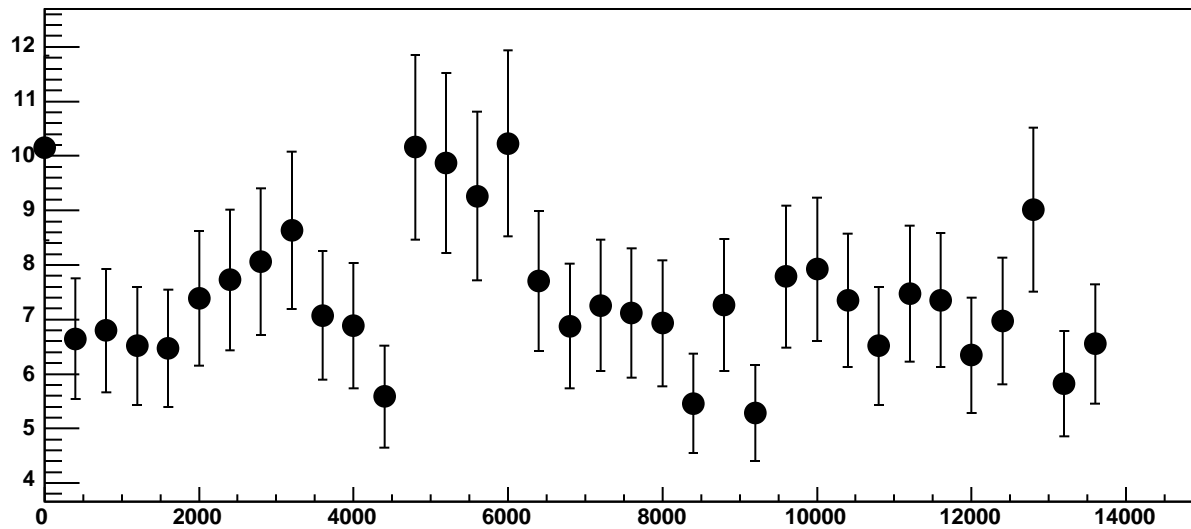


Chip 5, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

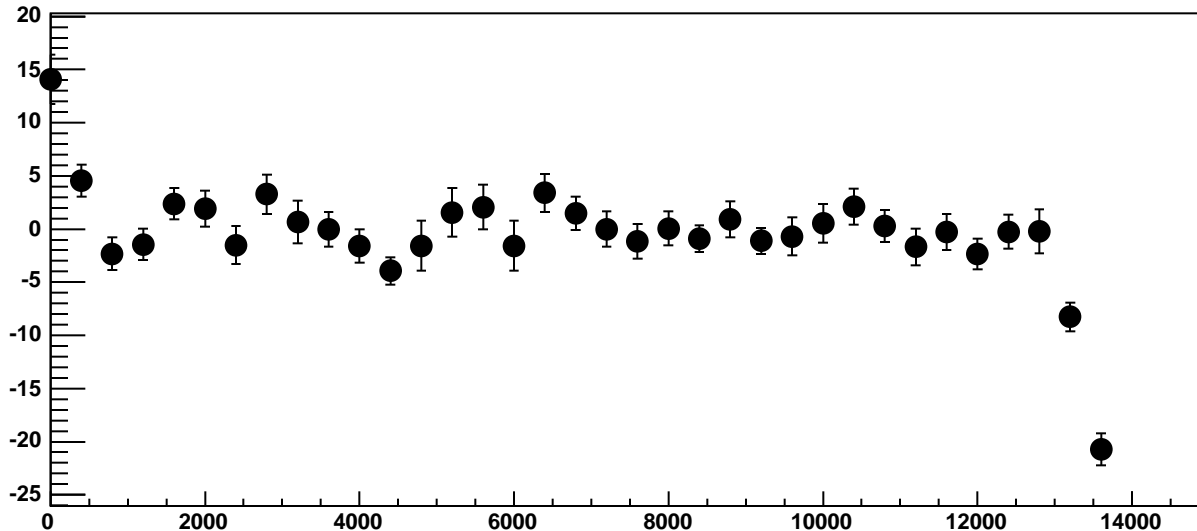


χ^2 / ndf 30.01 / 23
p0 -377.8 ± 0.7559
p1 0.02794 ± 0.0001103

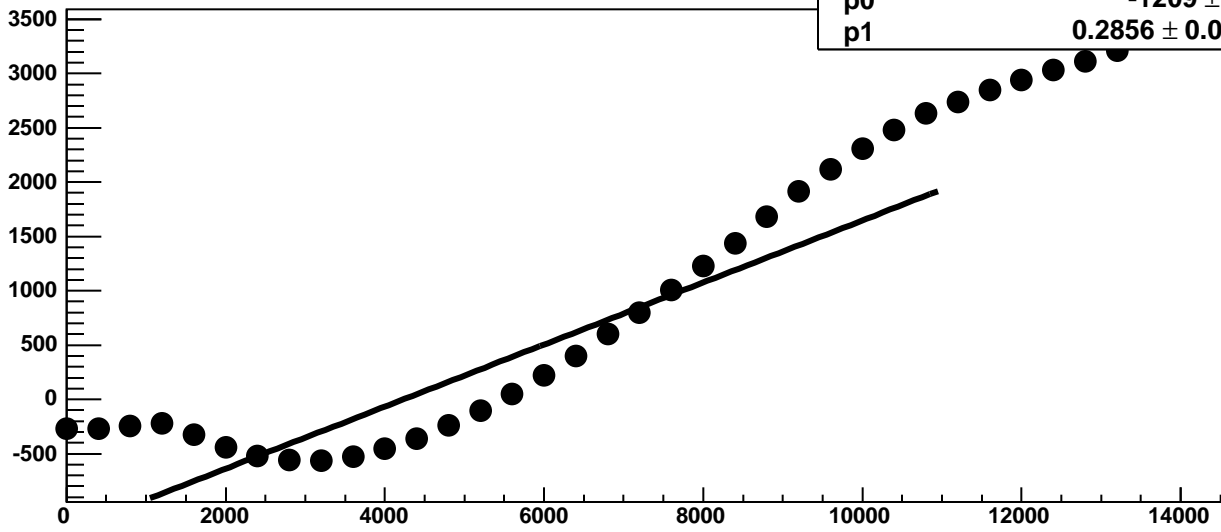
Chip 5, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



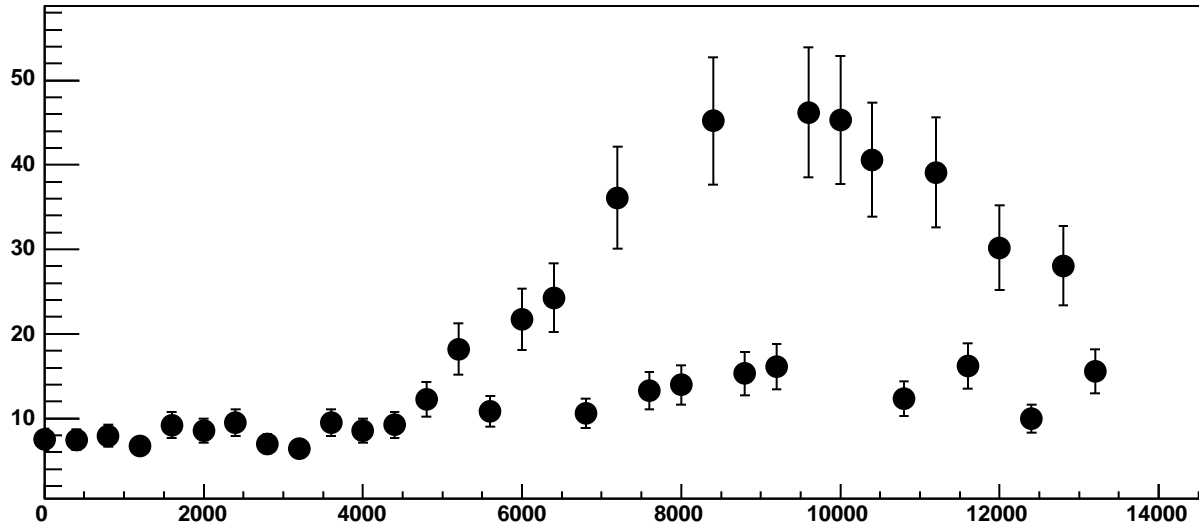
Chip 5, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



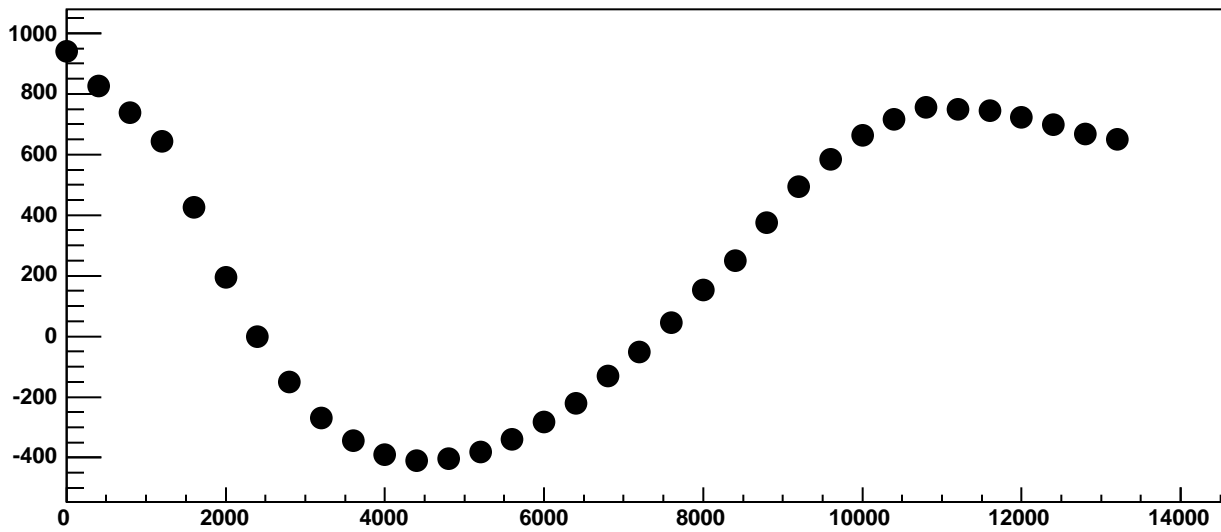
Chip 5, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC



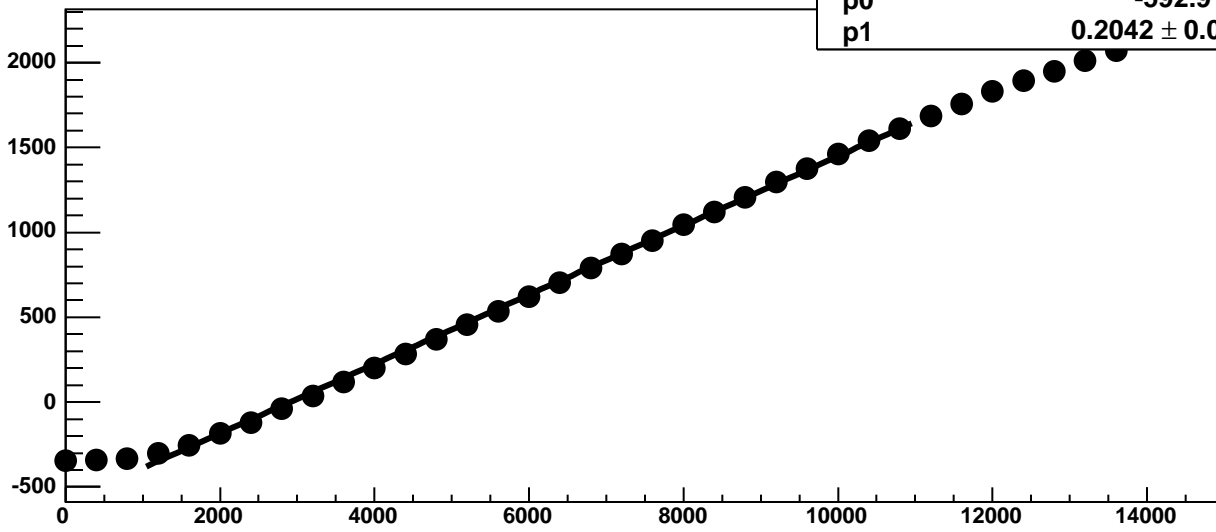
Chip 5, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



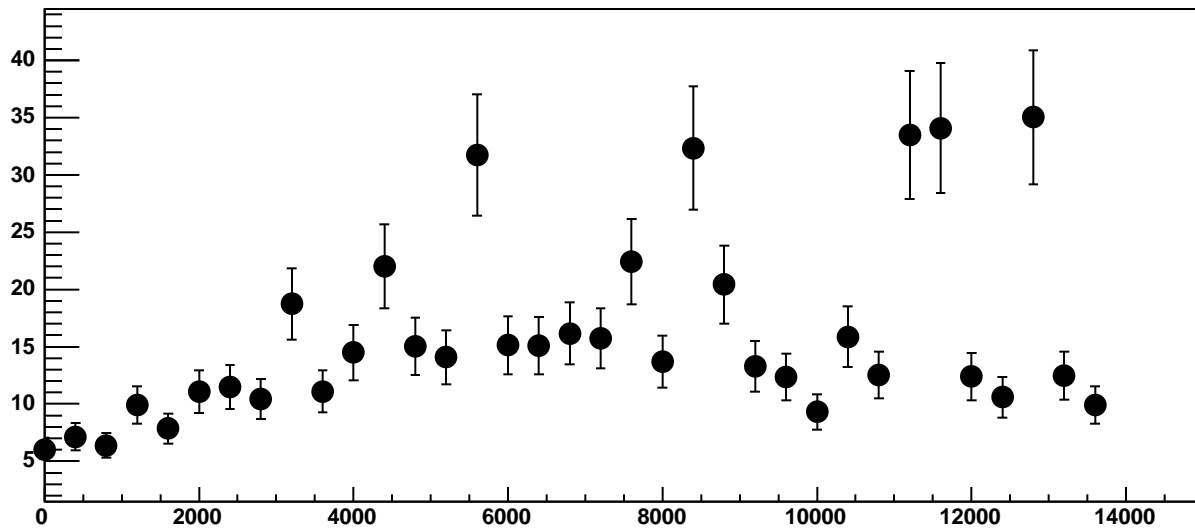
Chip 5, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC



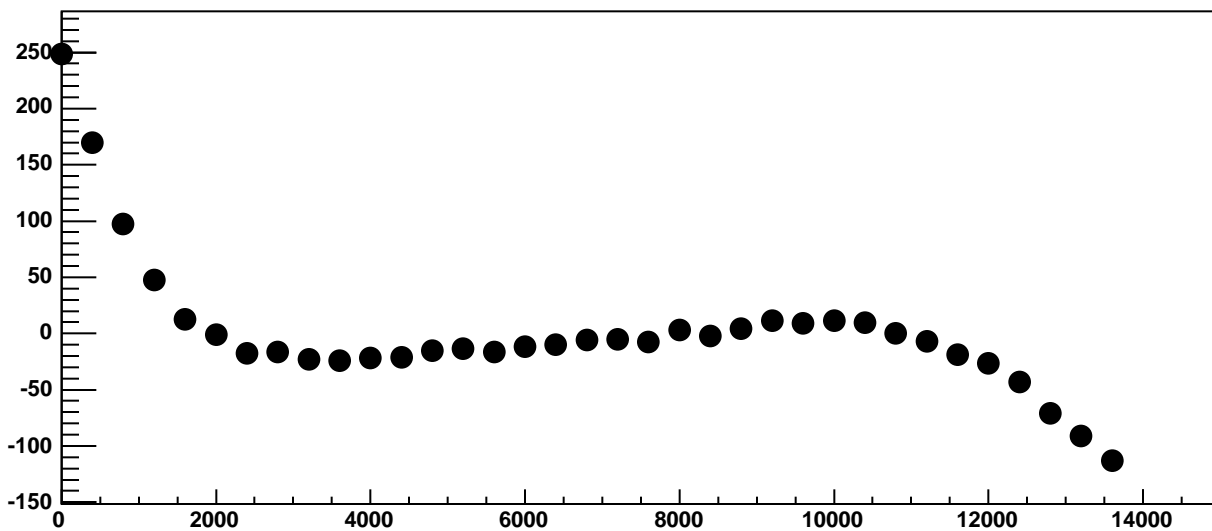
Chip 5, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



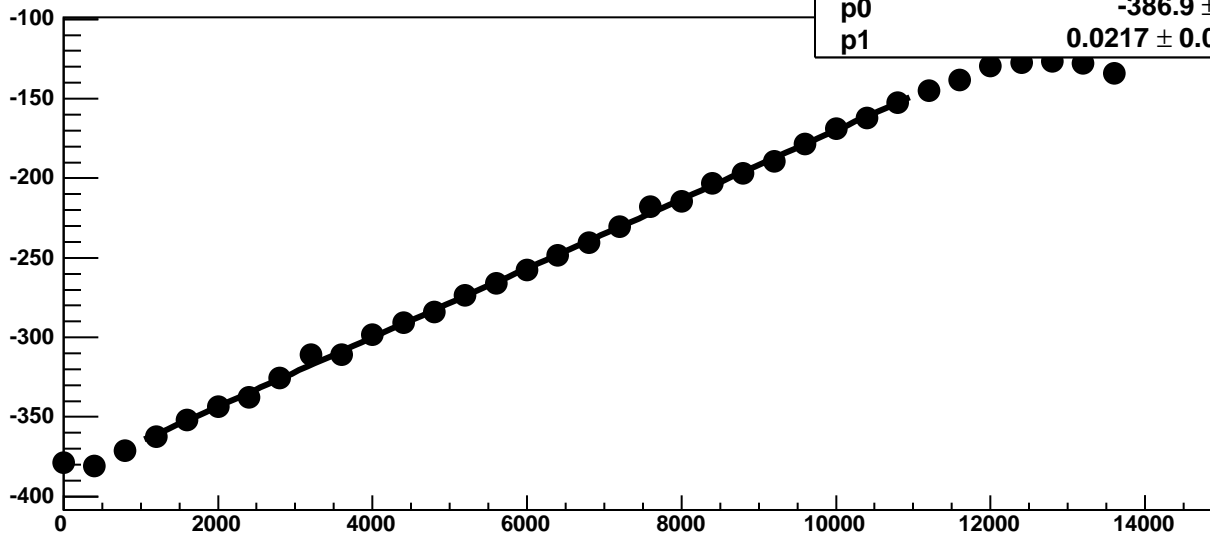
Chip 5, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



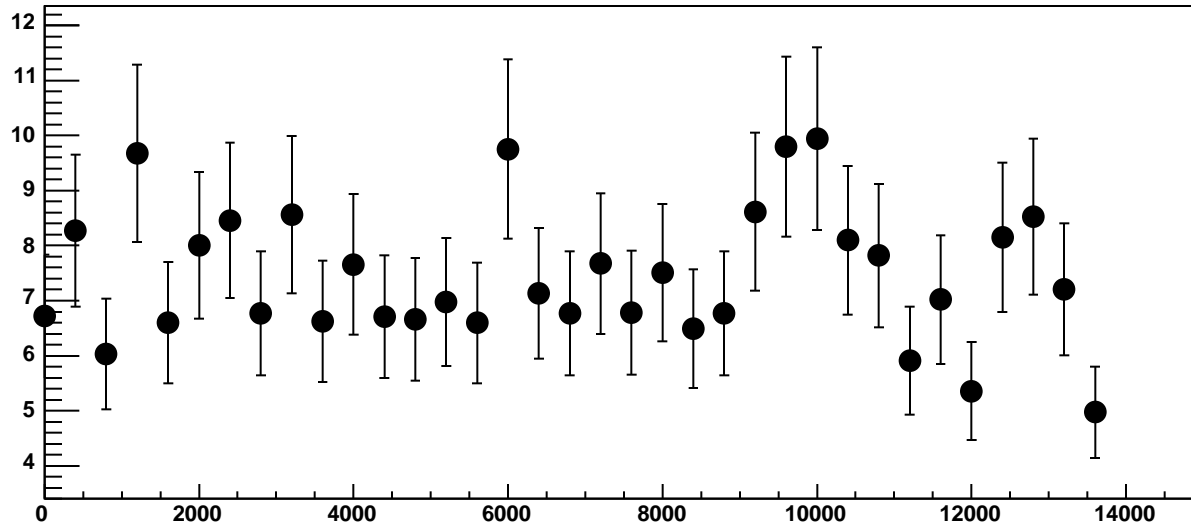
Chip 5, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



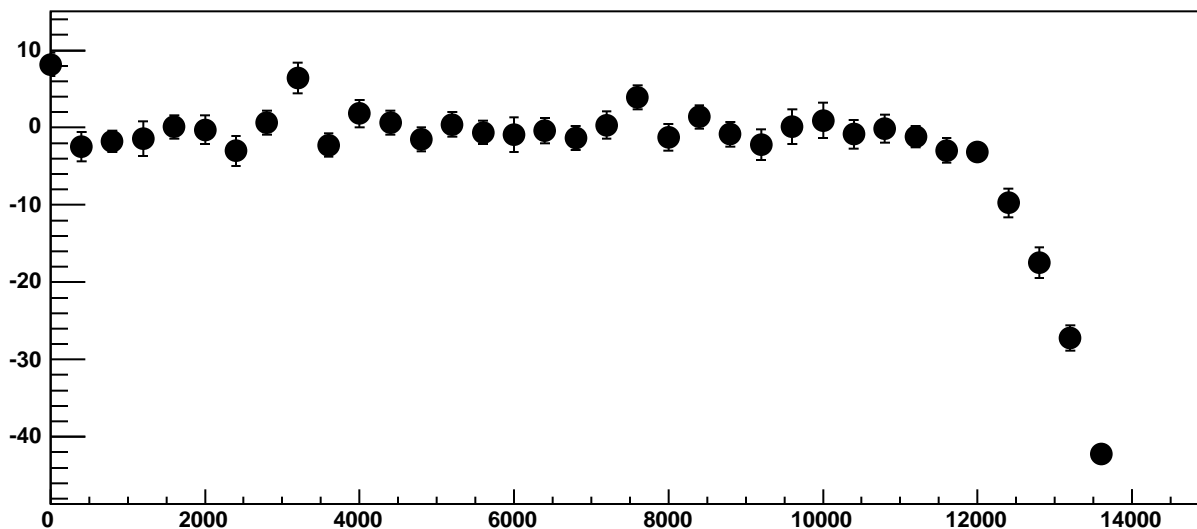
Chip 5, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



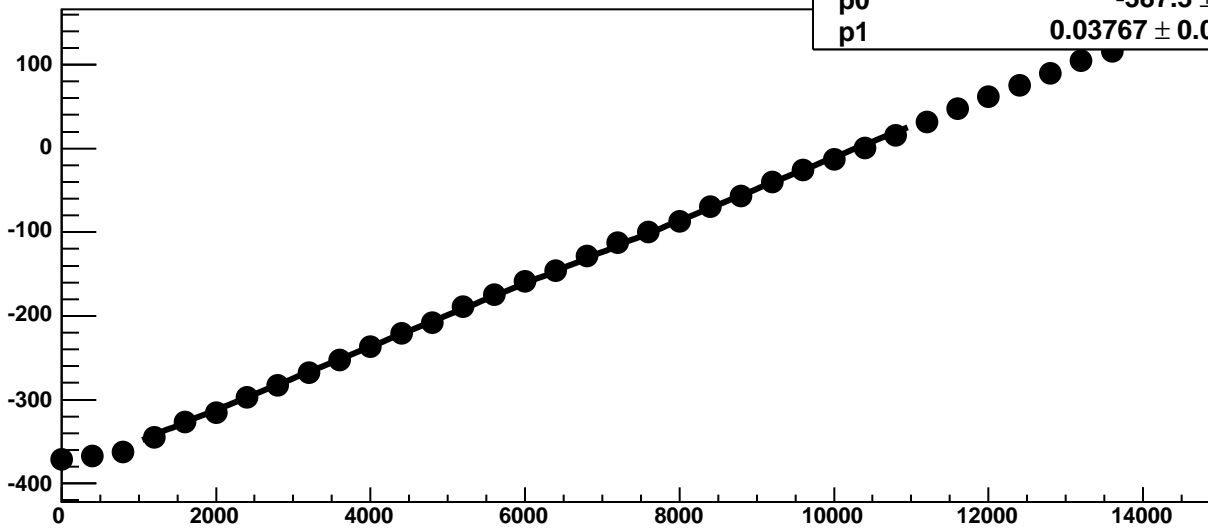
Chip 5, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

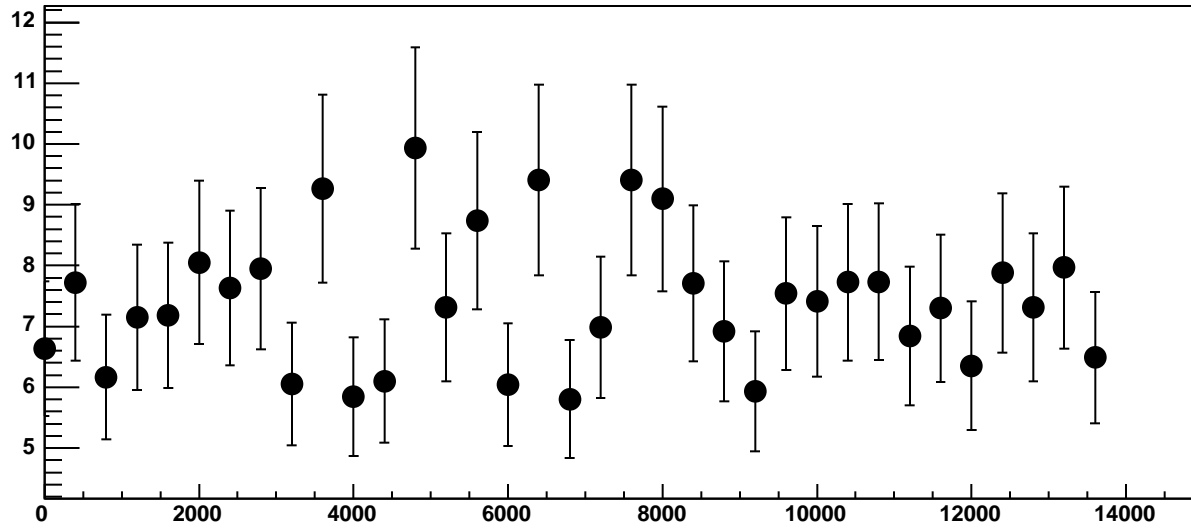


Chip 5, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC

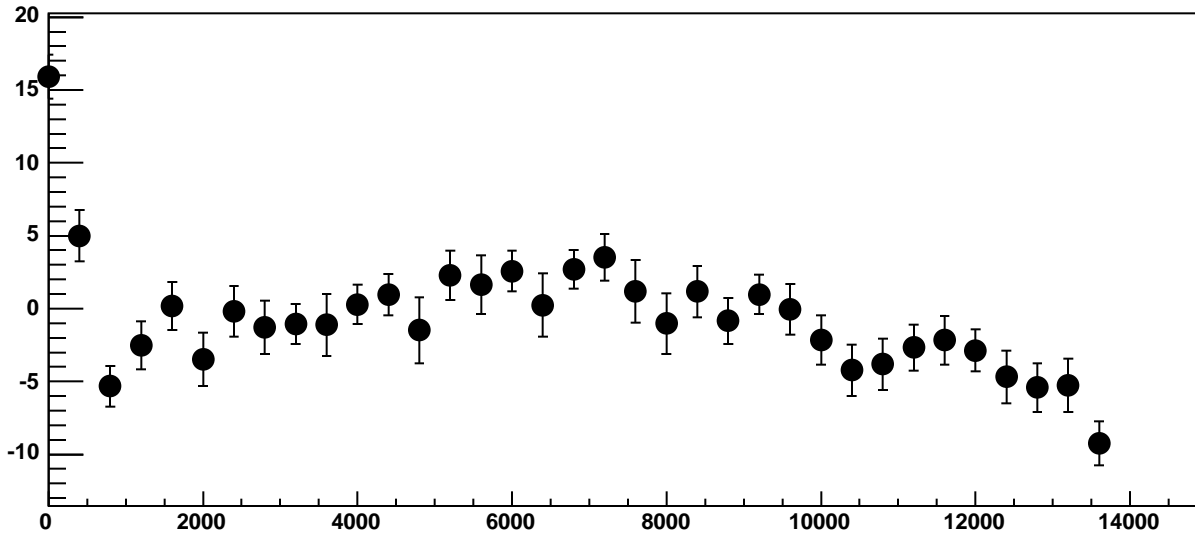


χ^2 / ndf 36.87 / 23
p0 -387.3 ± 0.7724
p1 0.03767 ± 0.0001168

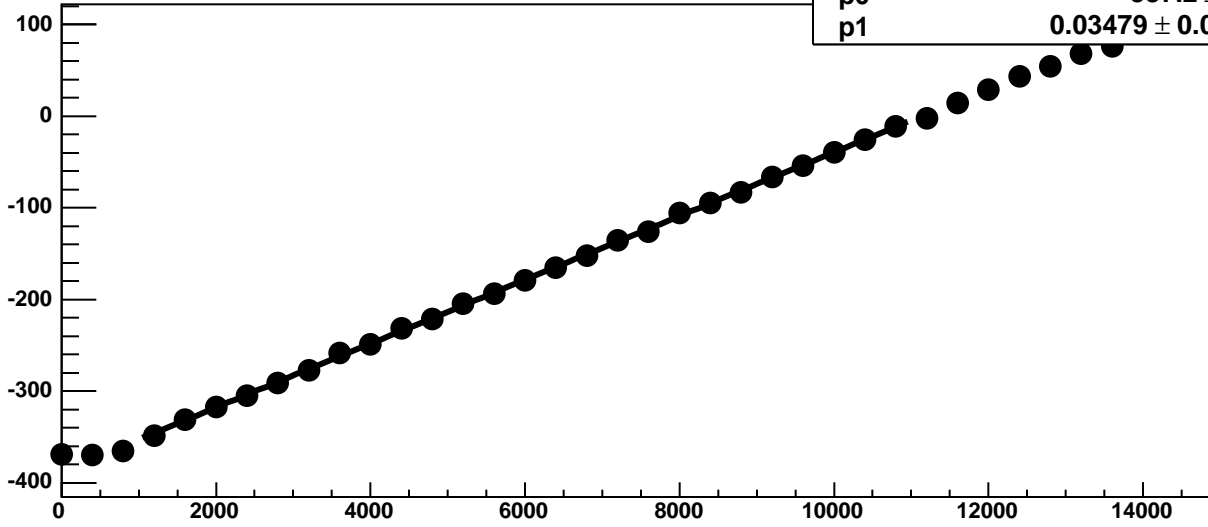
Chip 5, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC

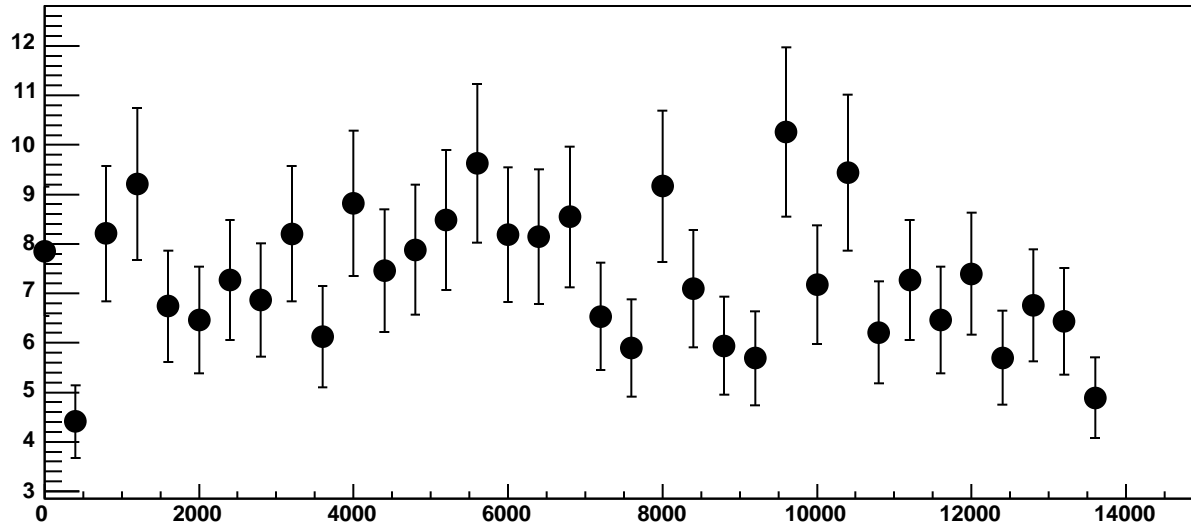


Chip 5, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

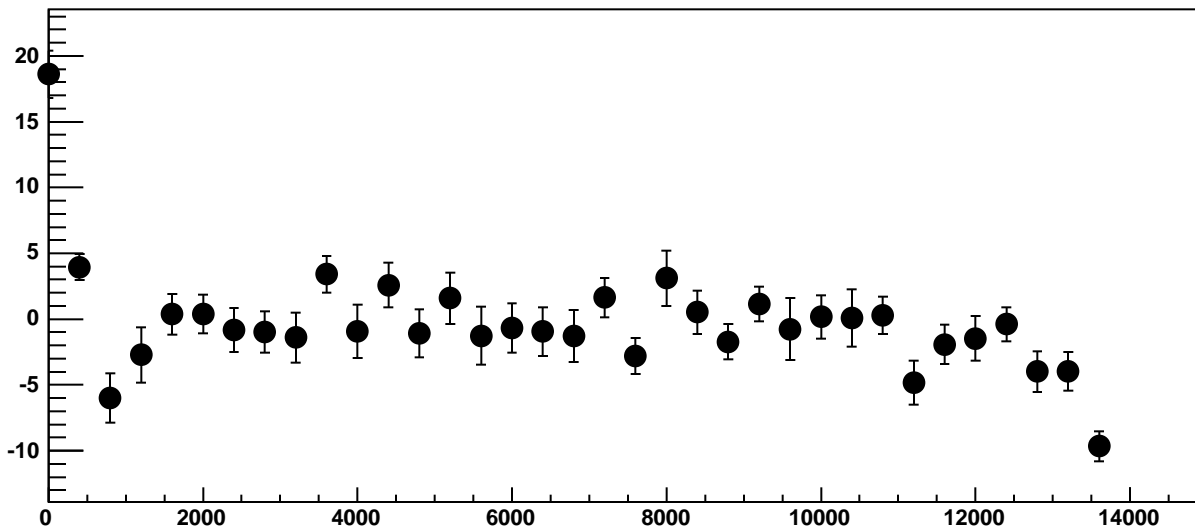


χ^2 / ndf 23.84 / 23
p0 -387.2 ± 0.7761
p1 0.03479 ± 0.0001146

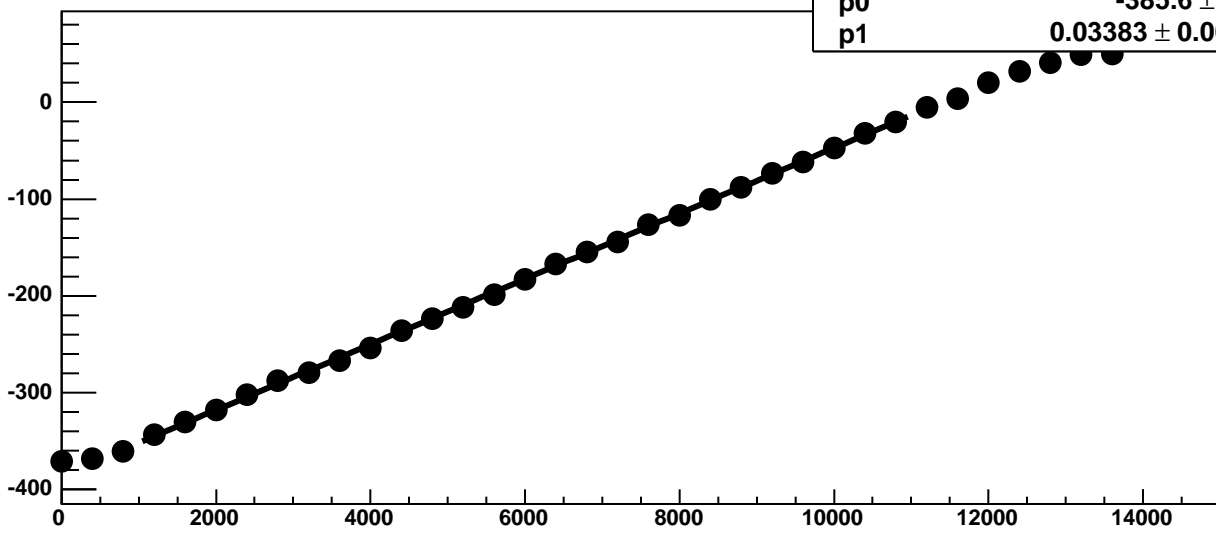
Chip 5, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

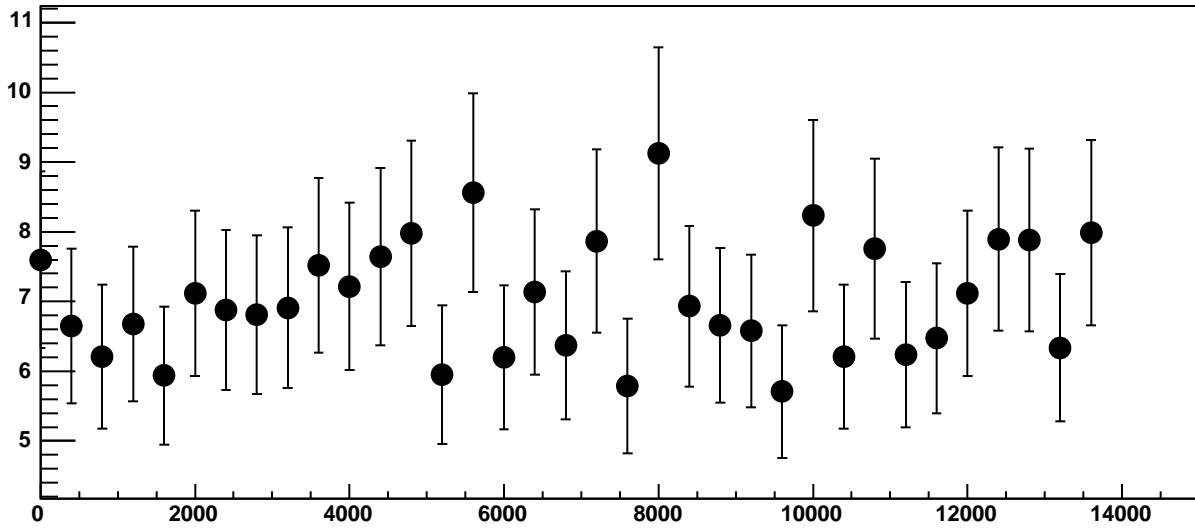


Chip 5, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

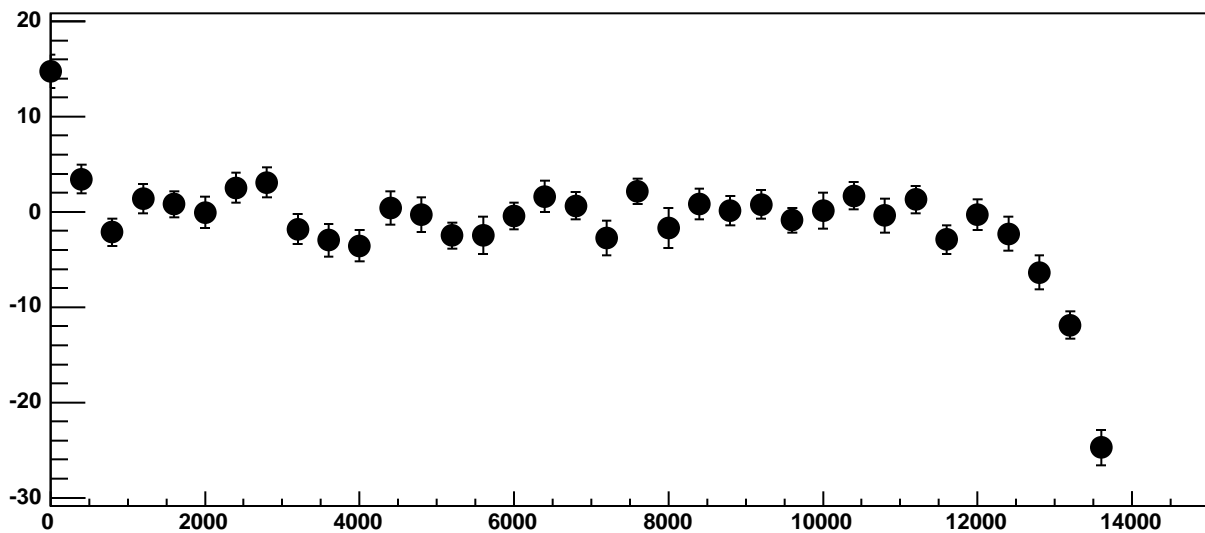


χ^2 / ndf 30.72 / 23
p0 -385.6 ± 0.7198
p1 0.03383 ± 0.0001078

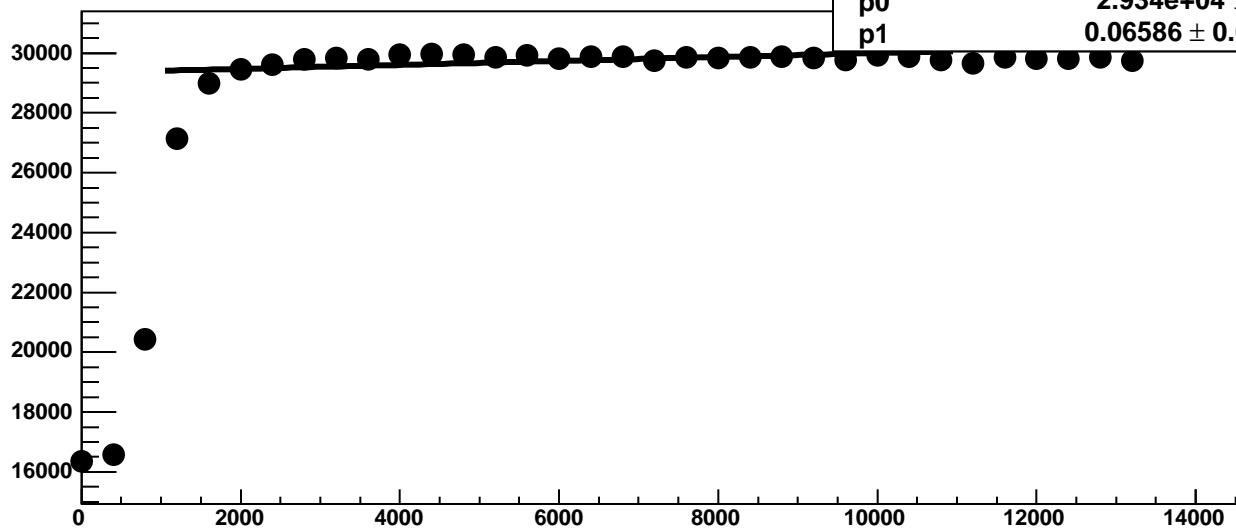
Chip 5, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



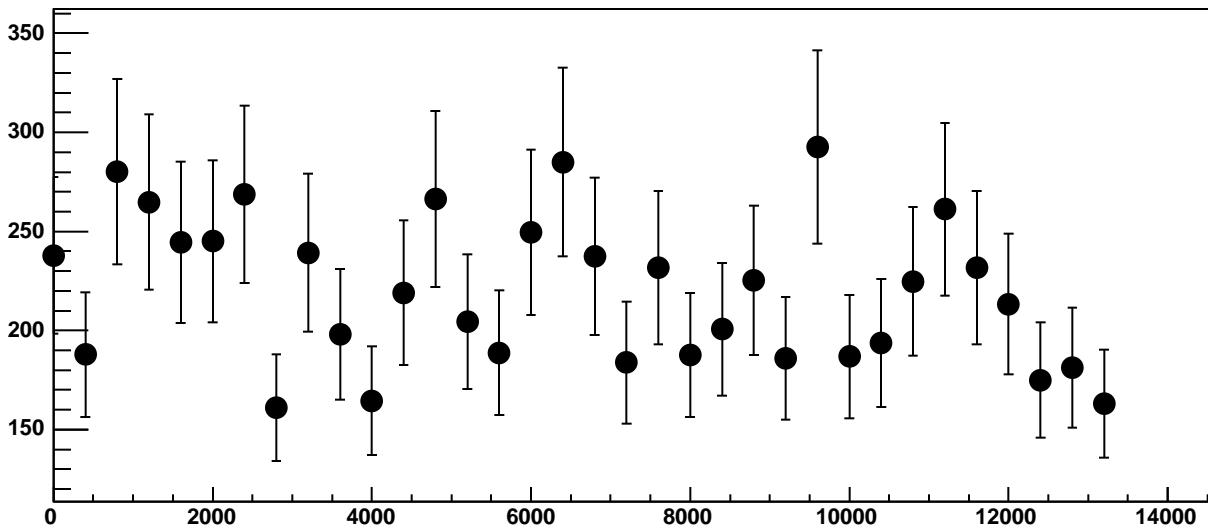
Chip 5, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



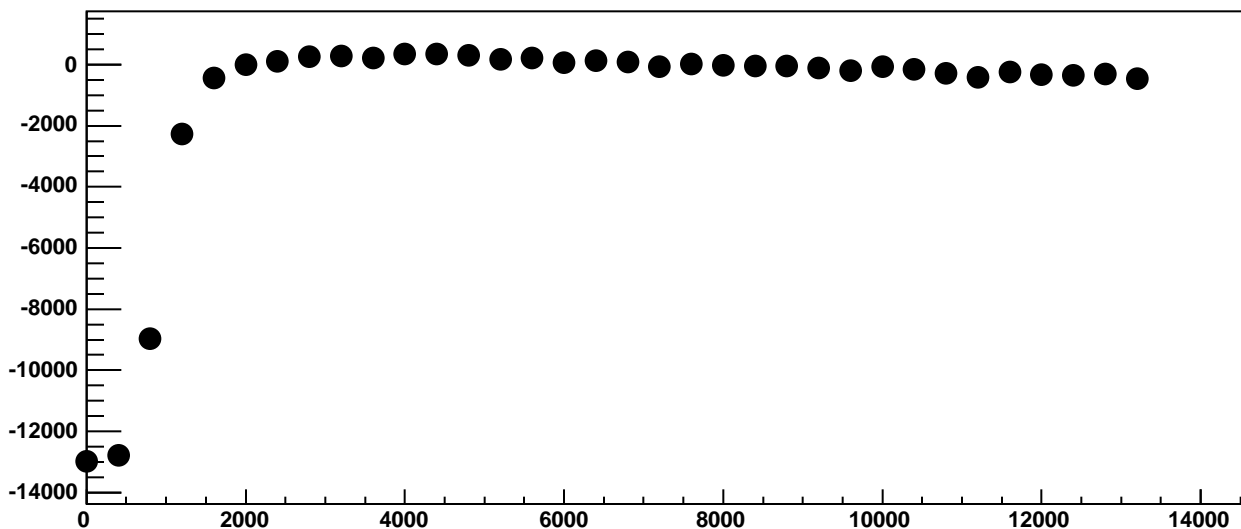
Chip 5, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC



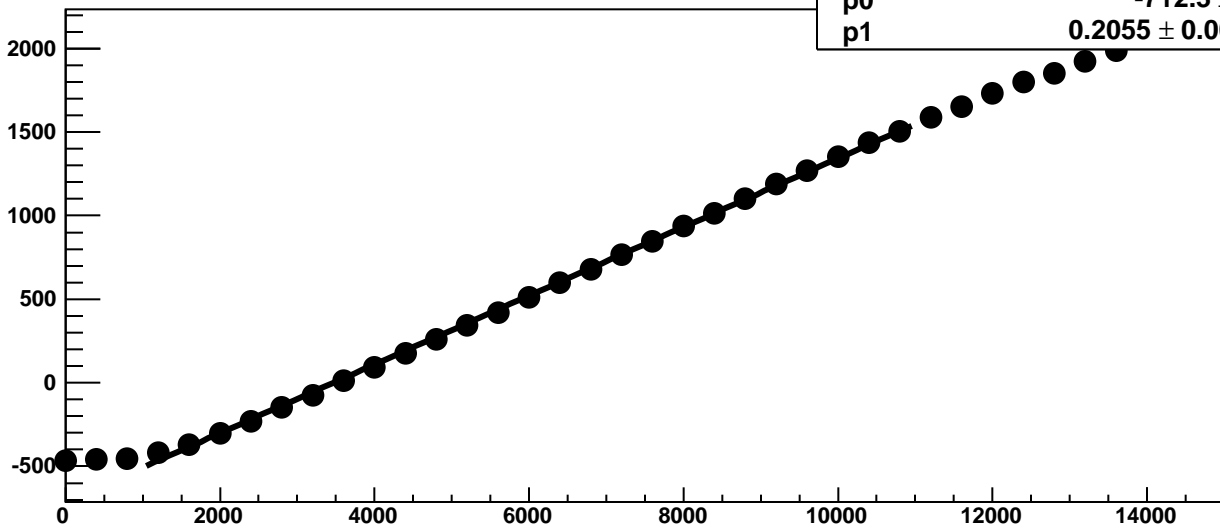
Chip 5, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC



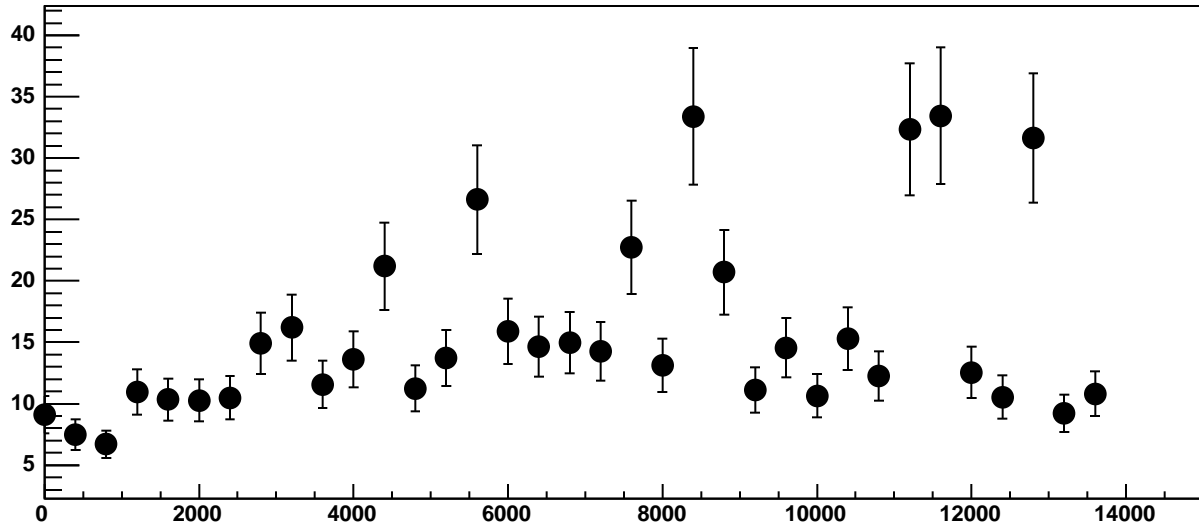
Chip 5, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC



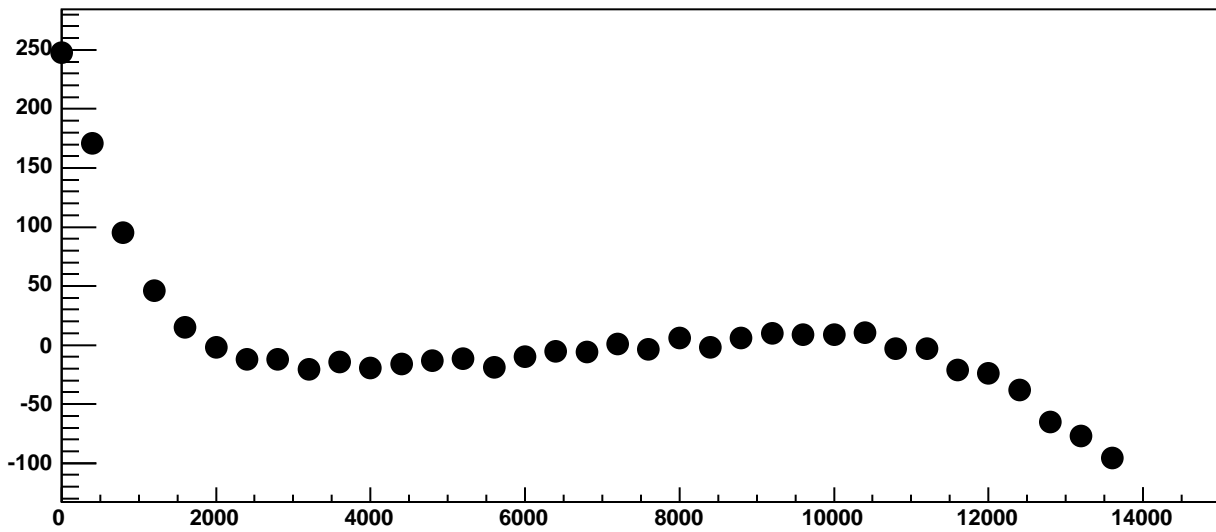
Chip 5, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC



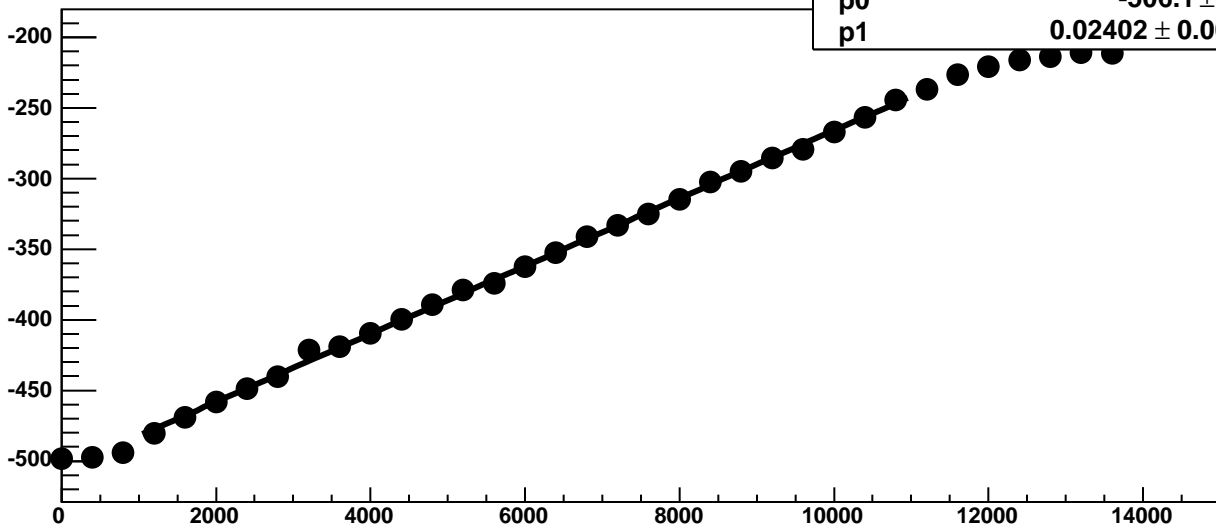
Chip 5, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

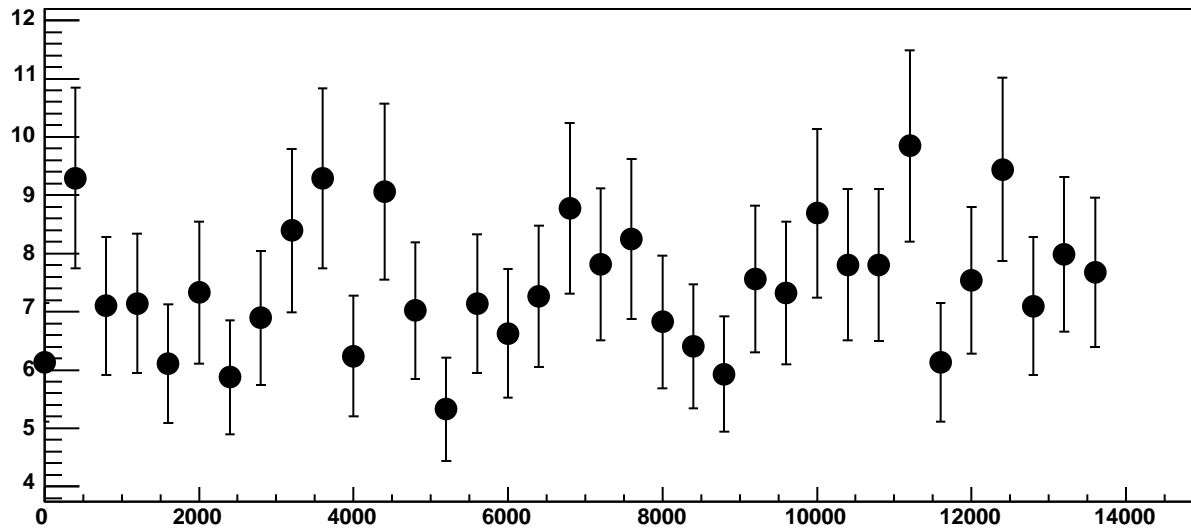


Chip 5, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

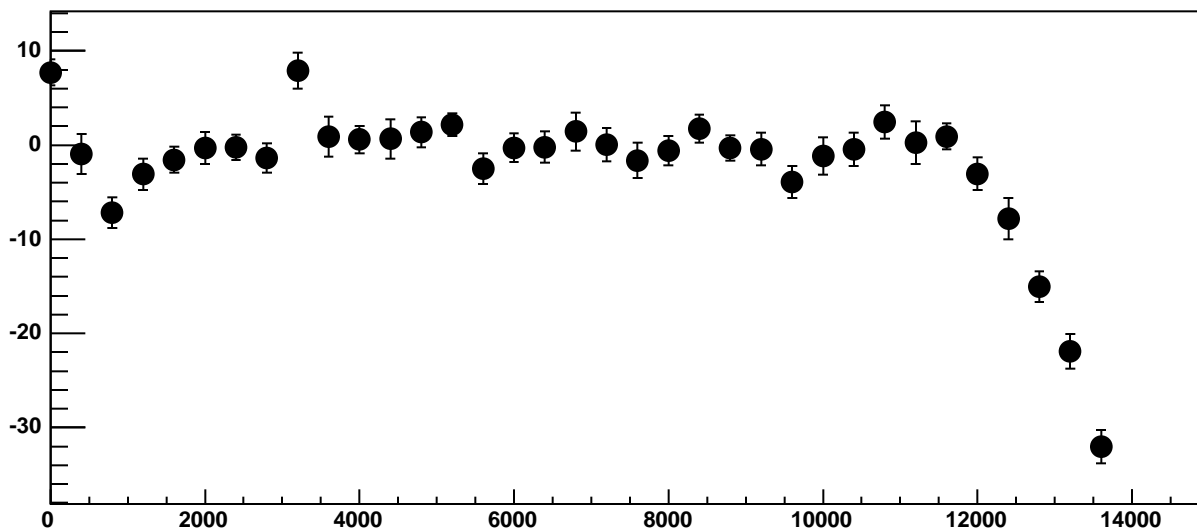


χ^2 / ndf 39.97 / 23
p0 -506.1 ± 0.7387
p1 0.02402 ± 0.0001138

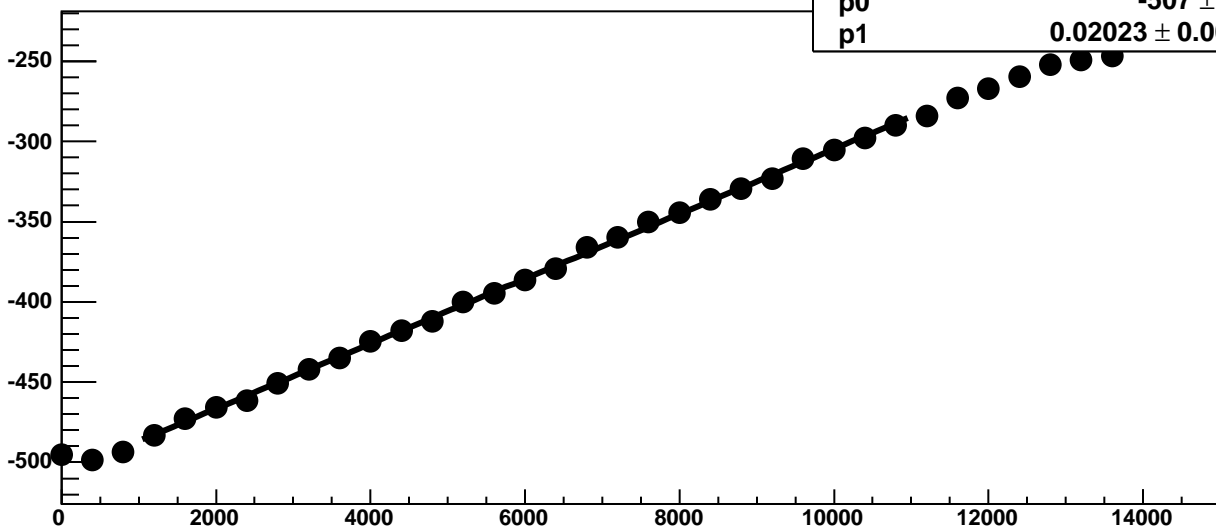
Chip 5, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

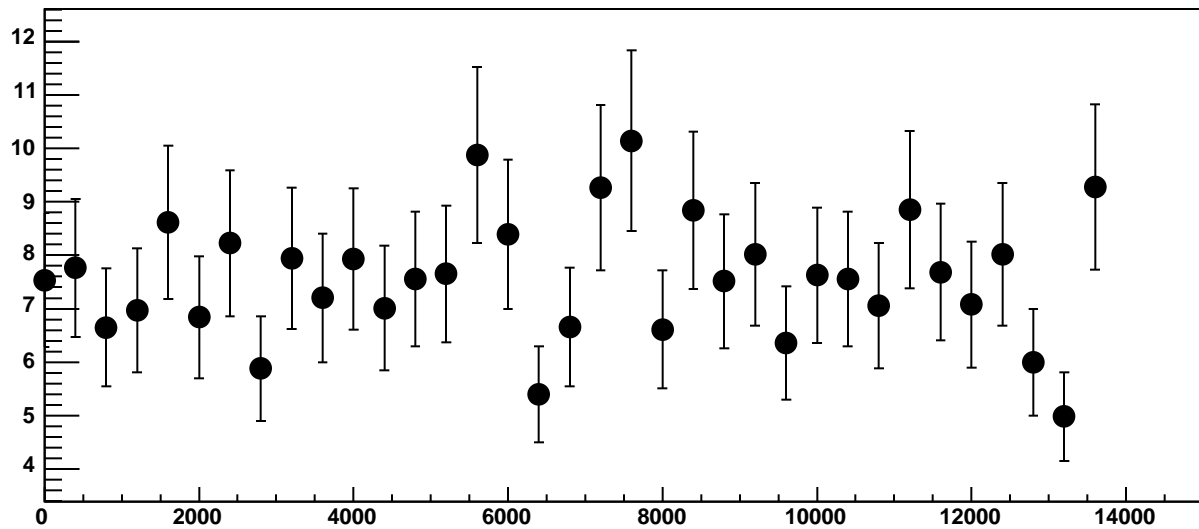


Chip 5, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

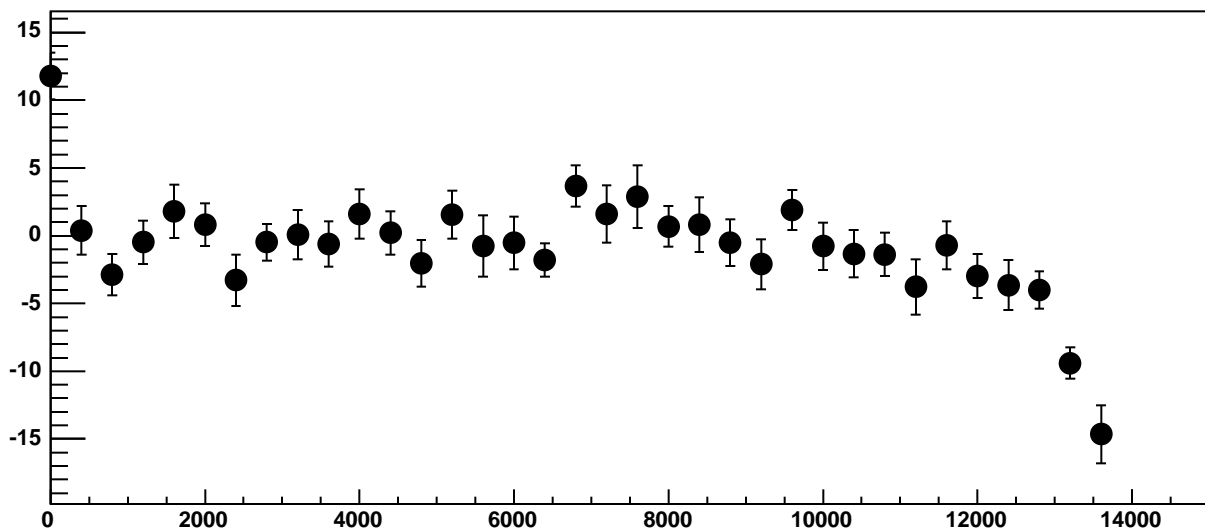


χ^2 / ndf 22.51 / 23
p0 -507 ± 0.7739
p1 0.02023 ± 0.0001165

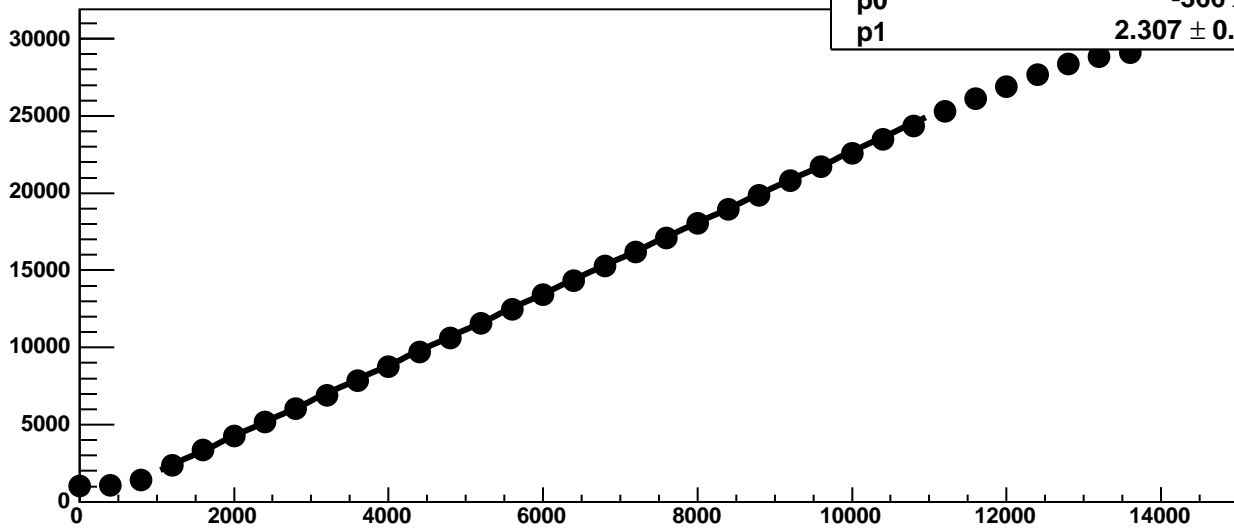
Chip 5, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

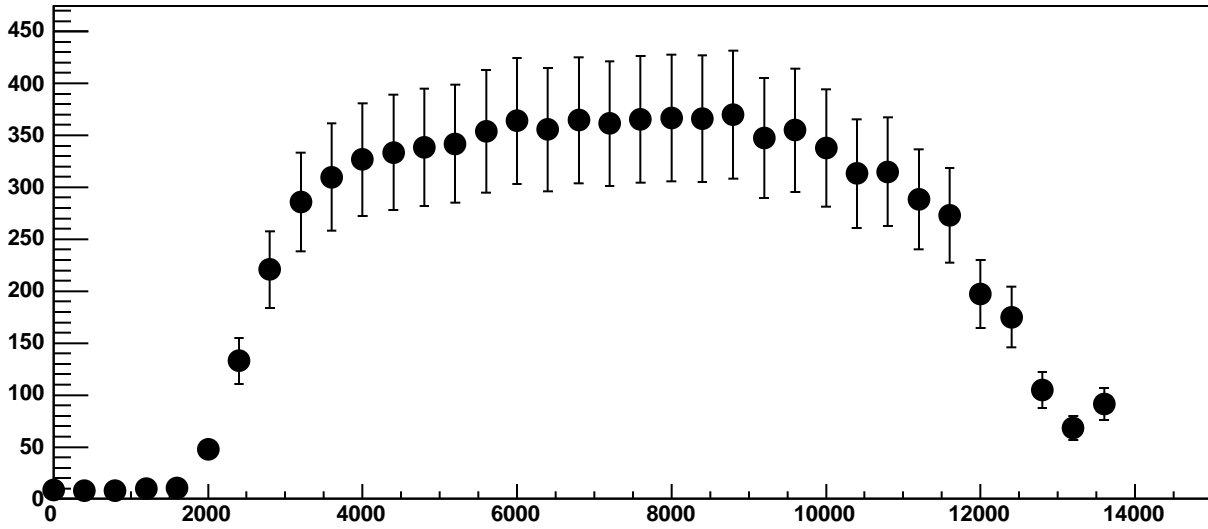


Chip 5, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC

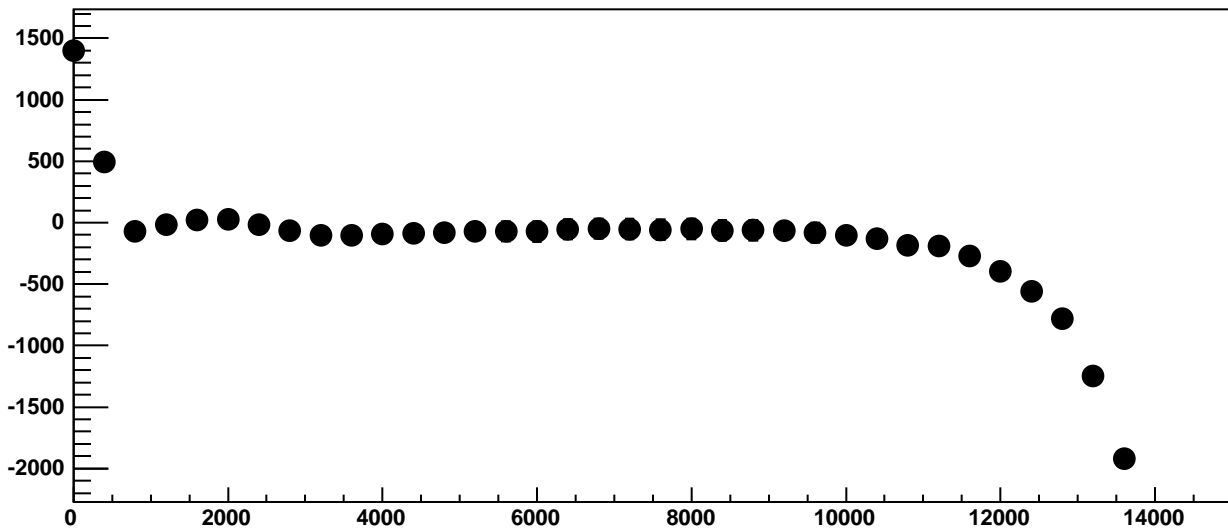


χ^2 / ndf 187.6 / 23
p0 -366 ± 4.222
p1 2.307 ± 0.002701

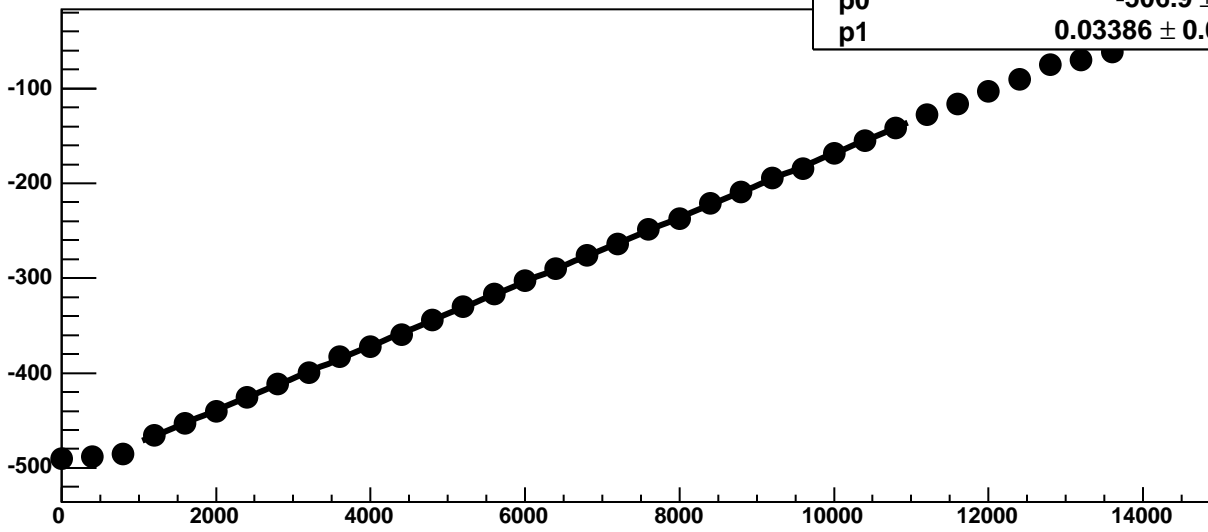
Chip 5, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC

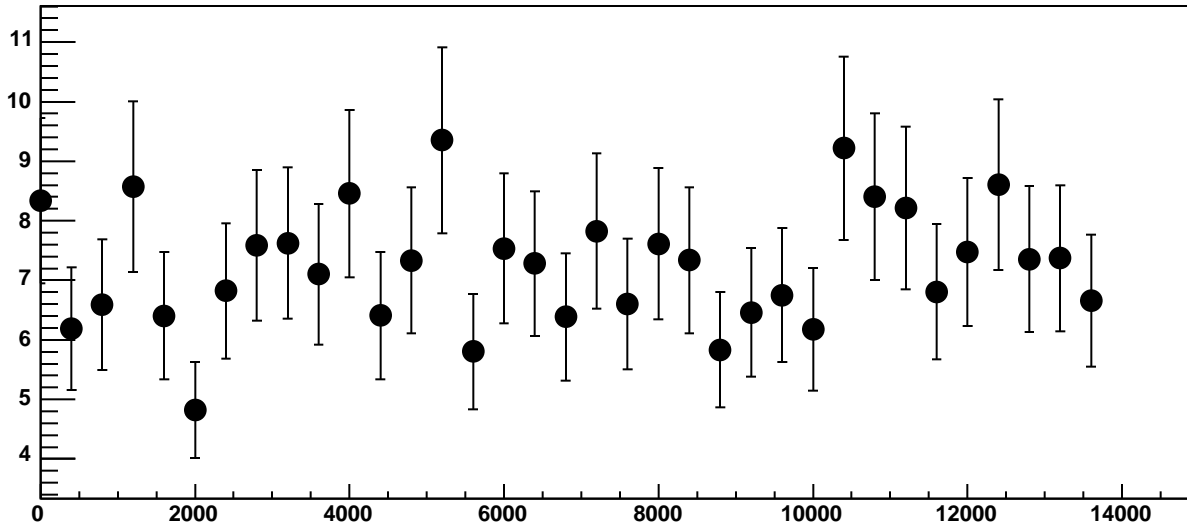


Chip 5, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

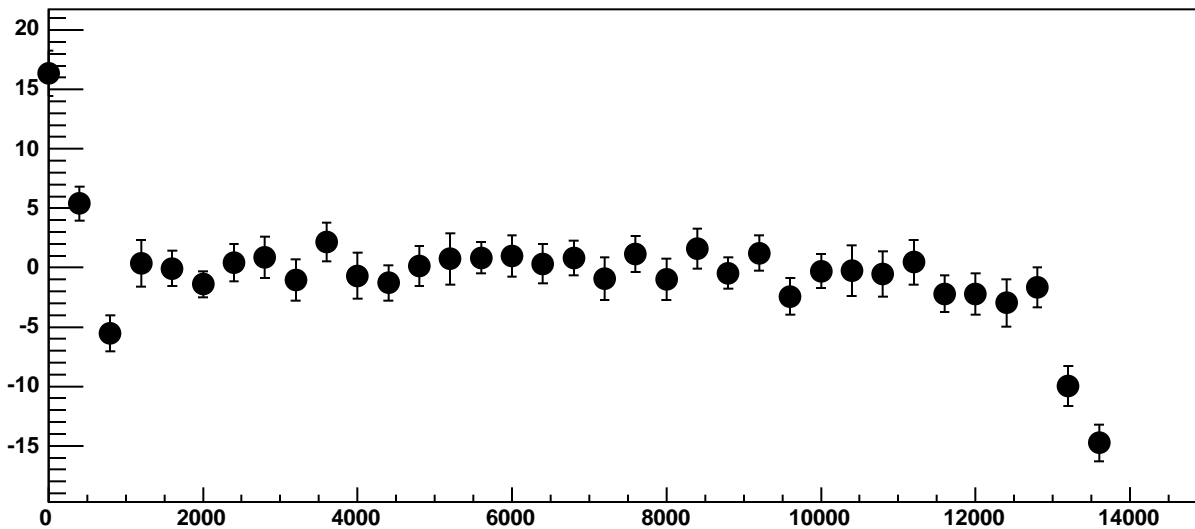


χ^2 / ndf 11.66 / 23
p0 -506.9 ± 0.7224
p1 0.03386 ± 0.0001101

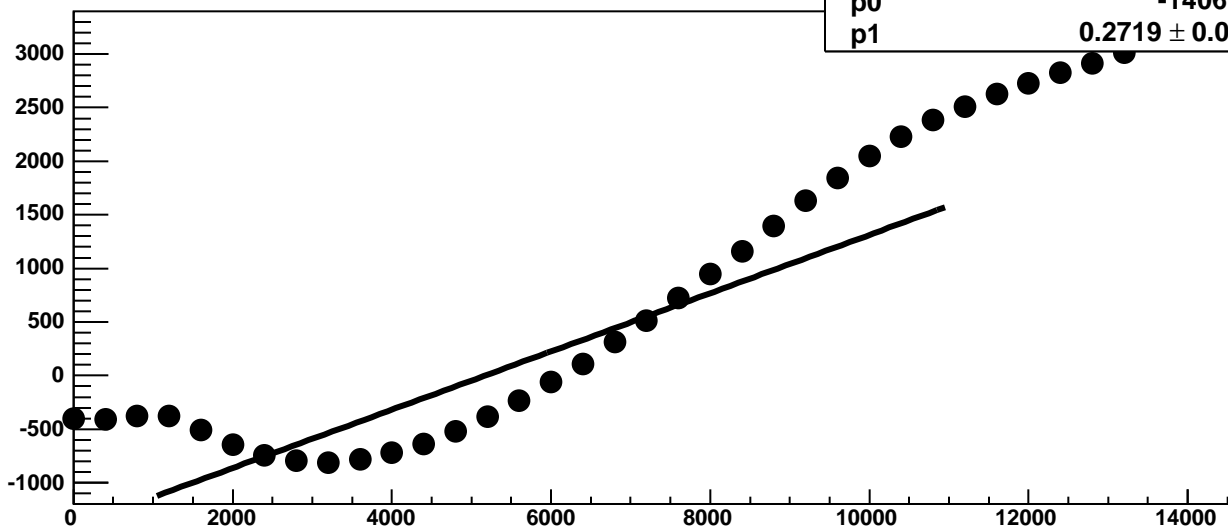
Chip 5, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



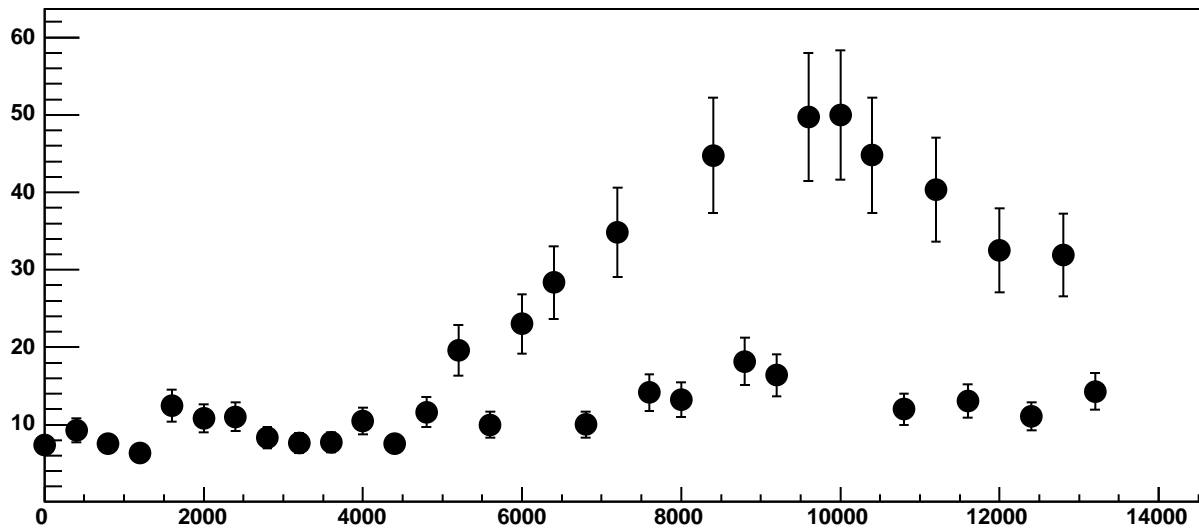
Chip 5, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



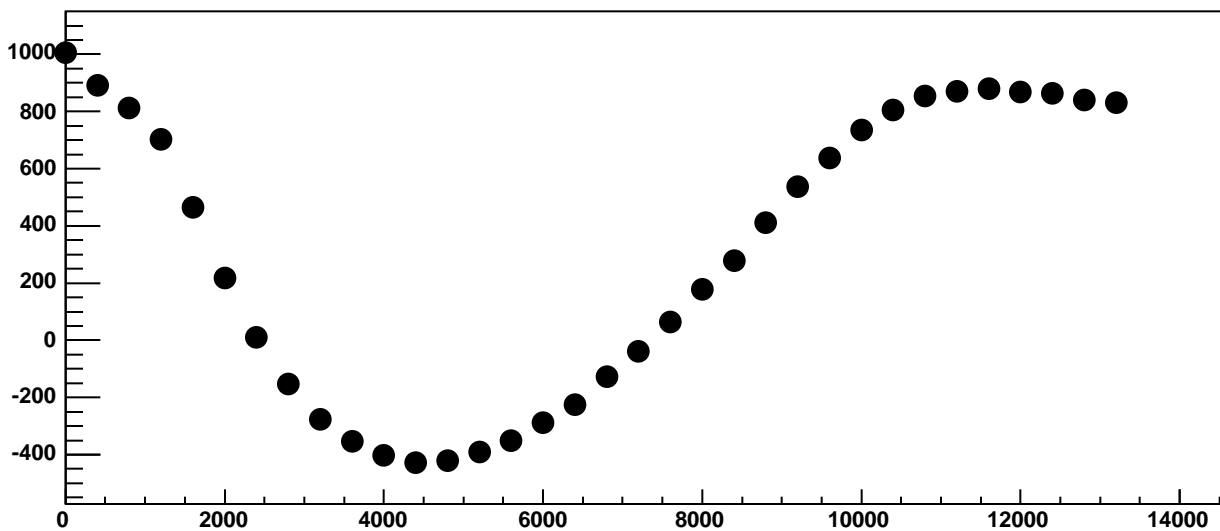
Chip 5, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



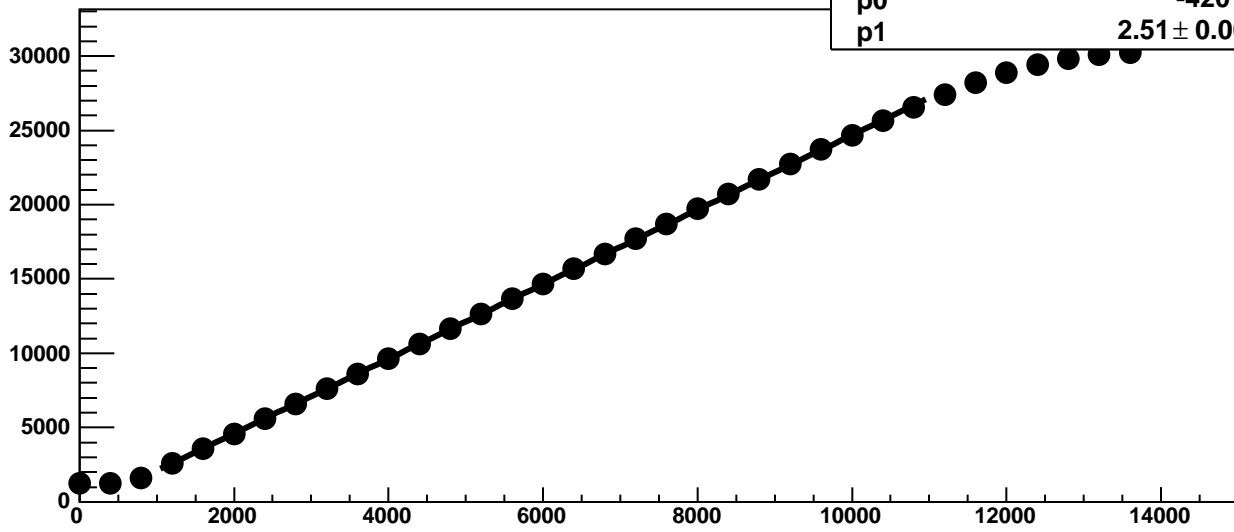
Chip 5, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



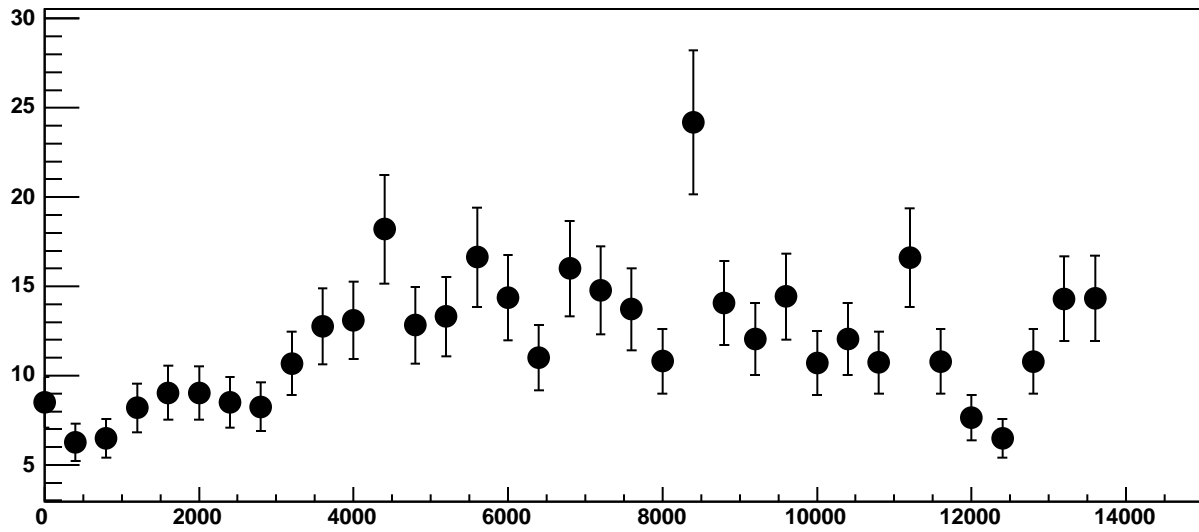
Chip 5, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



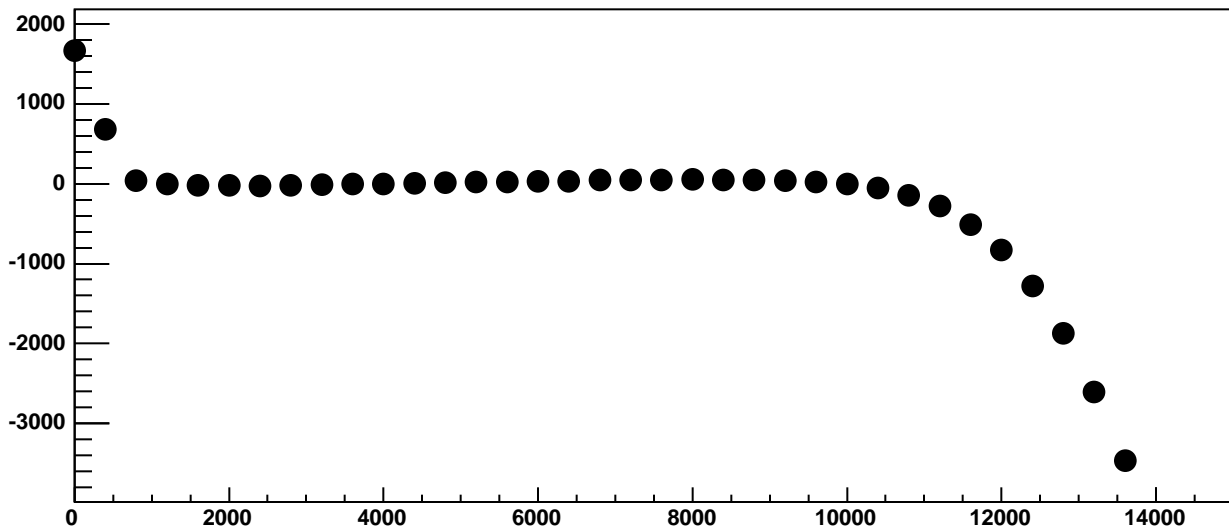
Chip 5, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC



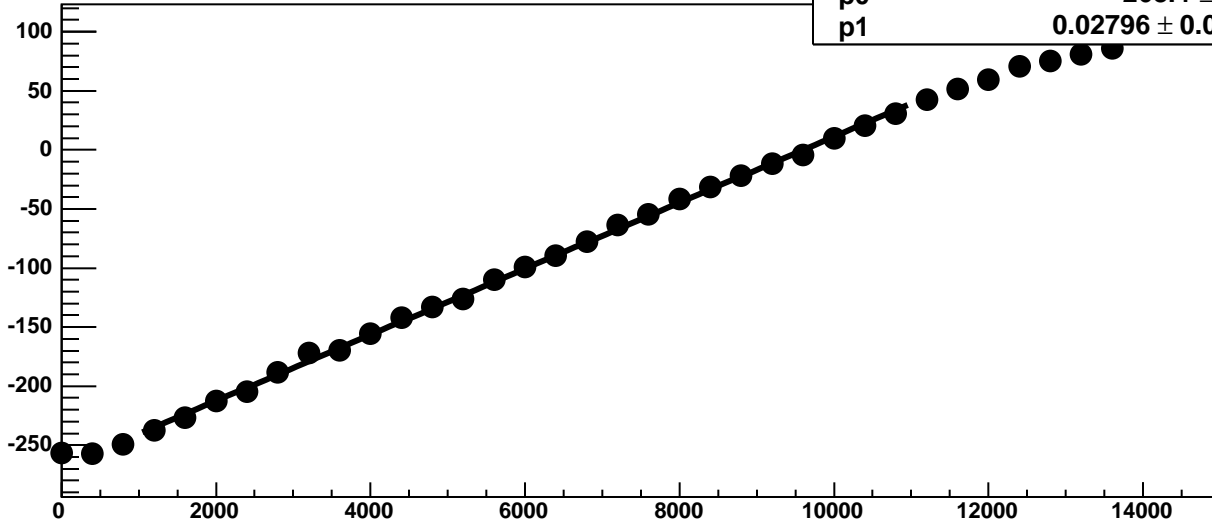
Chip 5, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC

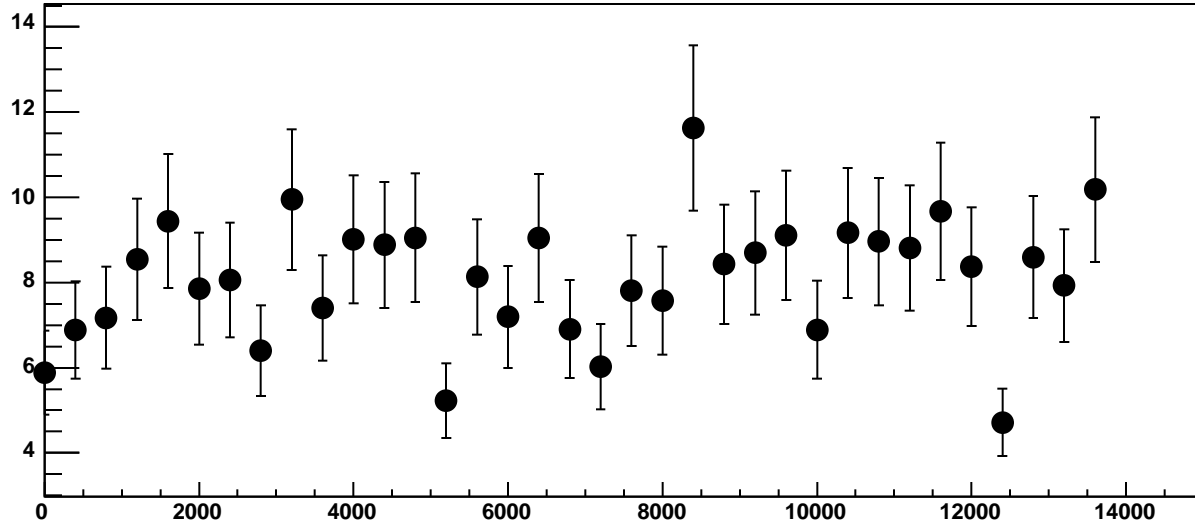


Chip 5, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

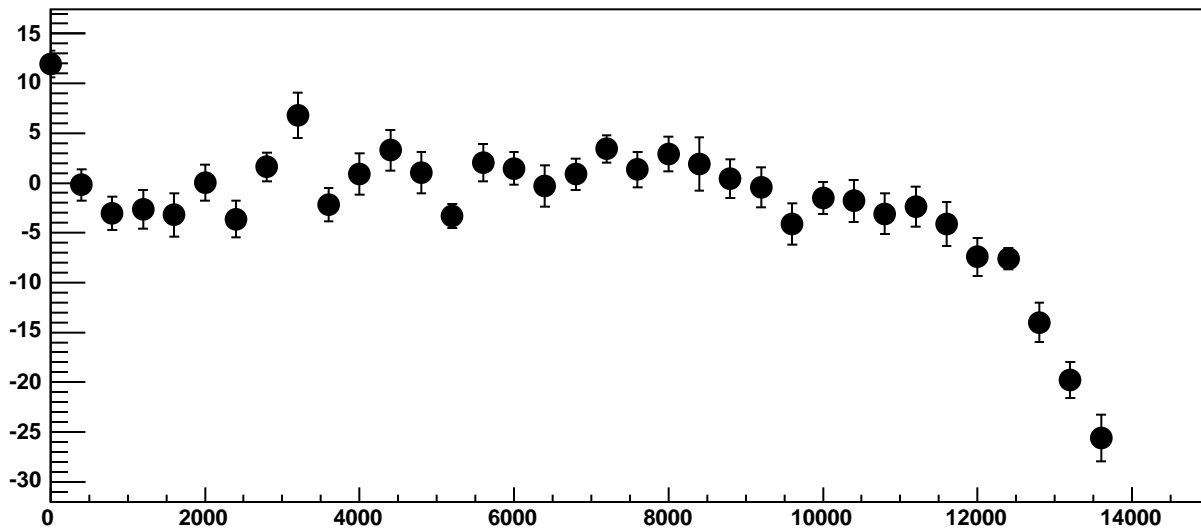


χ^2 / ndf 50.27 / 23
p0 -268.4 ± 0.8634
p1 0.02796 ± 0.0001321

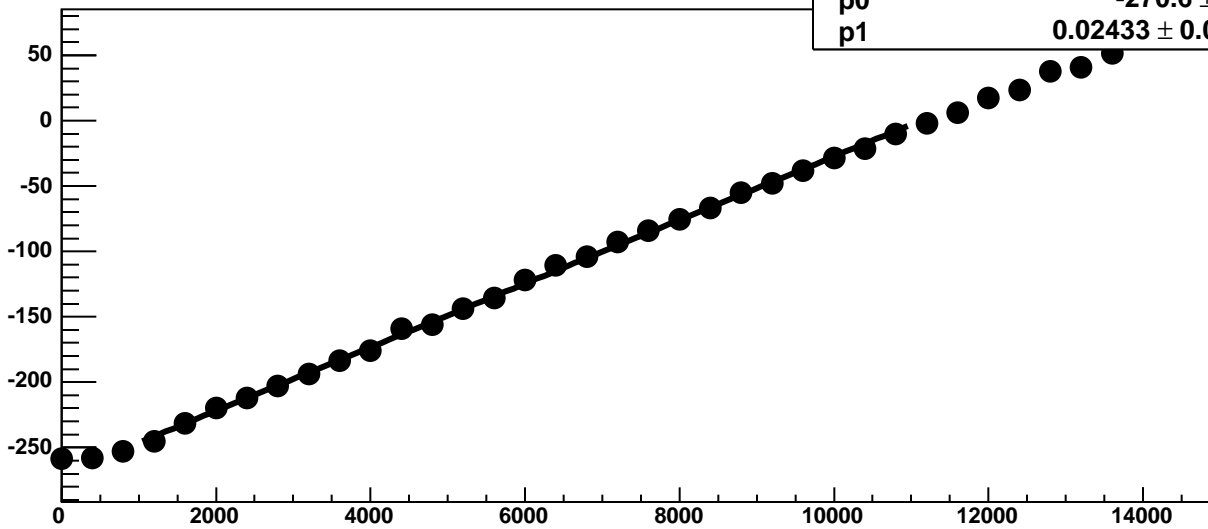
Chip 5, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

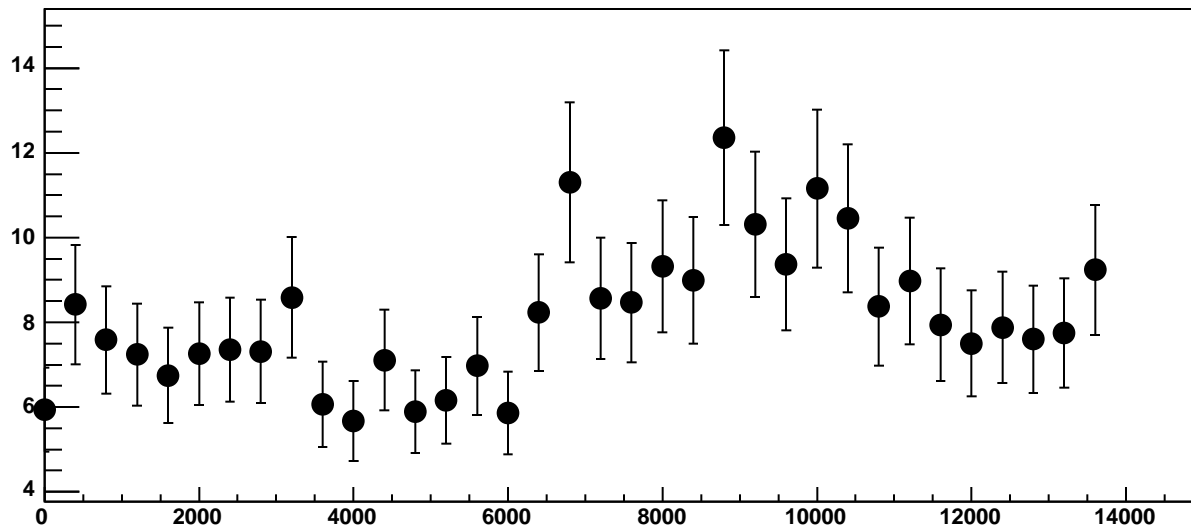


Chip 5, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

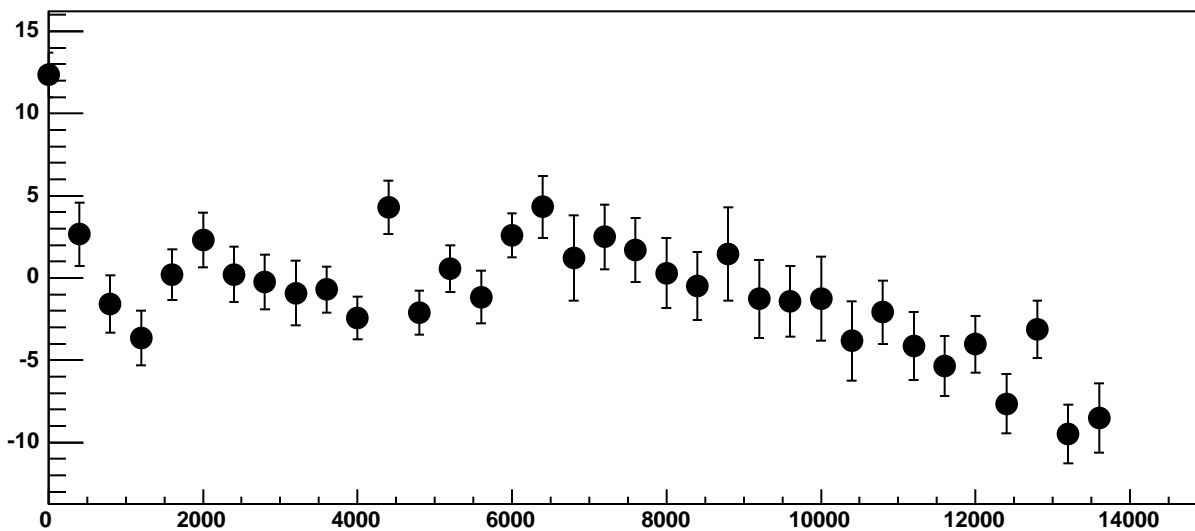


χ^2 / ndf 37.49 / 23
p0 -270.6 ± 0.7825
p1 0.02433 ± 0.0001331

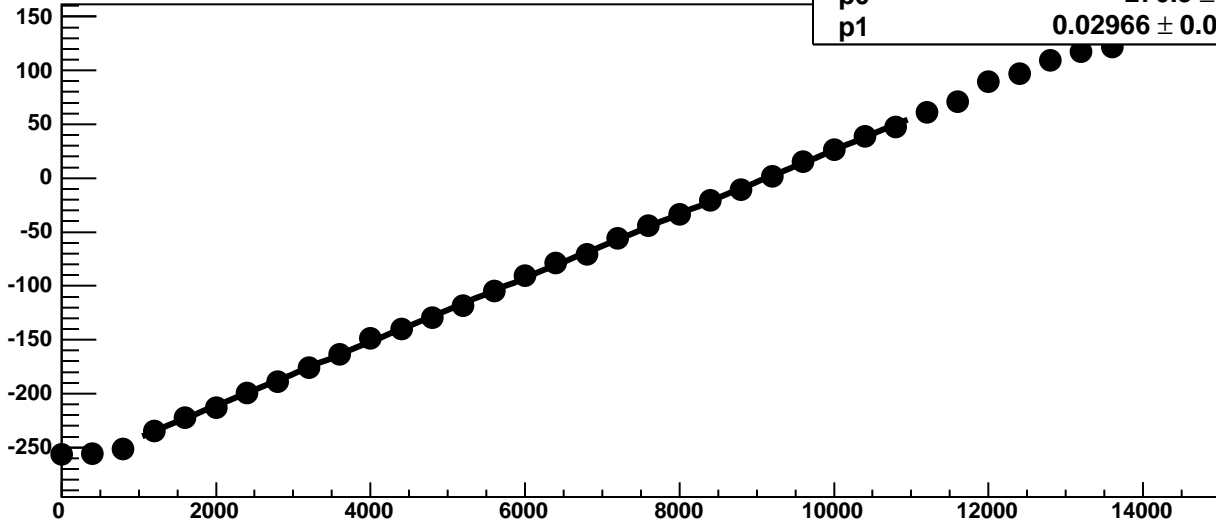
Chip 5, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



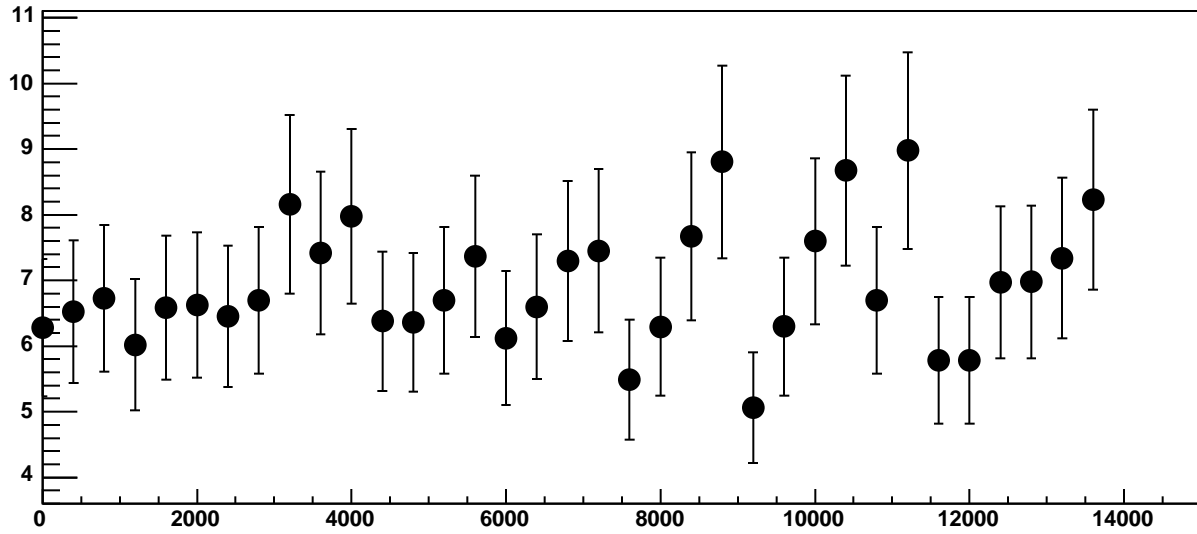
Chip 5, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



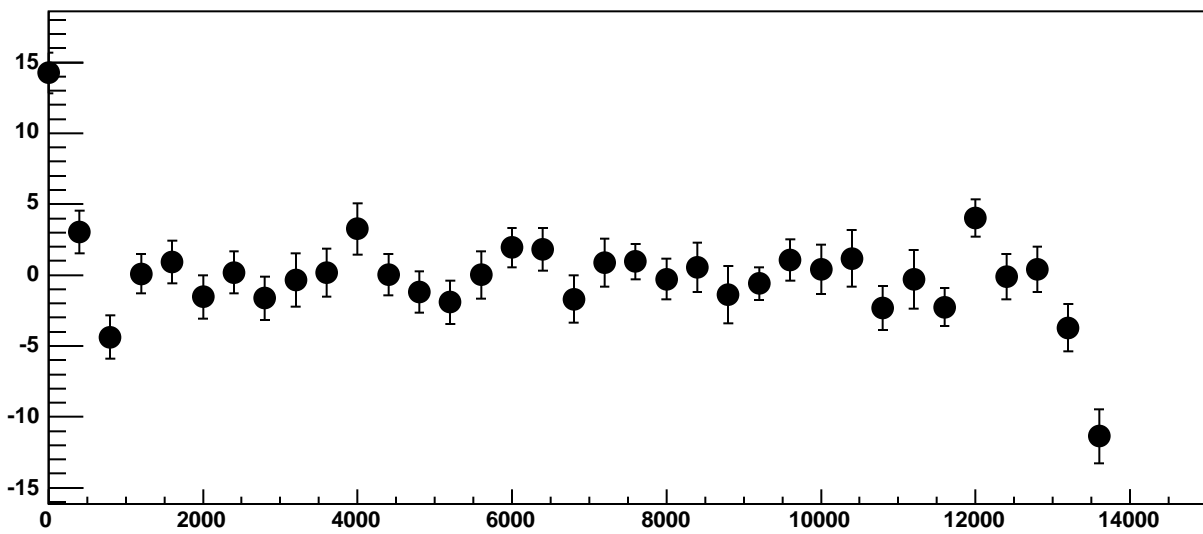
Chip 5, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC



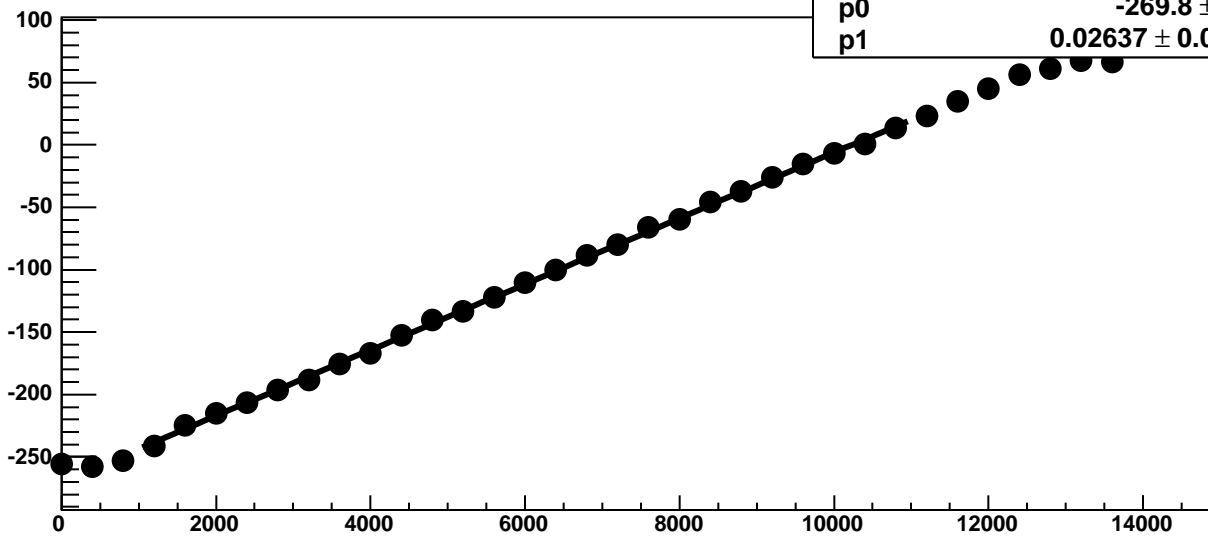
Chip 5, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



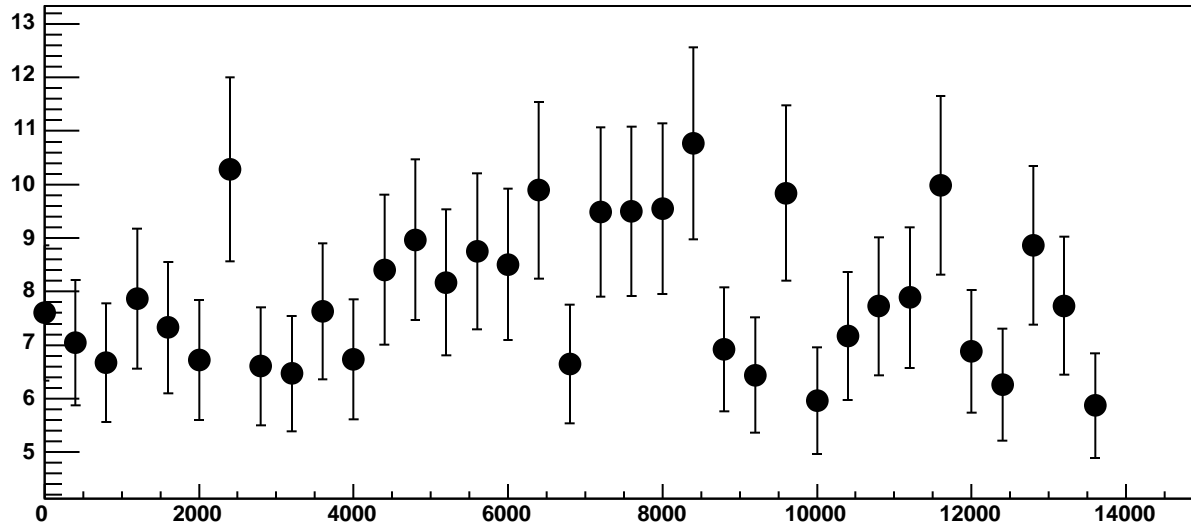
Chip 5, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC



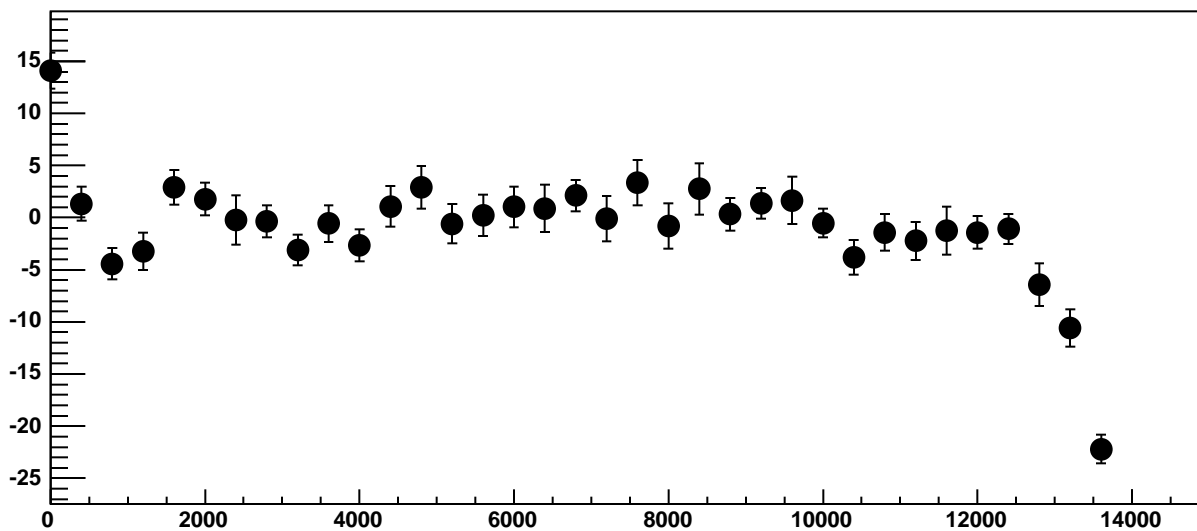
Chip 5, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



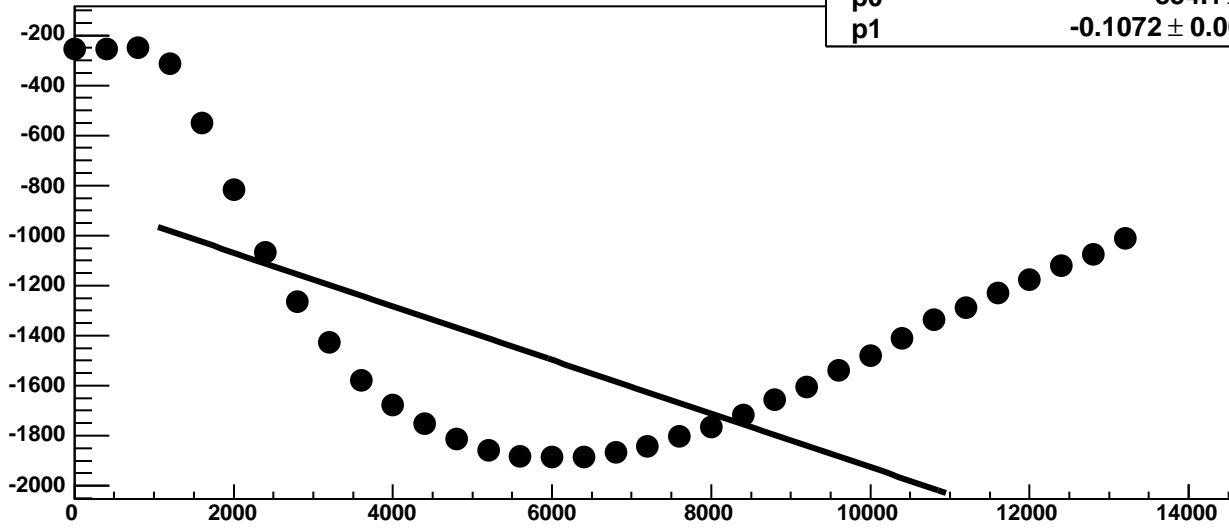
Chip 5, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

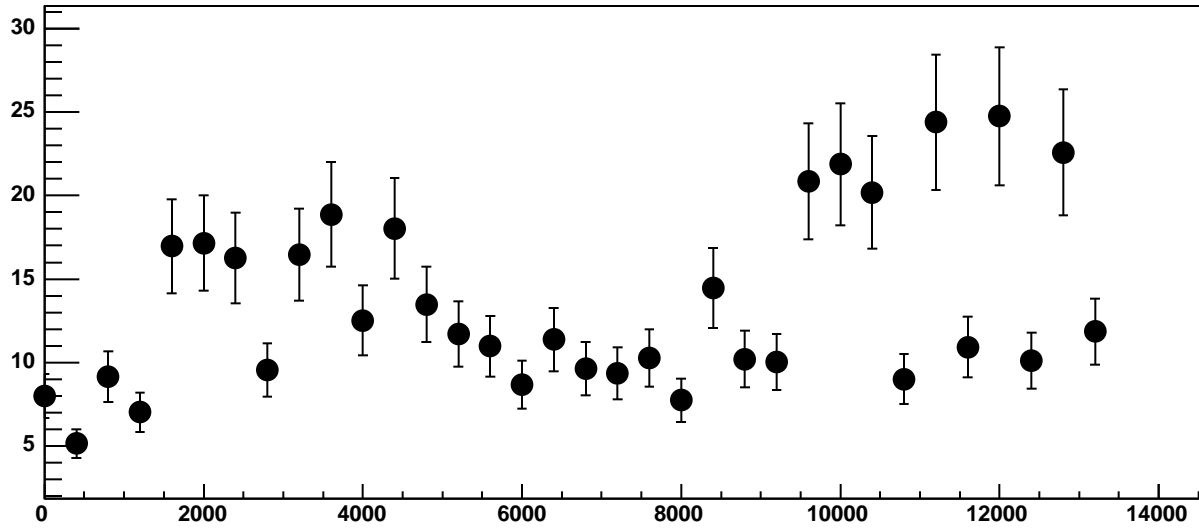


Chip 5, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

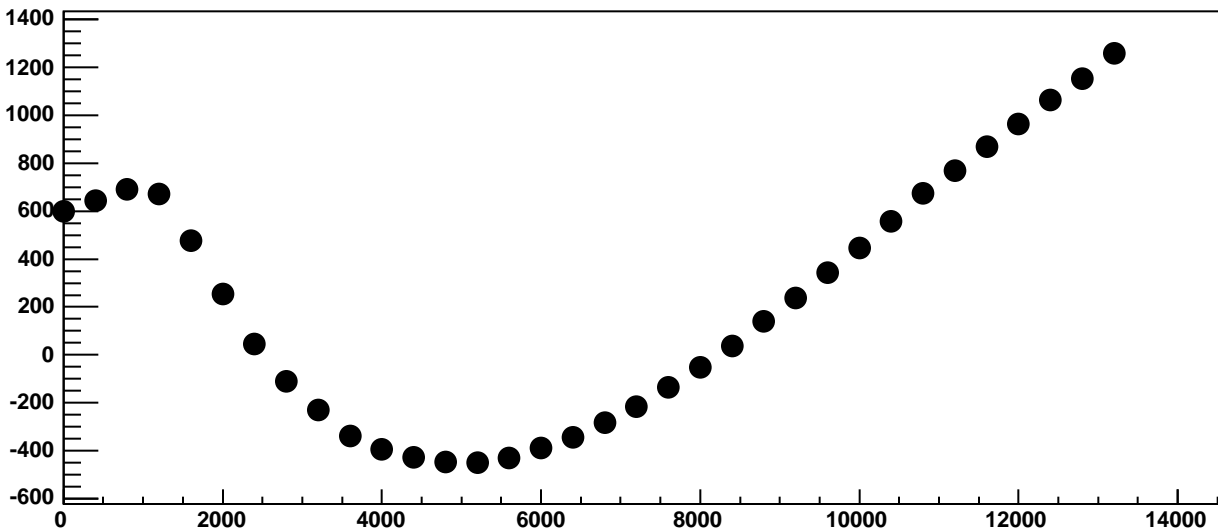


χ^2 / ndf 5.469e+05 / 23
p0 -854.1 ± 1.225
p1 -0.1072 ± 0.0001838

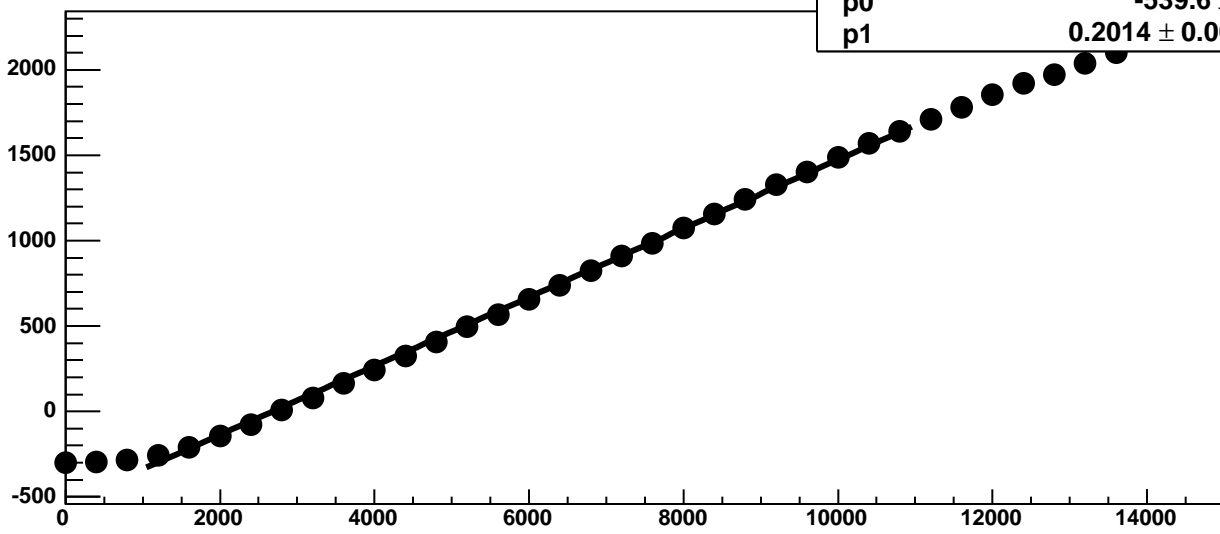
Chip 5, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



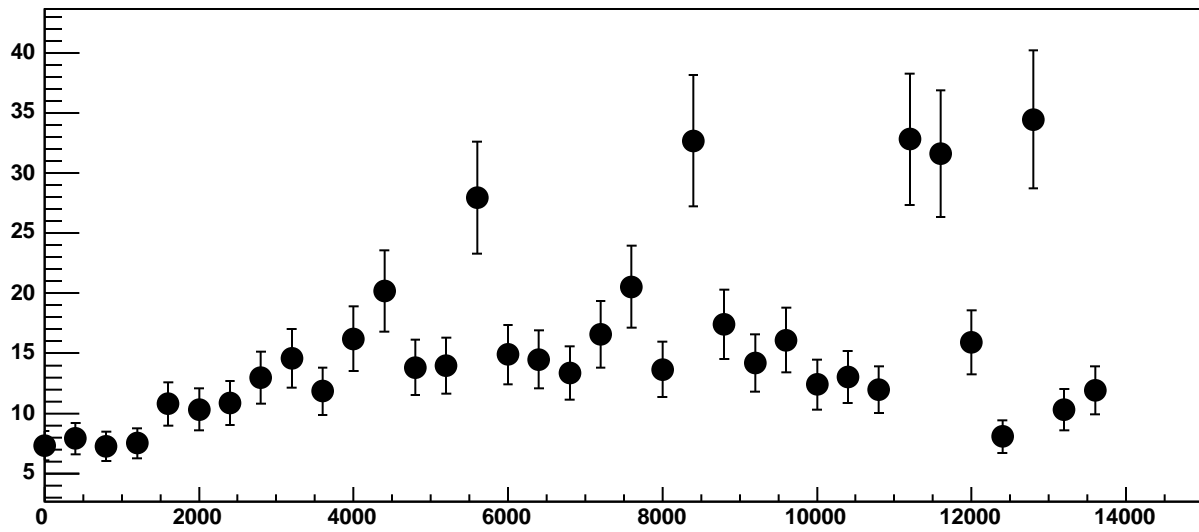
Chip 5, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



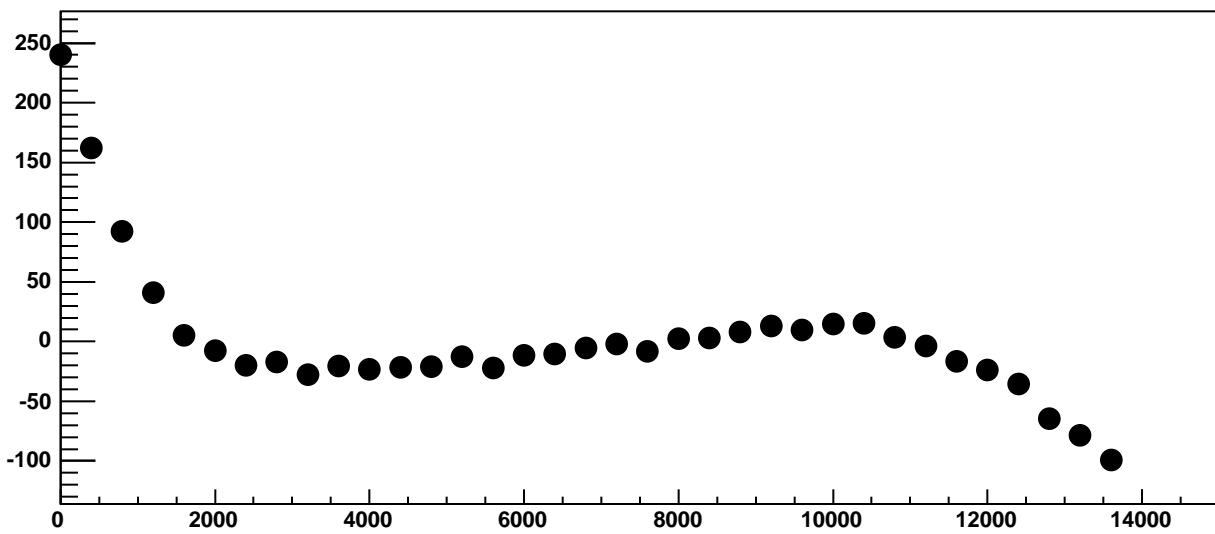
Chip 5, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC



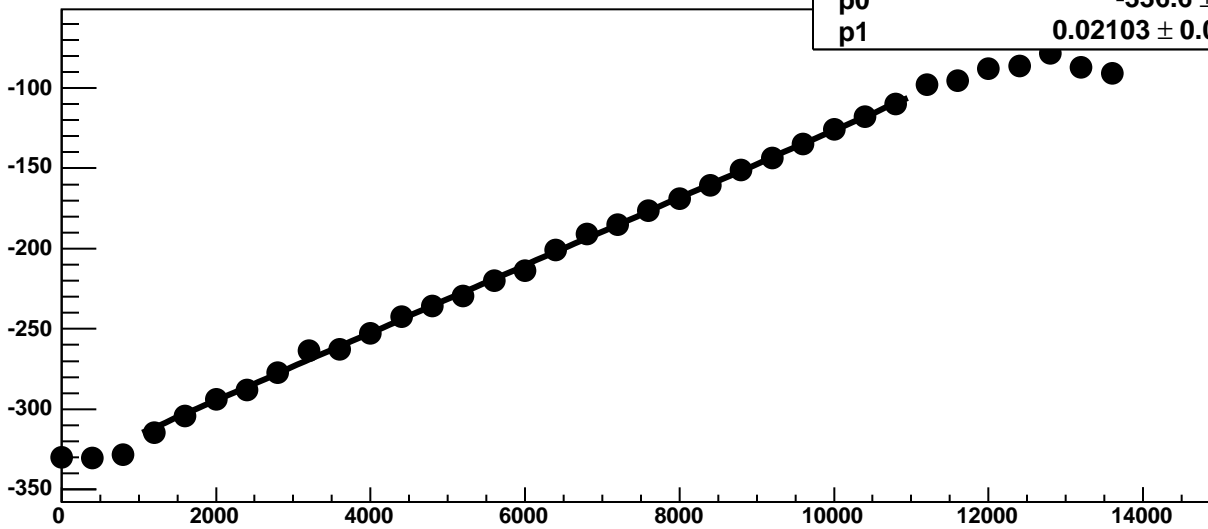
Chip 5, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



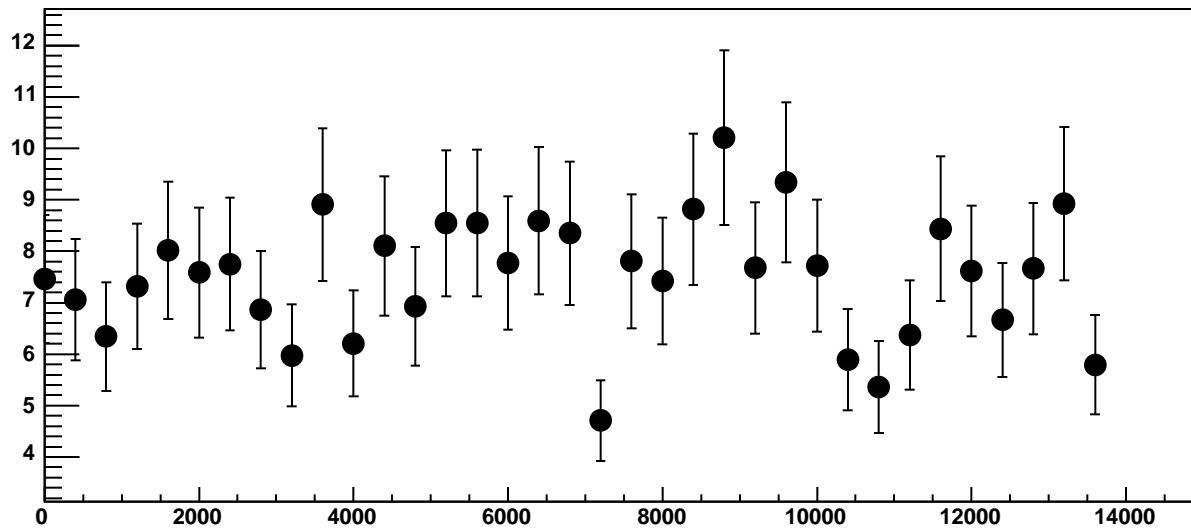
Chip 5, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC



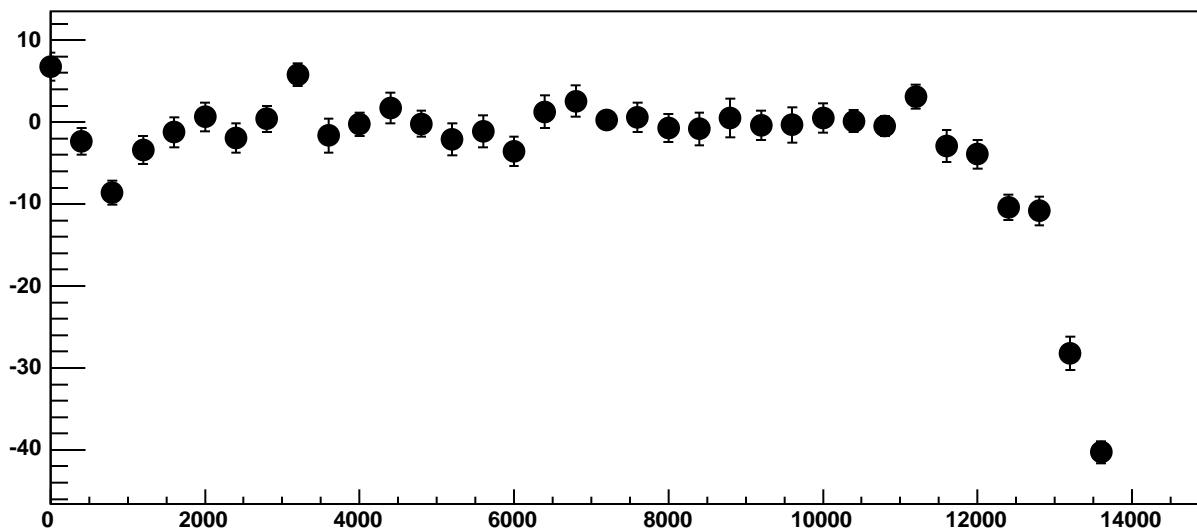
Chip 5, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



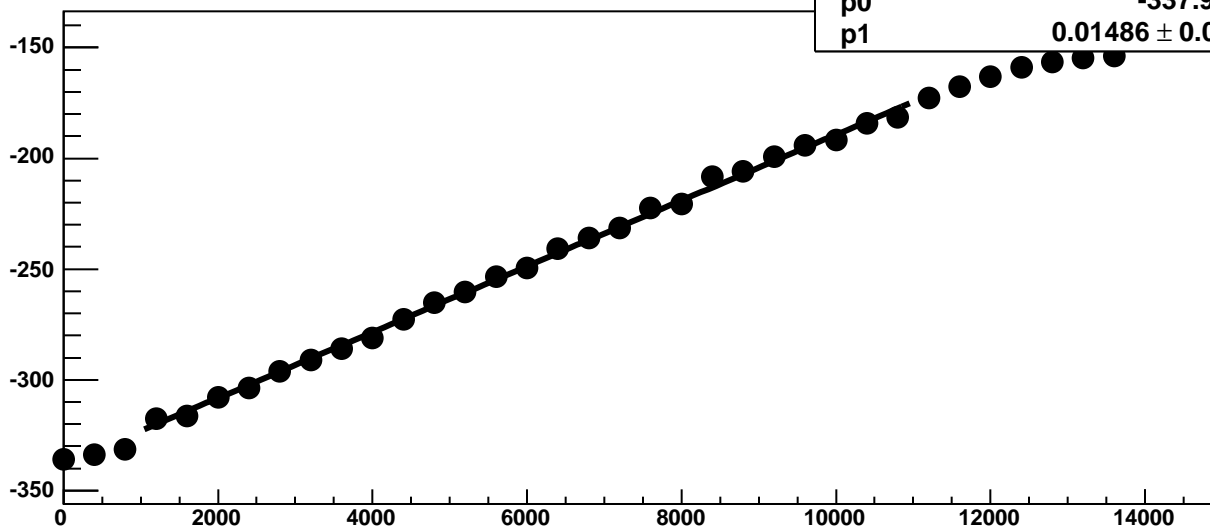
Chip 5, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

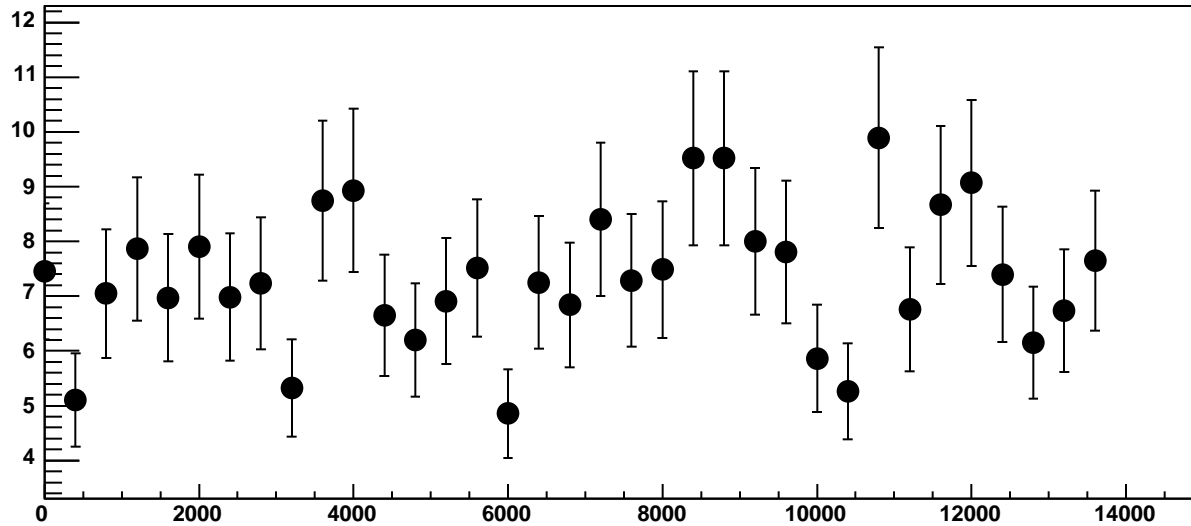


Chip 5, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

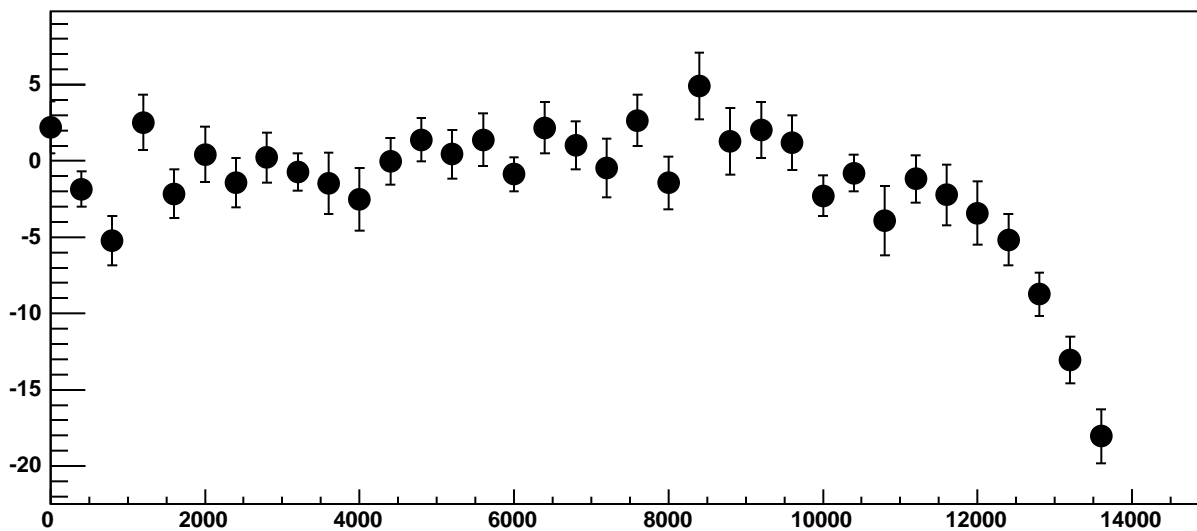


χ^2 / ndf 28.14 / 23
p0 -337.9 ± 0.751
p1 0.01486 ± 0.0001136

Chip 5, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

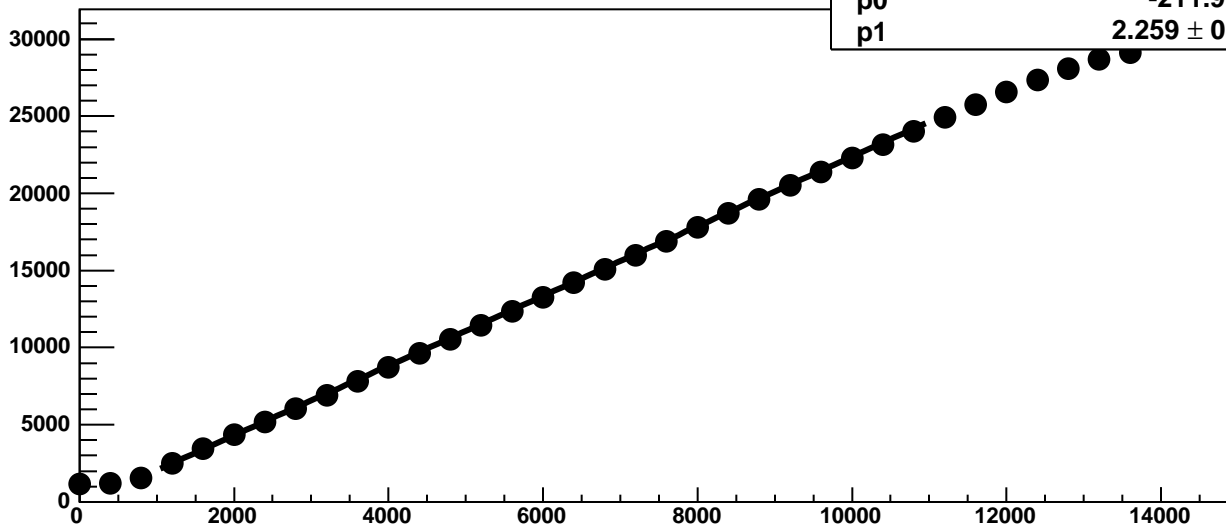


Chip 5, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

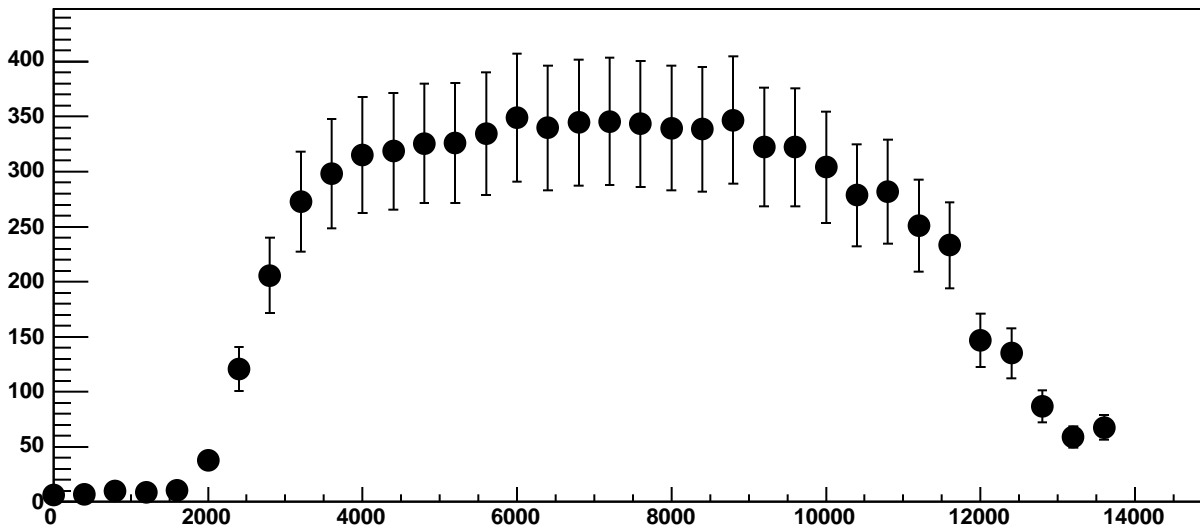


Chip 5, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC

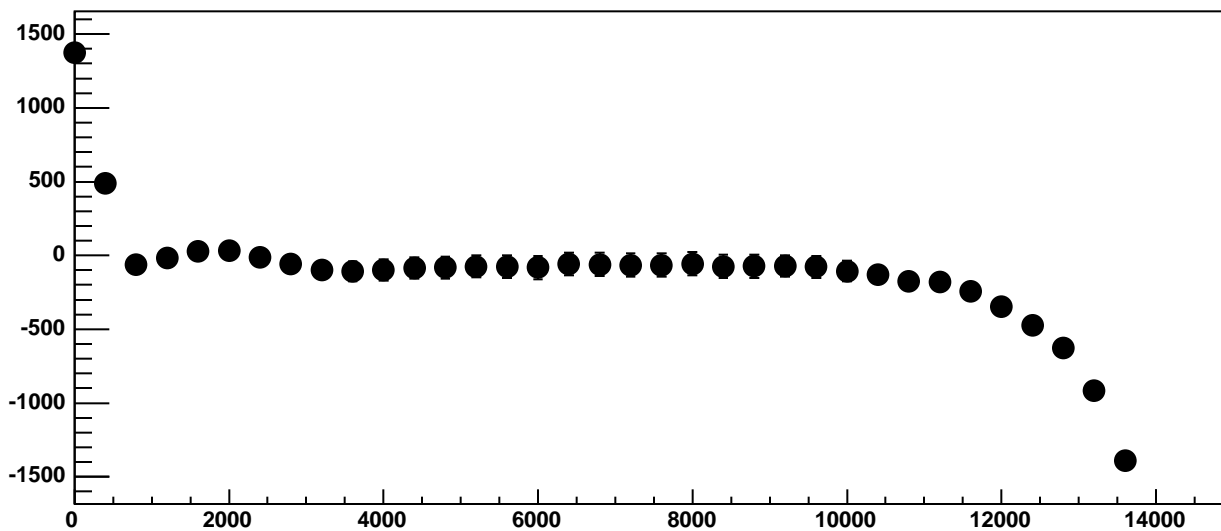
χ^2 / ndf 237.5 / 23
p0 -211.9 ± 3.827
p1 2.259 ± 0.002472



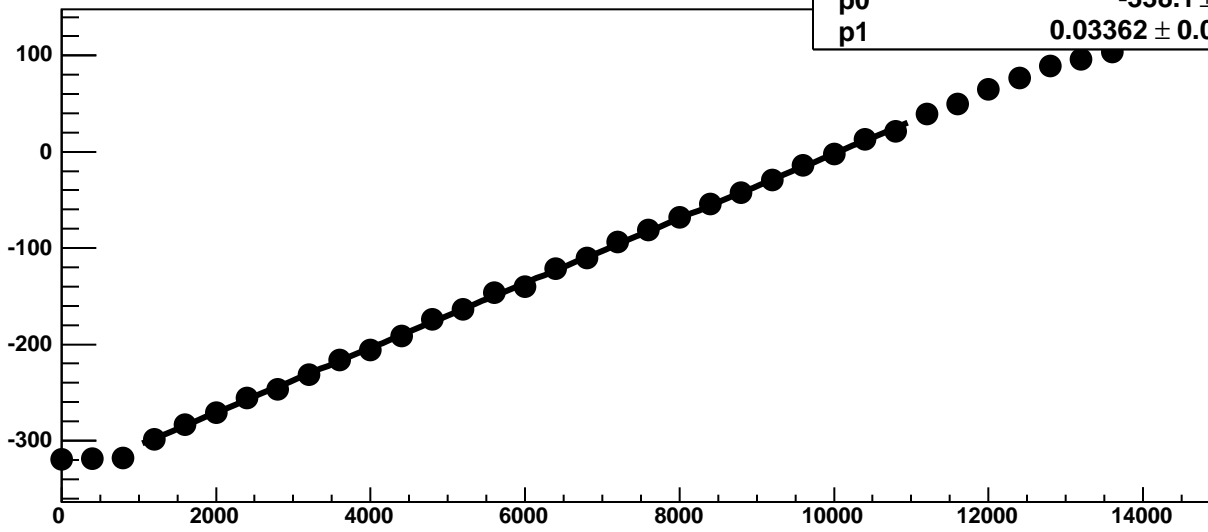
Chip 5, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC

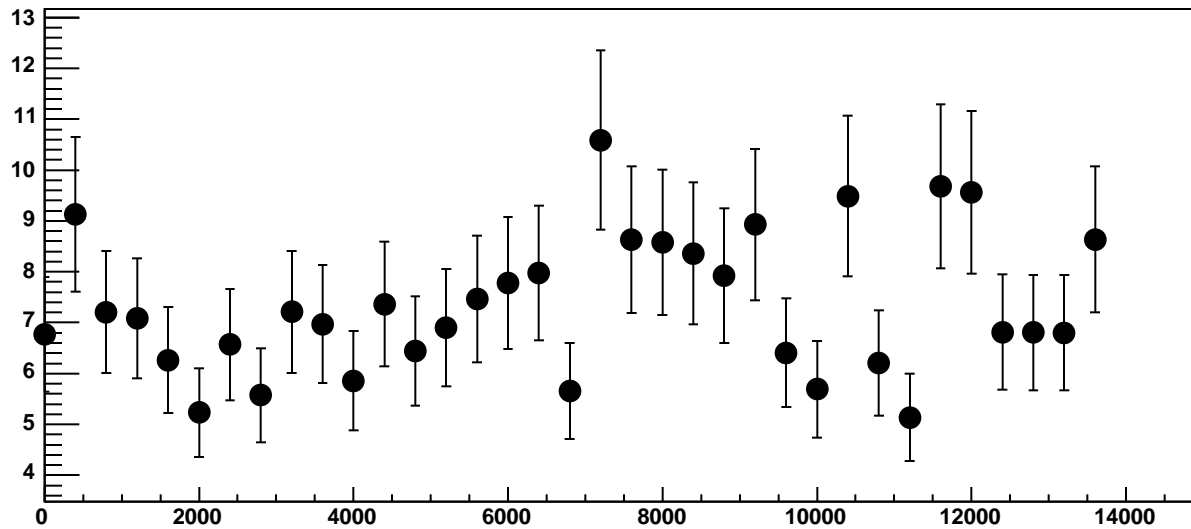


Chip 5, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

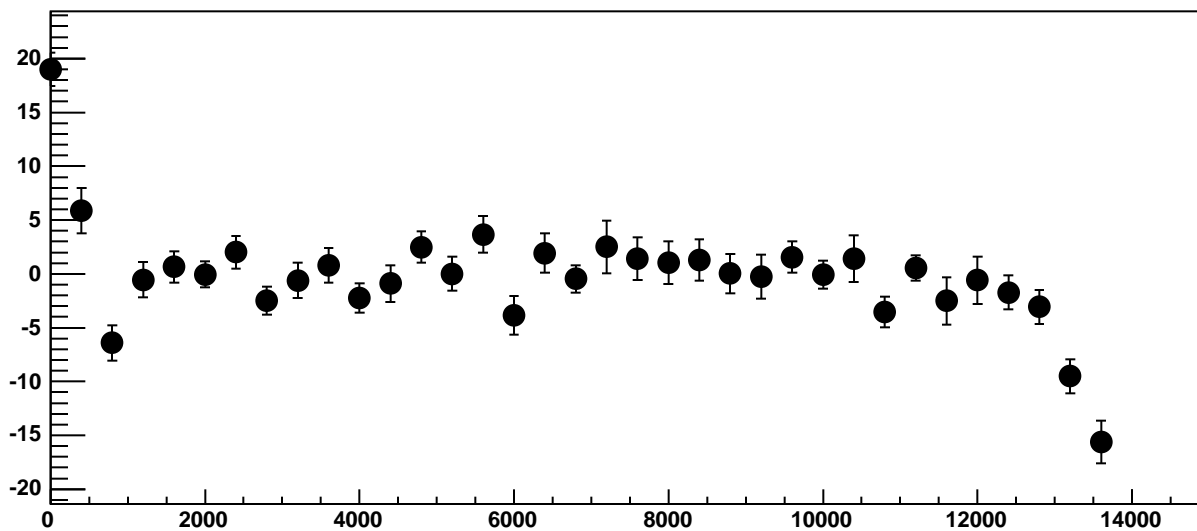


χ^2 / ndf 32.76 / 23
p0 -338.1 ± 0.6776
p1 0.03362 ± 0.0001062

Chip 5, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

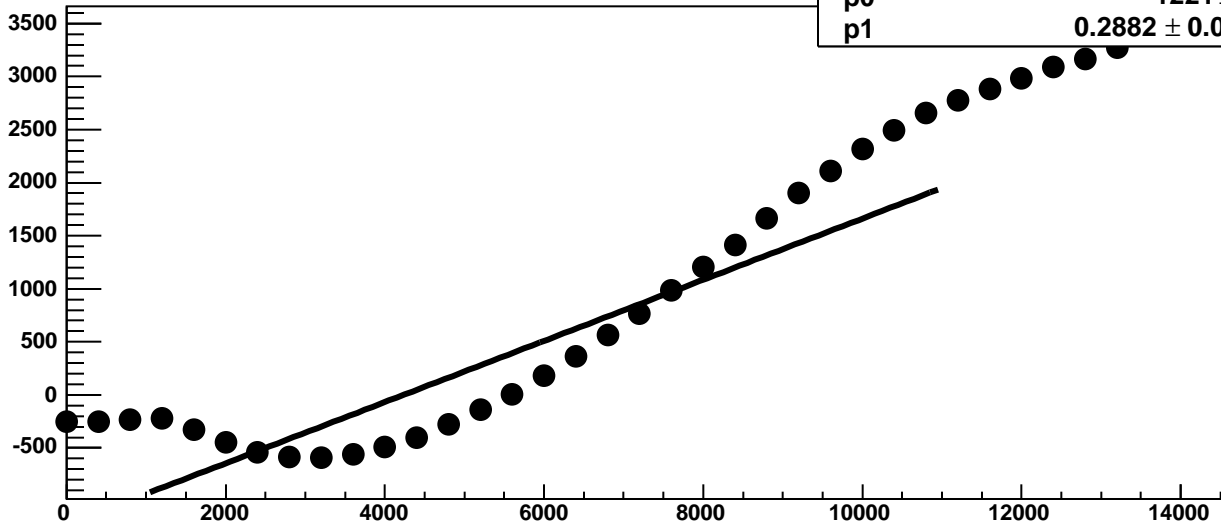


Chip 5, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

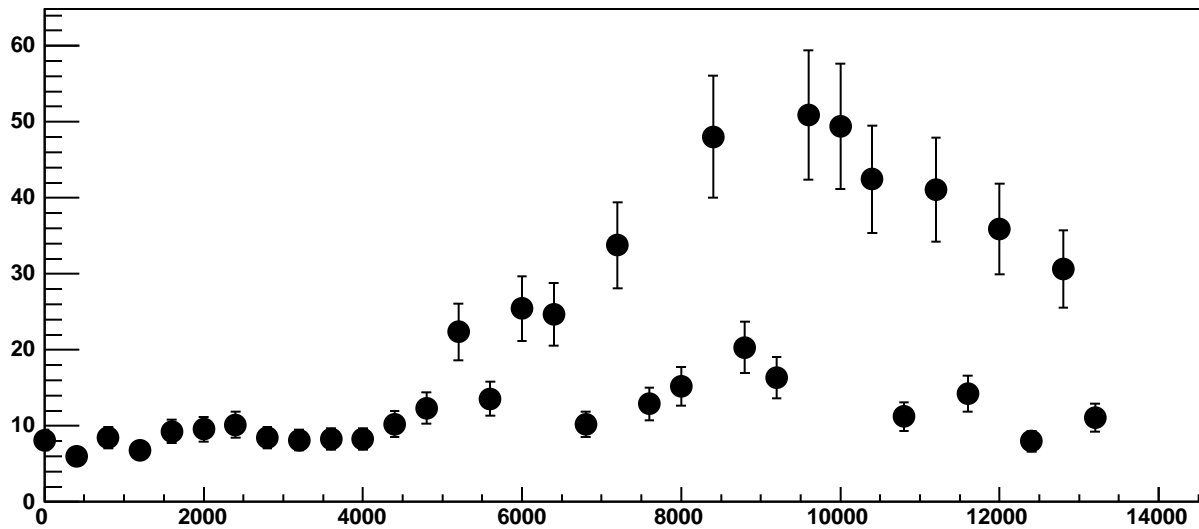


Chip 5, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

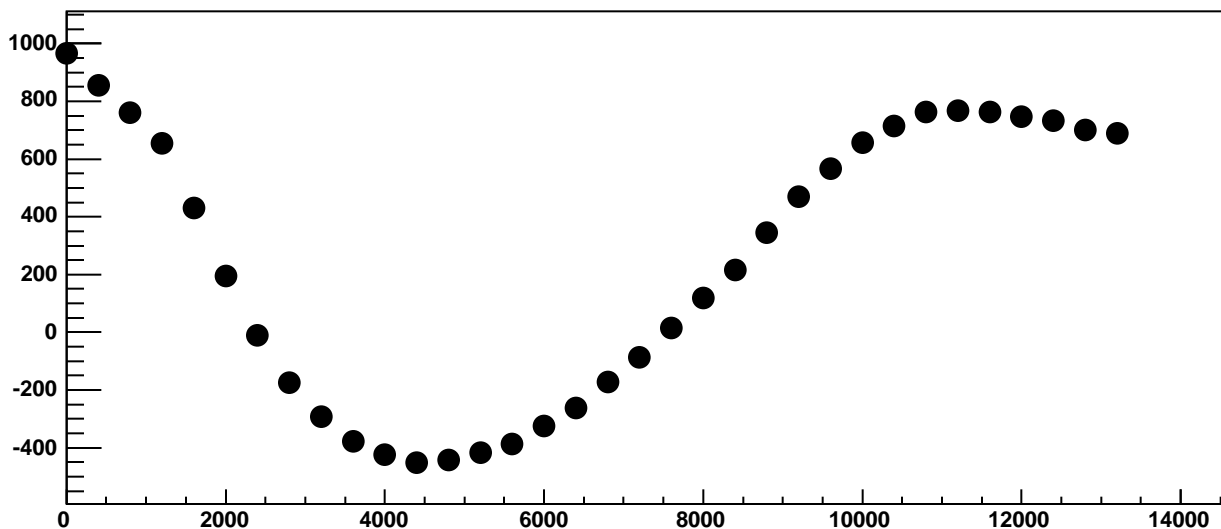
χ^2 / ndf 5.611e+05 / 23
p0 -1221 ± 1.037
p1 0.2882 ± 0.0002111



Chip 5, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

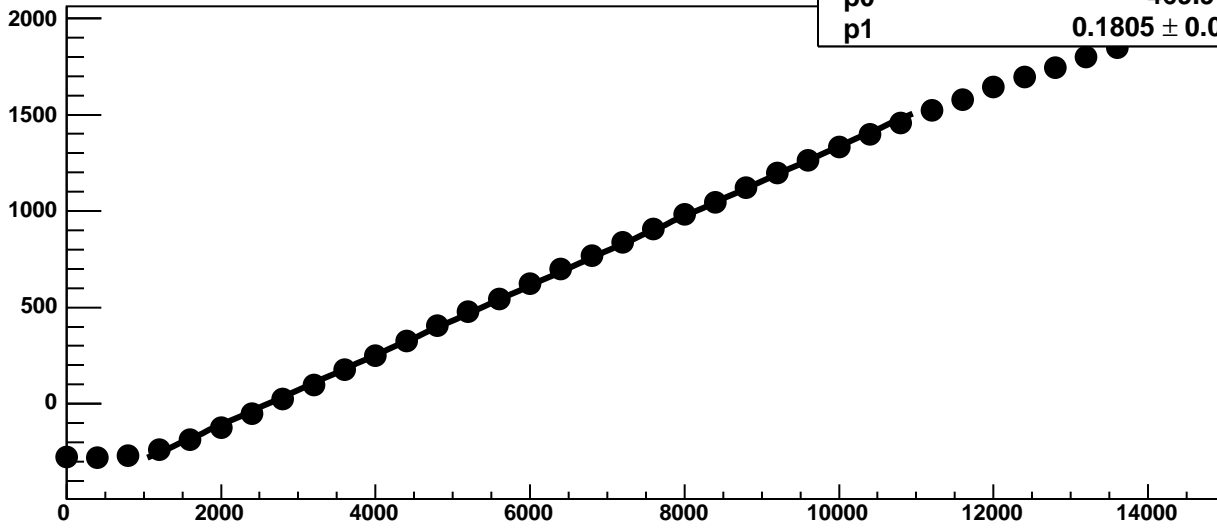


Chip 5, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

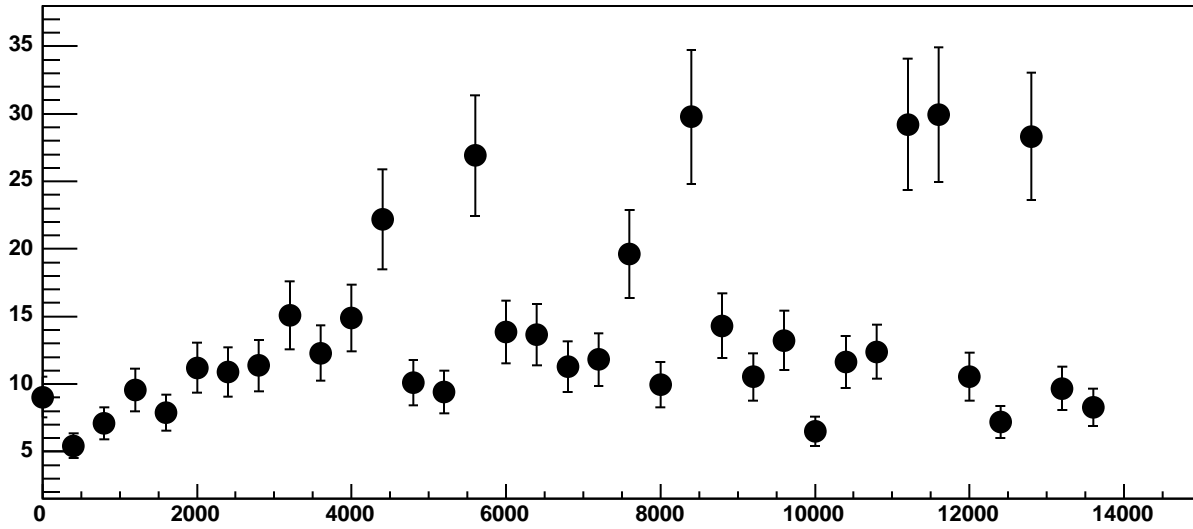


Chip 5, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

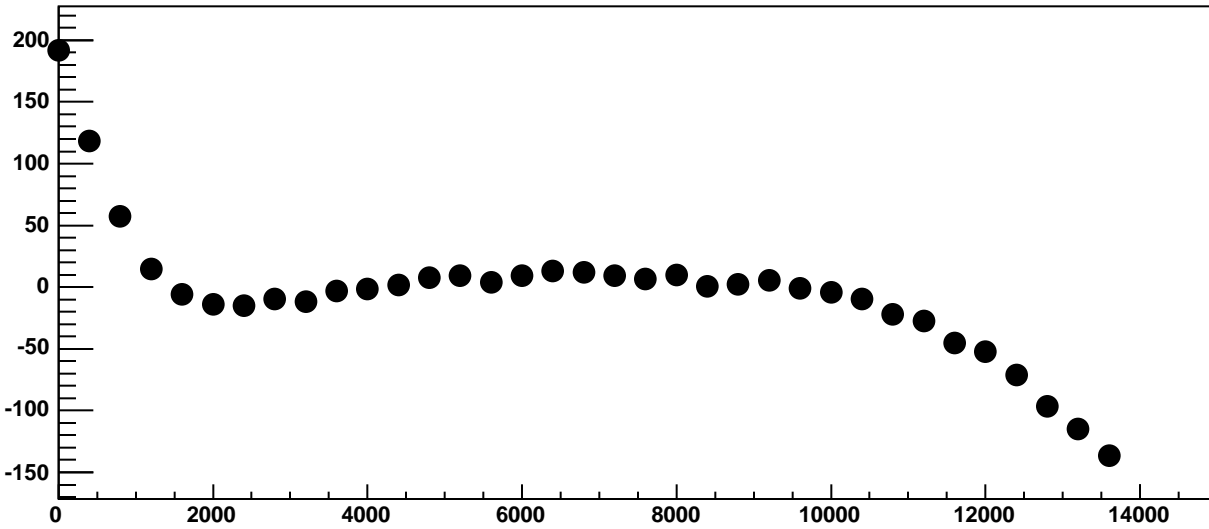
χ^2 / ndf 349.1 / 23
p0 -469.9 ± 1.114
p1 0.1805 ± 0.0001641



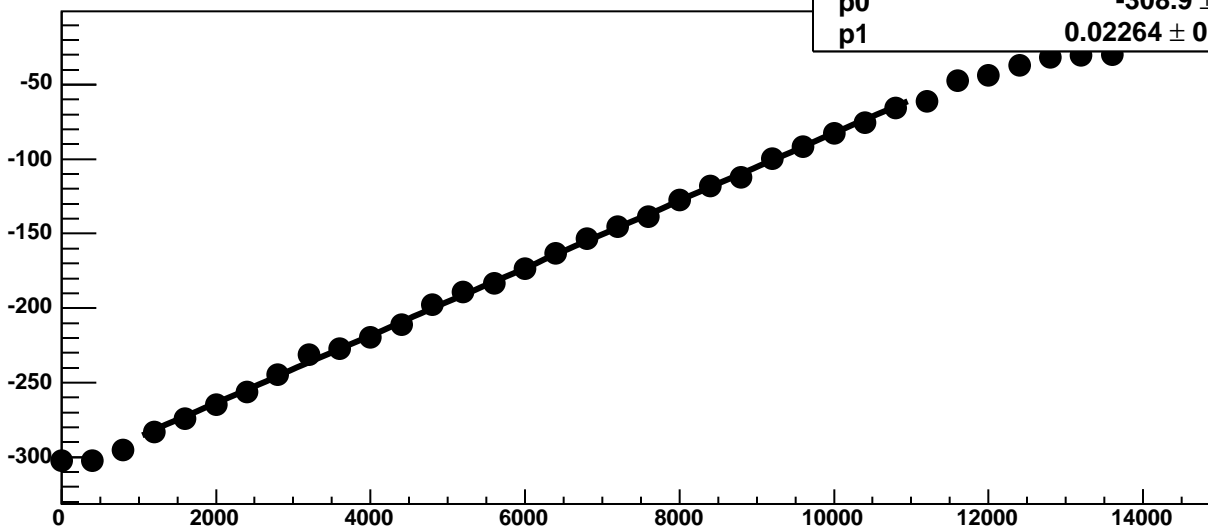
Chip 5, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



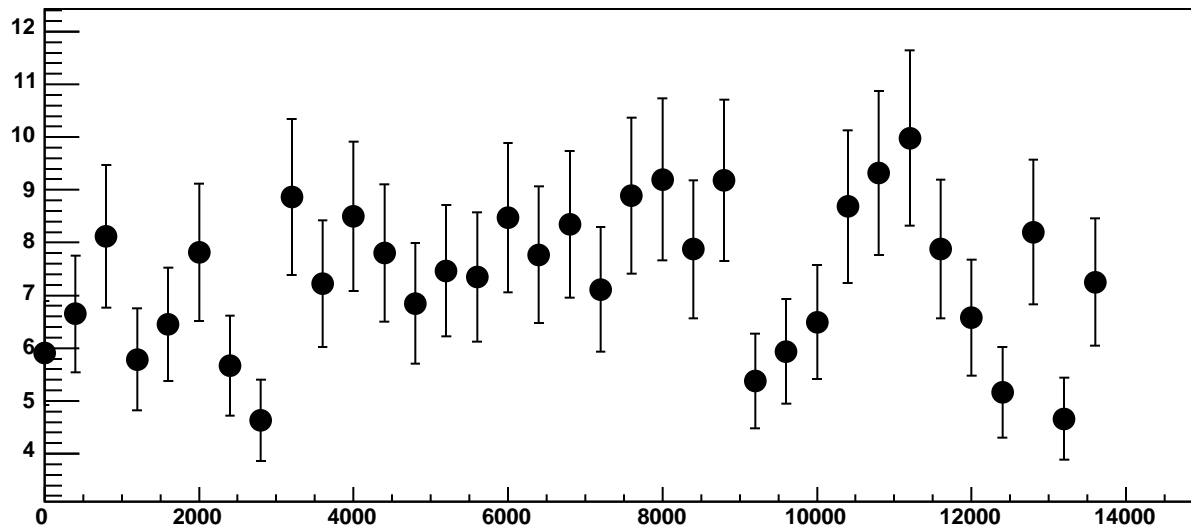
Chip 5, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



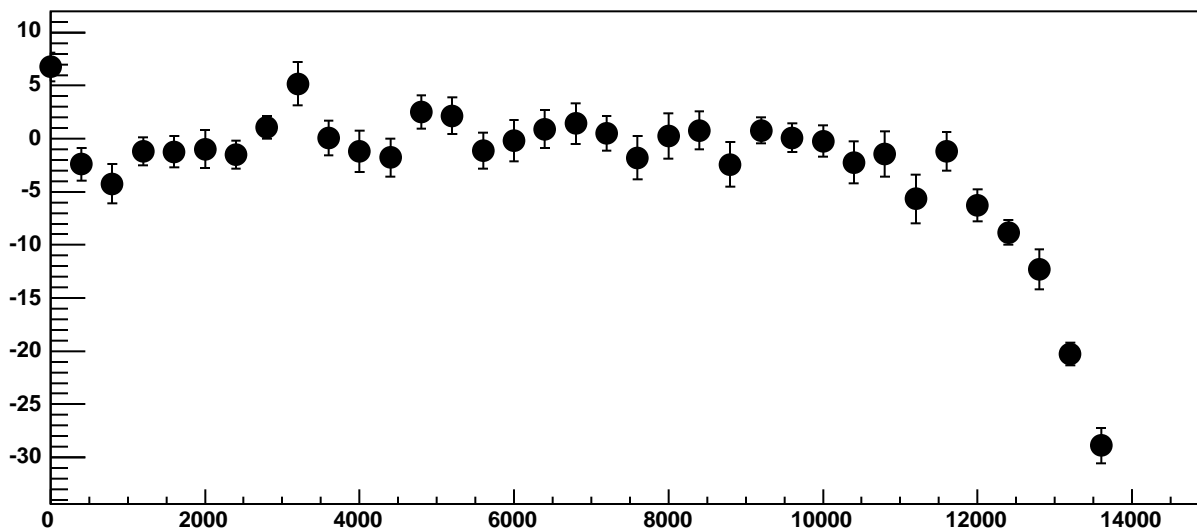
Chip 5, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC



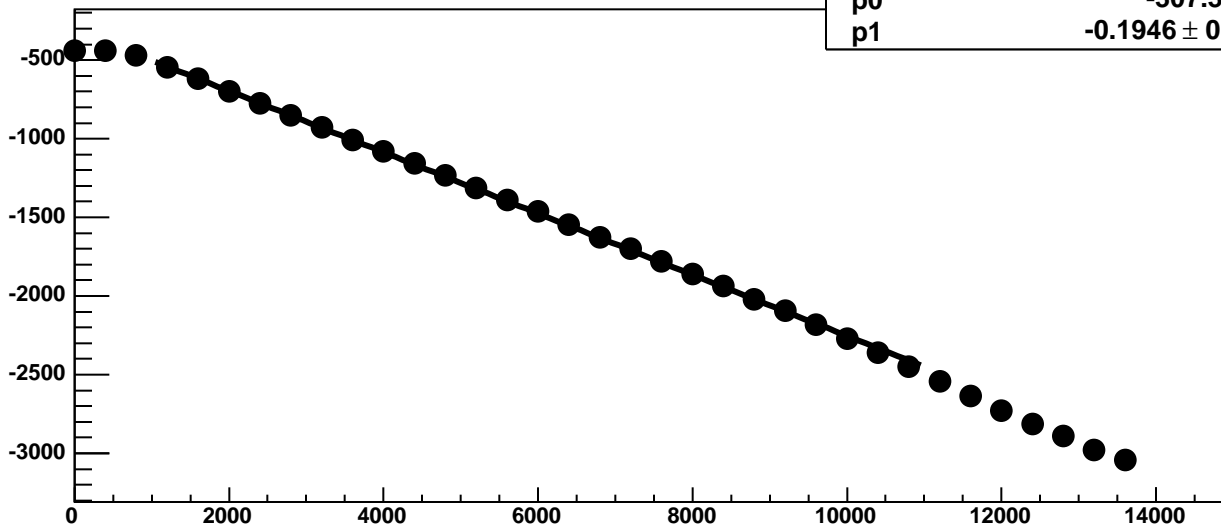
Chip 5, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC

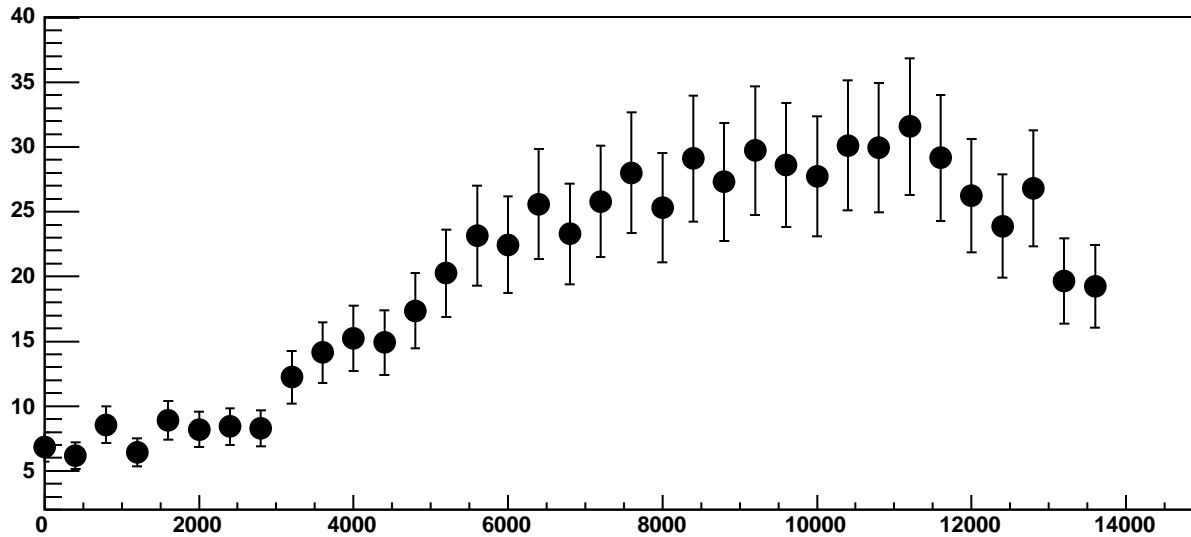


Chip 5, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

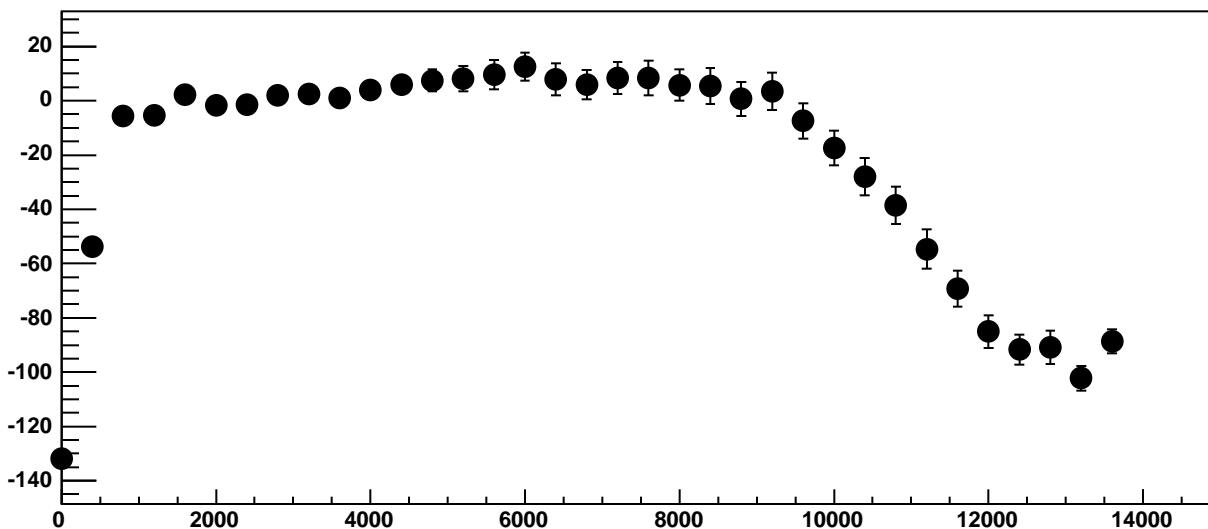


χ^2 / ndf	102.9 / 23
p0	-307.3 ± 1.117
p1	-0.1946 ± 0.000277

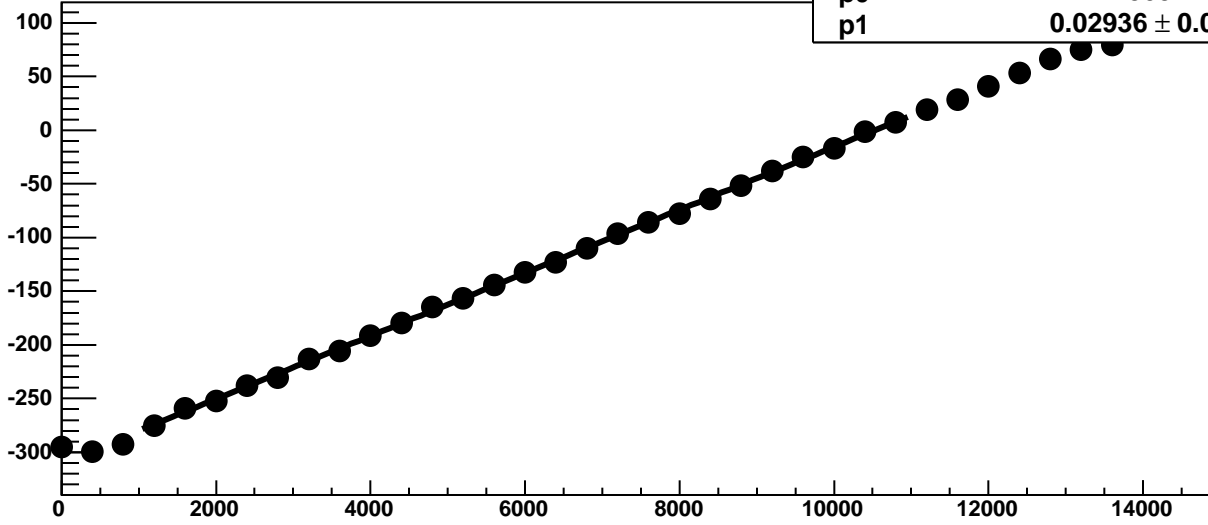
Chip 5, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



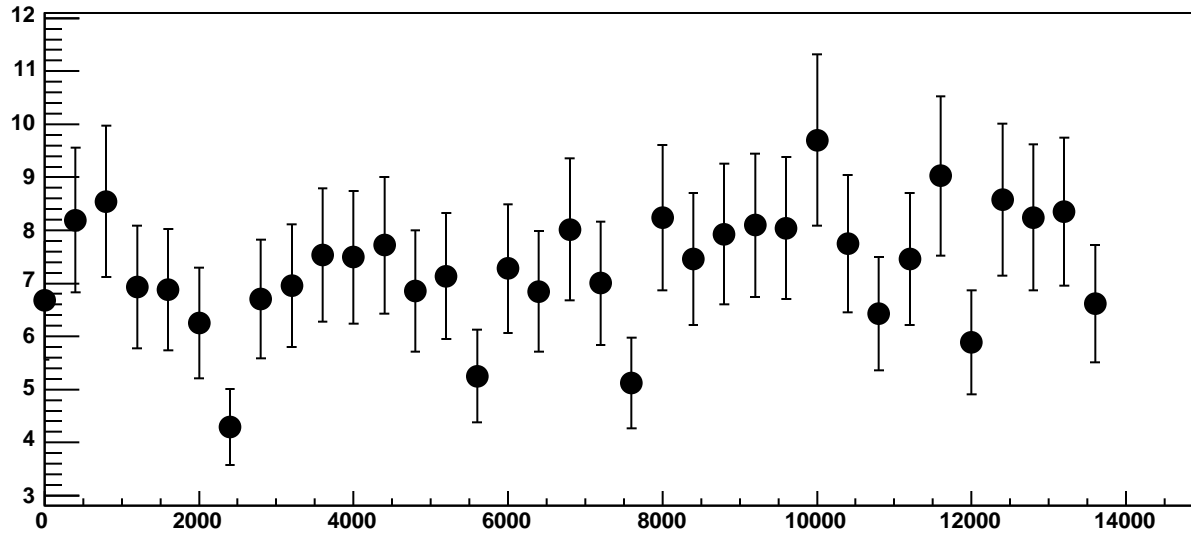
Chip 5, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



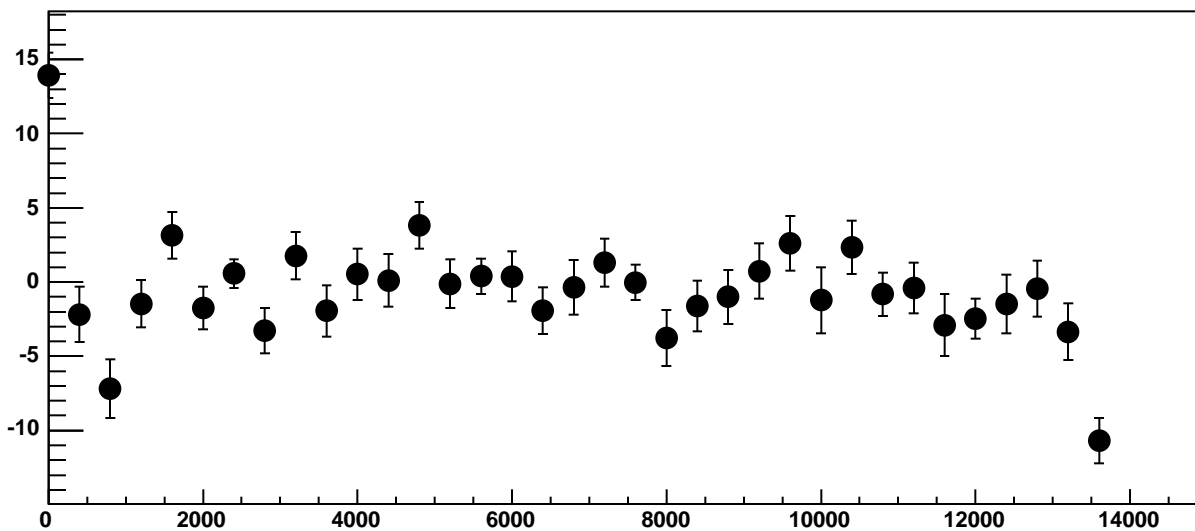
Chip 5, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



Chip 5, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC

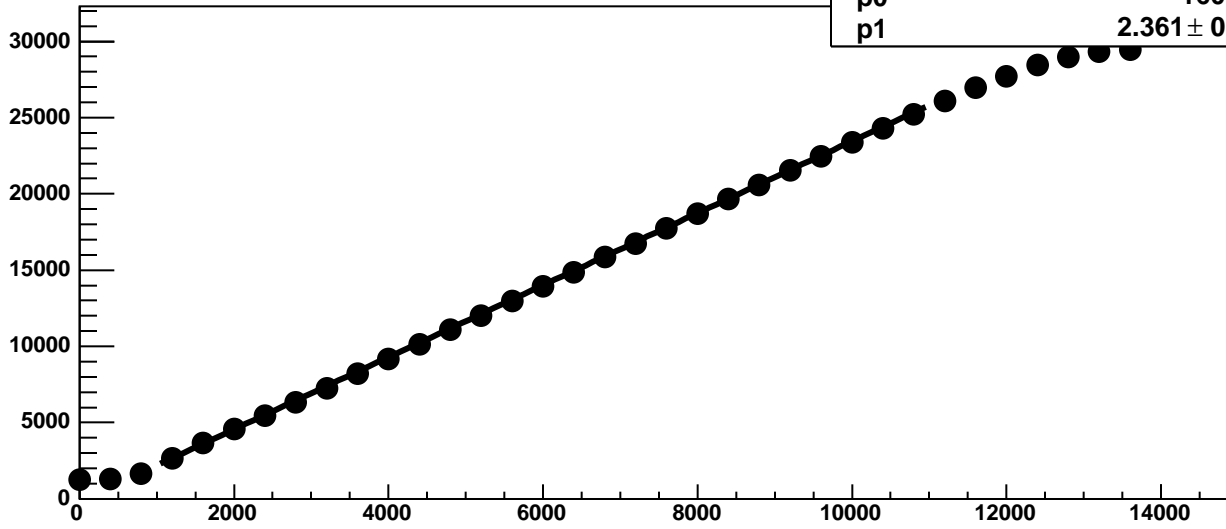


Chip 5, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

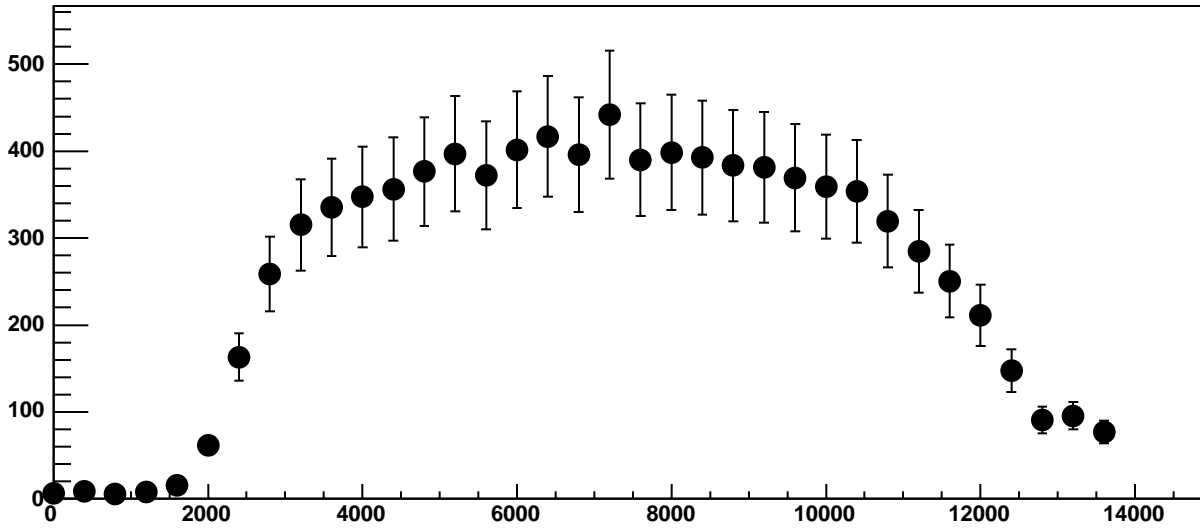


Chip 5, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC

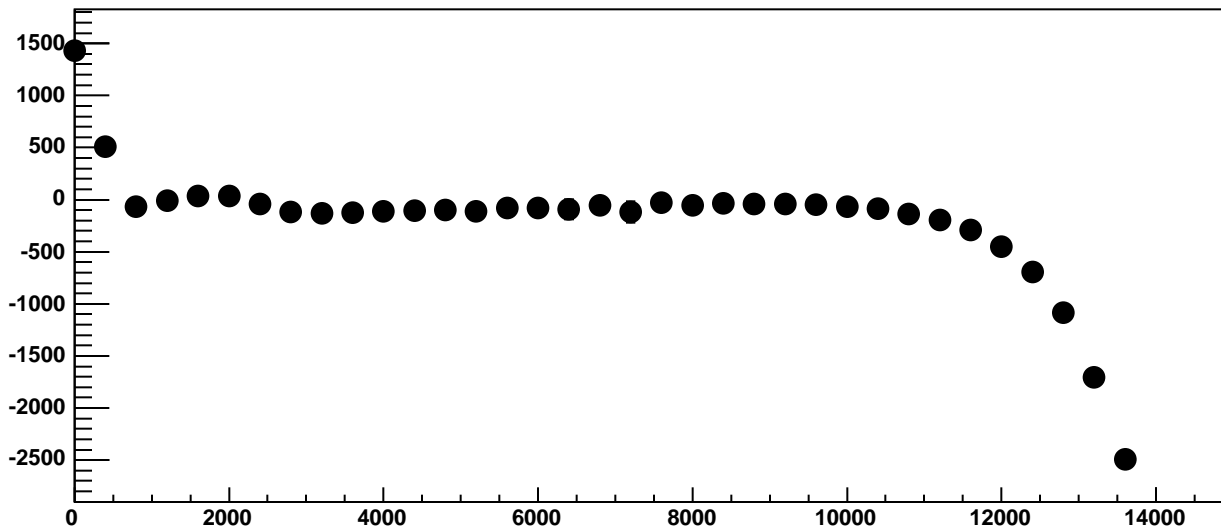
χ^2 / ndf 169.9 / 23
p0 -160 ± 4.169
p1 2.361 ± 0.002904



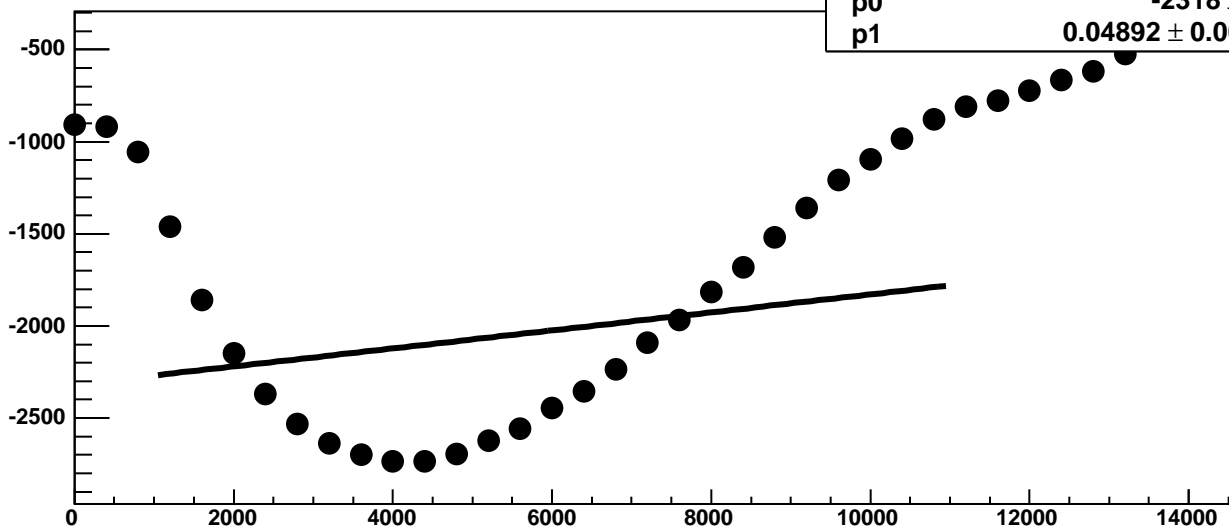
Chip 5, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC

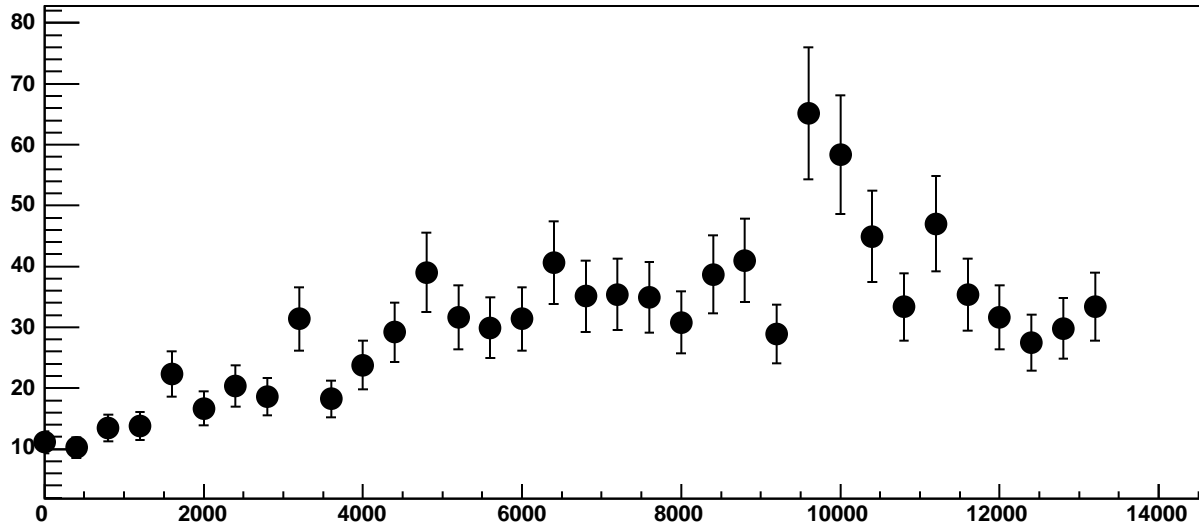


Chip 5, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

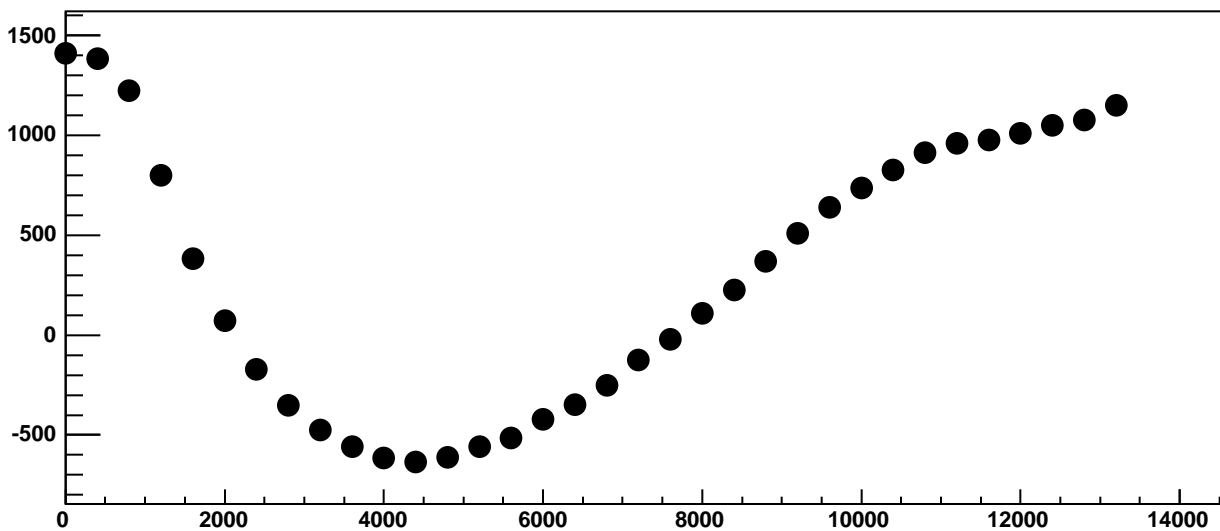


χ^2 / ndf 1.774e+05 / 23
p0 -2318 ± 2.226
p1 0.04892 ± 0.0004414

Chip 5, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

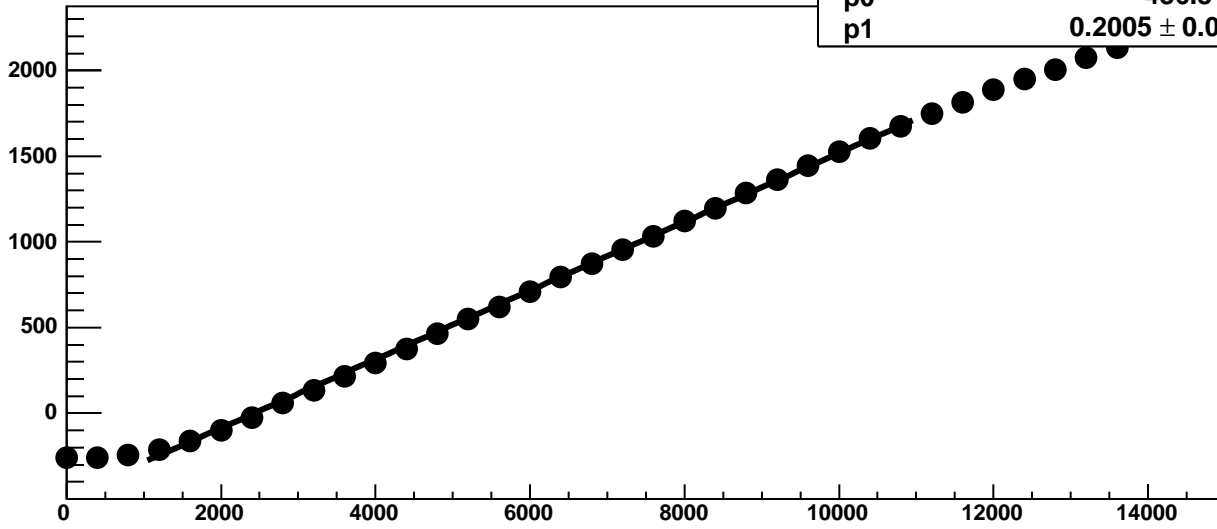


Chip 5, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

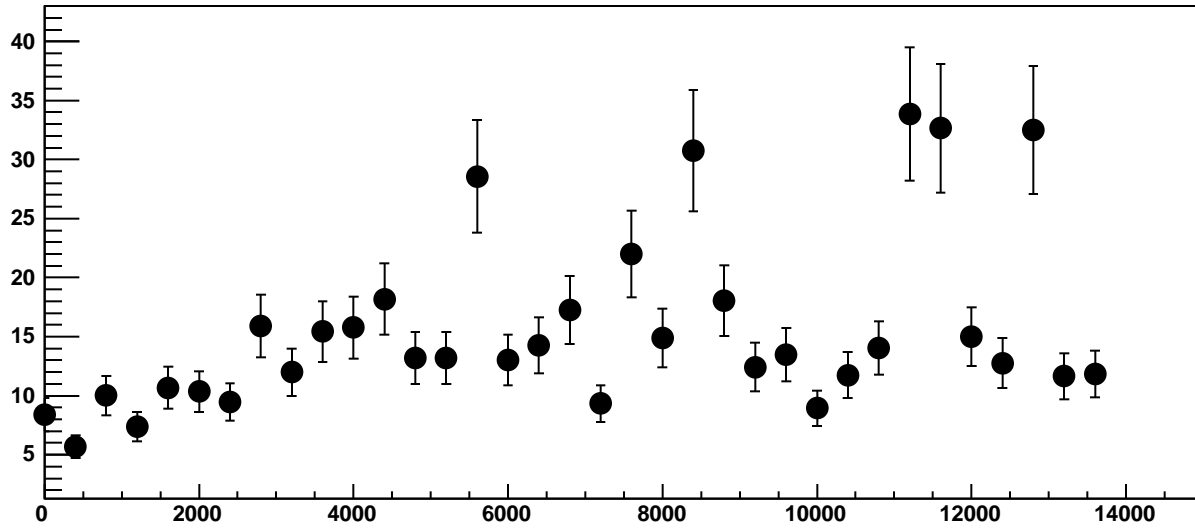


Chip 5, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

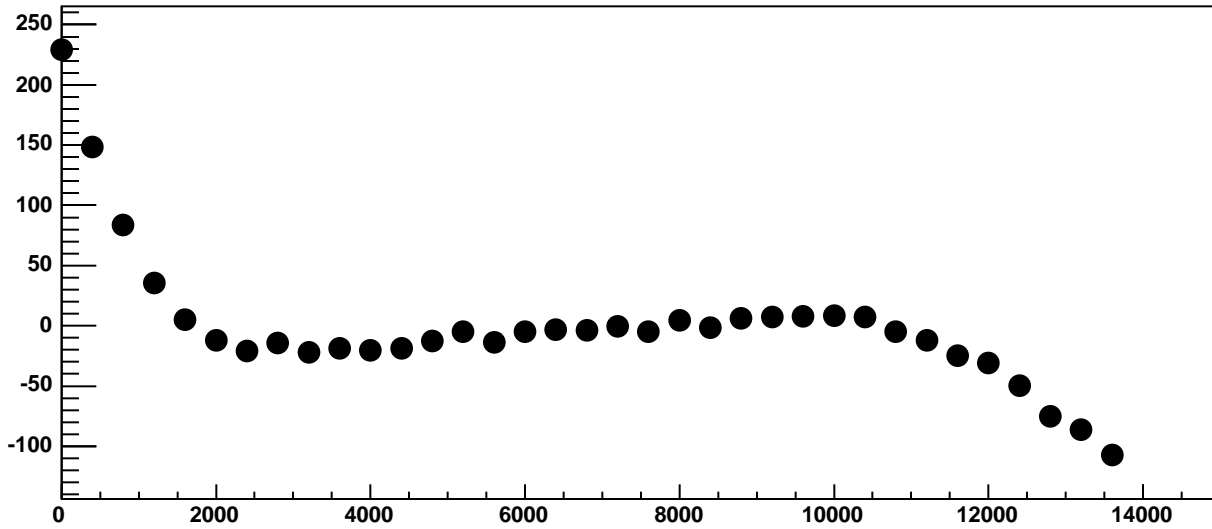
χ^2 / ndf 800.5 / 23
p0 -486.8 ± 1.133
p1 0.2005 ± 0.0001774



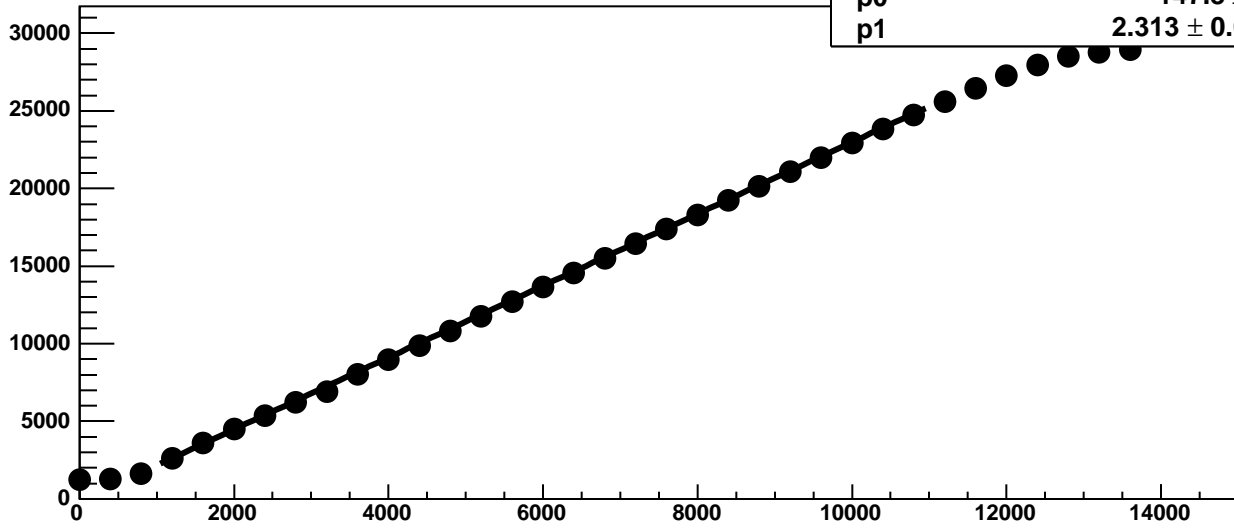
Chip 5, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



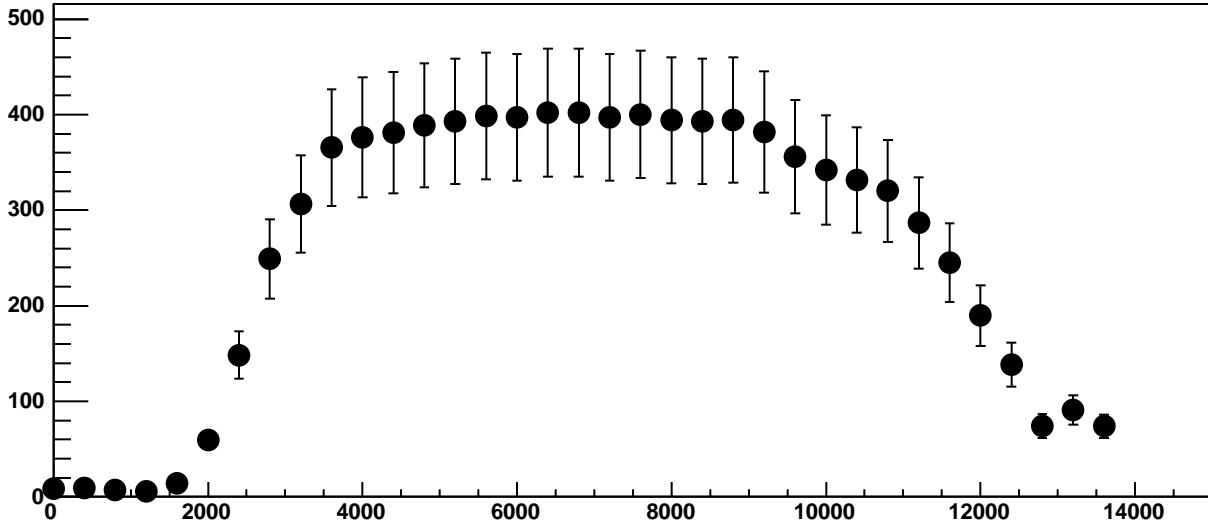
Chip 5, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



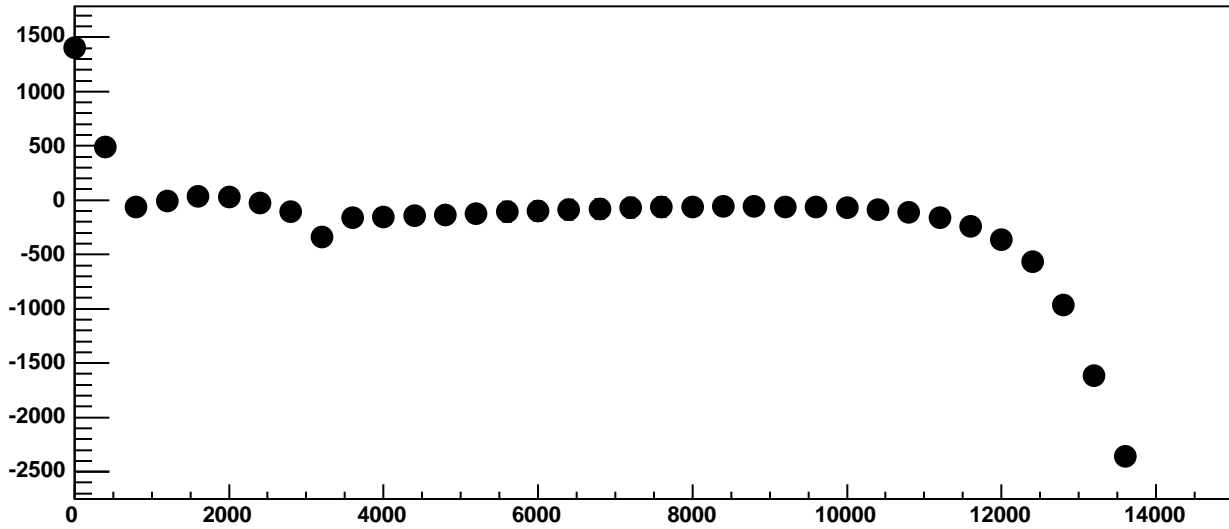
Chip 5, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC



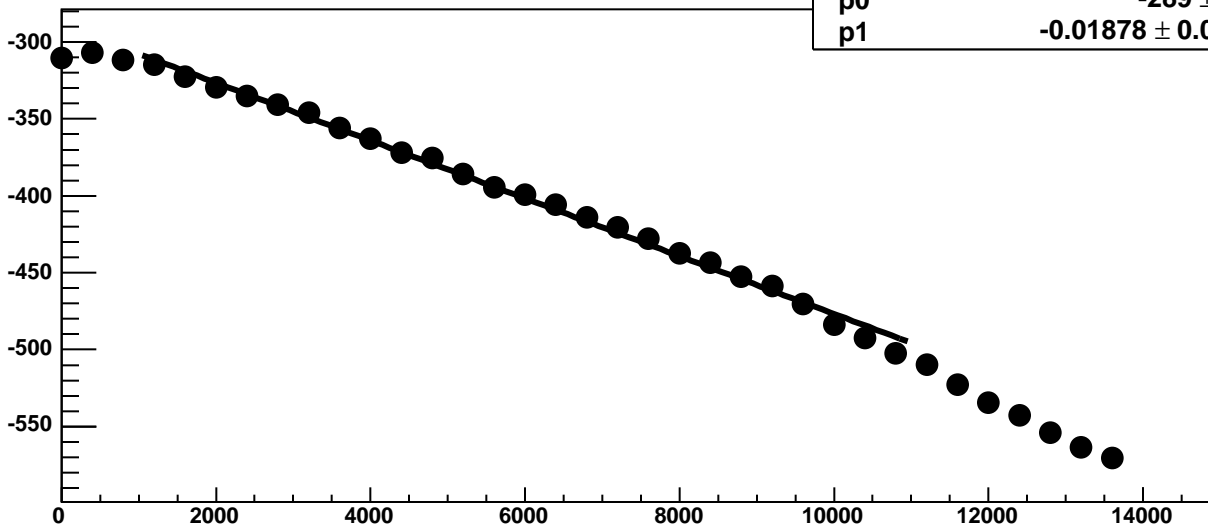
Chip 5, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

66.5 / 23

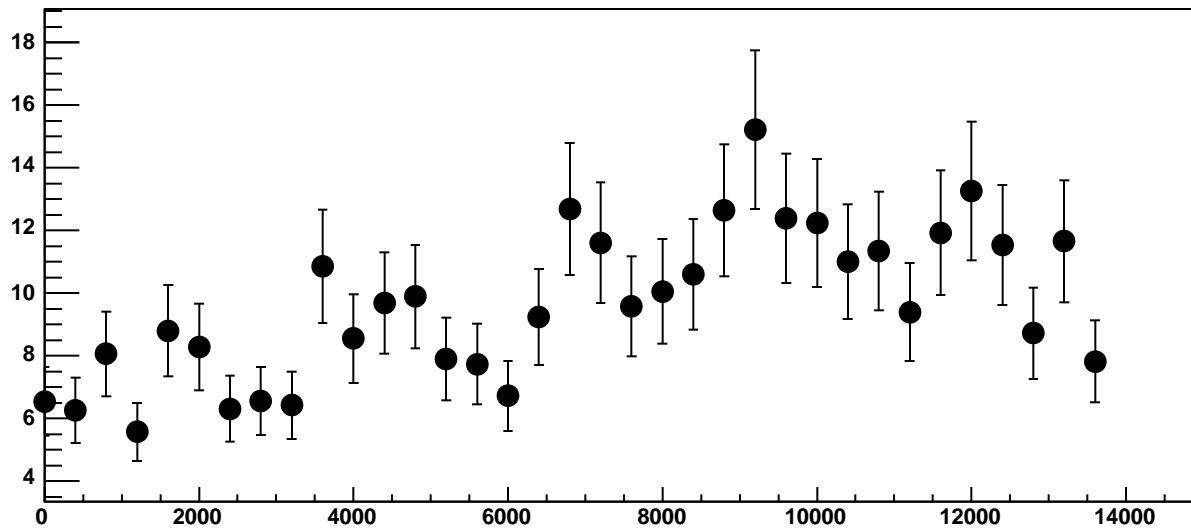
p0

-289 ± 0.8153

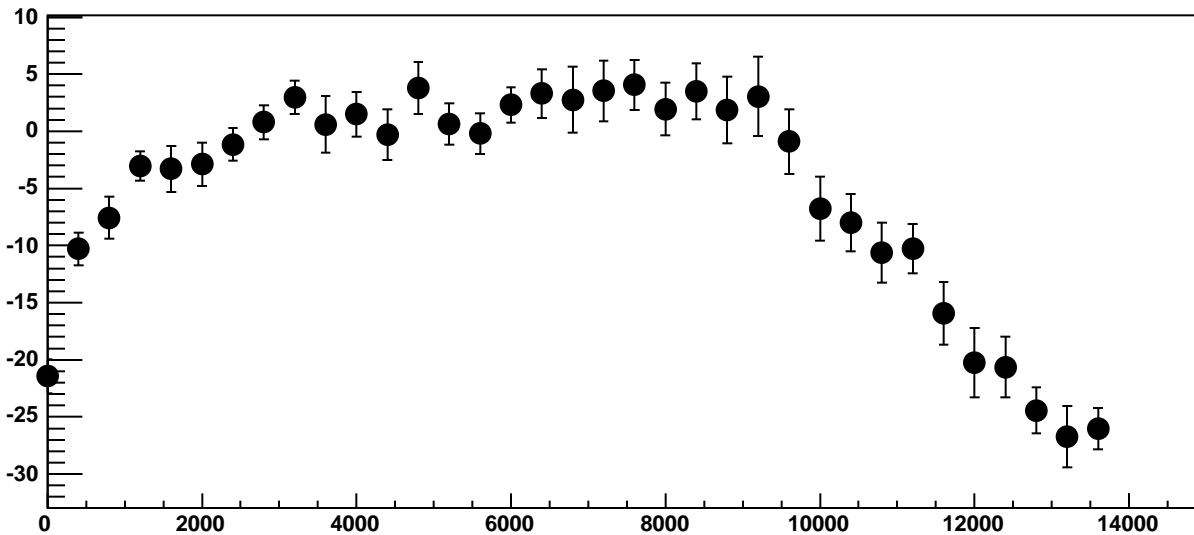
p1

-0.01878 ± 0.0001456

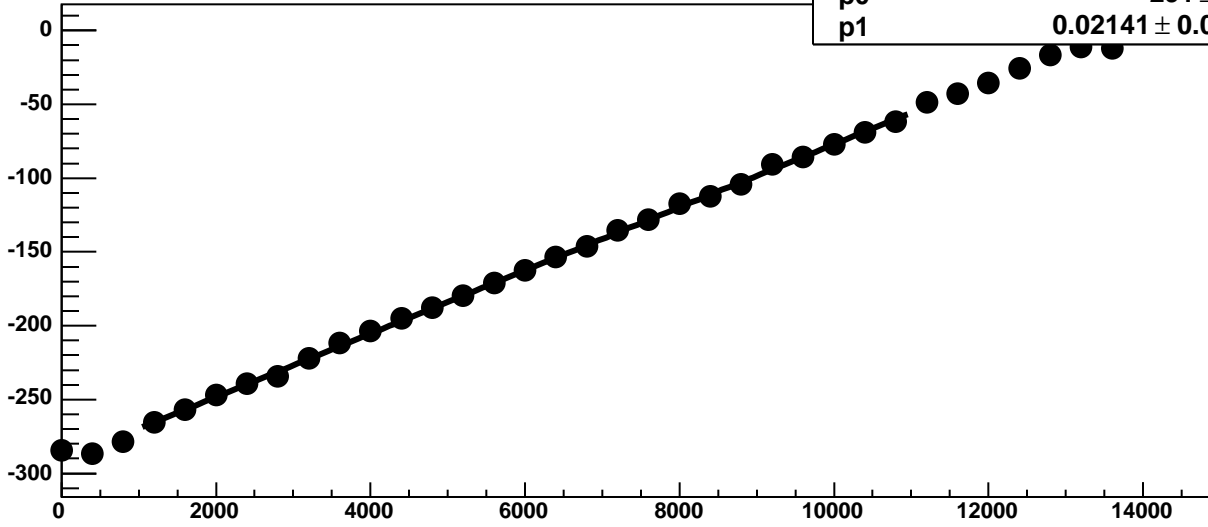
Chip 5, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

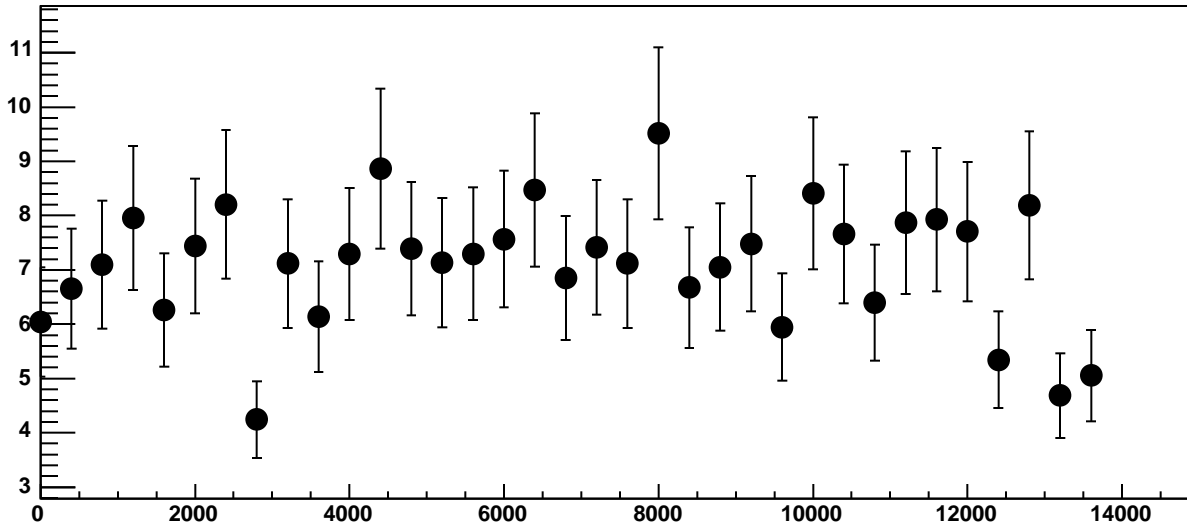


Chip 5, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

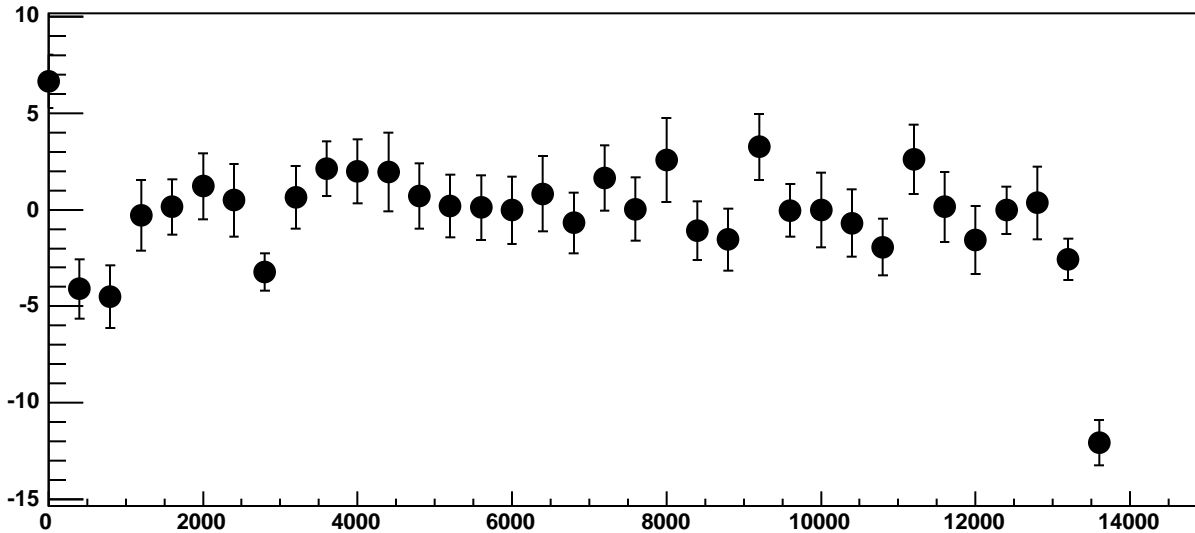


χ^2 / ndf 26.15 / 23
p0 -291 ± 0.7094
p1 0.02141 ± 0.0001089

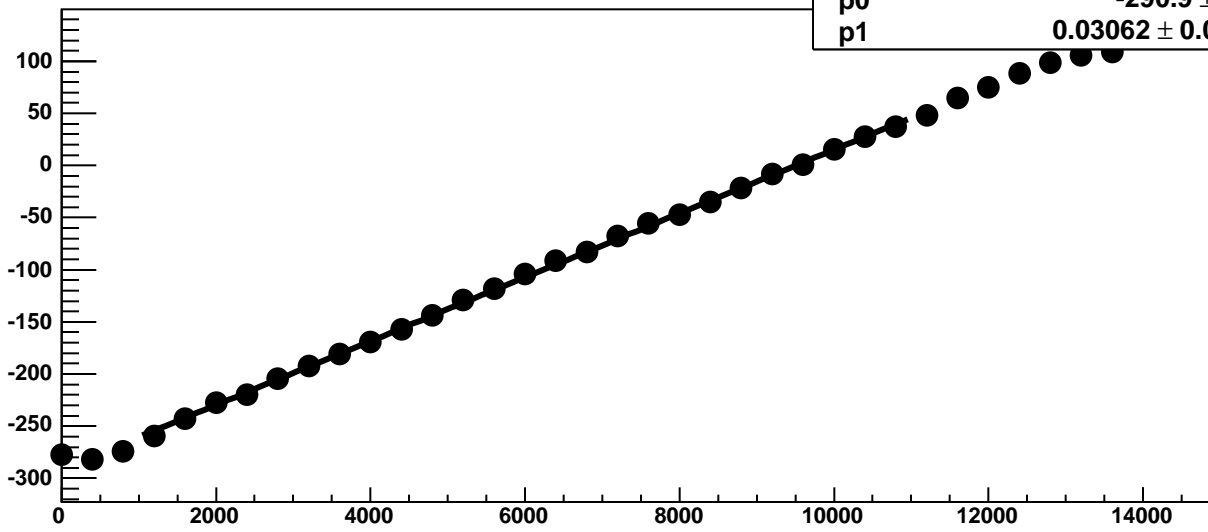
Chip 5, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

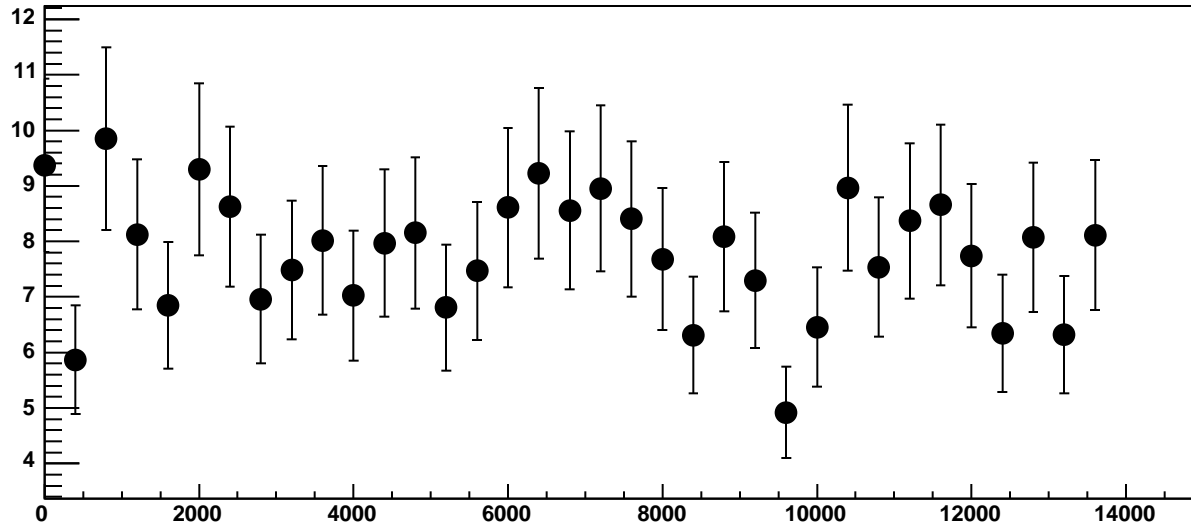


Chip 5, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC

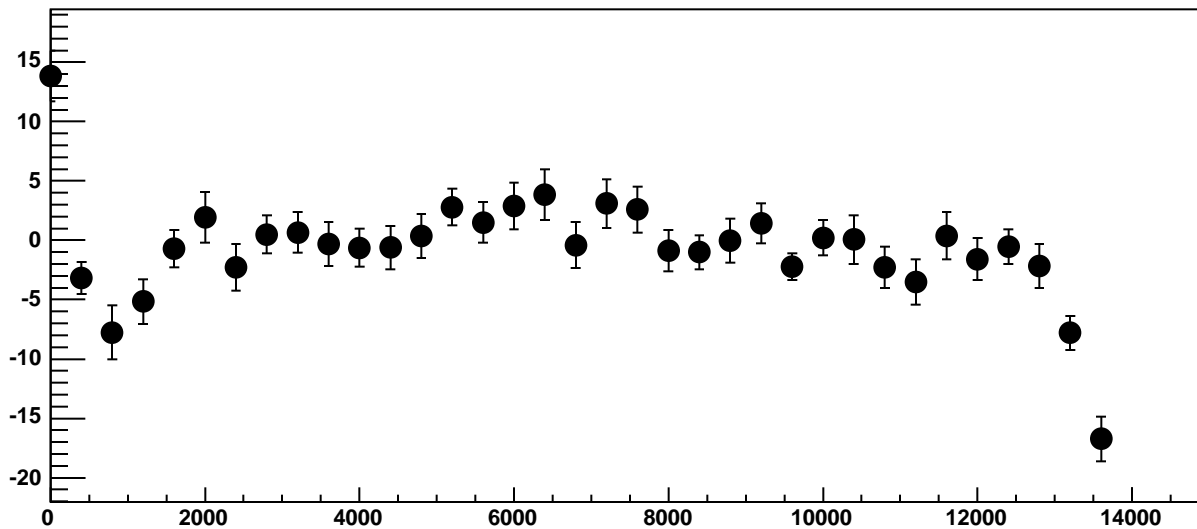


χ^2 / ndf 31.21 / 23
p0 -290.9 ± 0.8082
p1 0.03062 ± 0.0001169

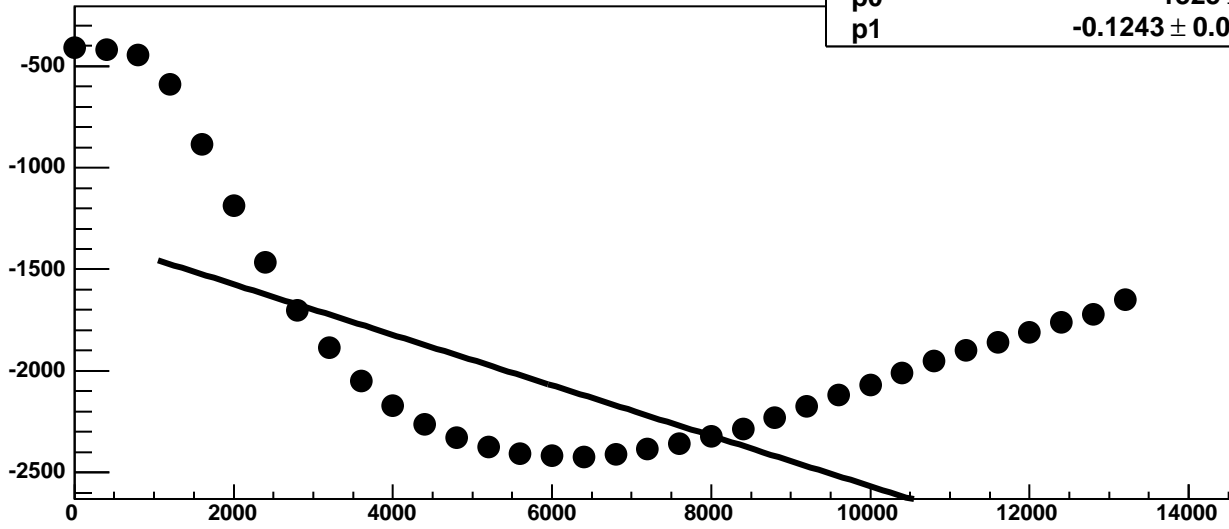
Chip 5, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC

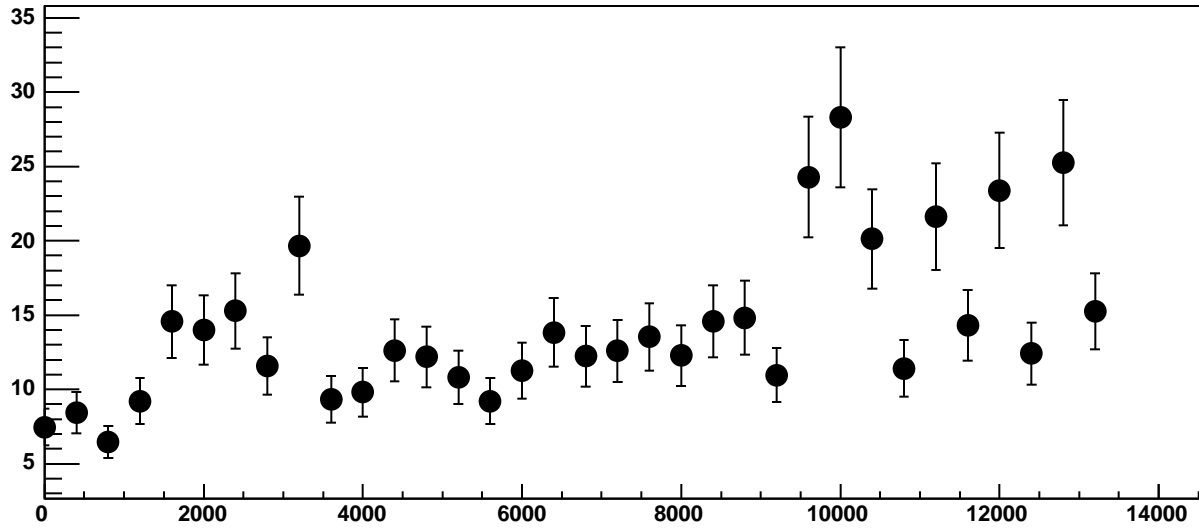


Chip 5, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

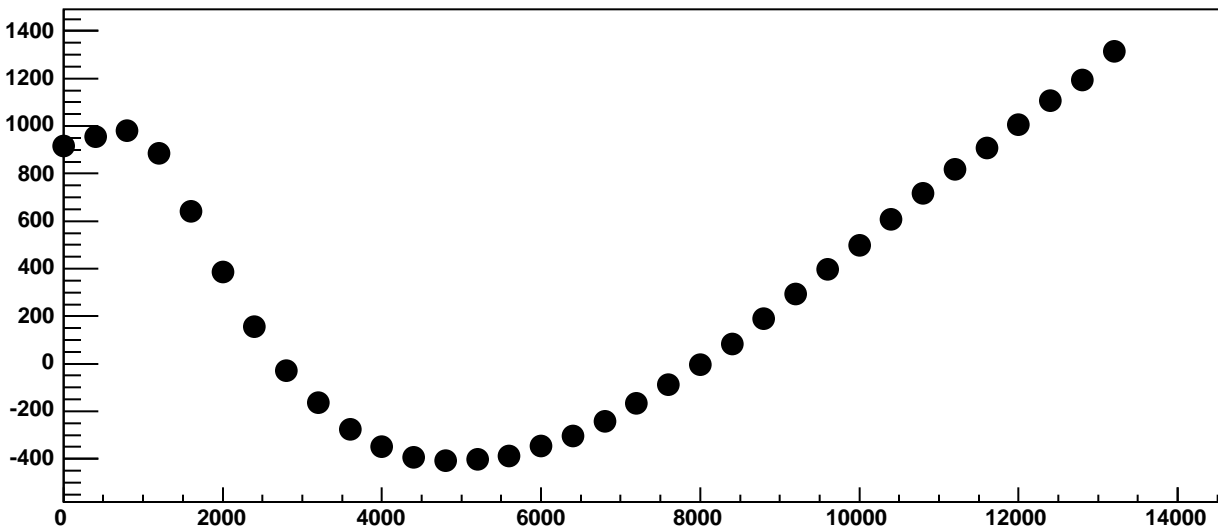


χ^2 / ndf 5.284e+05 / 23
p0 -1325 ± 1.305
p1 -0.1243 ± 0.0002121

Chip 5, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

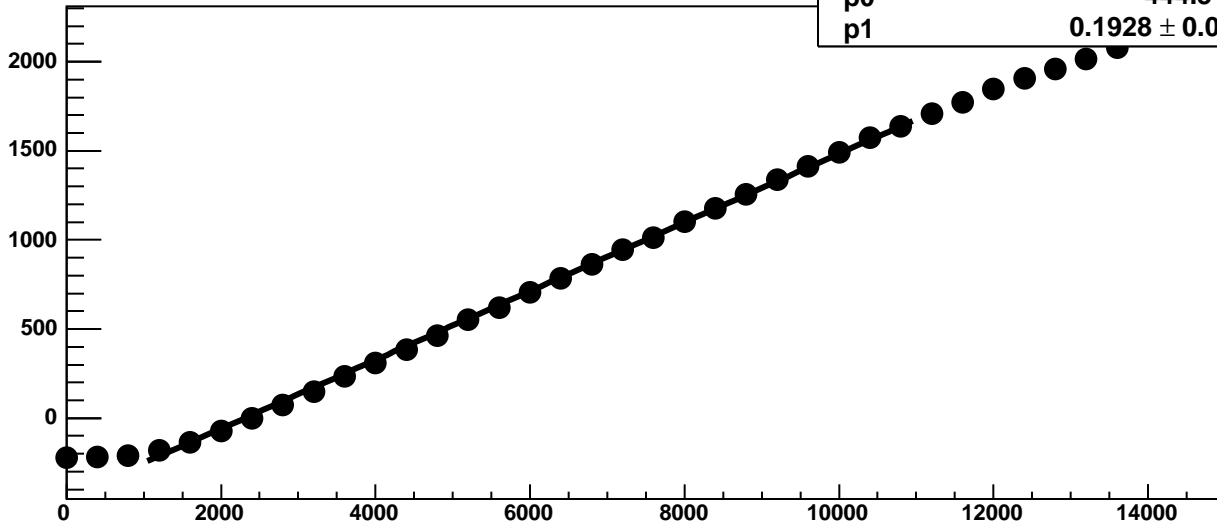


Chip 5, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

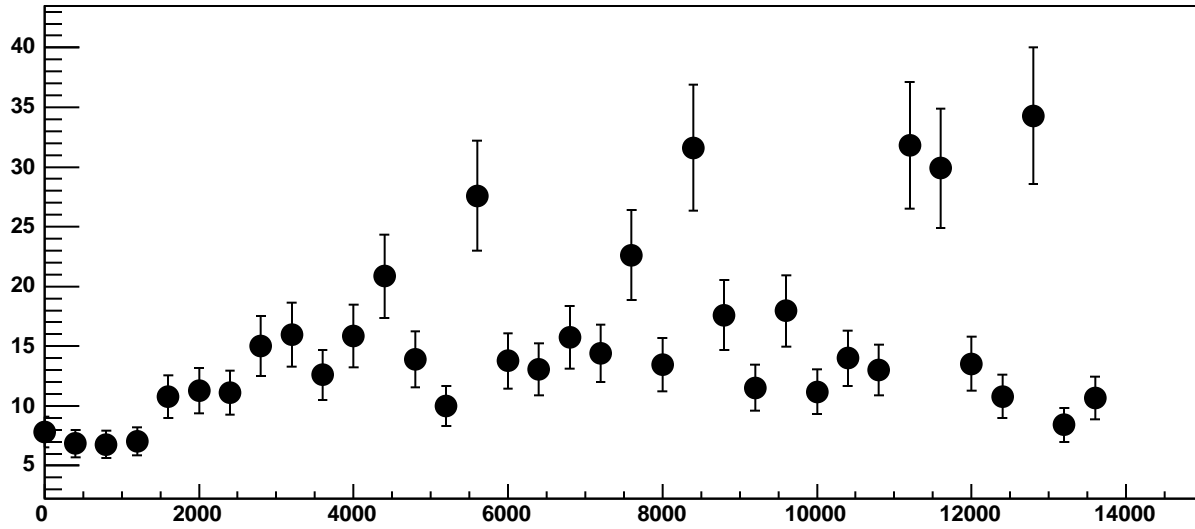


Chip 5, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

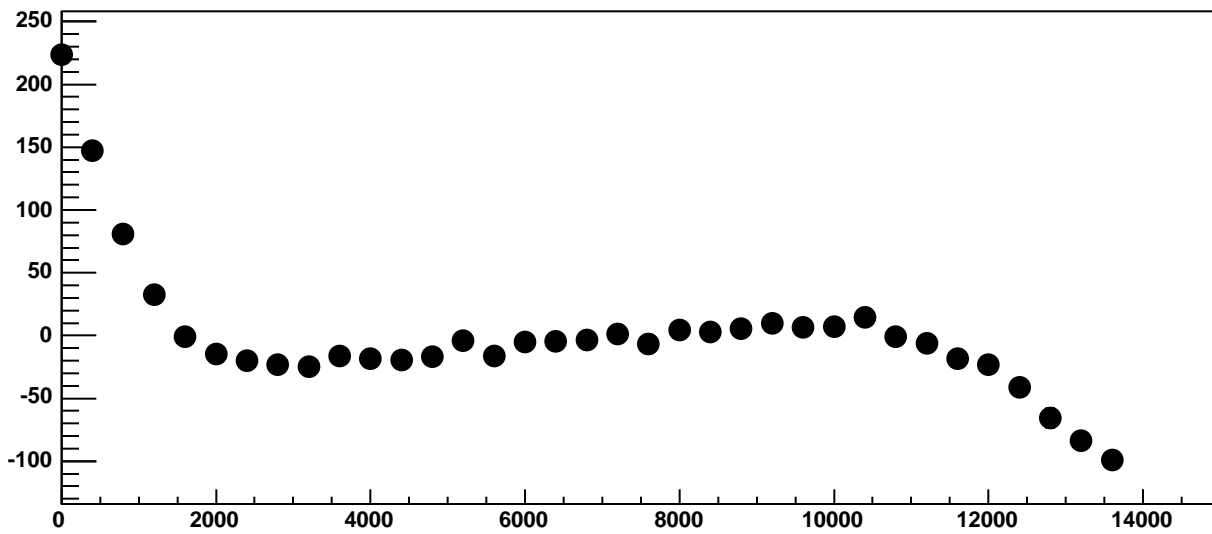
χ^2 / ndf 768.8 / 23
p0 -444.5 ± 1.159
p1 0.1928 ± 0.0001878



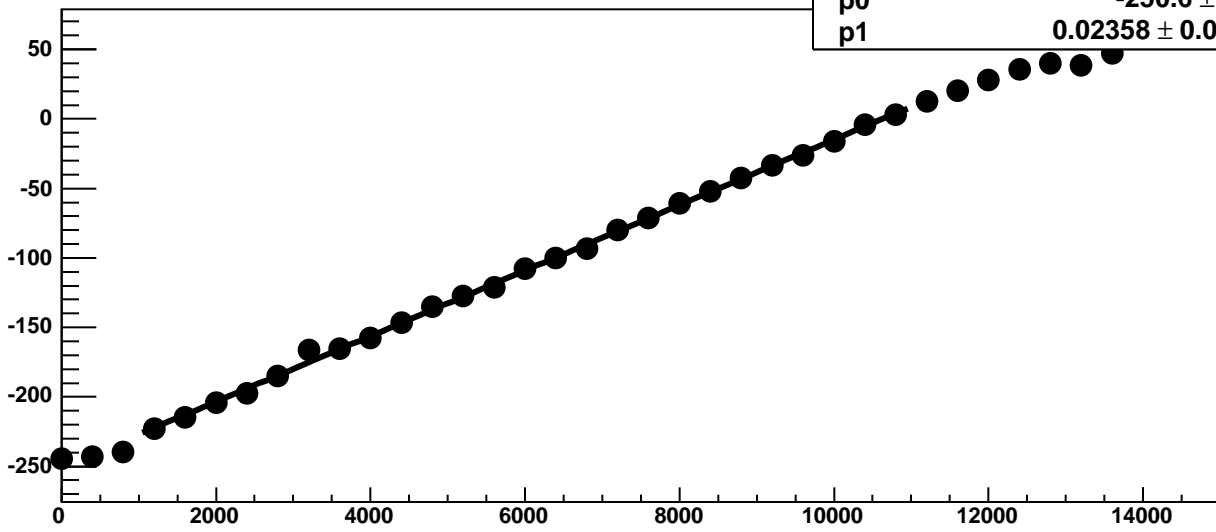
Chip 5, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



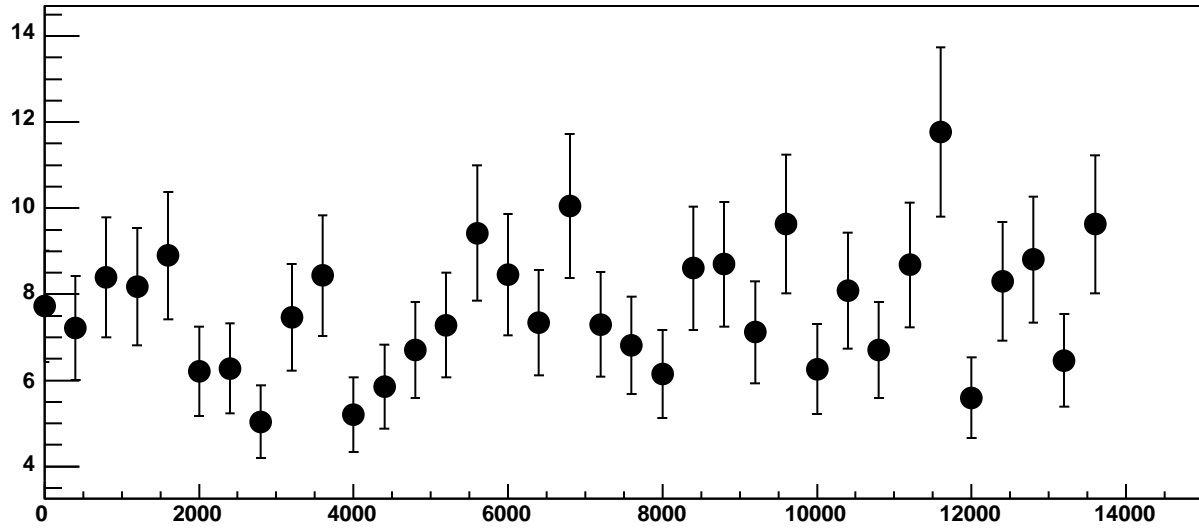
Chip 5, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



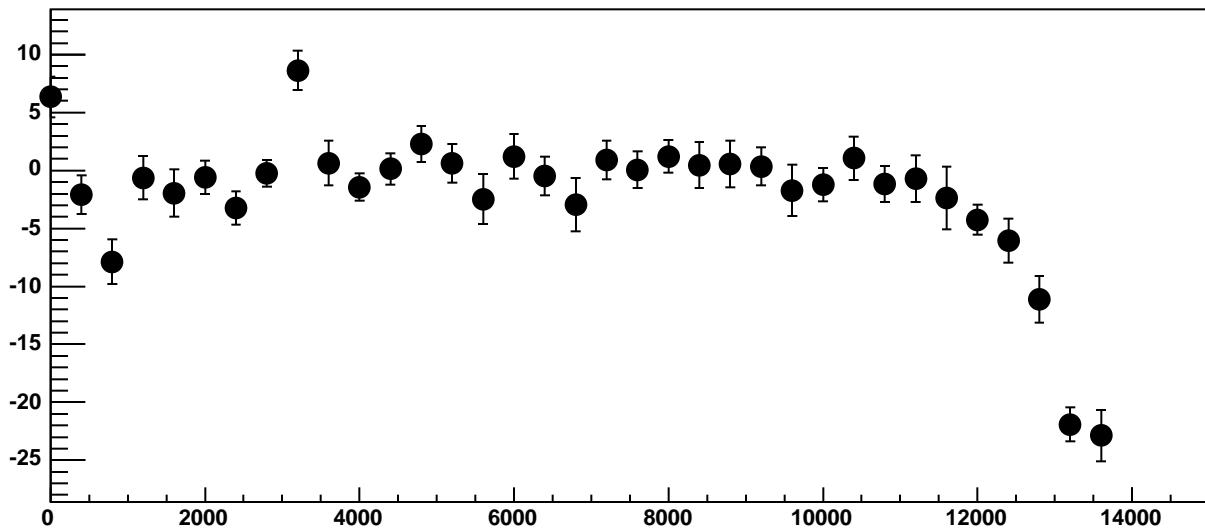
Chip 5, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC



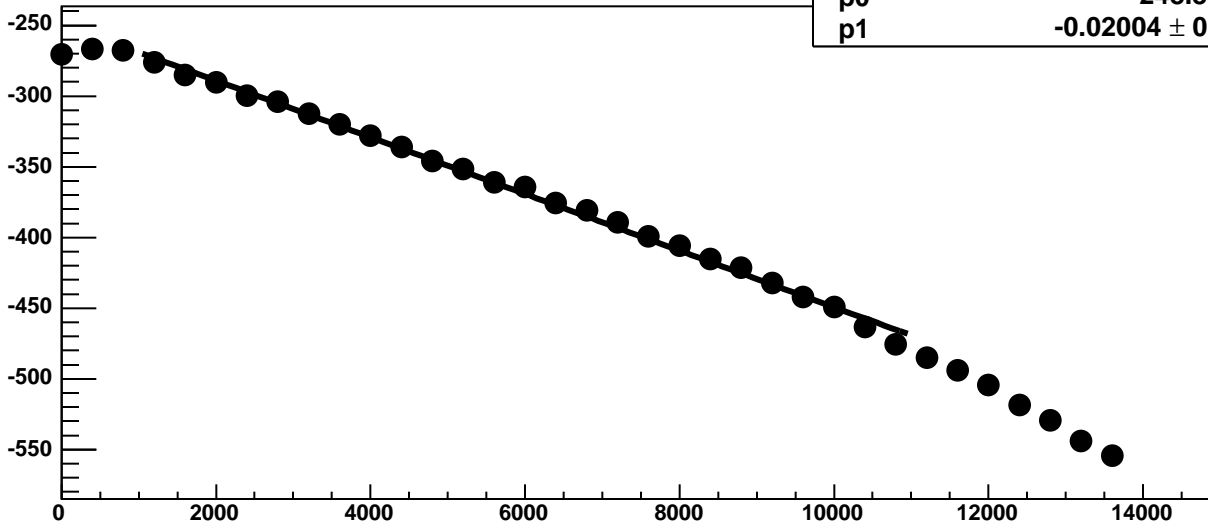
Chip 5, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



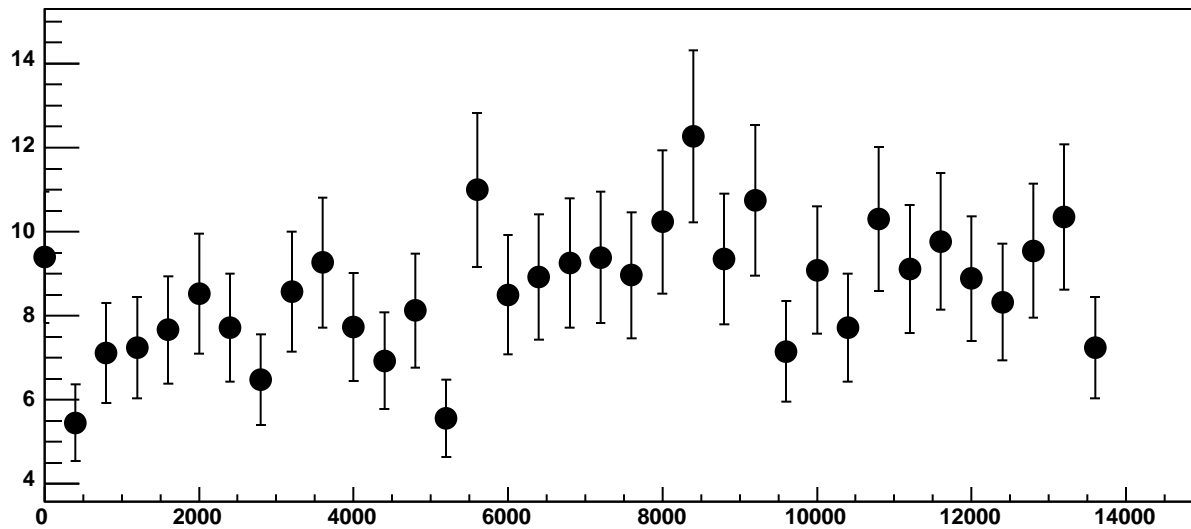
Chip 5, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC



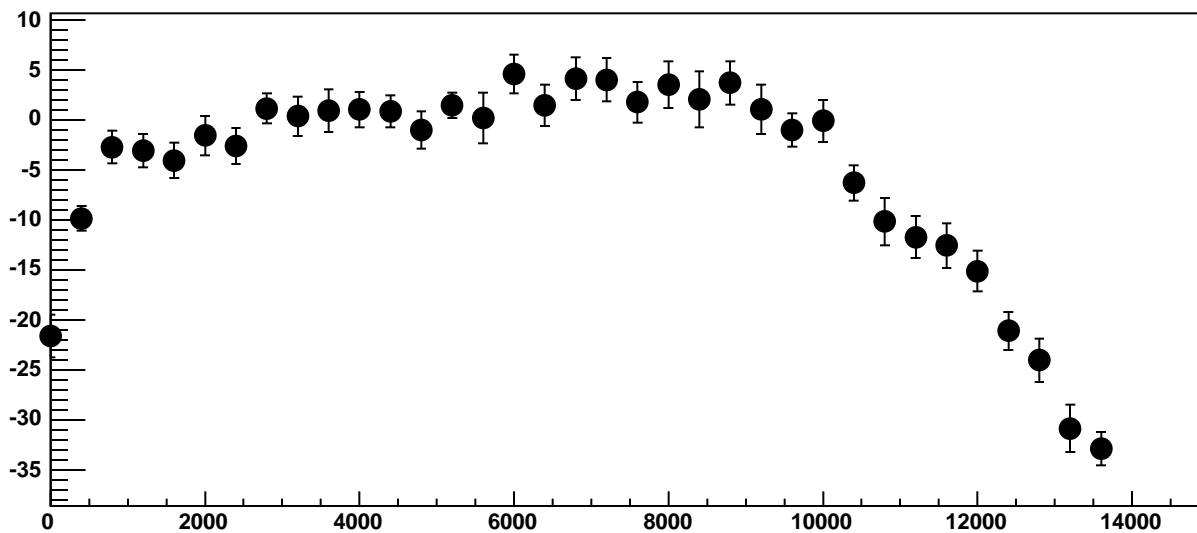
Chip 5, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



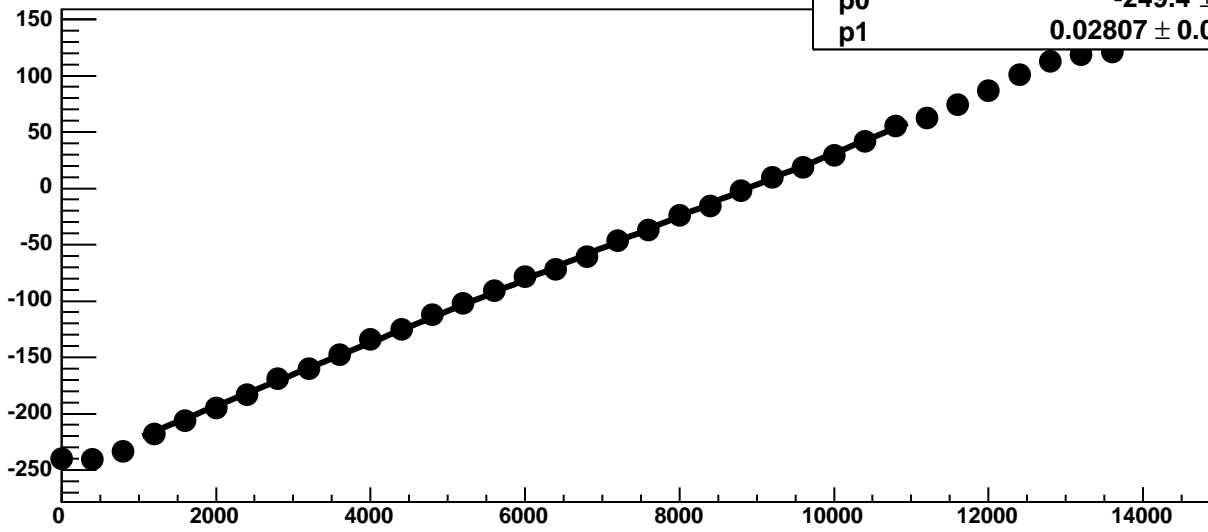
Chip 5, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



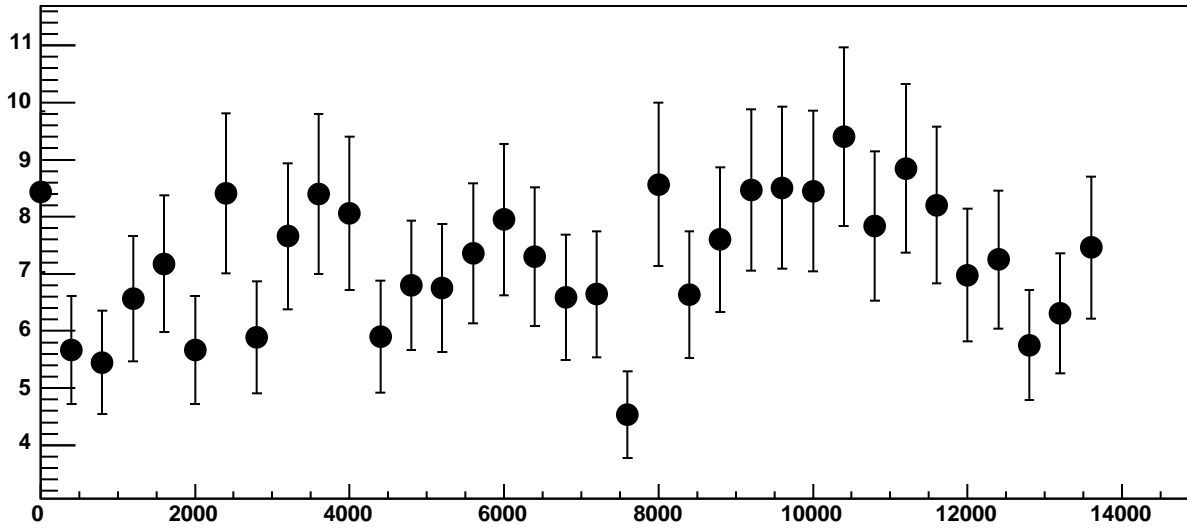
Chip 5, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



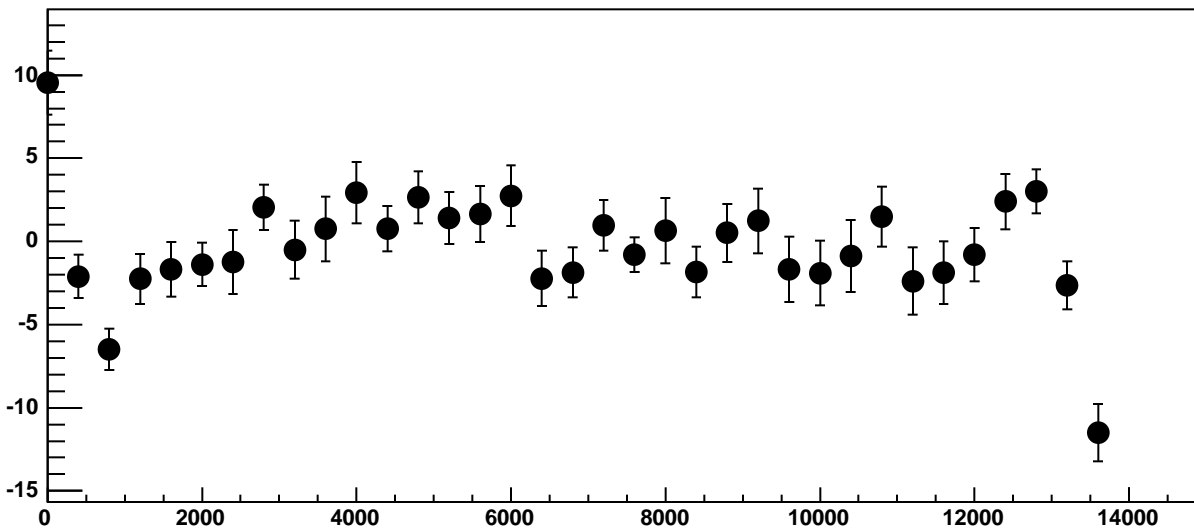
Chip 5, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



Chip 5, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC

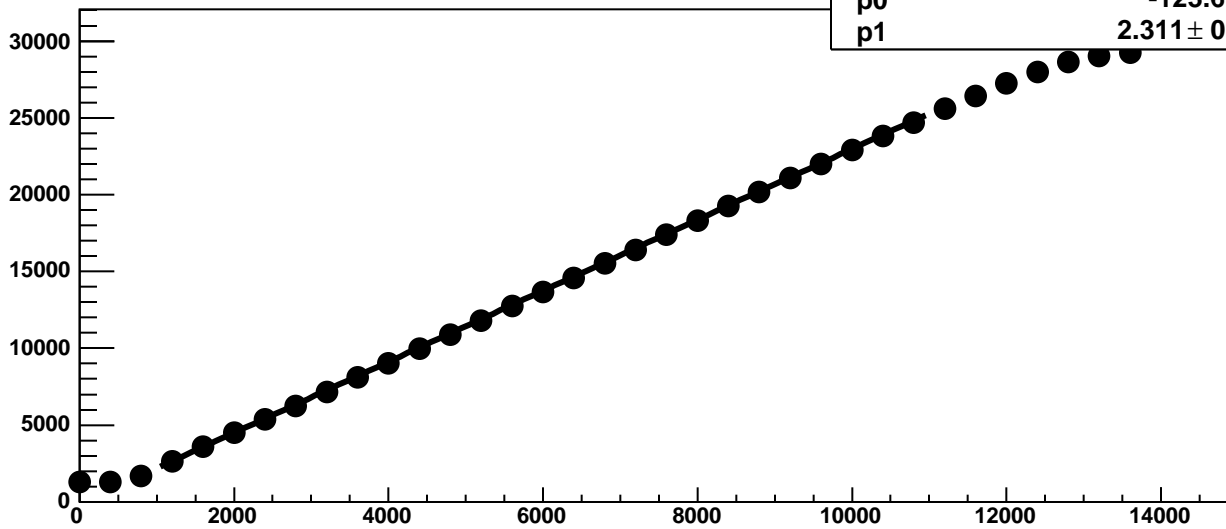


Chip 5, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

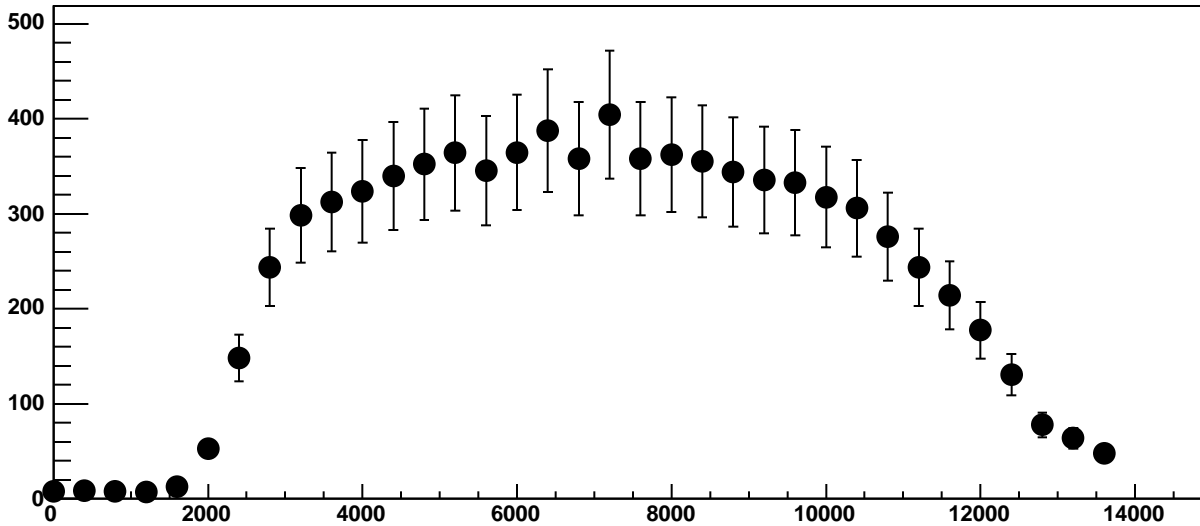


Chip 5, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC

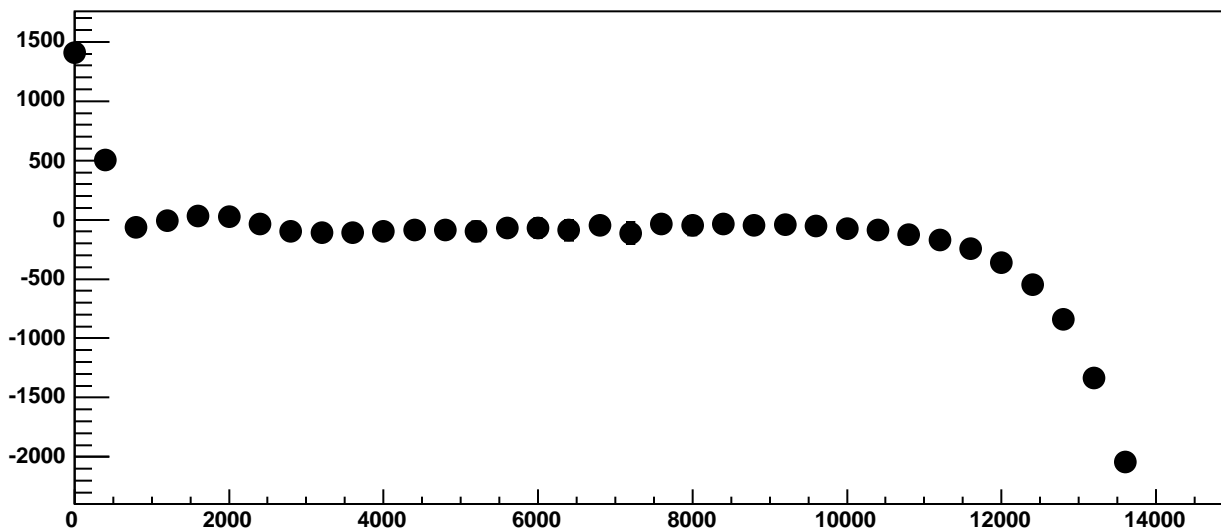
χ^2 / ndf 175.9 / 23
p0 -123.6 ± 3.814
p1 2.311 ± 0.002593



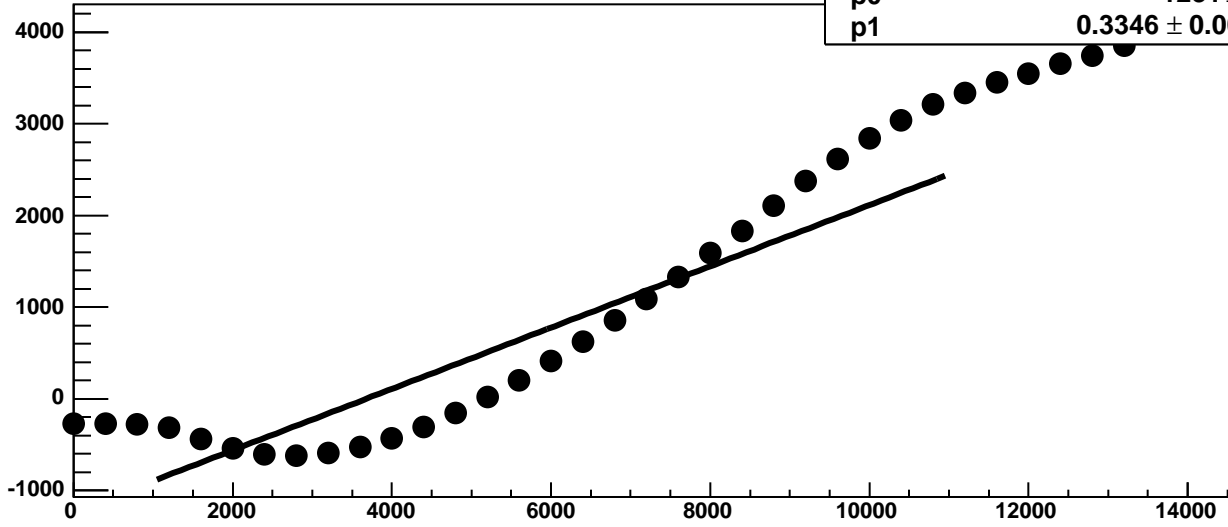
Chip 5, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC

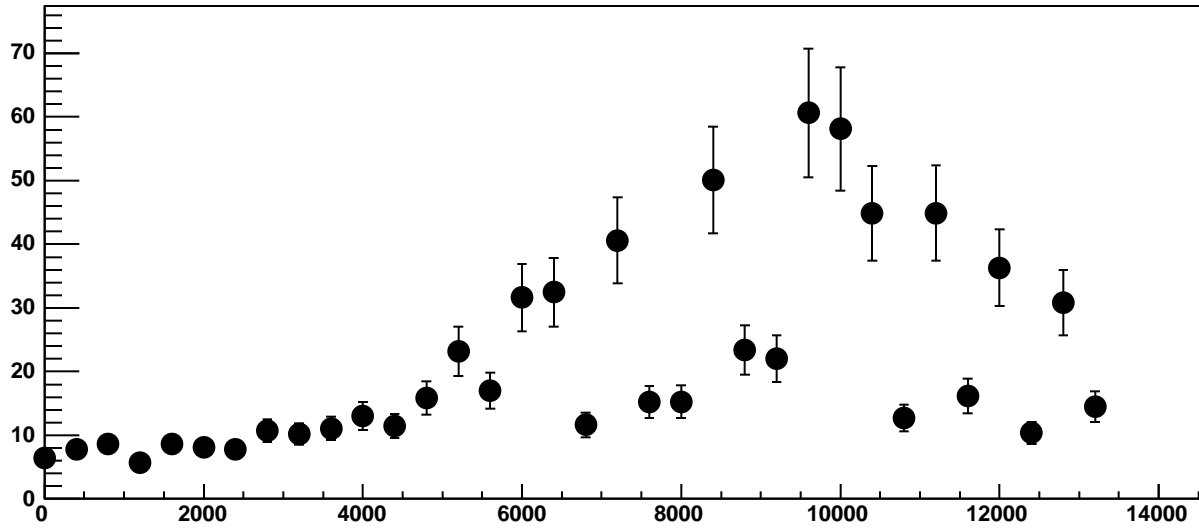


Chip 5, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

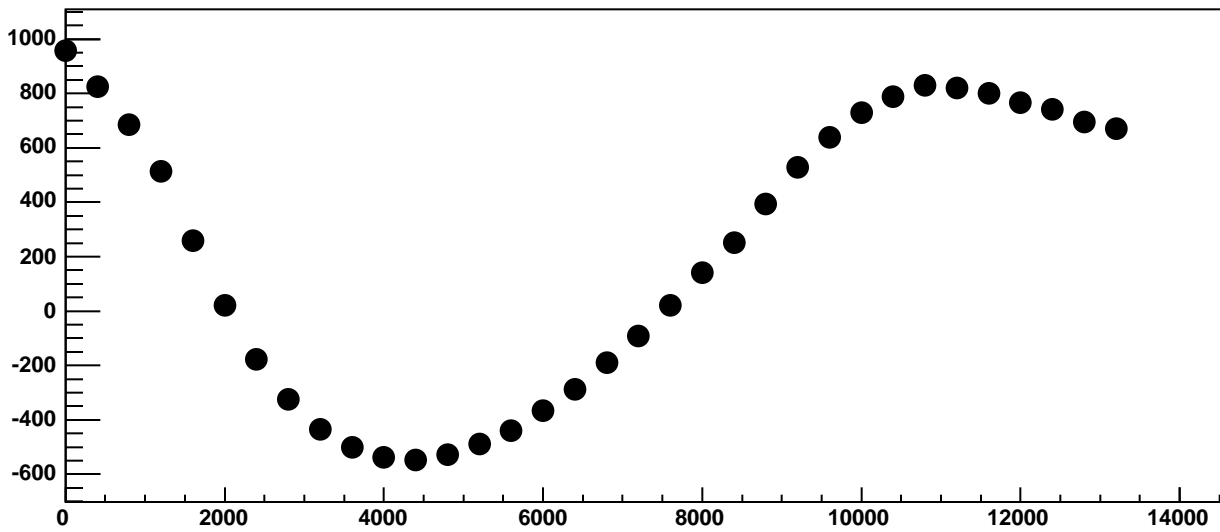


χ^2 / ndf 5.065e+05 / 23
p0 -1231 ± 1.003
p1 0.3346 ± 0.0002223

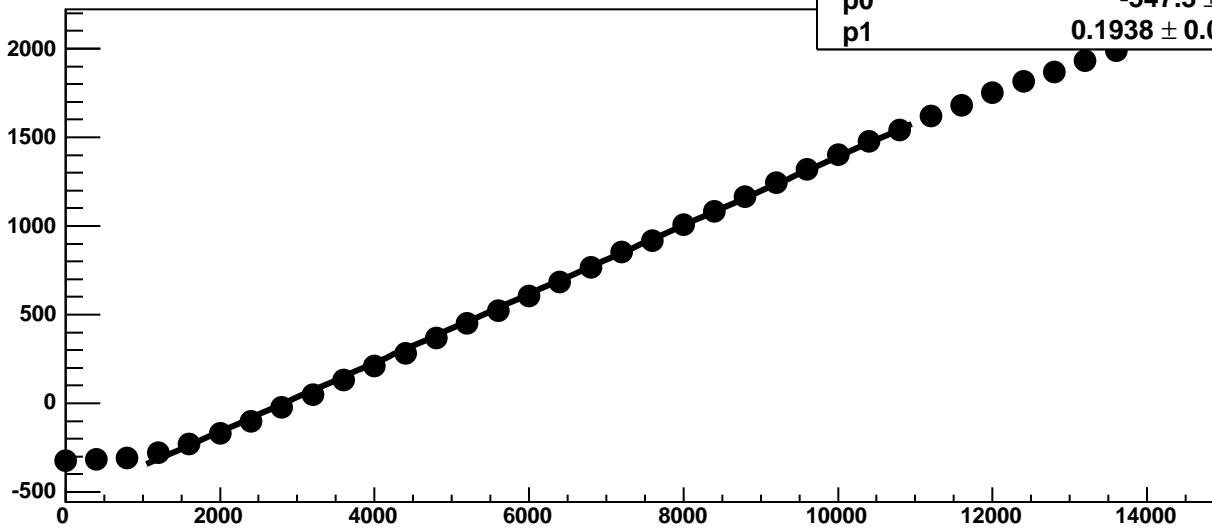
Chip 5, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



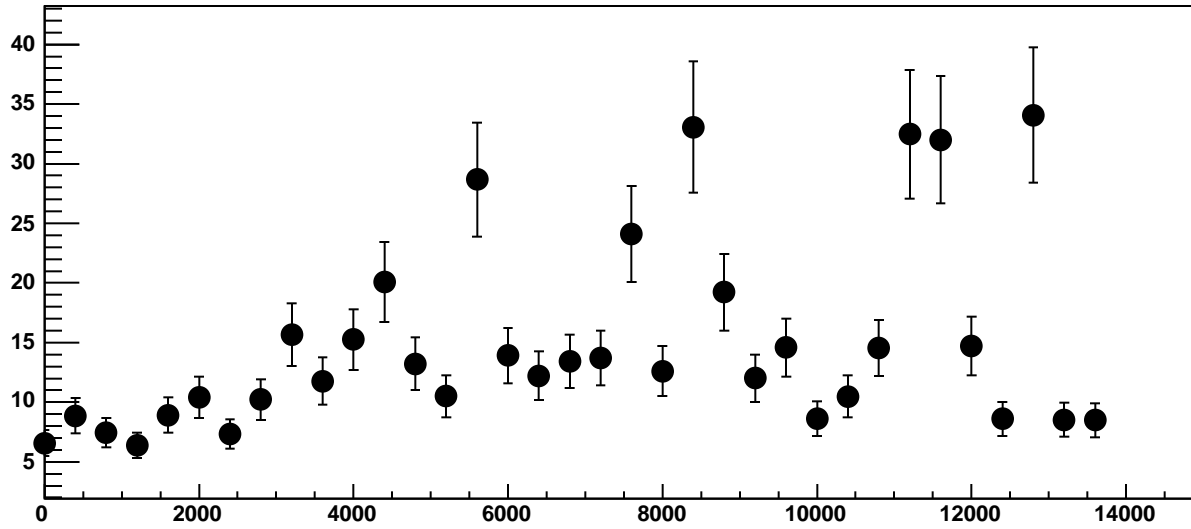
Chip 5, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



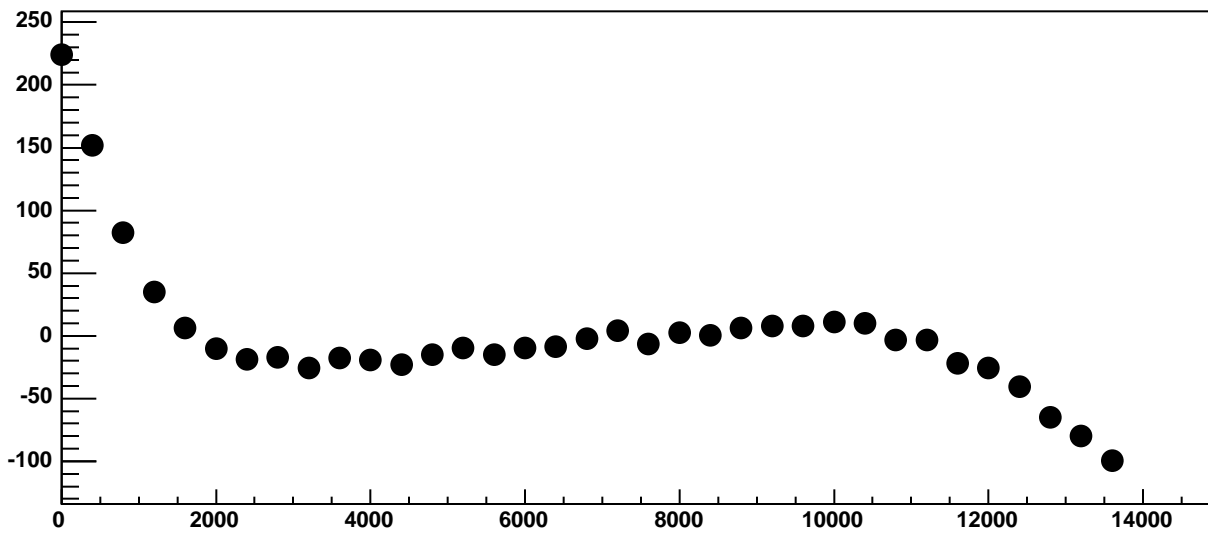
Chip 5, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



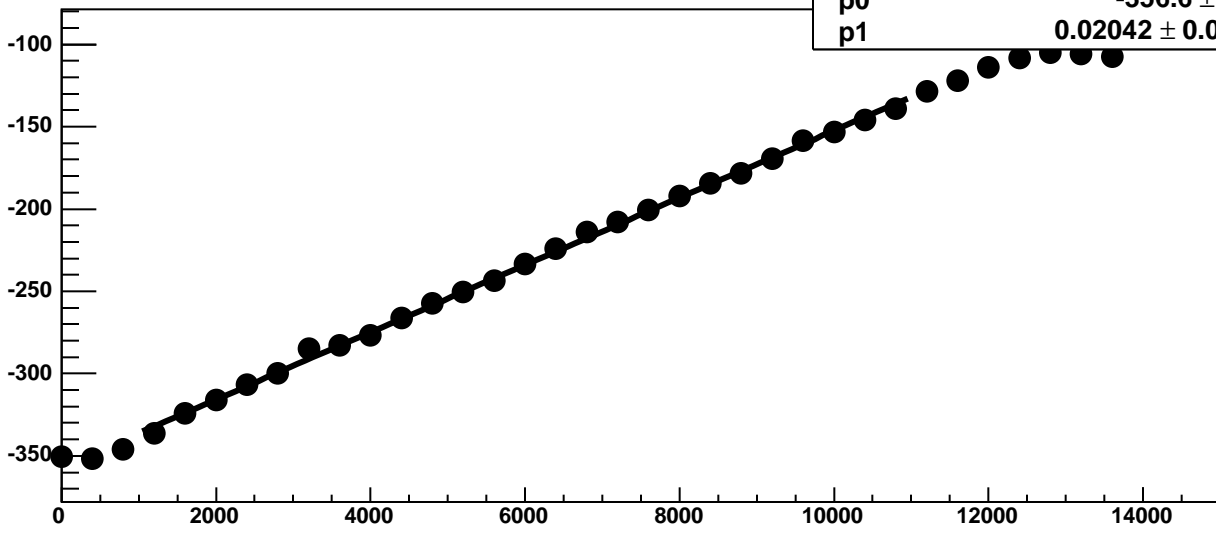
Chip 5, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



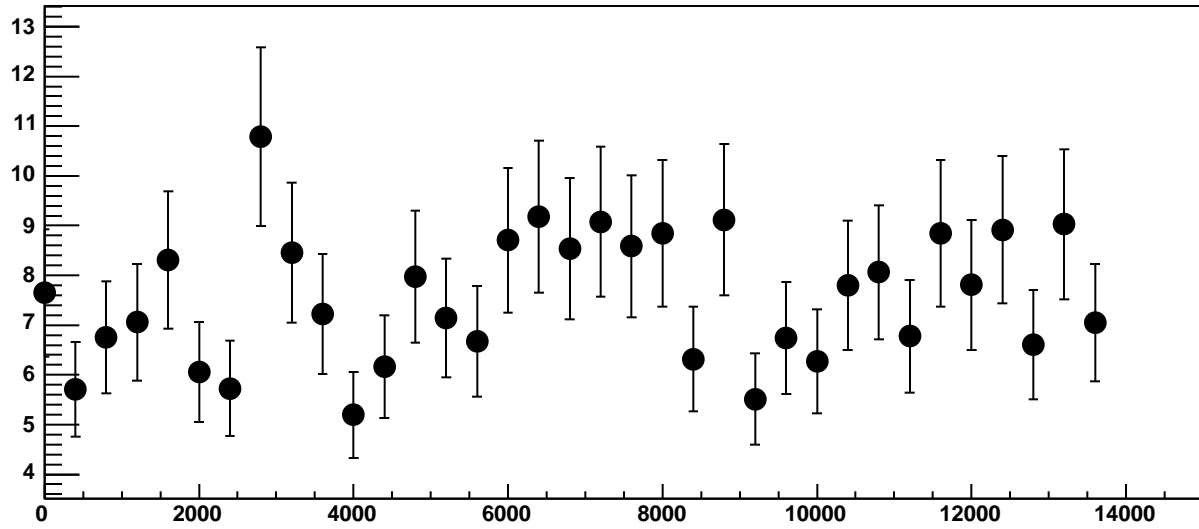
Chip 5, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



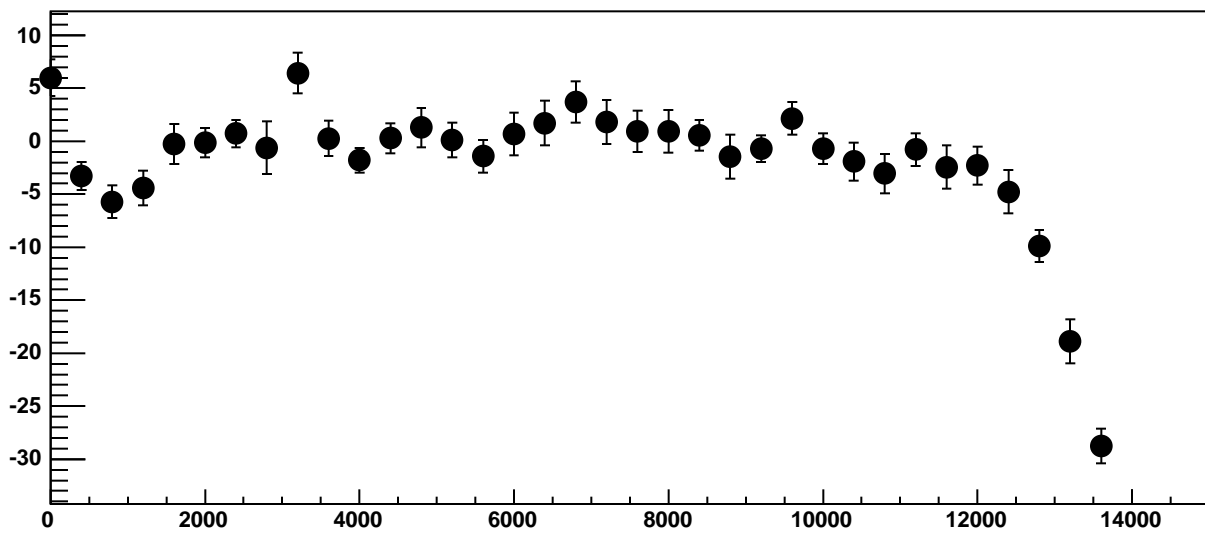
Chip 5, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



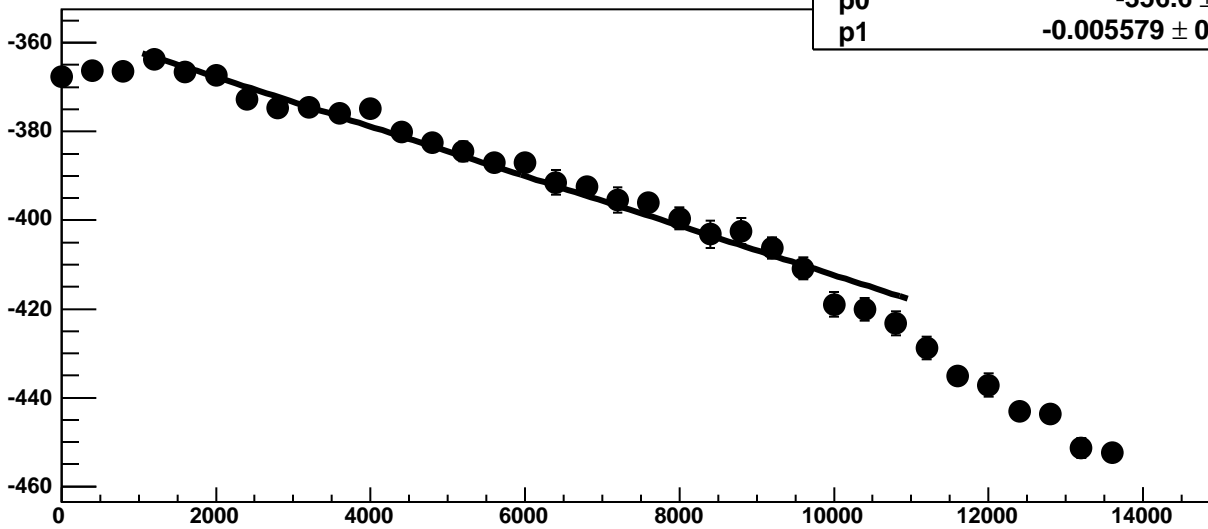
Chip 5, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



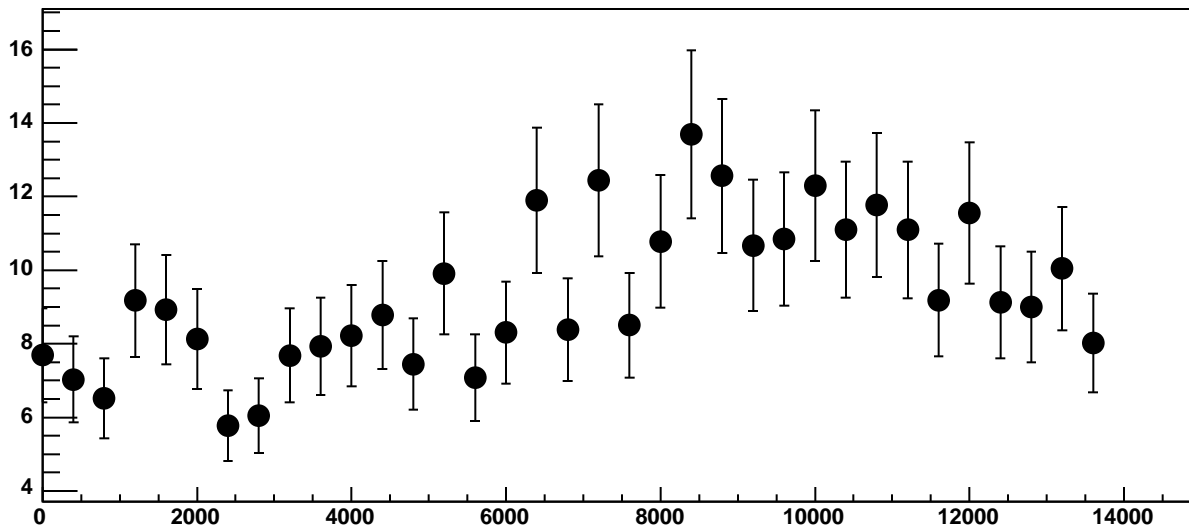
Chip 5, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



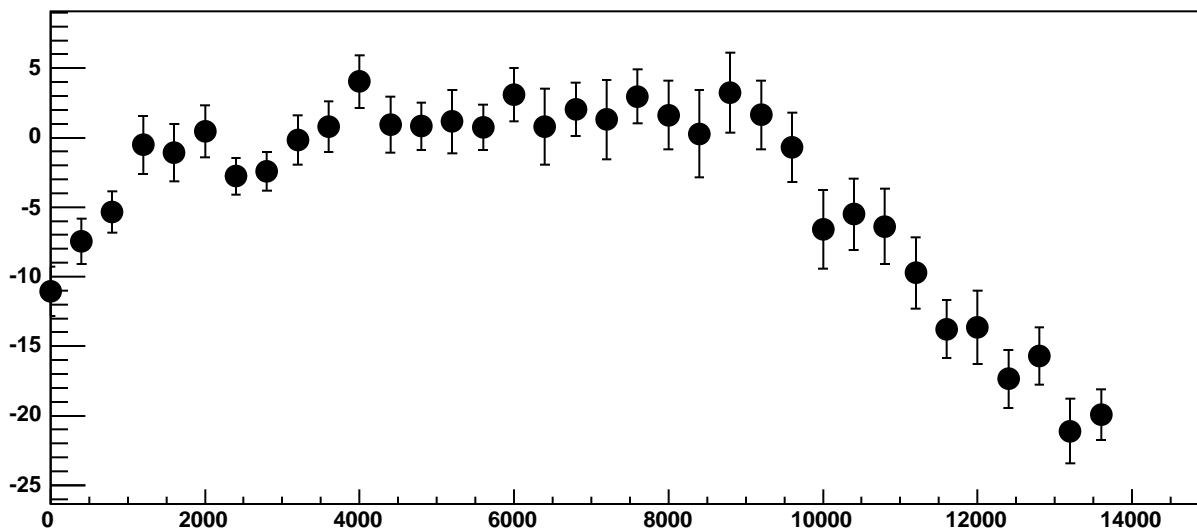
Chip 5, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC



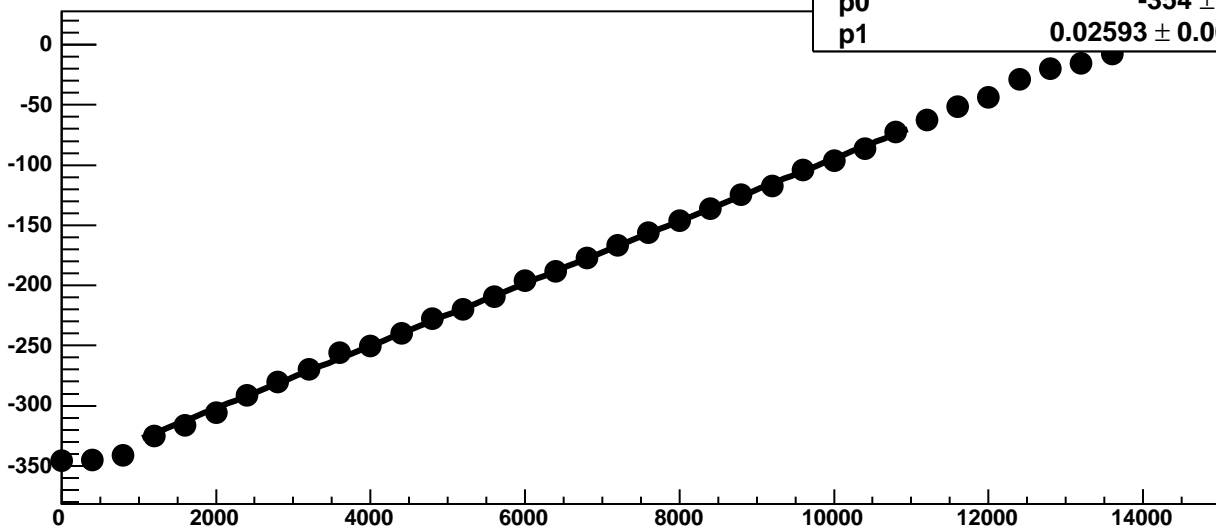
Chip 5, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC

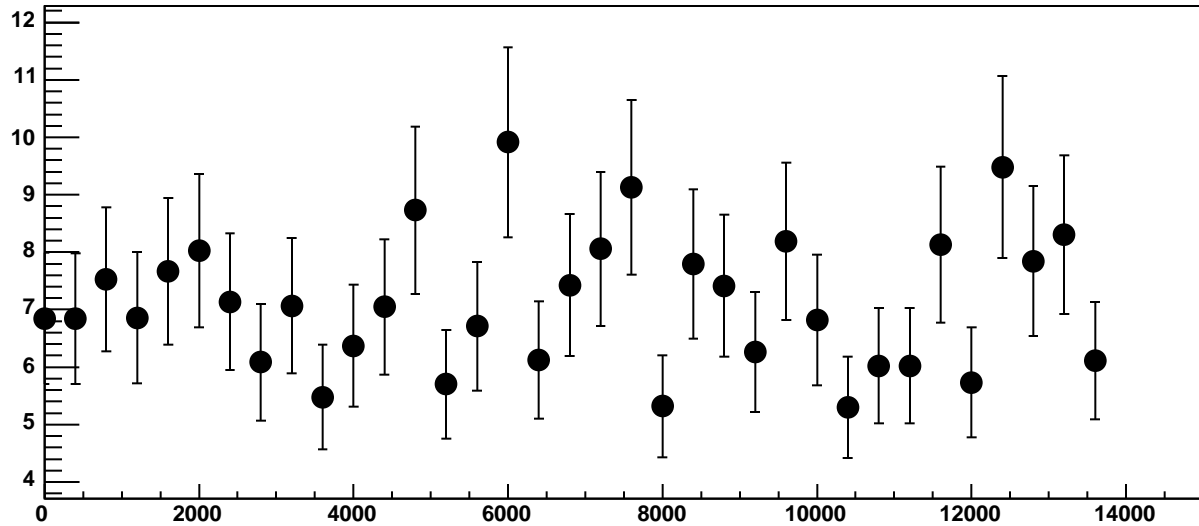


Chip 5, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

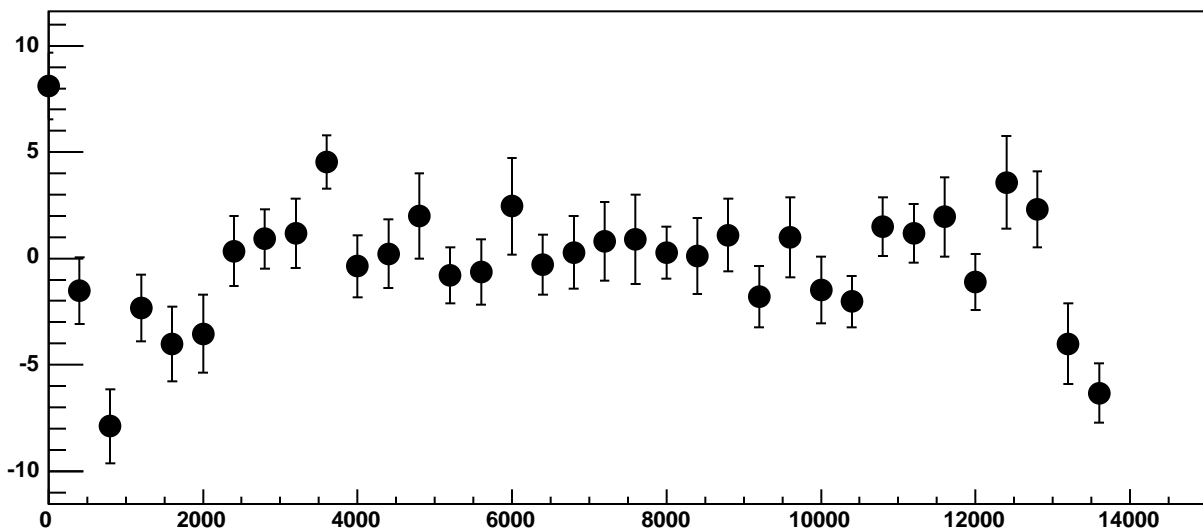


χ^2 / ndf 35.55 / 23
p0 -354 ± 0.7198
p1 0.02593 ± 0.0001058

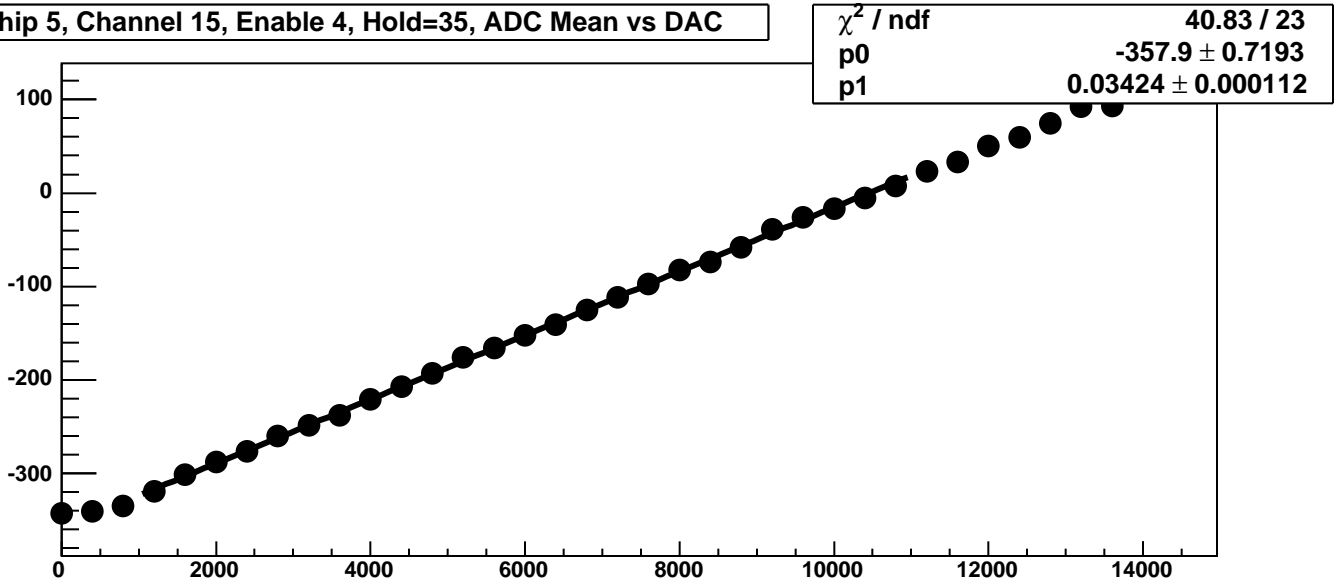
Chip 5, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



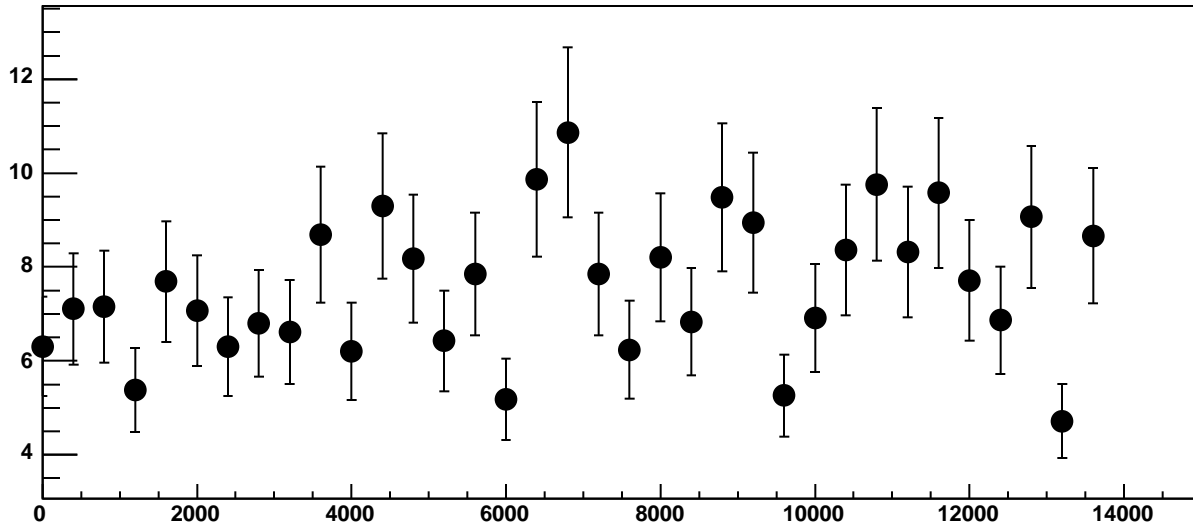
Chip 5, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



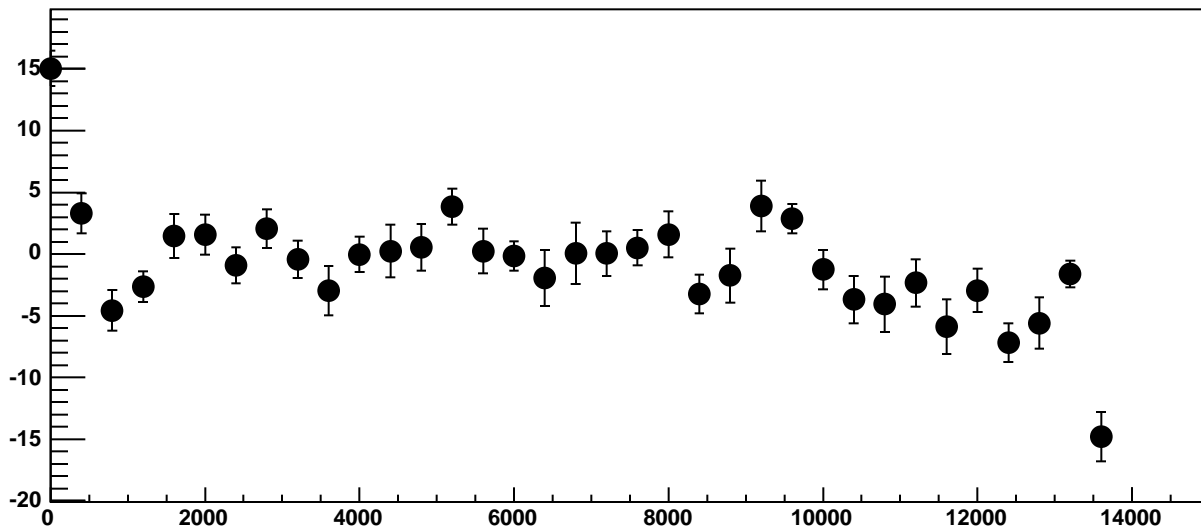
Chip 5, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



Chip 5, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

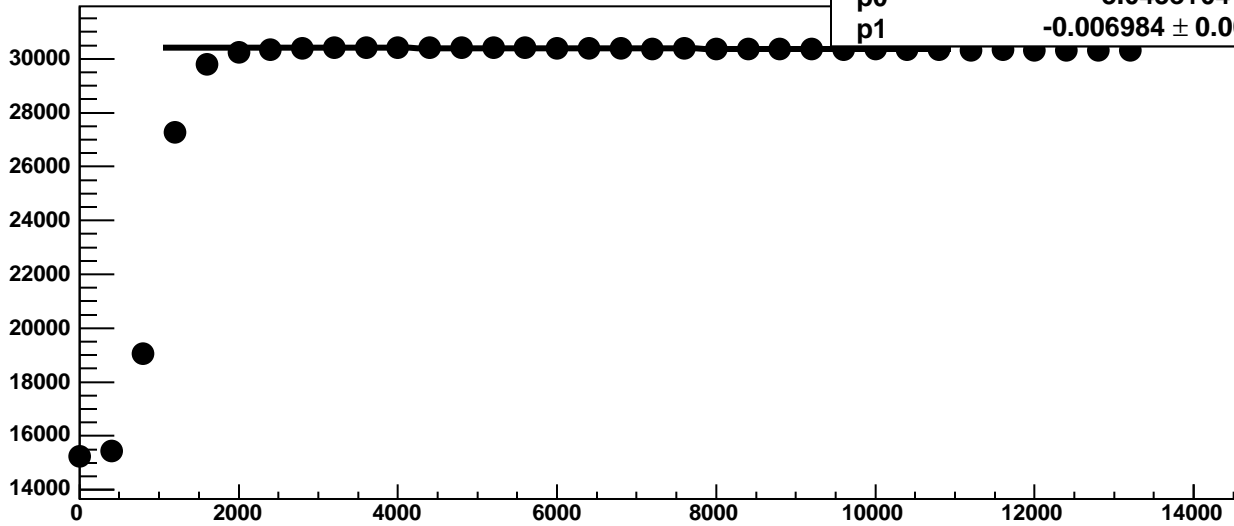
4260 / 23

p0

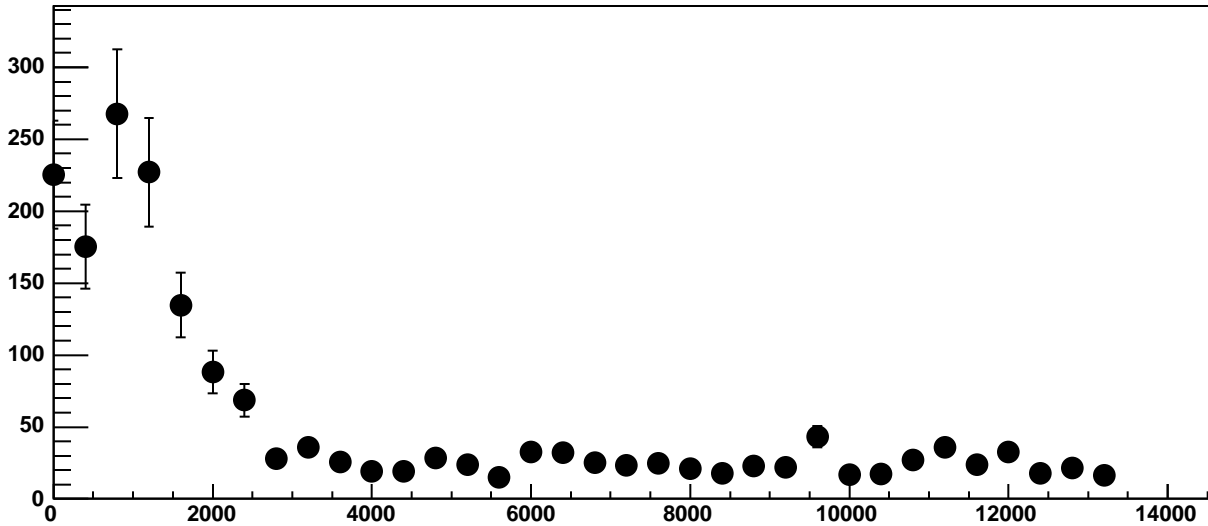
$3.043\text{e}+04 \pm 3.43$

p1

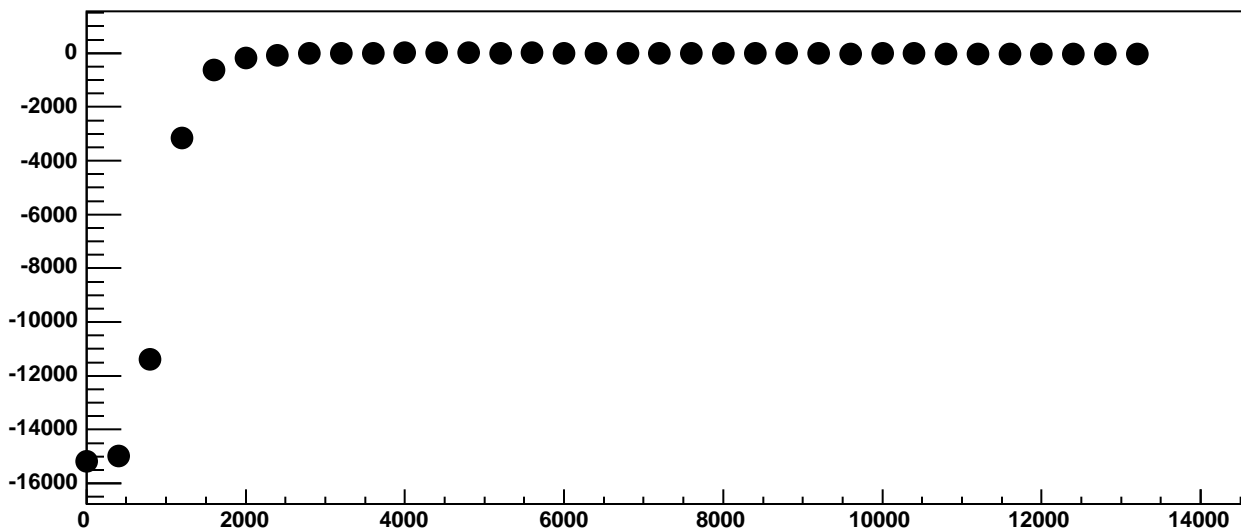
-0.006984 ± 0.0004638



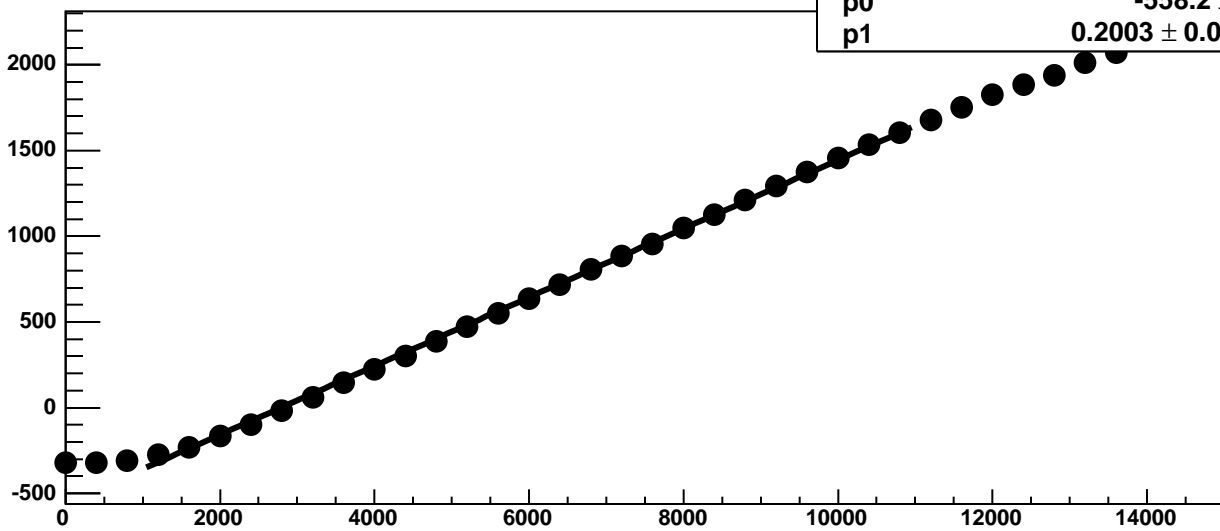
Chip 5, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC



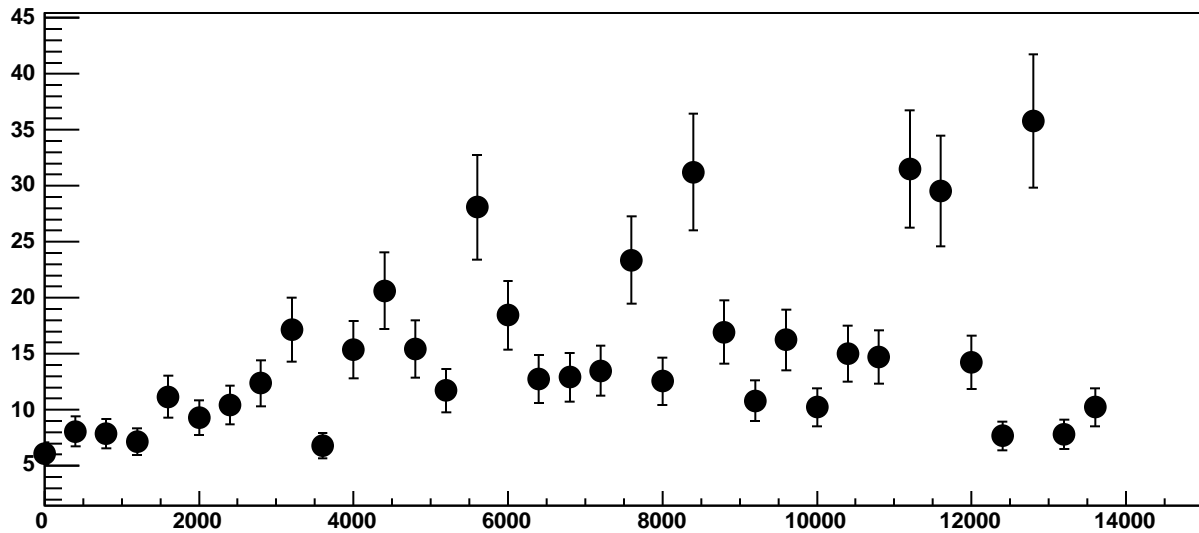
Chip 5, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC



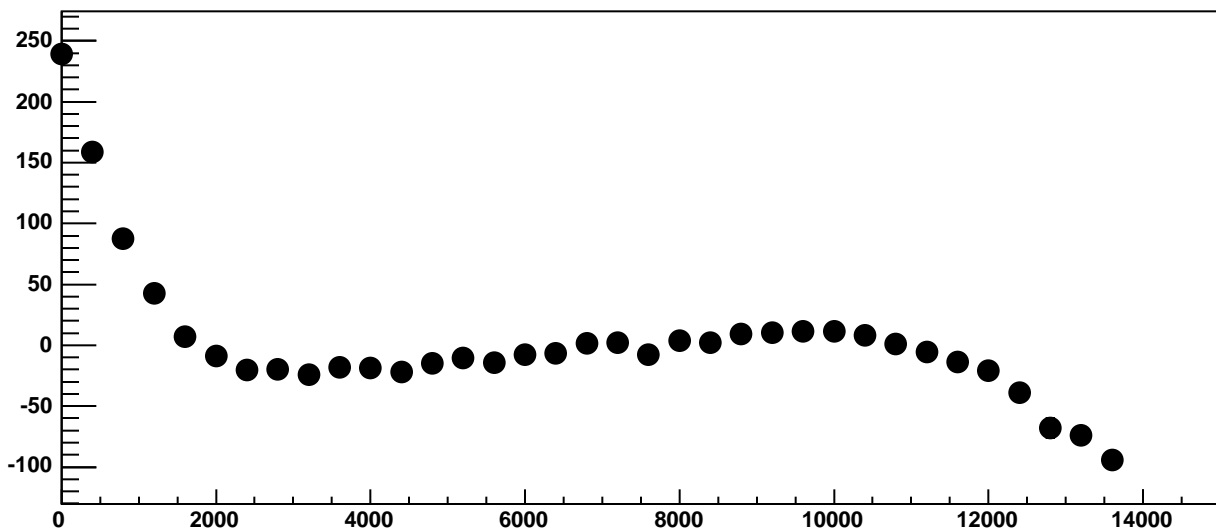
Chip 5, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



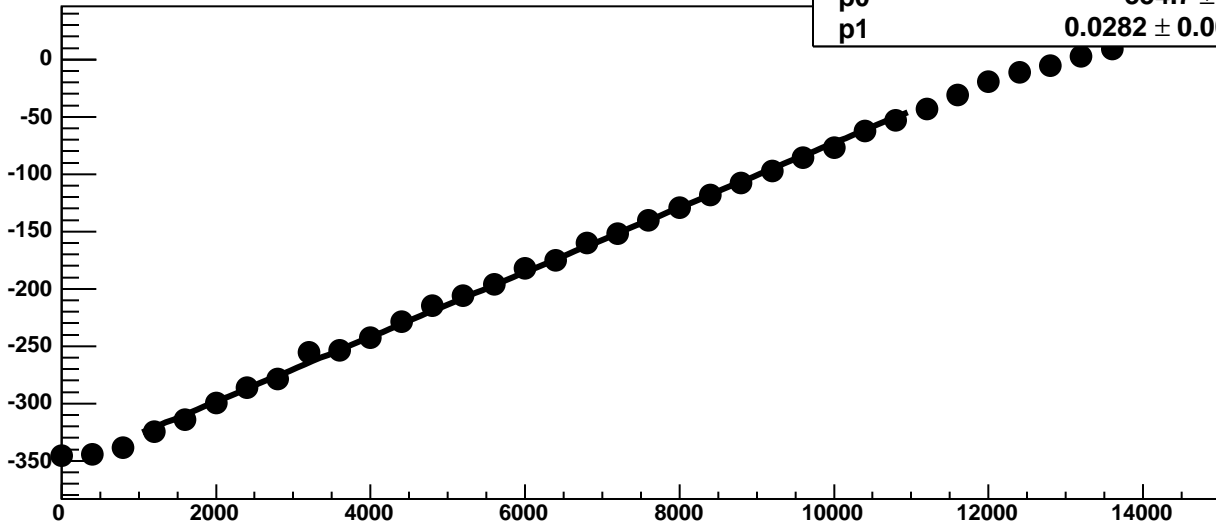
Chip 5, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



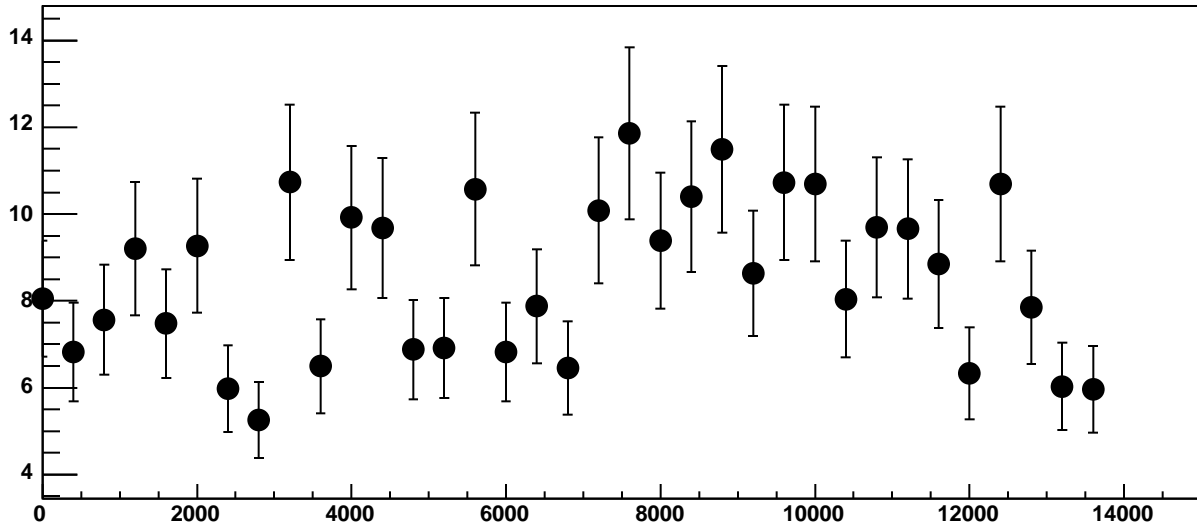
Chip 5, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



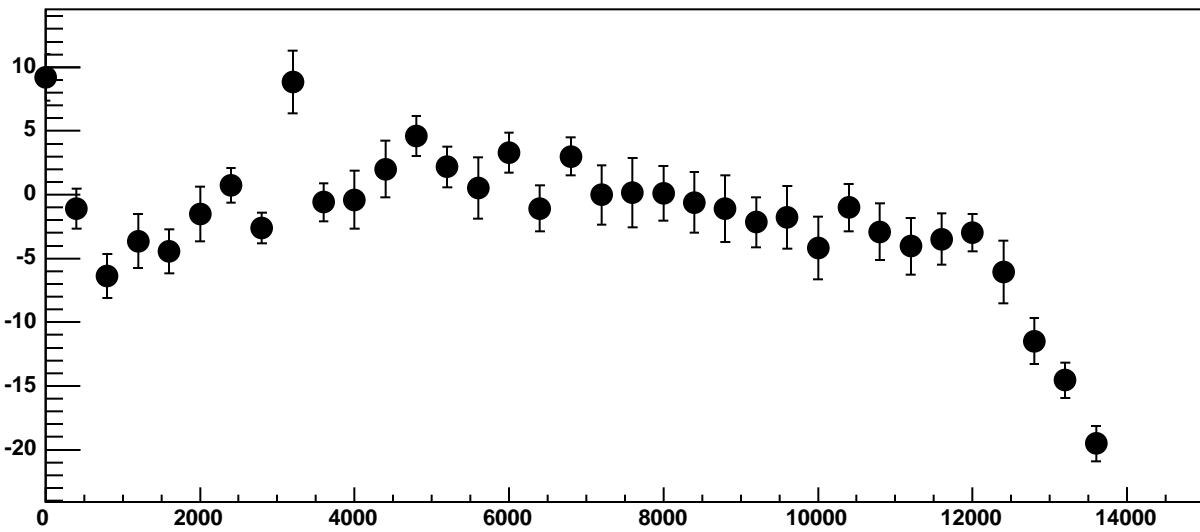
Chip 5, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



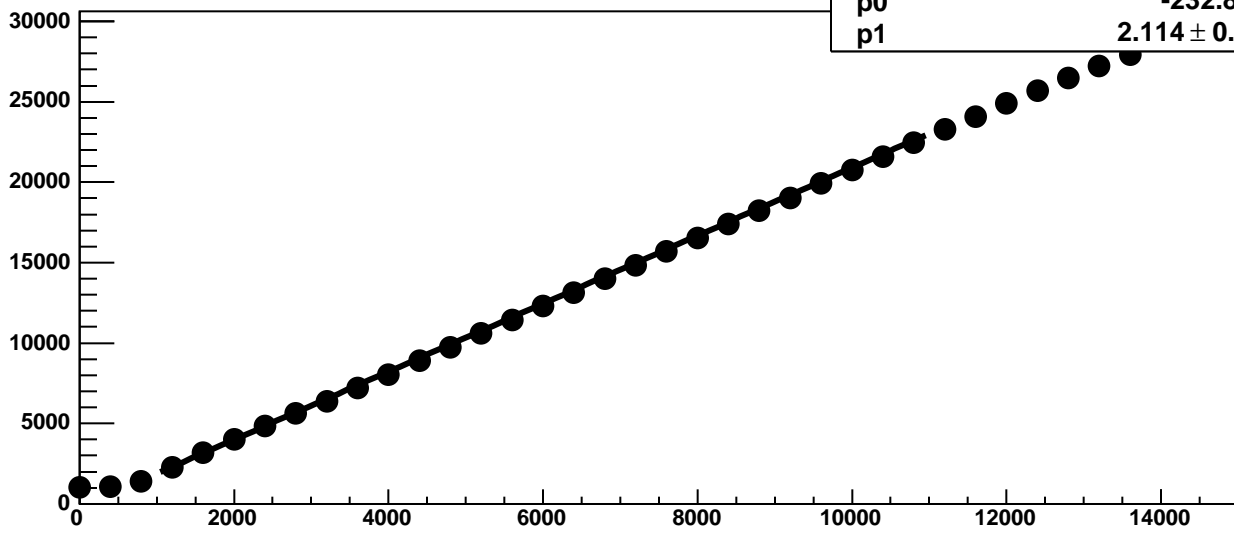
Chip 5, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

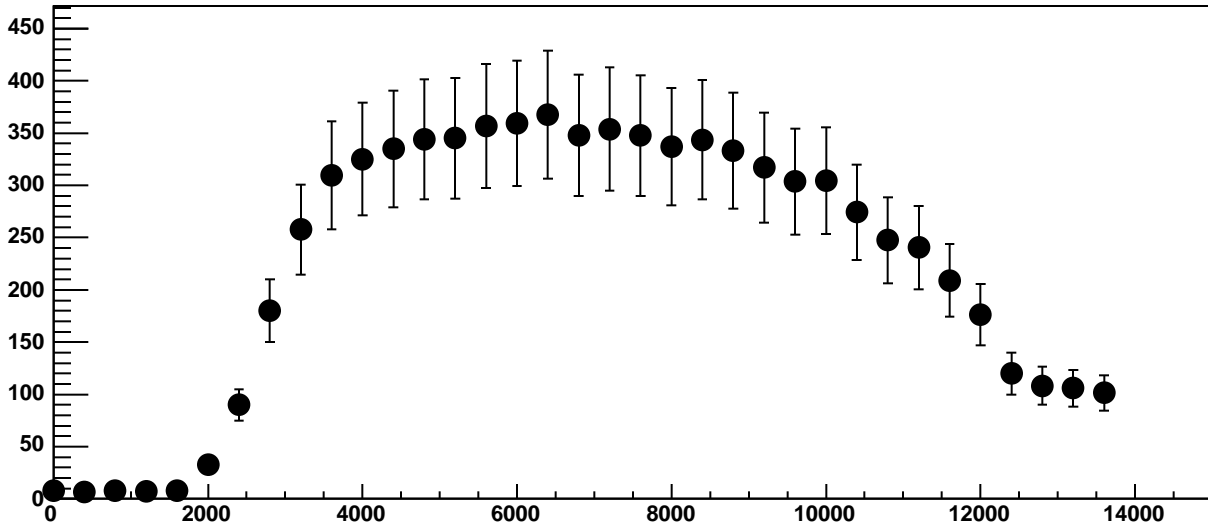


Chip 5, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC

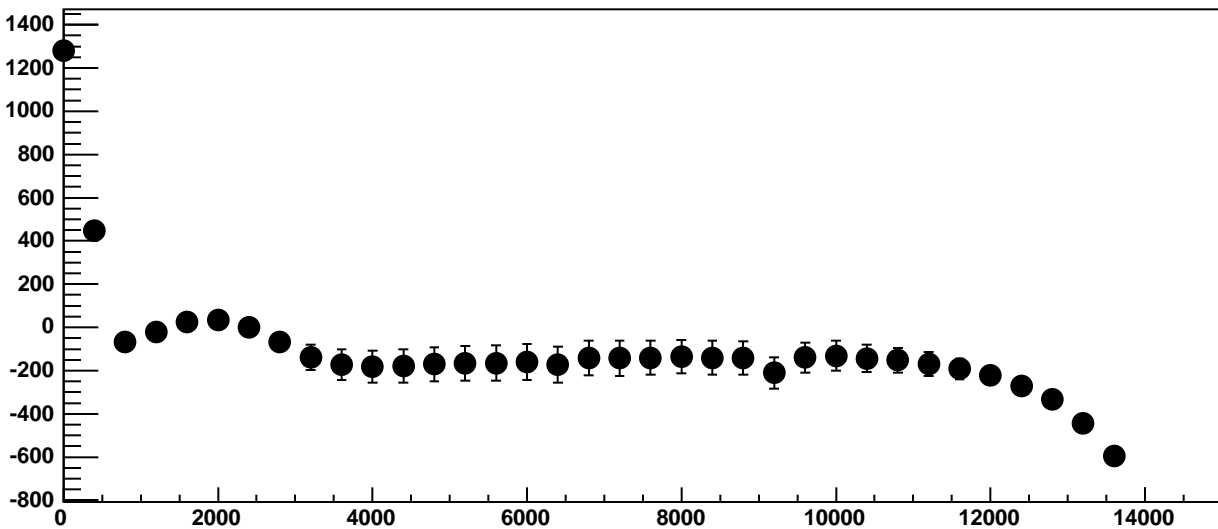


χ^2 / ndf 454.2 / 23
p0 -232.8 ± 3.61
p1 2.114 ± 0.002351

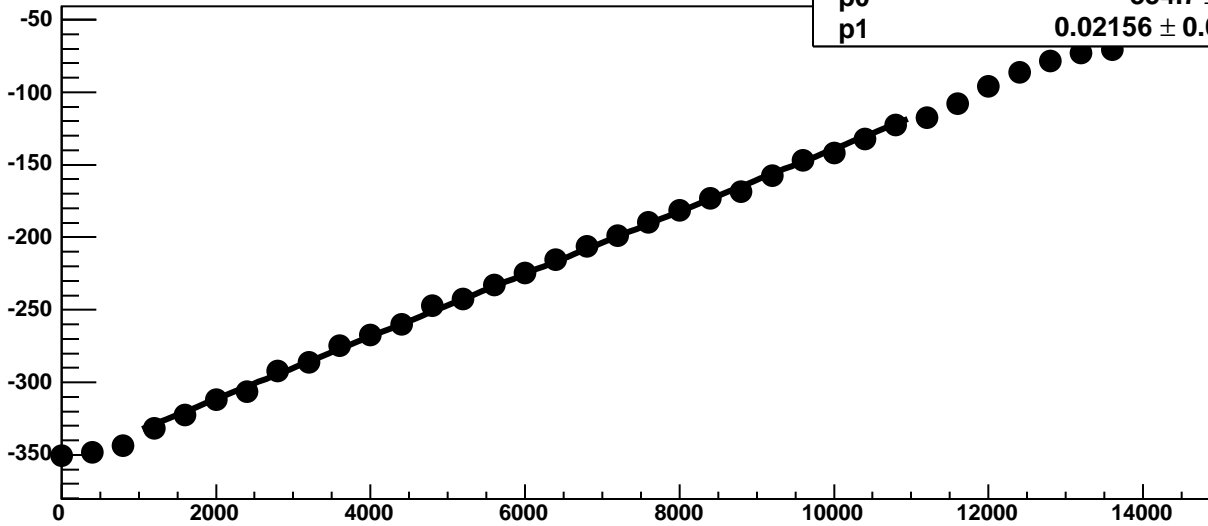
Chip 5, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC

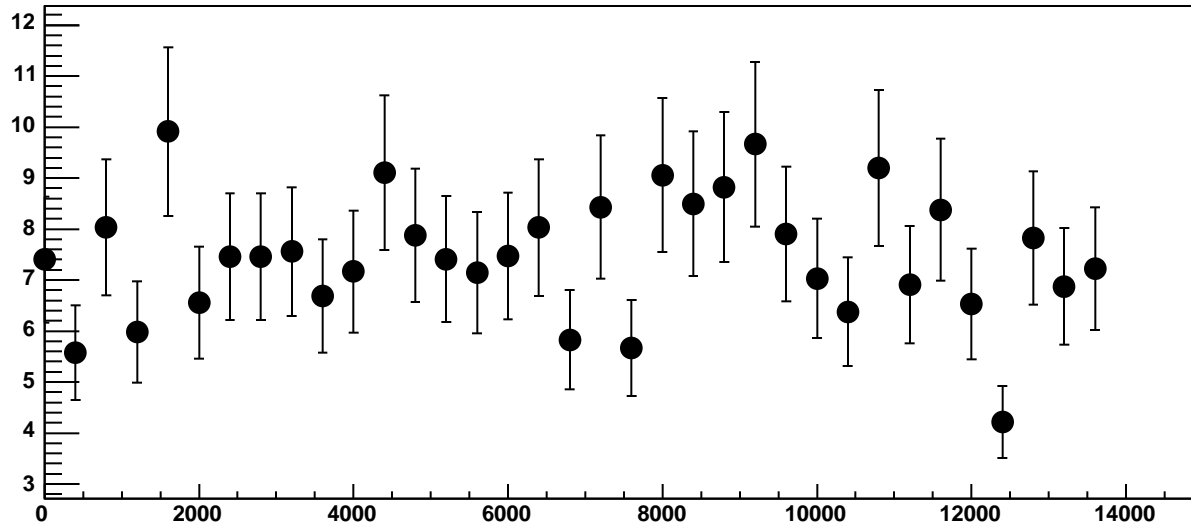


Chip 5, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

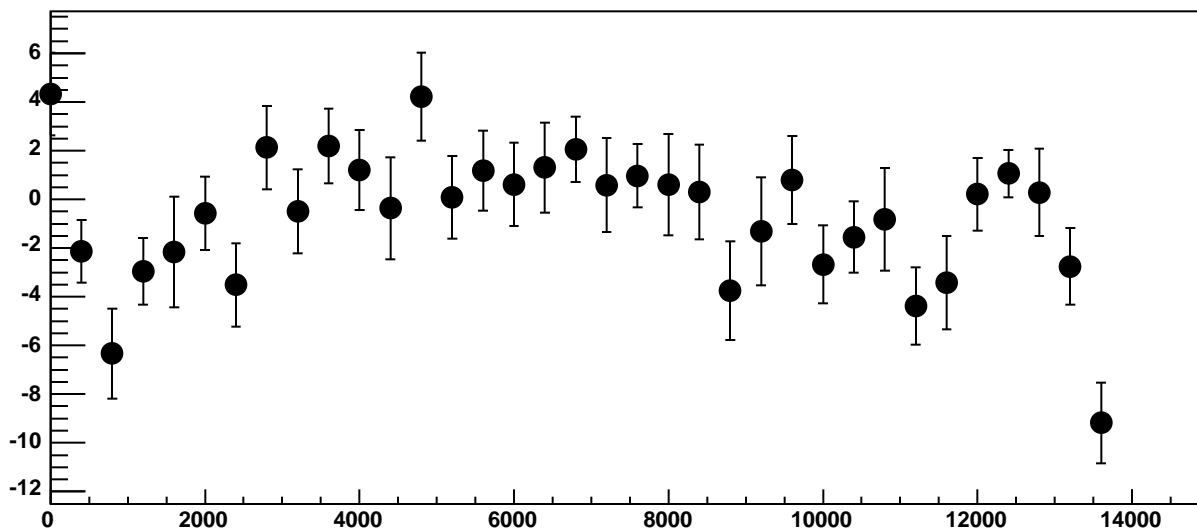


χ^2 / ndf 31.88 / 23
p0 -354.7 ± 0.7701
p1 0.02156 ± 0.0001181

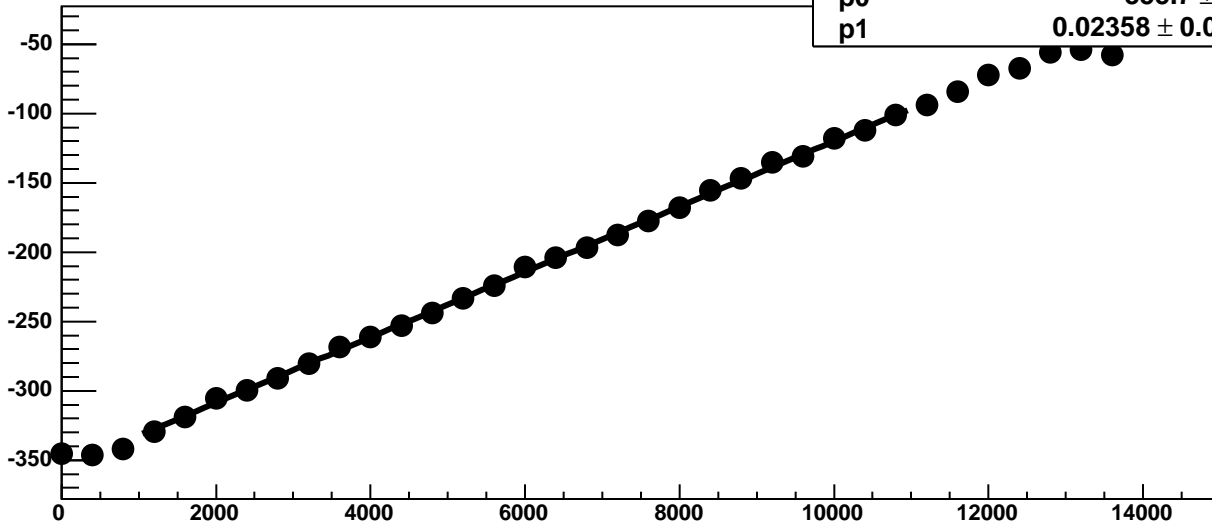
Chip 5, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

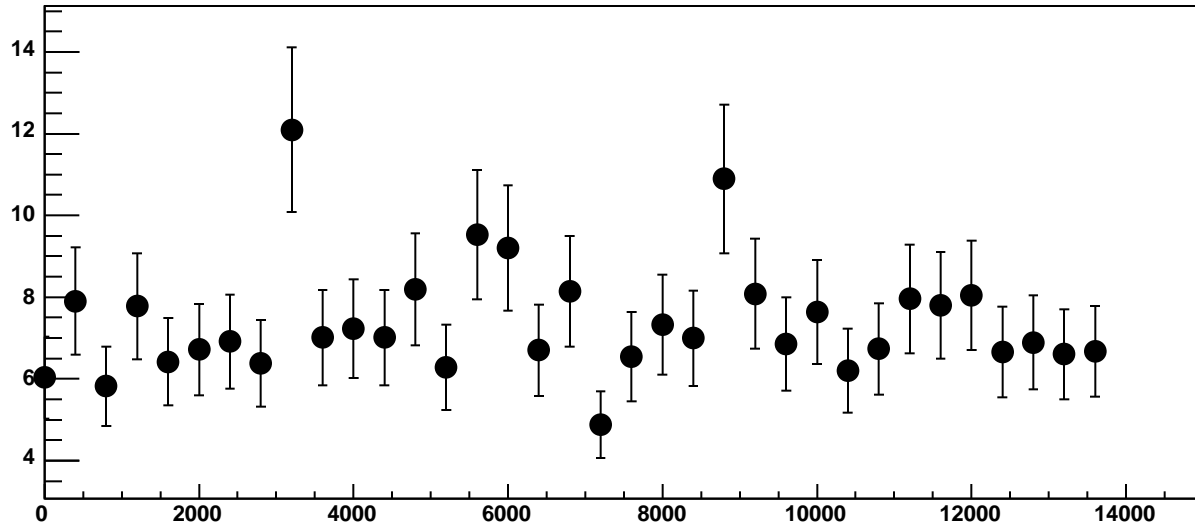


Chip 5, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

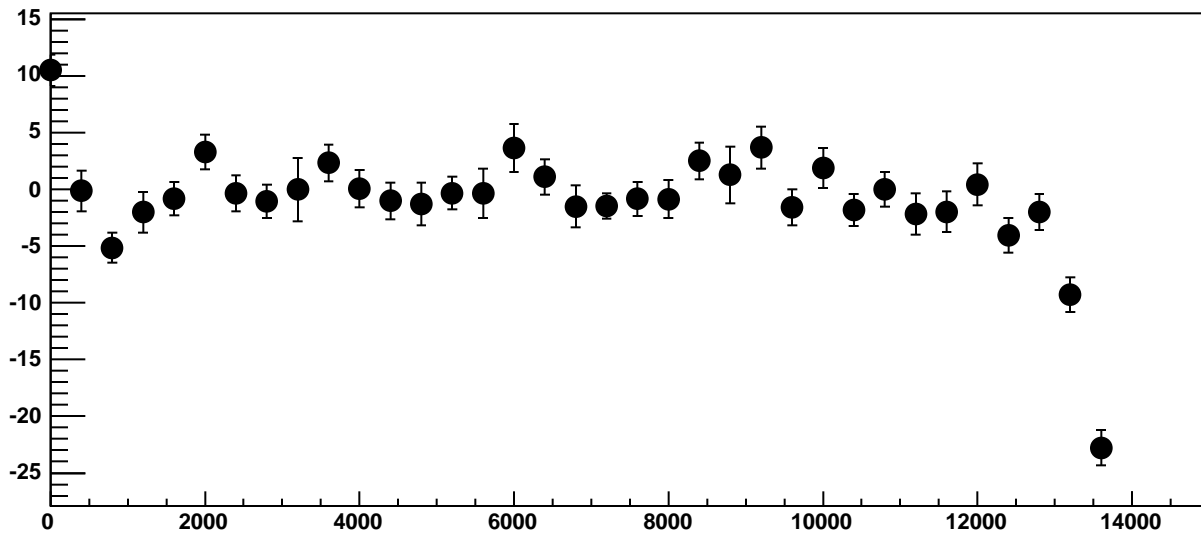


χ^2 / ndf 26.58 / 23
p0 -355.7 ± 0.7536
p1 0.02358 ± 0.0001122

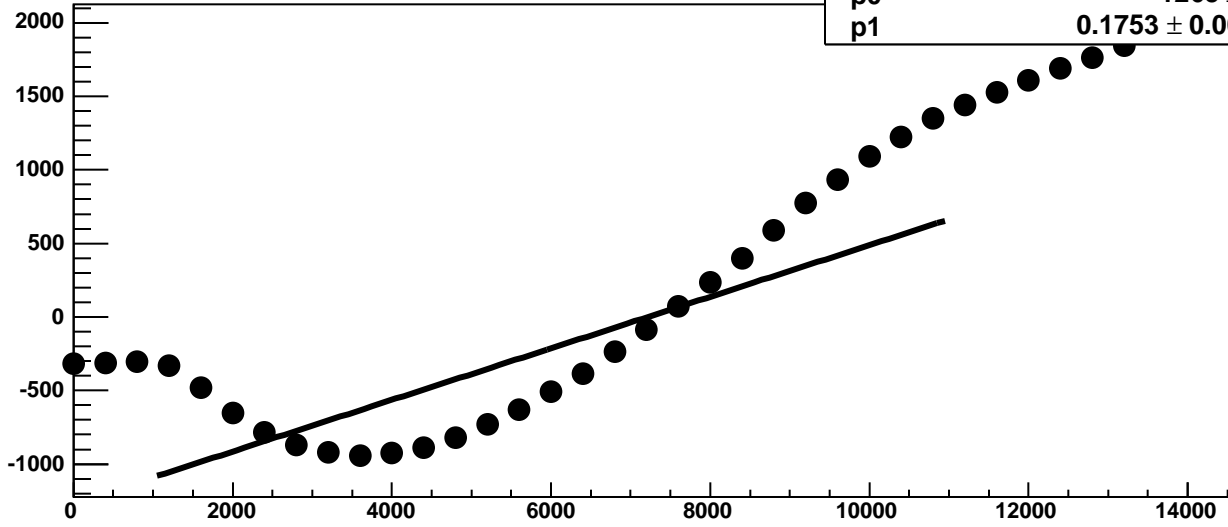
Chip 5, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

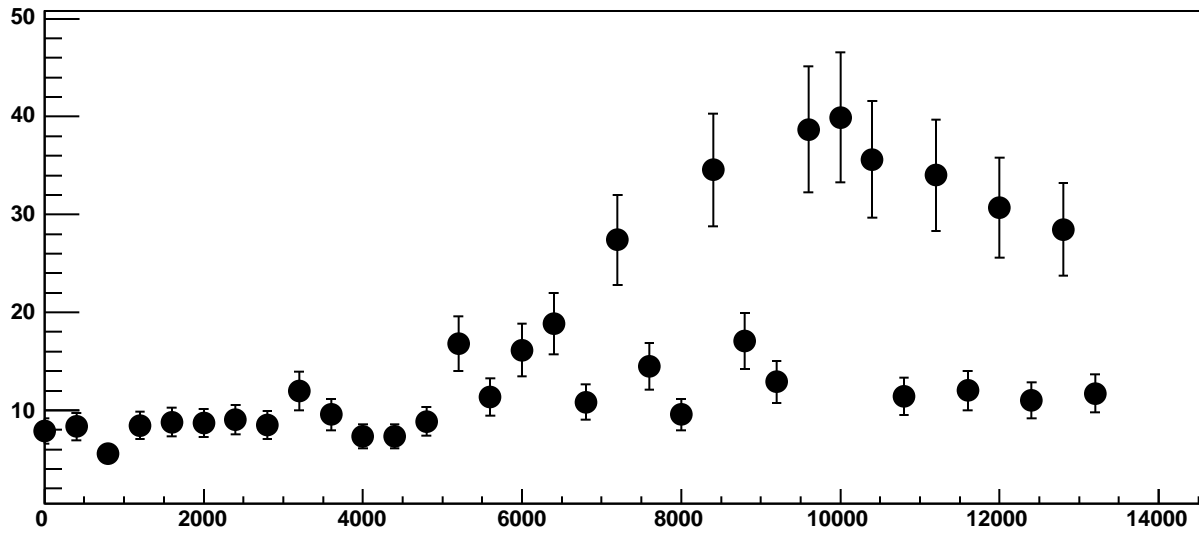


Chip 5, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC

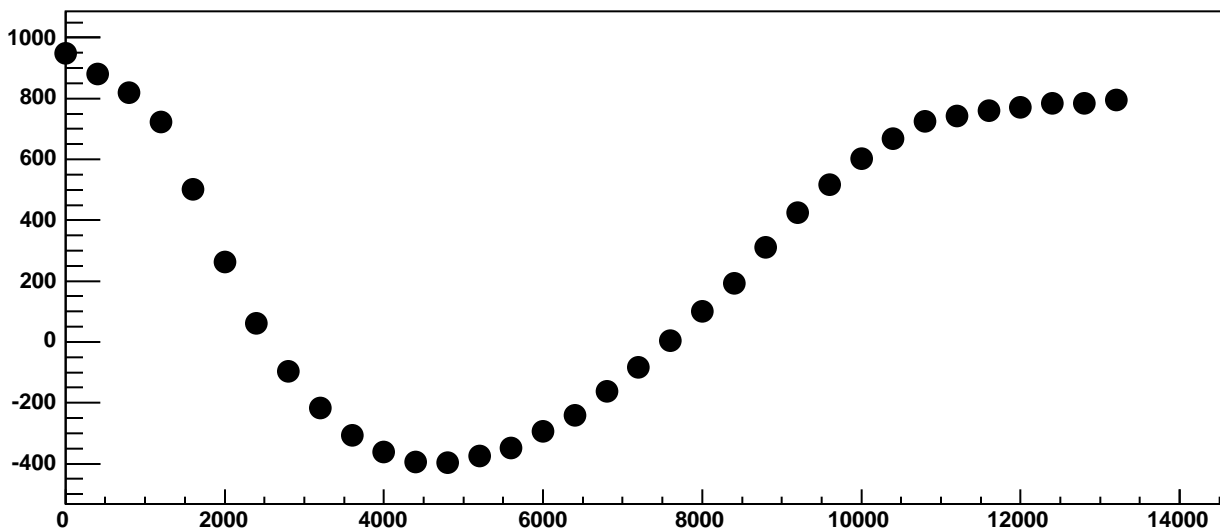


χ^2 / ndf 5.479e+05 / 23
p0 -1265 ± 1.052
p1 0.1753 ± 0.0002008

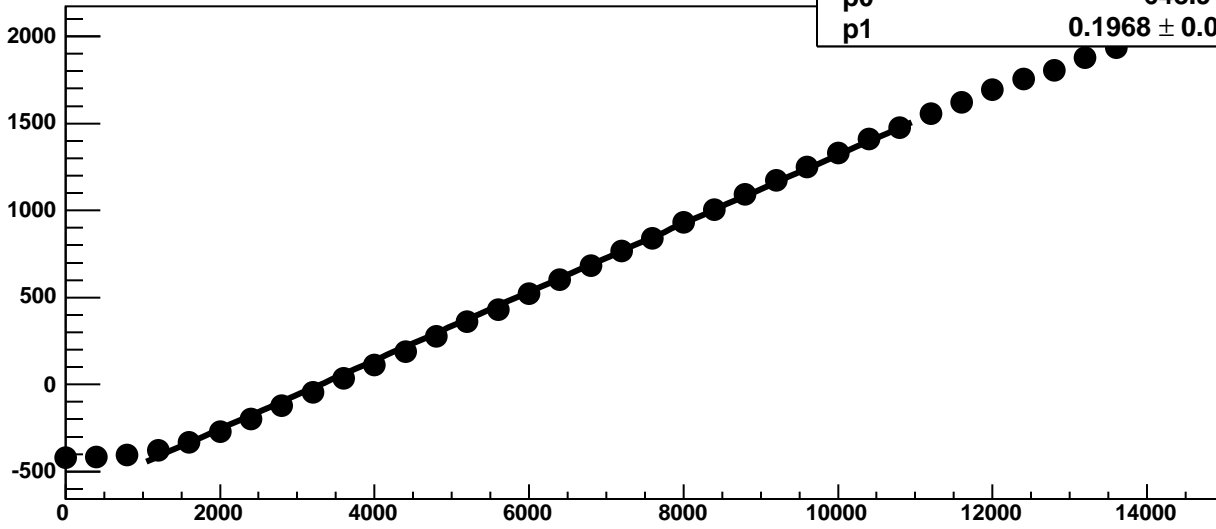
Chip 5, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC

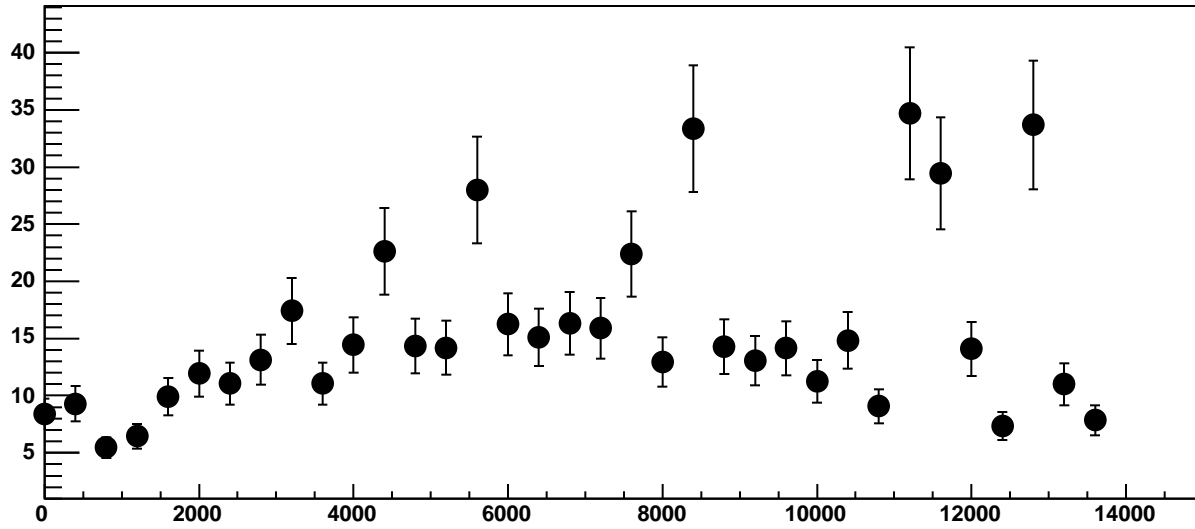


Chip 5, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

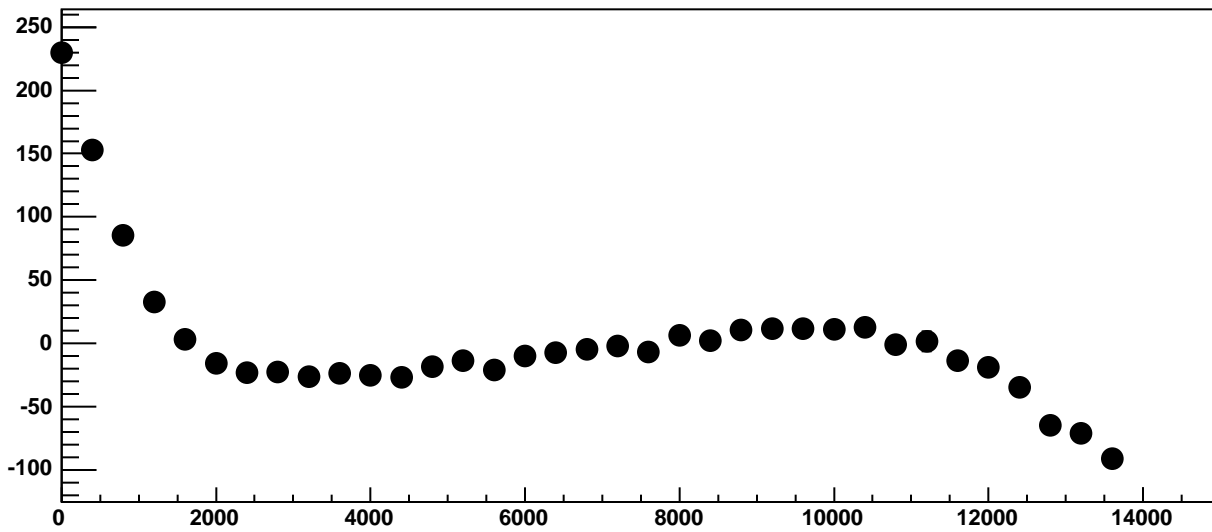


χ^2 / ndf 1022 / 23
p0 -648.9 ± 1.093
p1 0.1968 ± 0.0001716

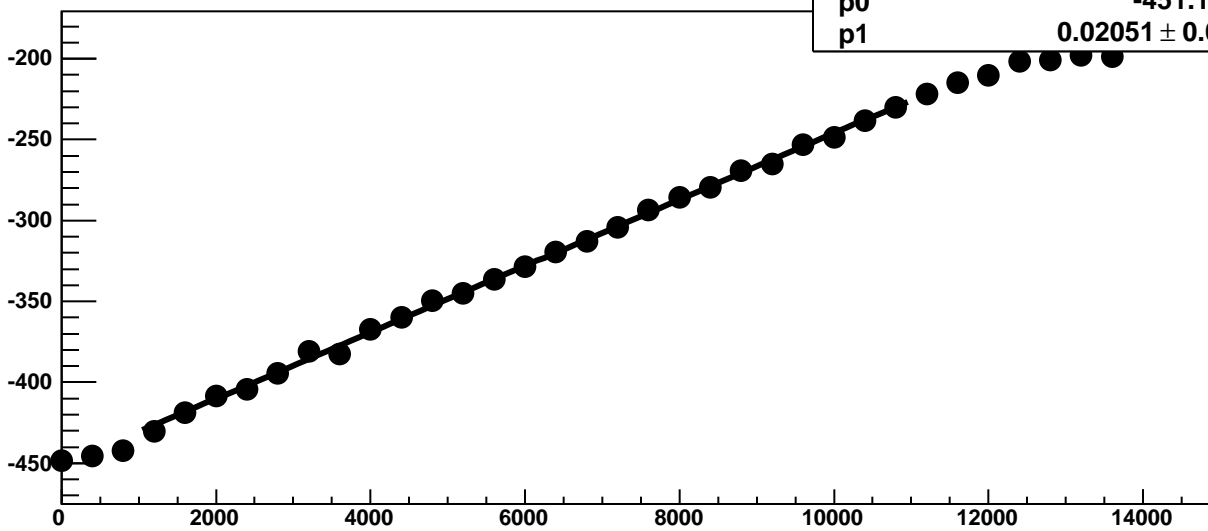
Chip 5, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

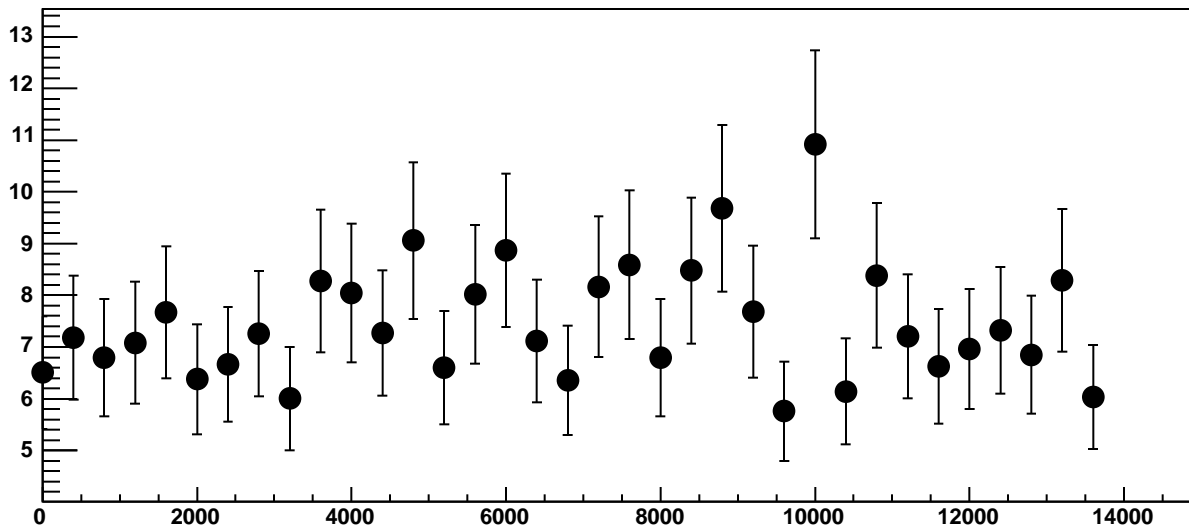


Chip 5, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

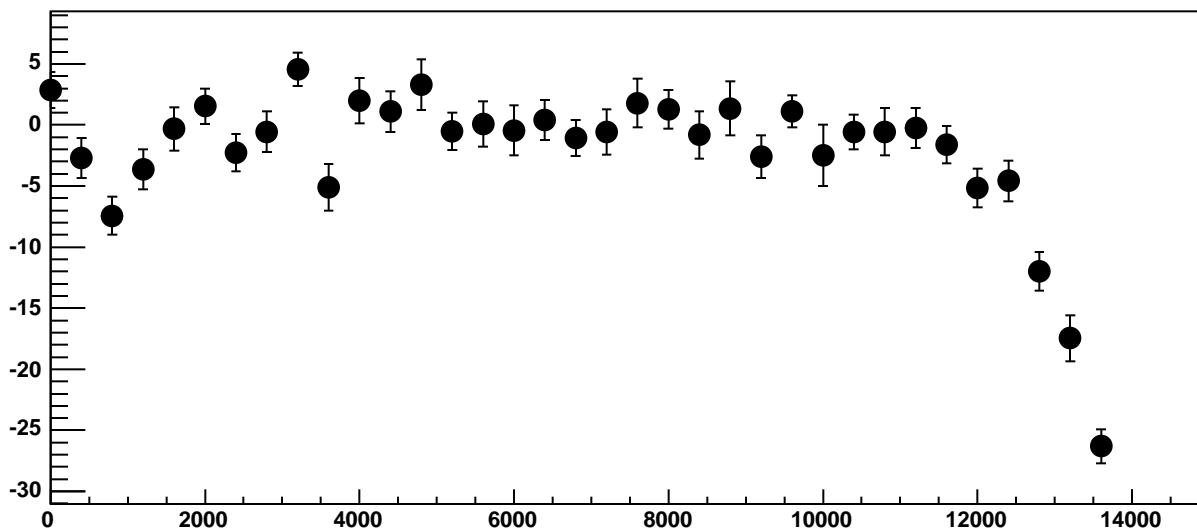


χ^2 / ndf 37.78 / 23
p0 -451.1 ± 0.754
p1 0.02051 ± 0.0001151

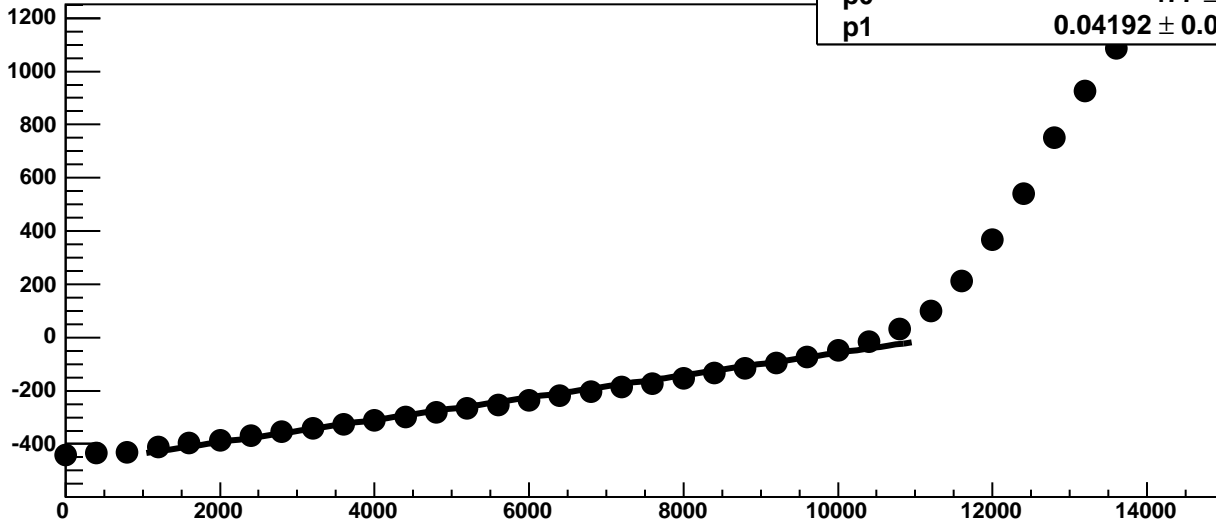
Chip 5, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



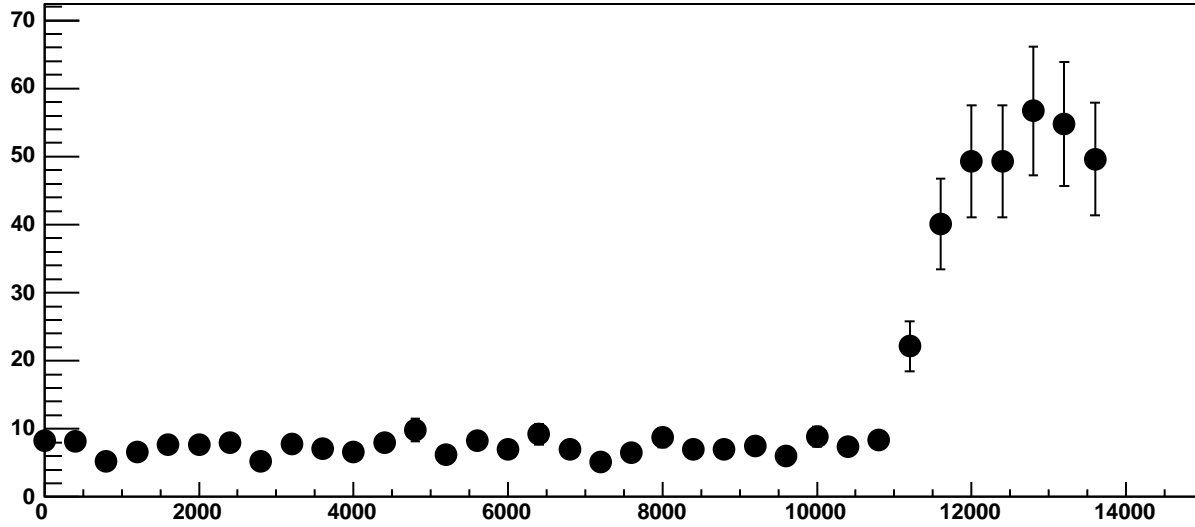
Chip 5, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



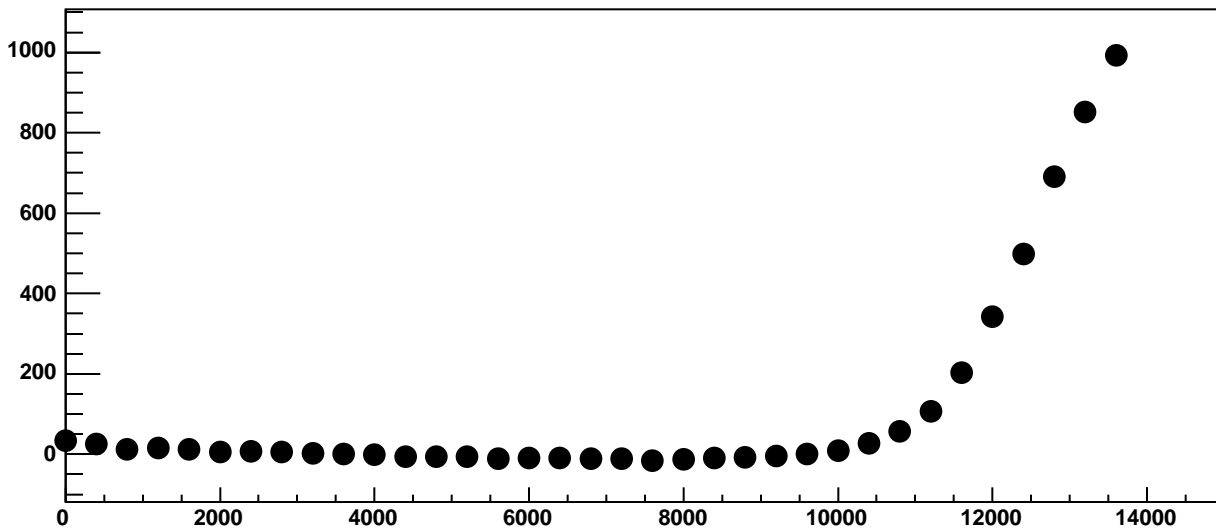
Chip 5, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC



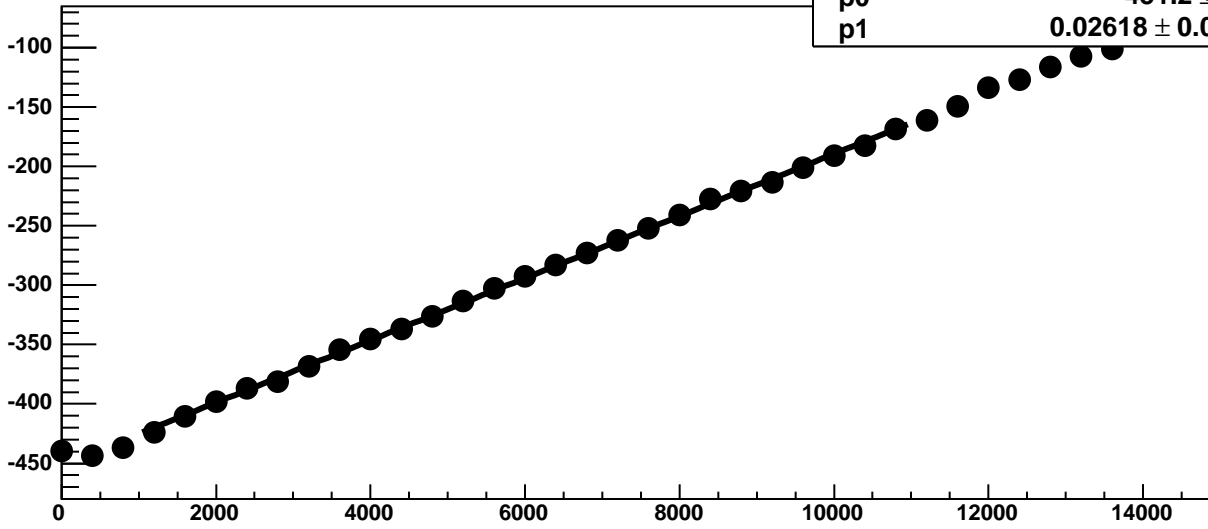
Chip 5, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC

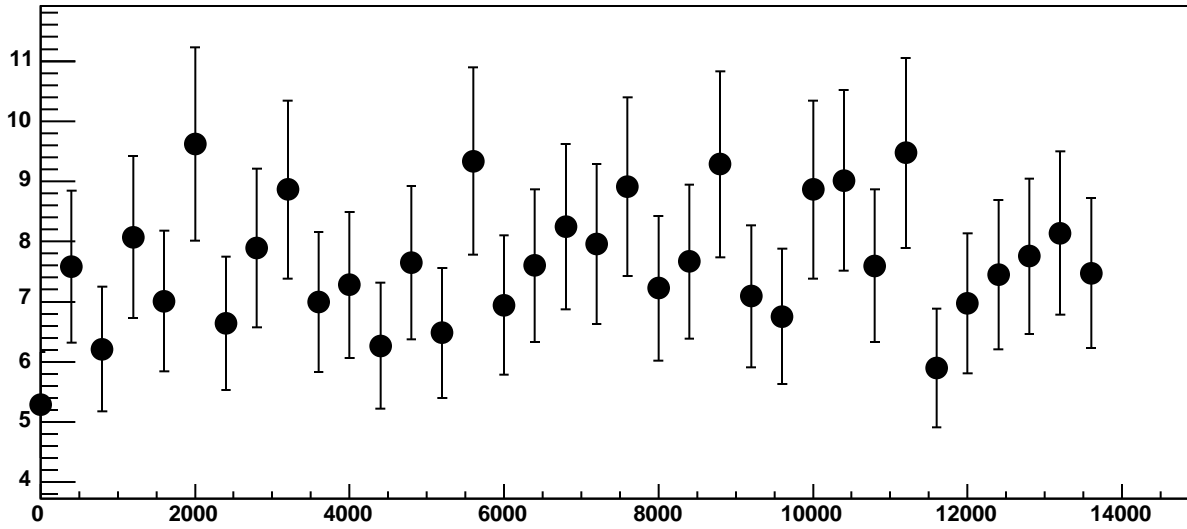


Chip 5, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

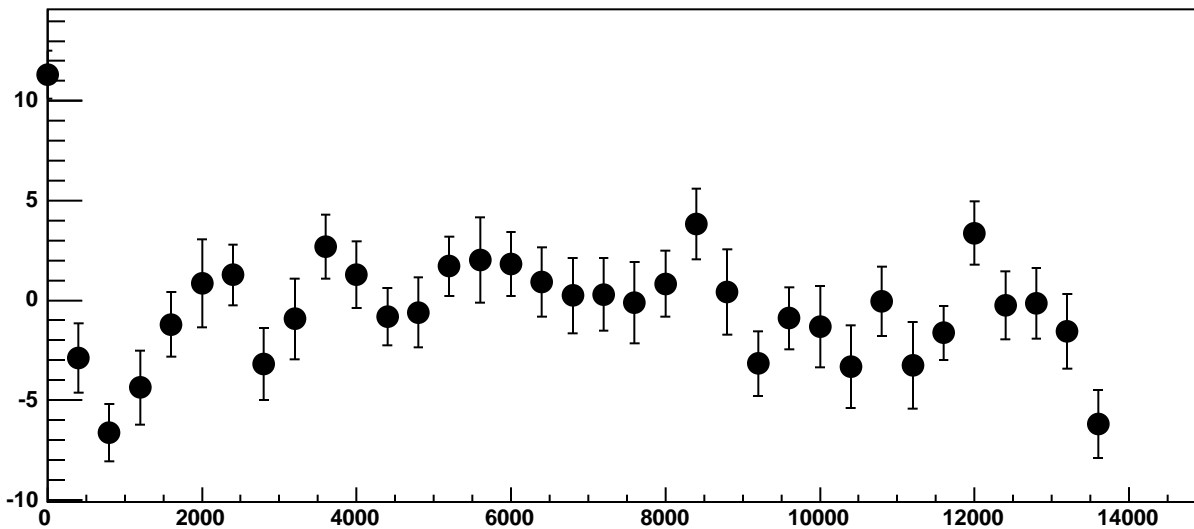


χ^2 / ndf 30.15 / 23
p0 -451.2 ± 0.8092
p1 0.02618 ± 0.0001238

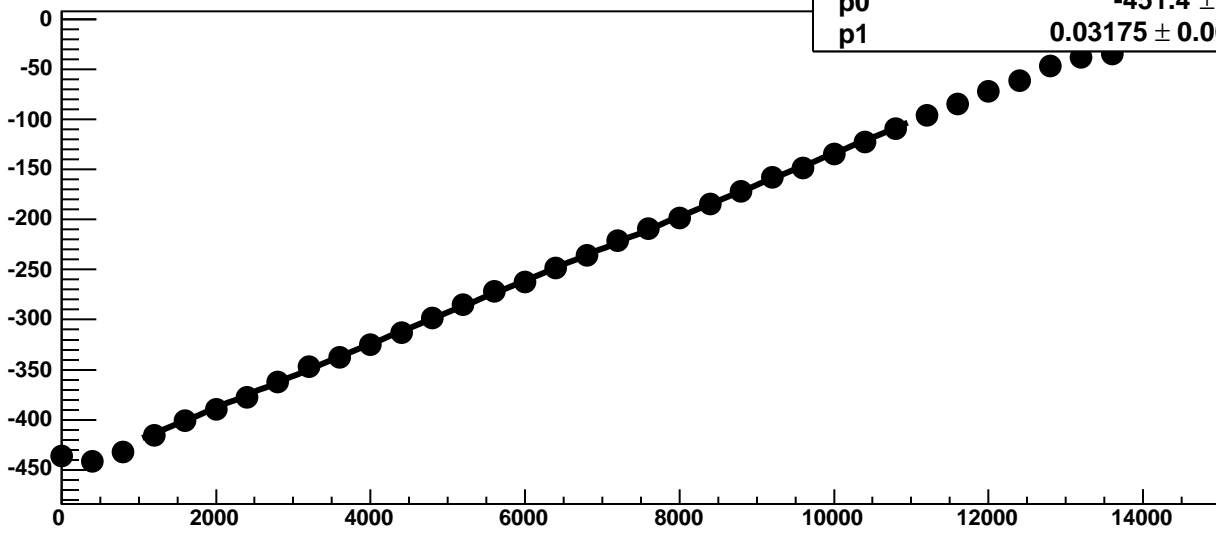
Chip 5, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

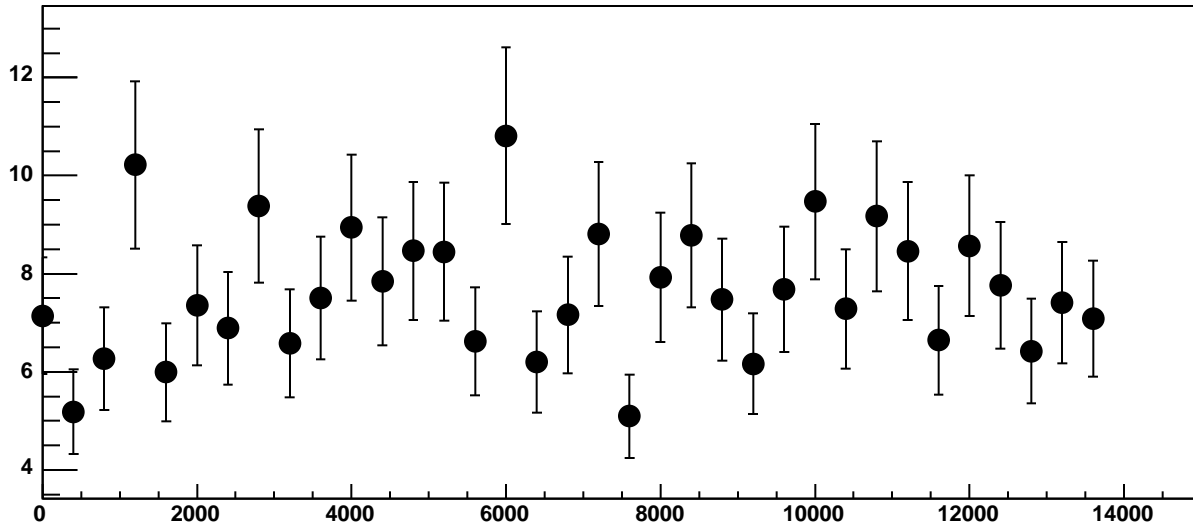


Chip 5, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

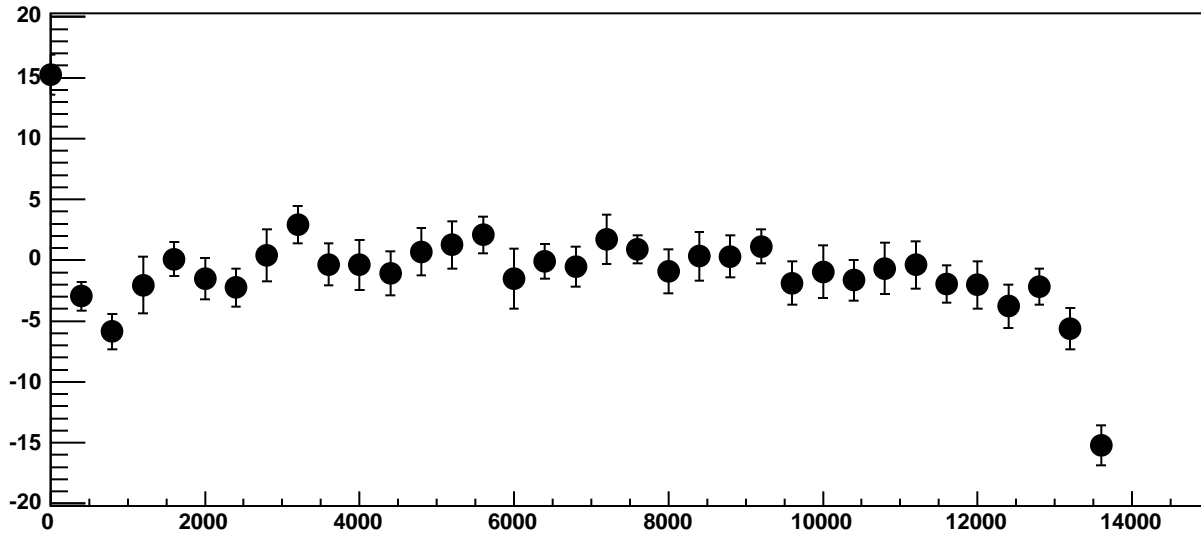


χ^2 / ndf 15.38 / 23
p0 -451.4 \pm 0.8044
p1 0.03175 \pm 0.0001214

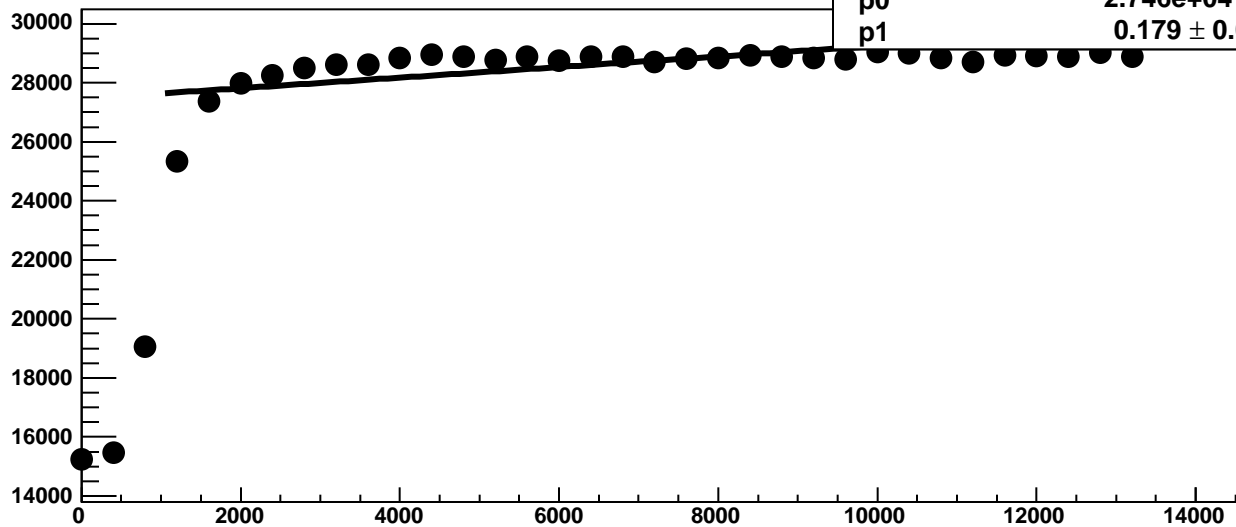
Chip 5, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



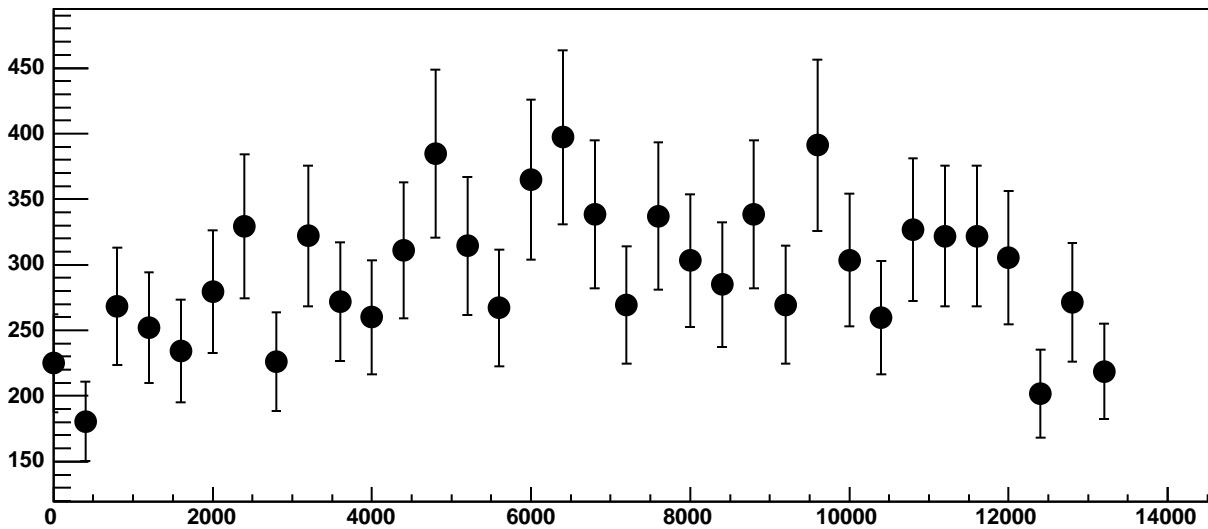
Chip 5, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



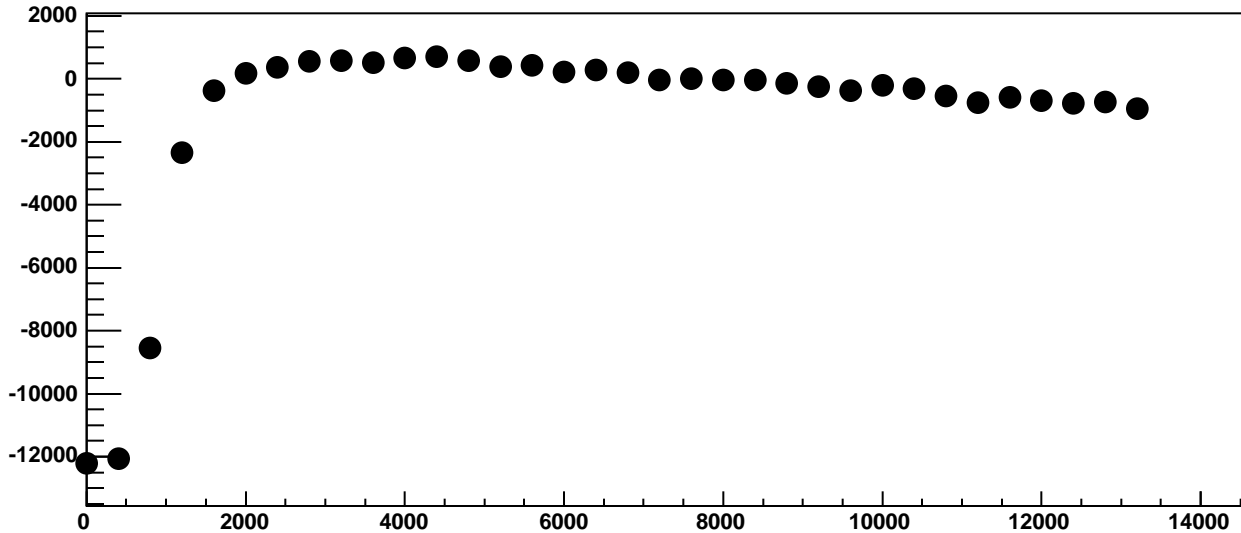
Chip 5, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC



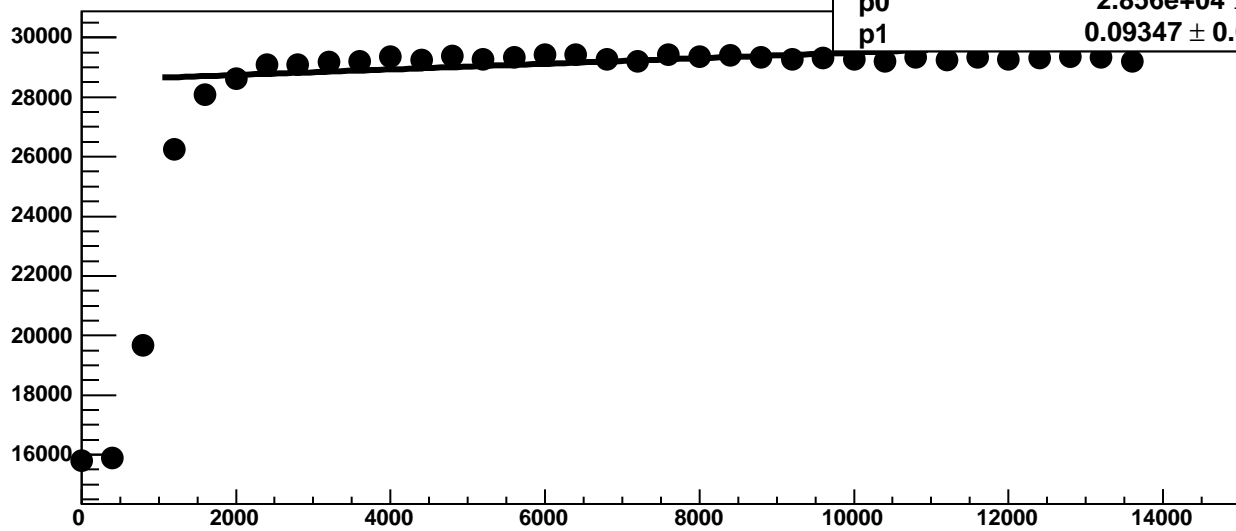
Chip 5, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC



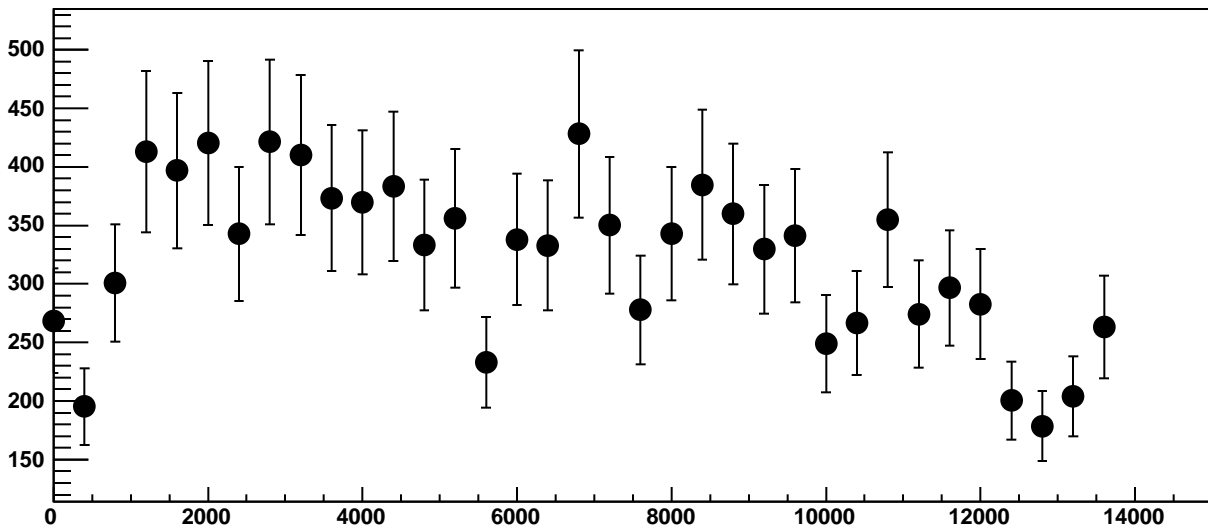
Chip 5, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC



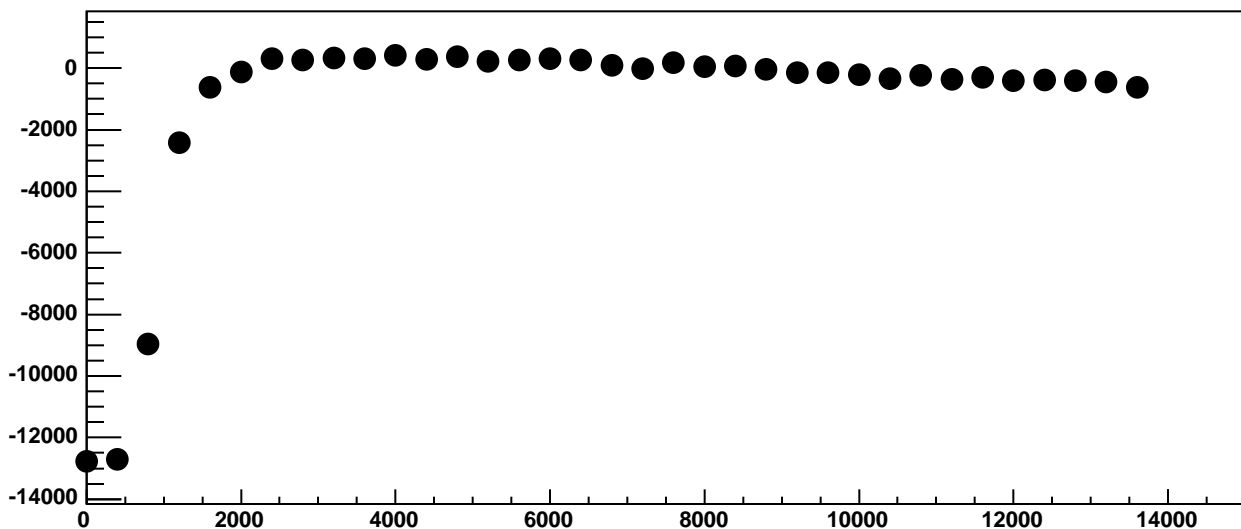
Chip 6, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



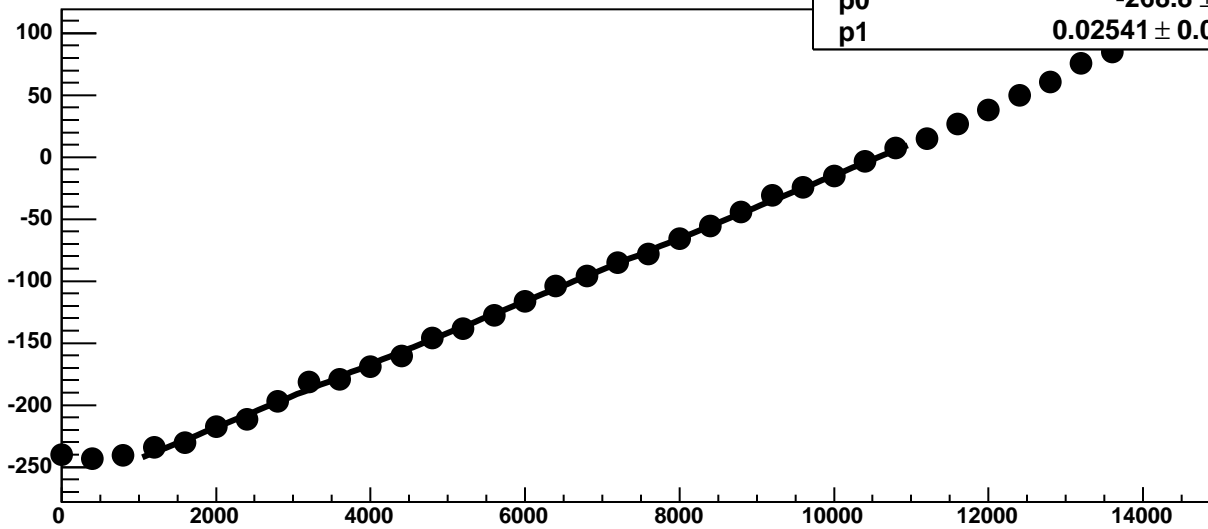
Chip 6, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC

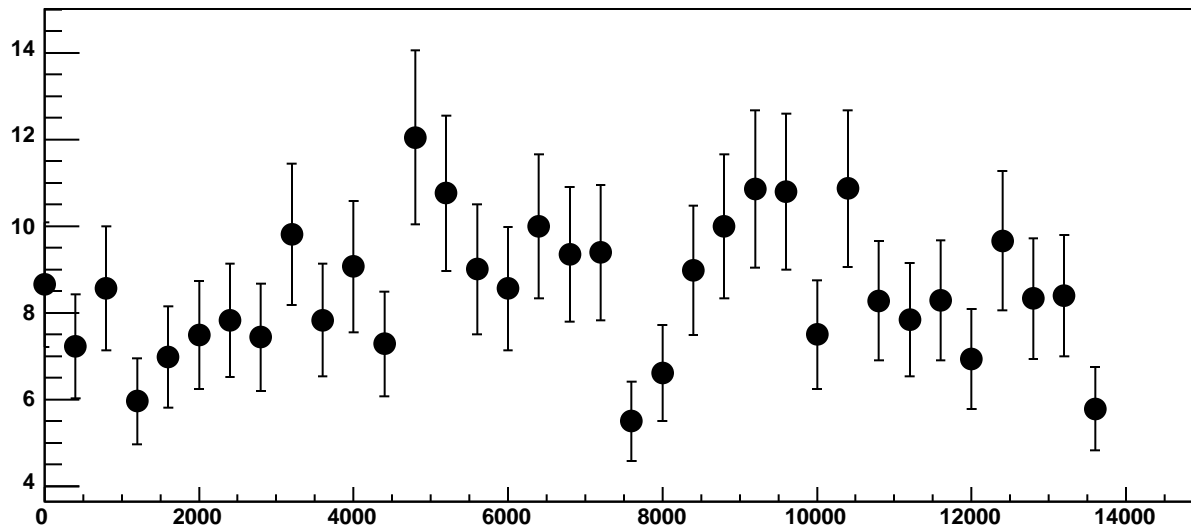


Chip 6, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

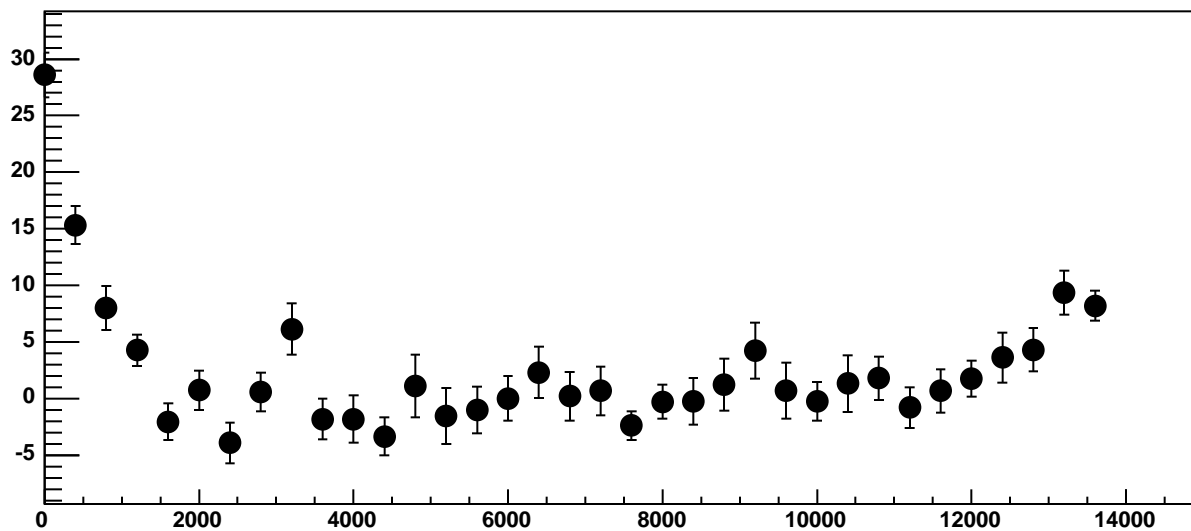


χ^2 / ndf 39.63 / 23
p0 -268.8 ± 0.8049
p1 0.02541 ± 0.0001263

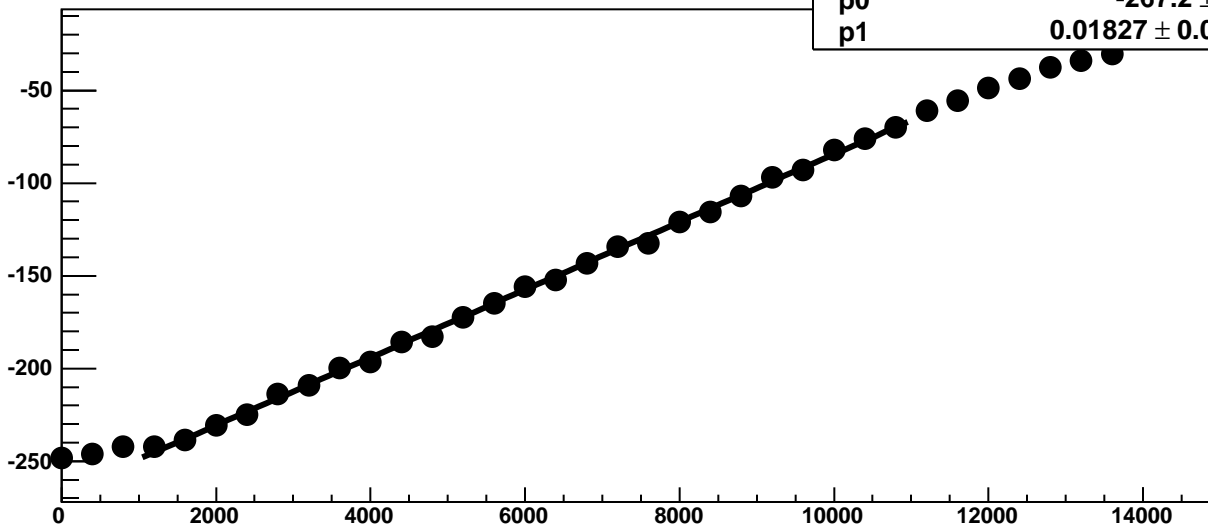
Chip 6, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 6, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

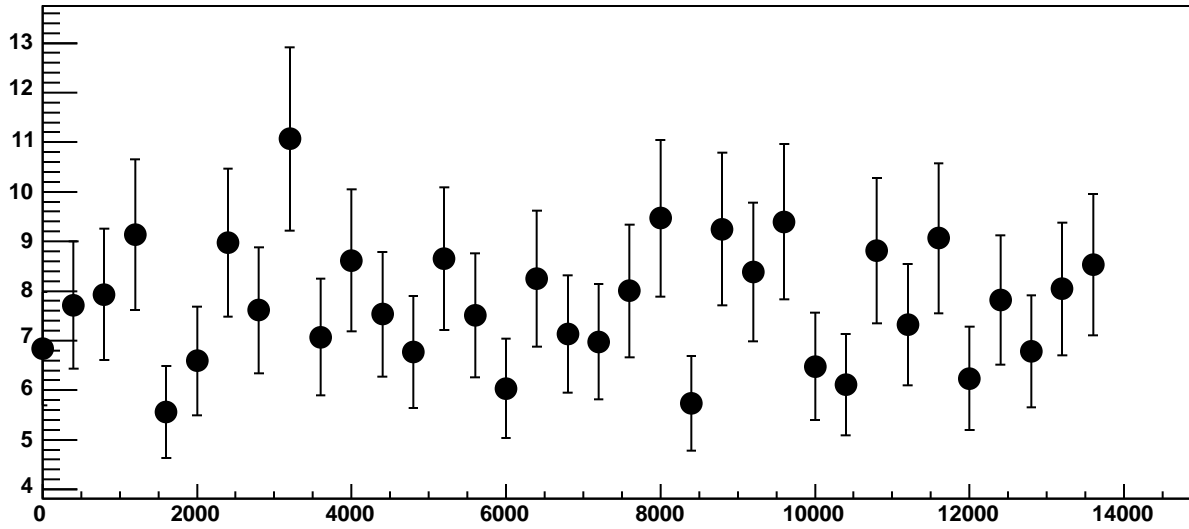


Chip 6, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

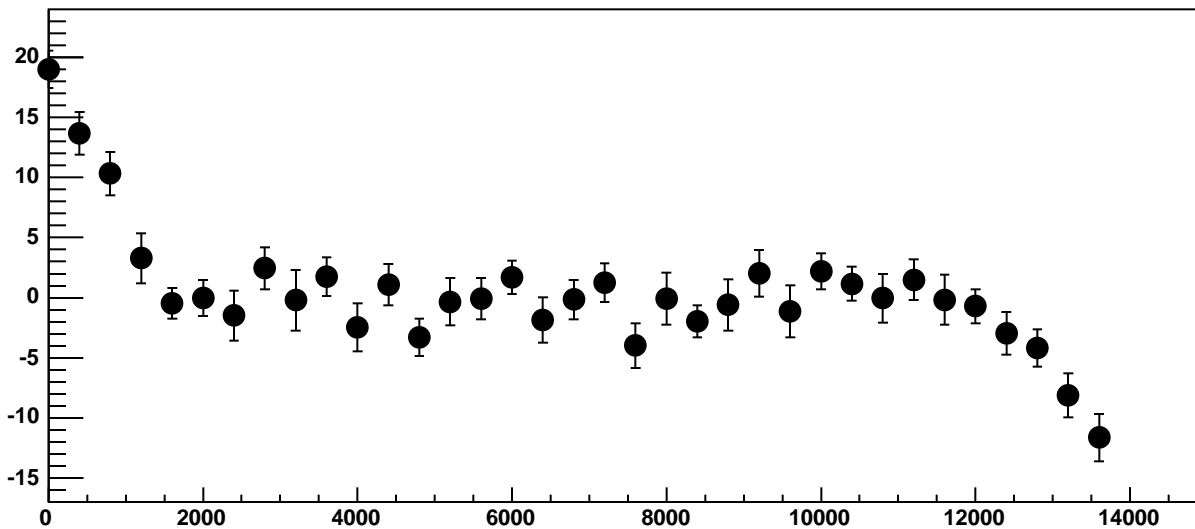


χ^2 / ndf 27.1 / 23
p0 -267.2 \pm 0.7833
p1 0.01827 \pm 0.0001175

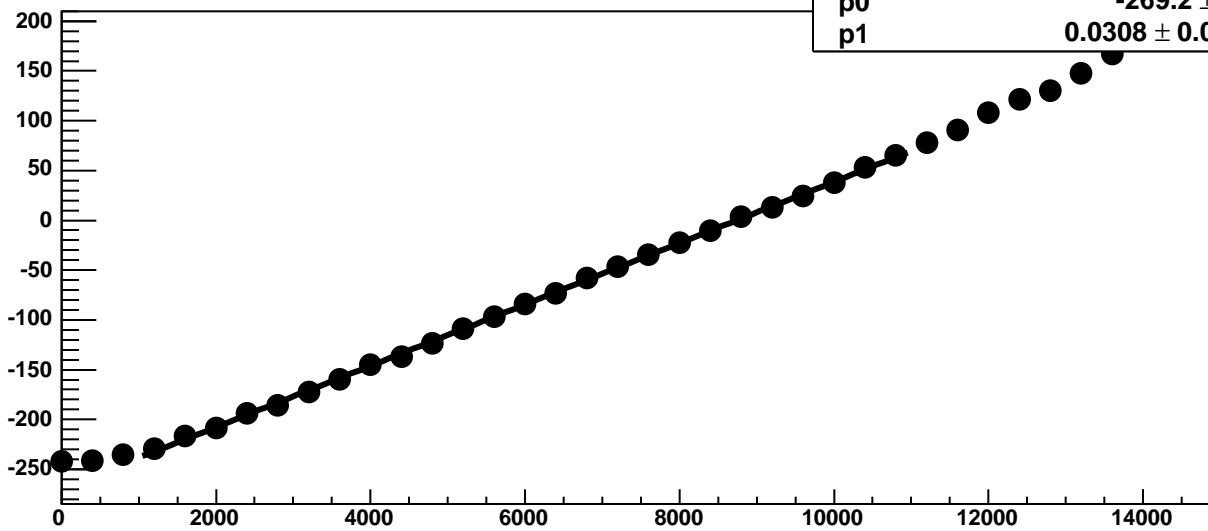
Chip 6, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

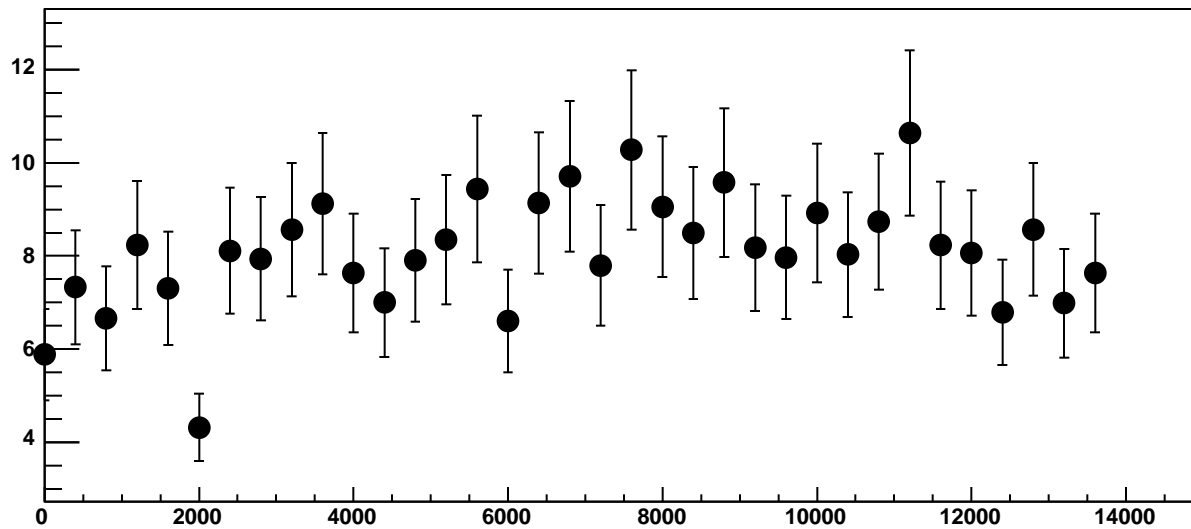


Chip 6, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

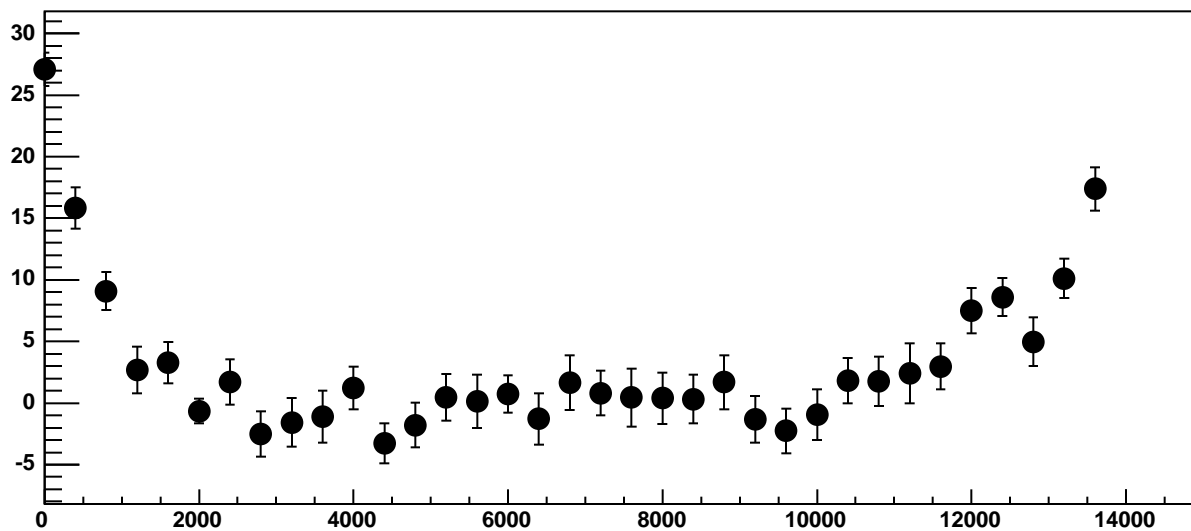


χ^2 / ndf 21.65 / 23
p0 -269.2 ± 0.7552
p1 0.0308 ± 0.0001216

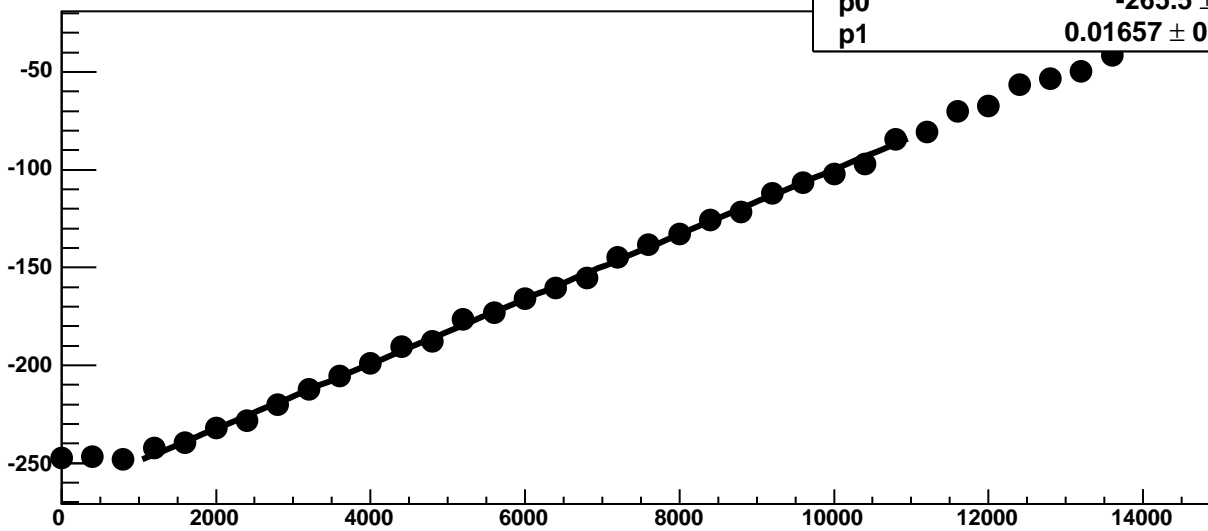
Chip 6, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC

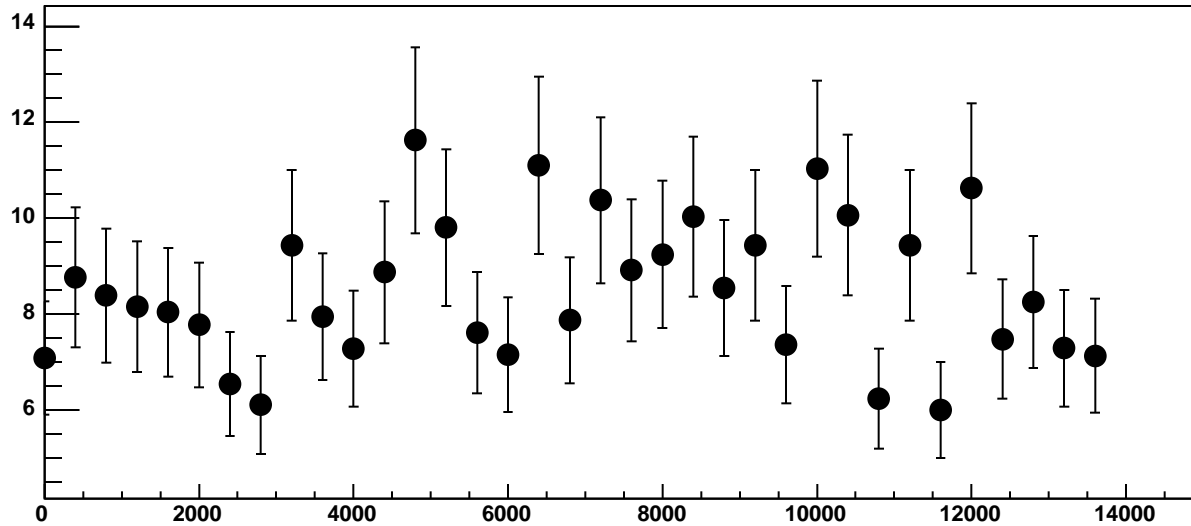


Chip 6, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

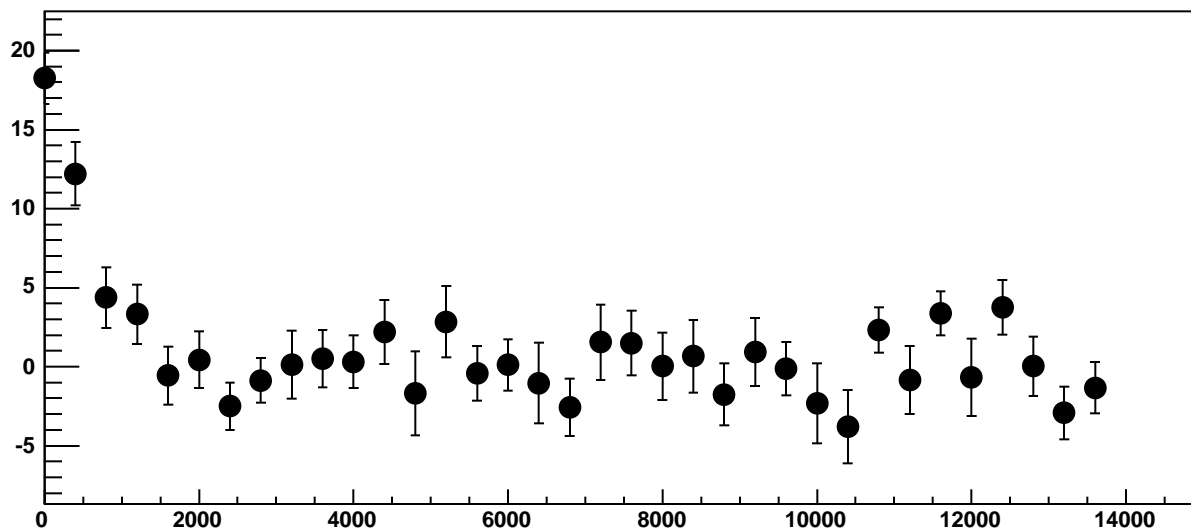


χ^2 / ndf 20.22 / 23
p0 -265.5 ± 0.8175
p1 0.01657 ± 0.000126

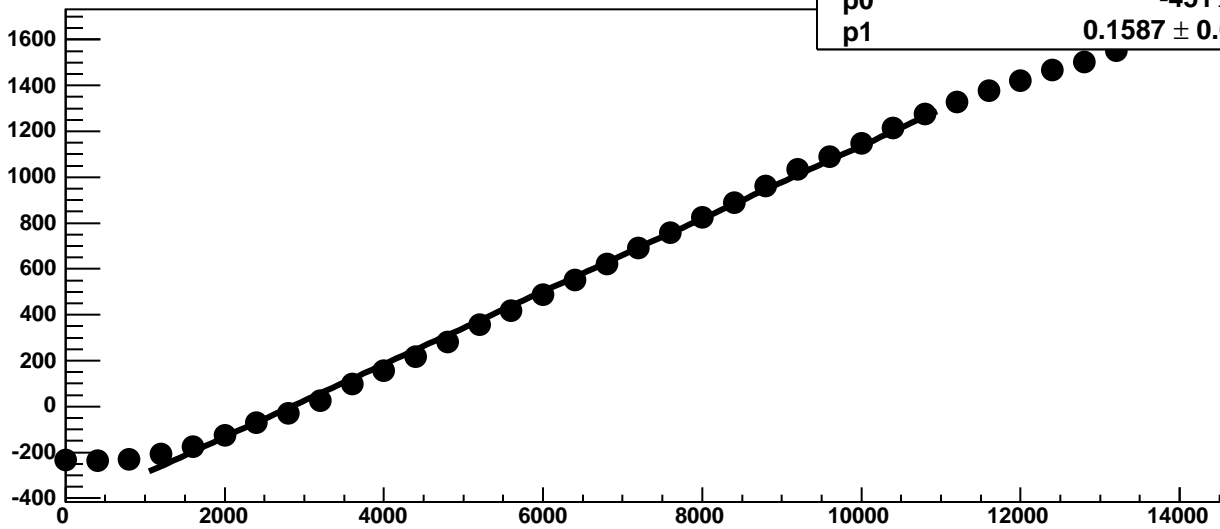
Chip 6, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 6, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 6, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

2003 / 23

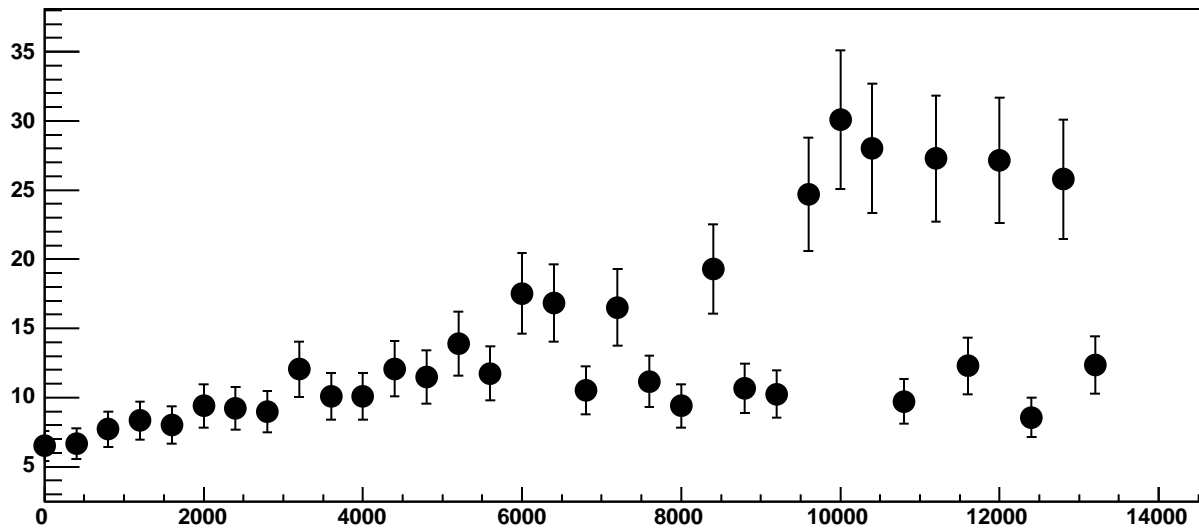
p0

-451 ± 1.039

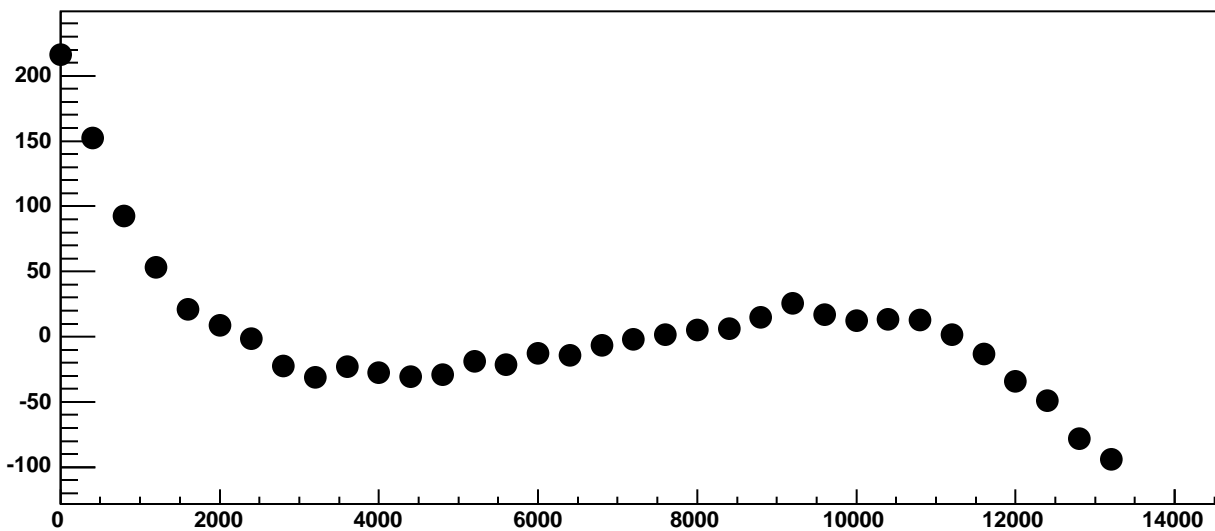
p1

0.1587 ± 0.000177

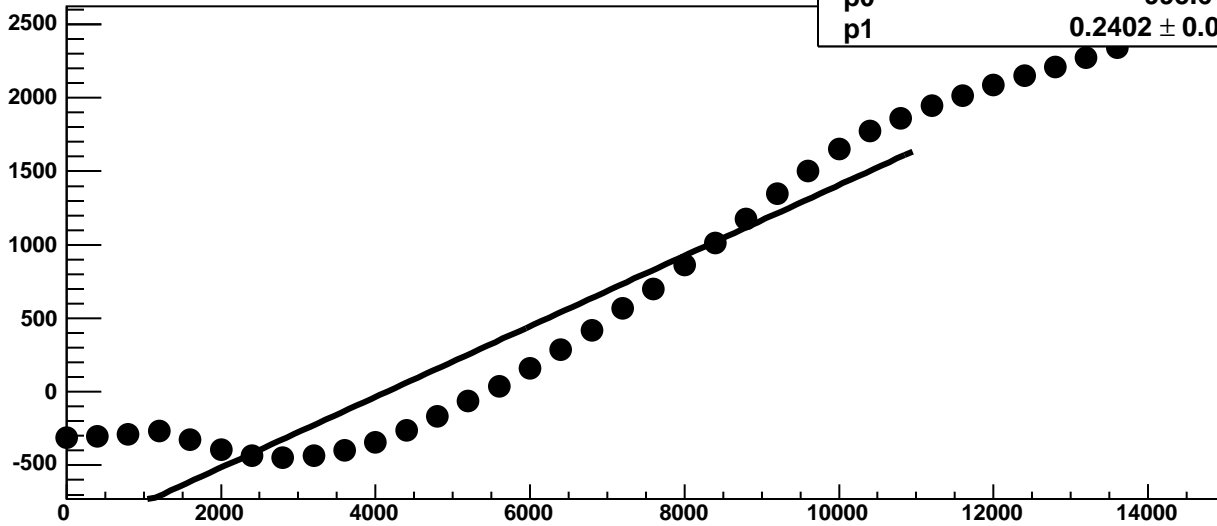
Chip 6, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

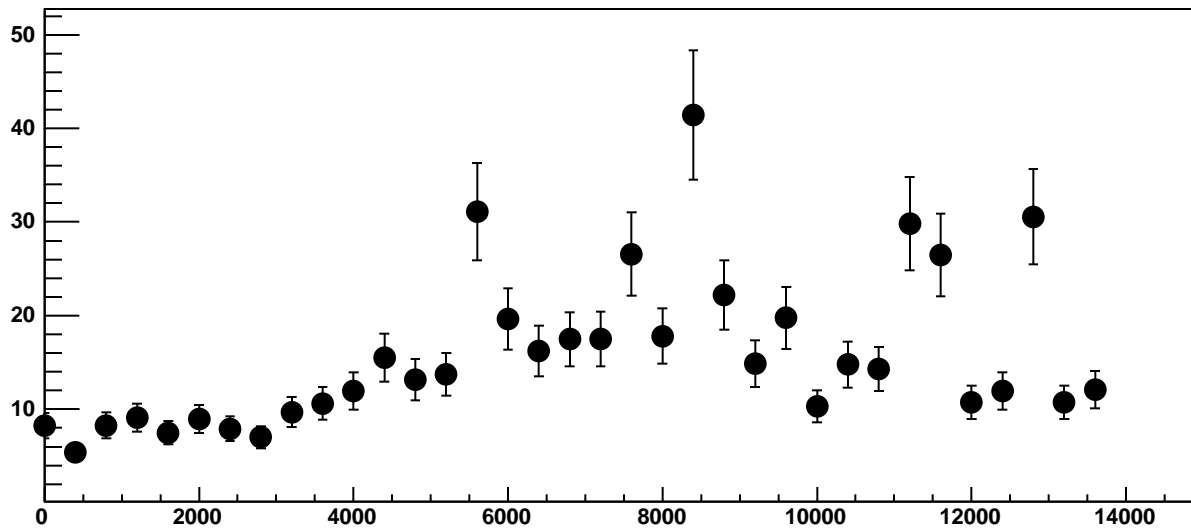


Chip 6, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC

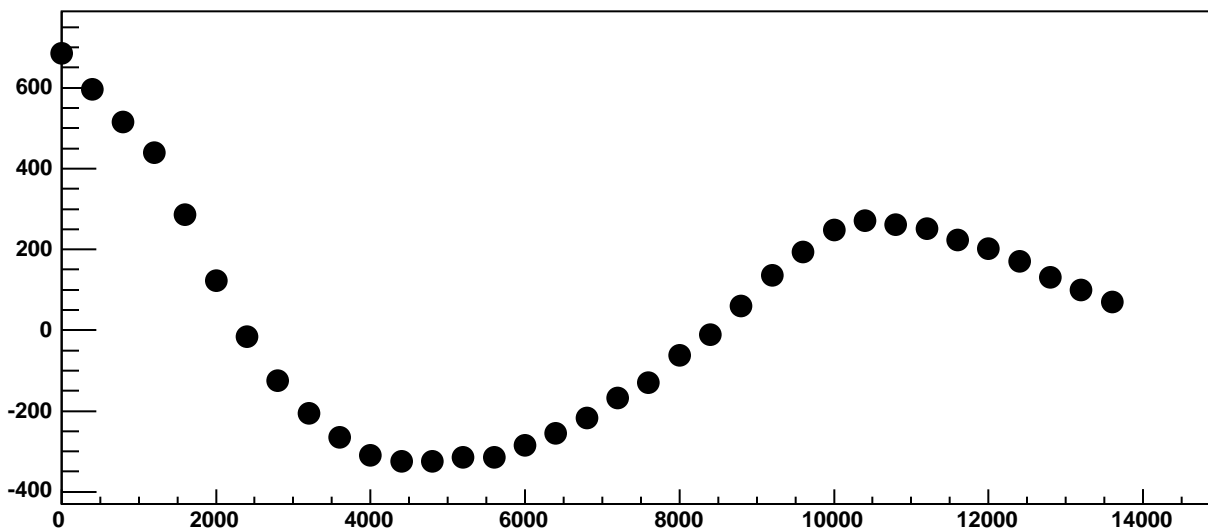


χ^2 / ndf 1.88e+05 / 23
p0 -995.6 ± 1.004
p1 0.2402 ± 0.0001878

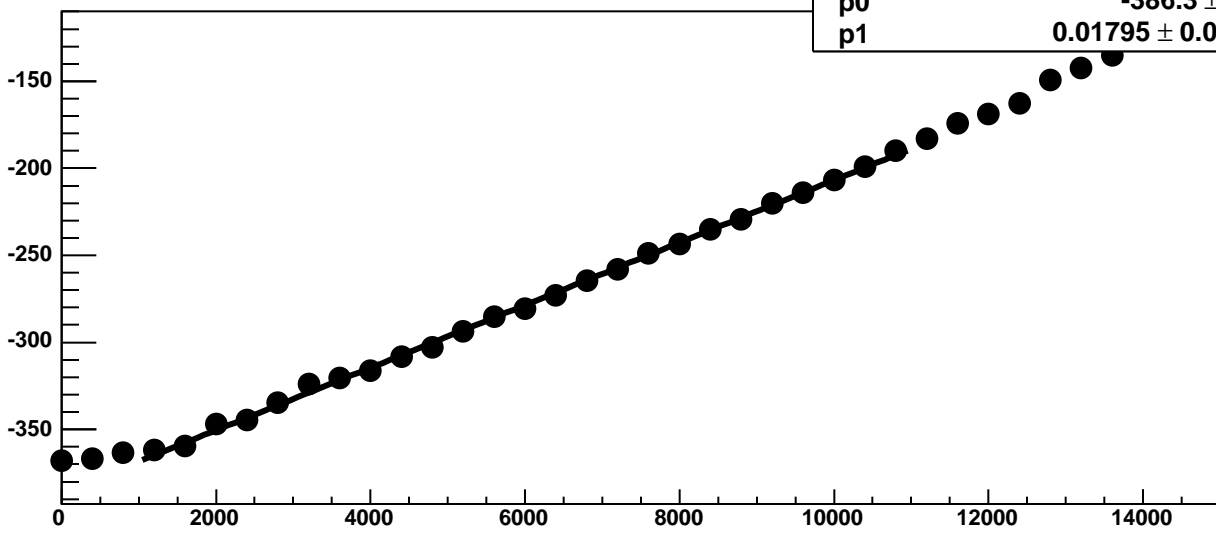
Chip 6, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC

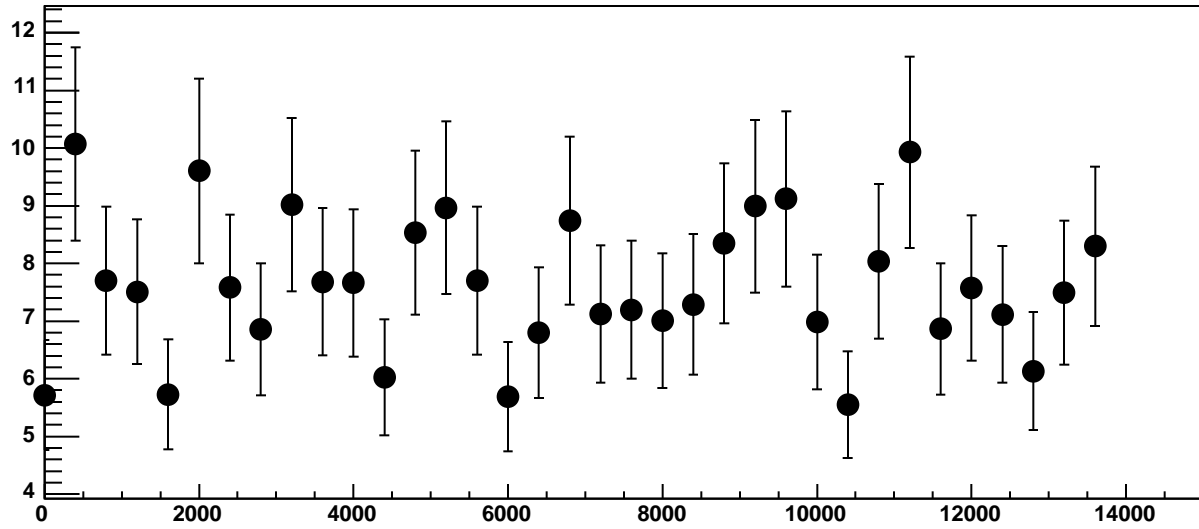


Chip 6, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

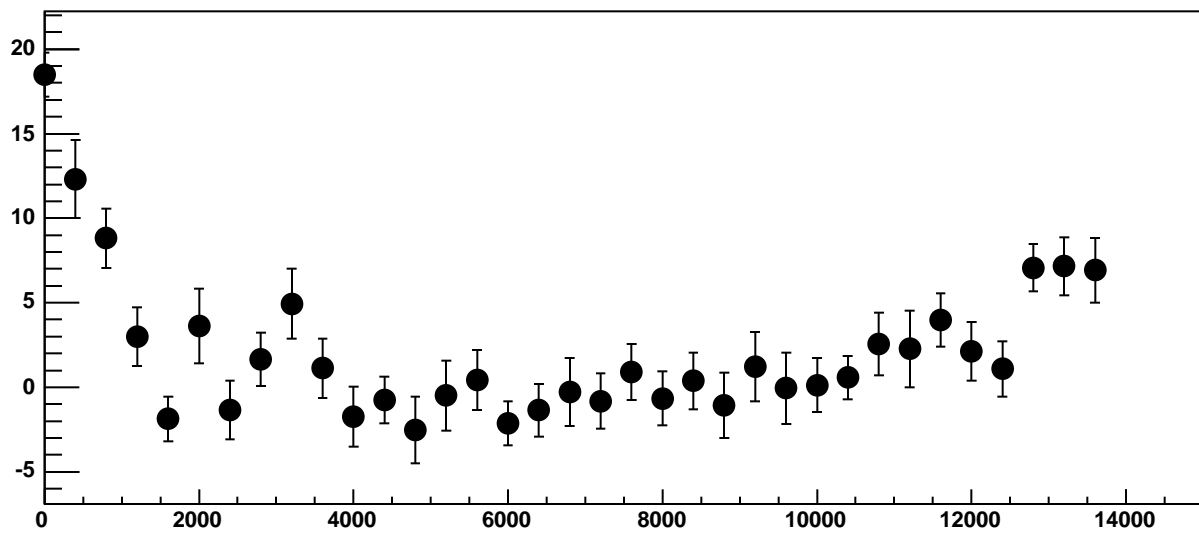


χ^2 / ndf 25.58 / 23
p0 -386.3 ± 0.7691
p1 0.01795 ± 0.0001152

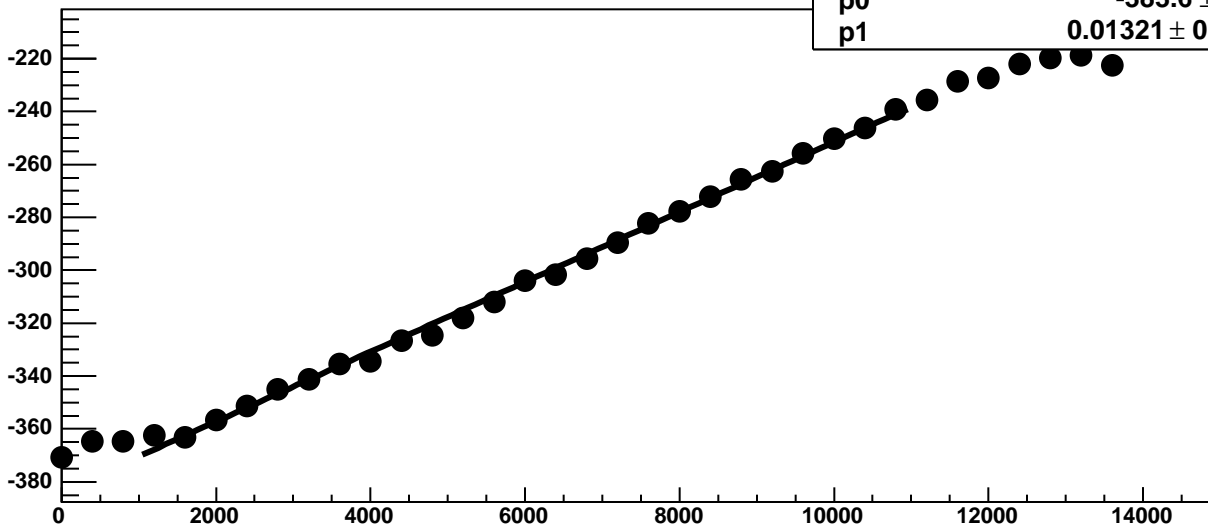
Chip 6, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 6, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

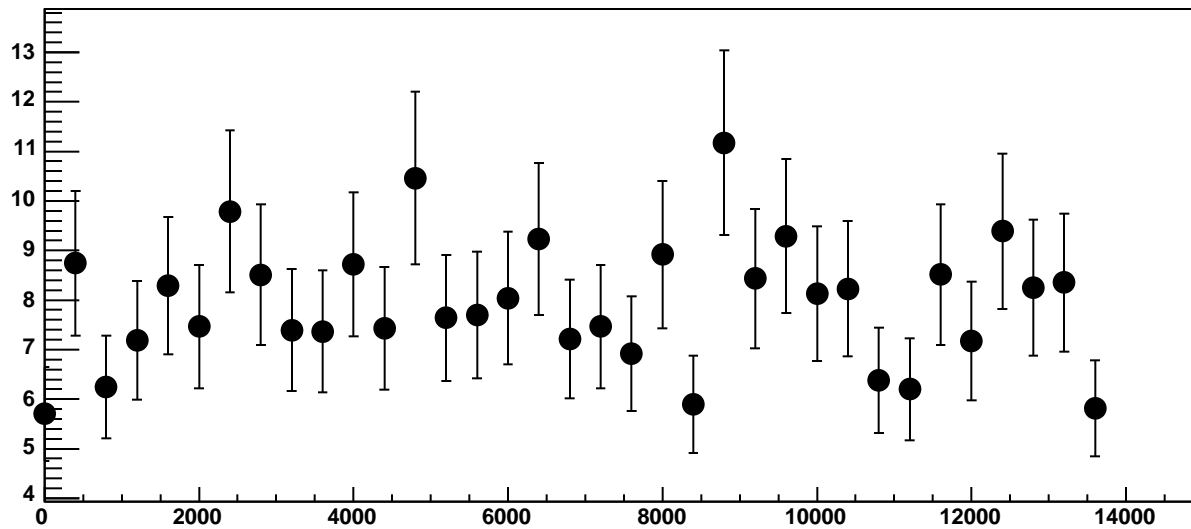


Chip 6, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

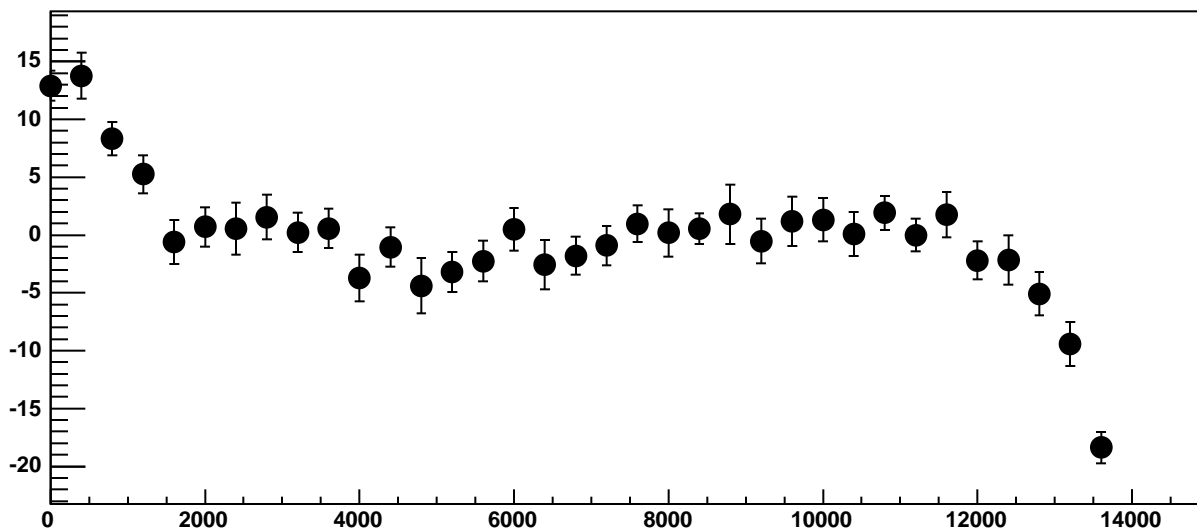


χ^2 / ndf 30.03 / 23
p0 -383.6 ± 0.8352
p1 0.01321 ± 0.000124

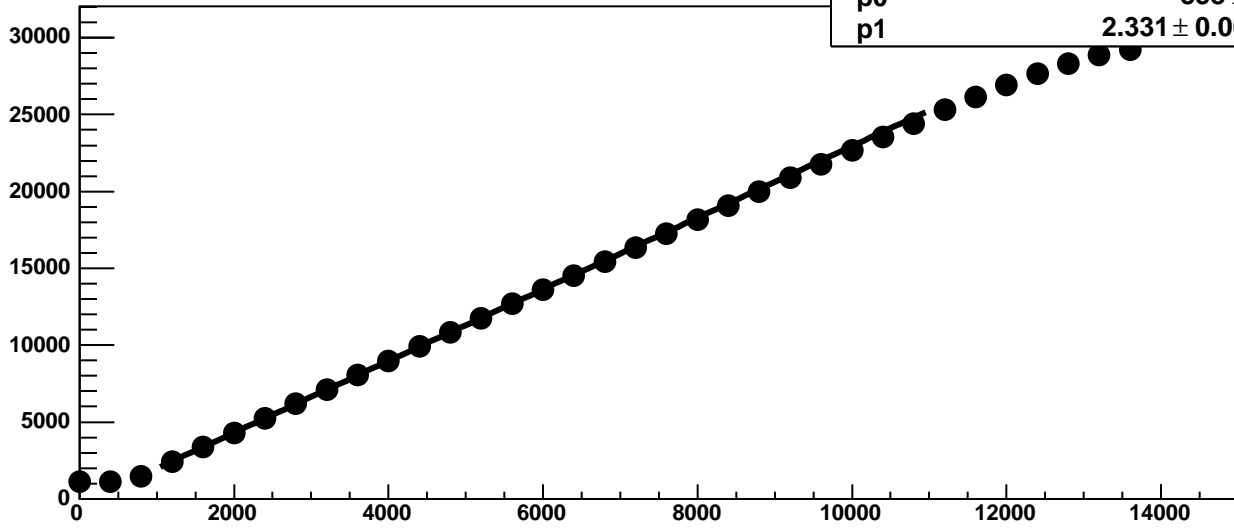
Chip 6, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



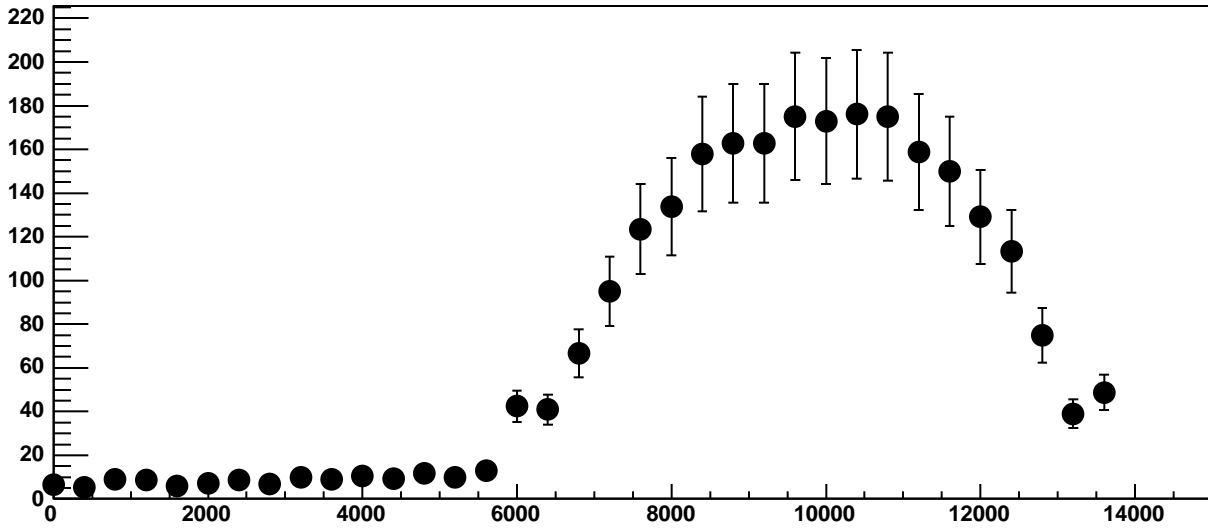
Chip 6, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



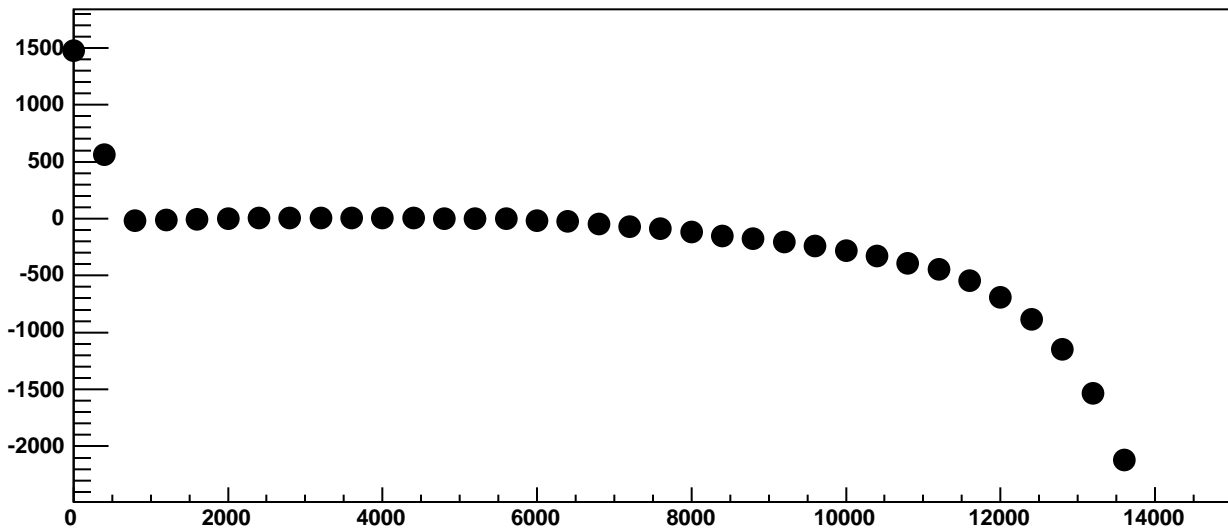
Chip 6, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC



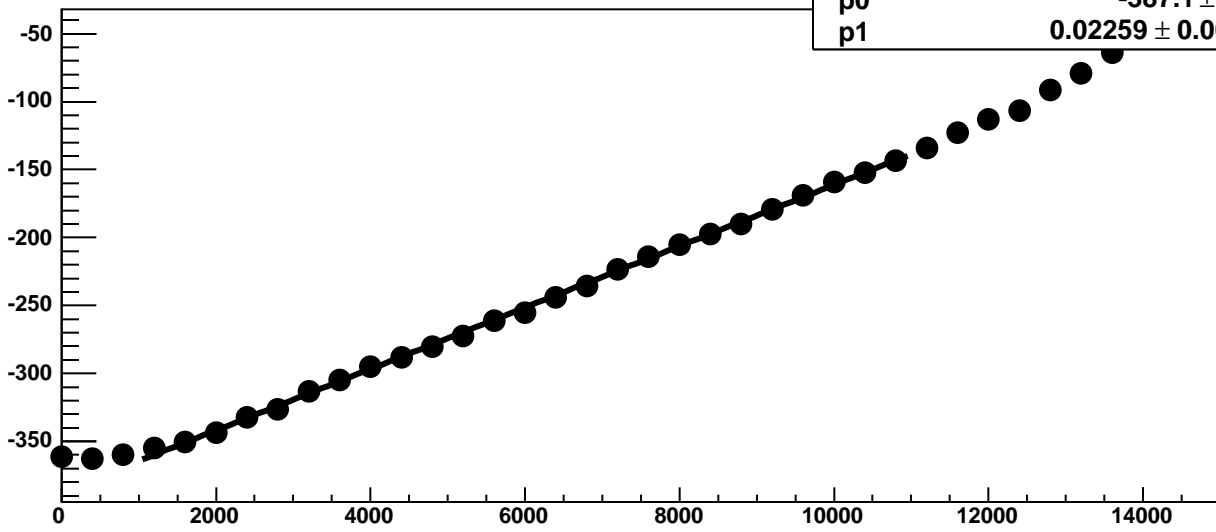
Chip 6, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC

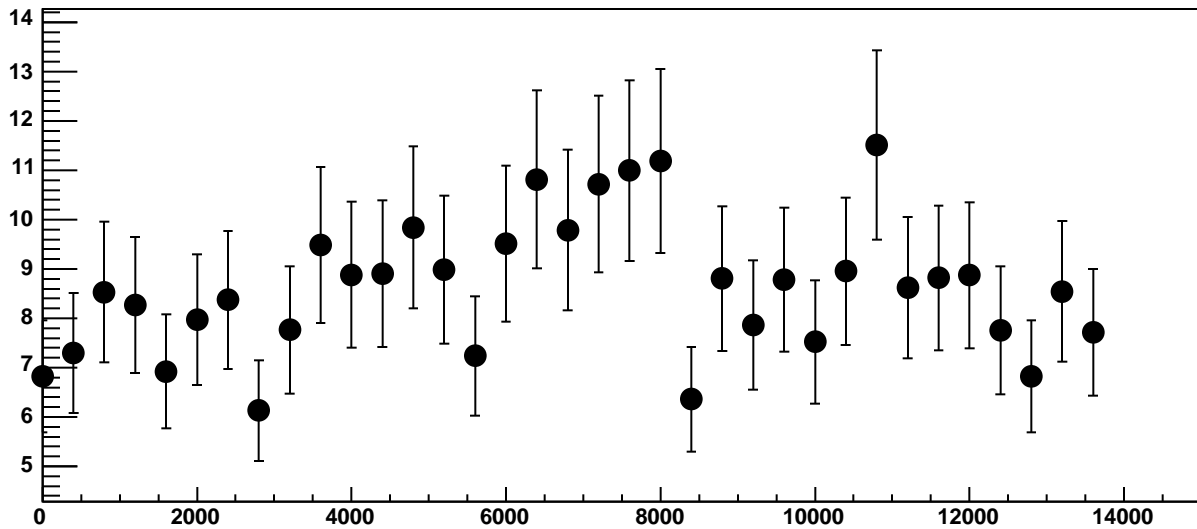


Chip 6, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

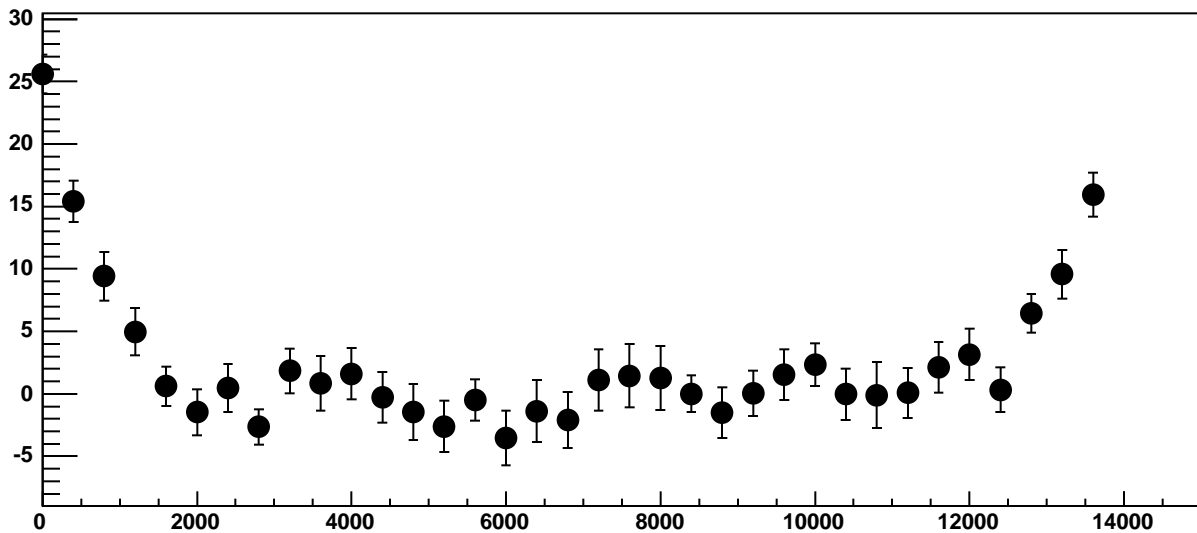


χ^2 / ndf 22.77 / 23
p0 -387.1 ± 0.8422
p1 0.02259 ± 0.0001309

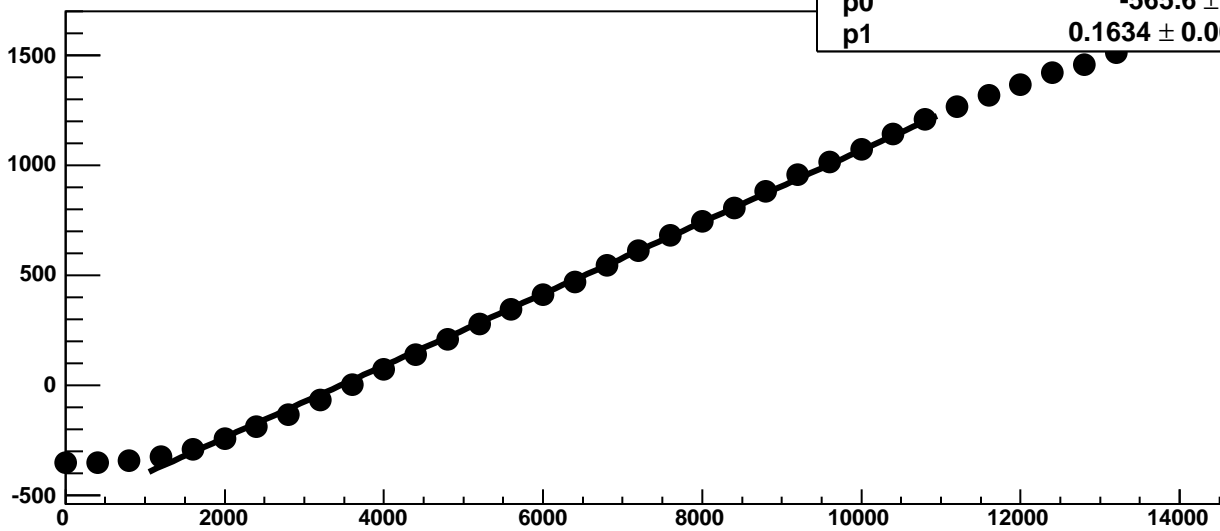
Chip 6, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



Chip 6, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

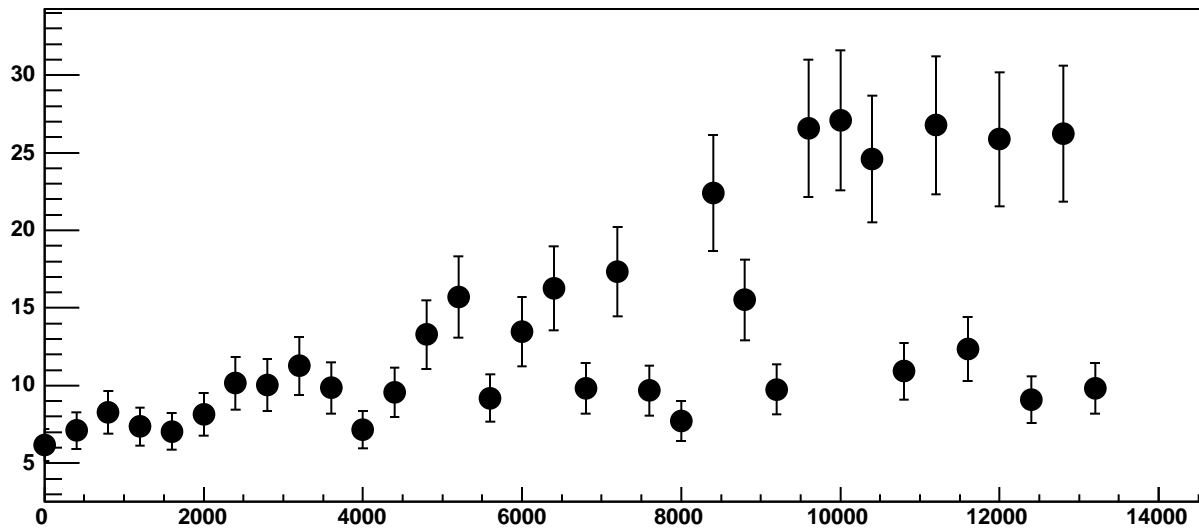


Chip 6, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

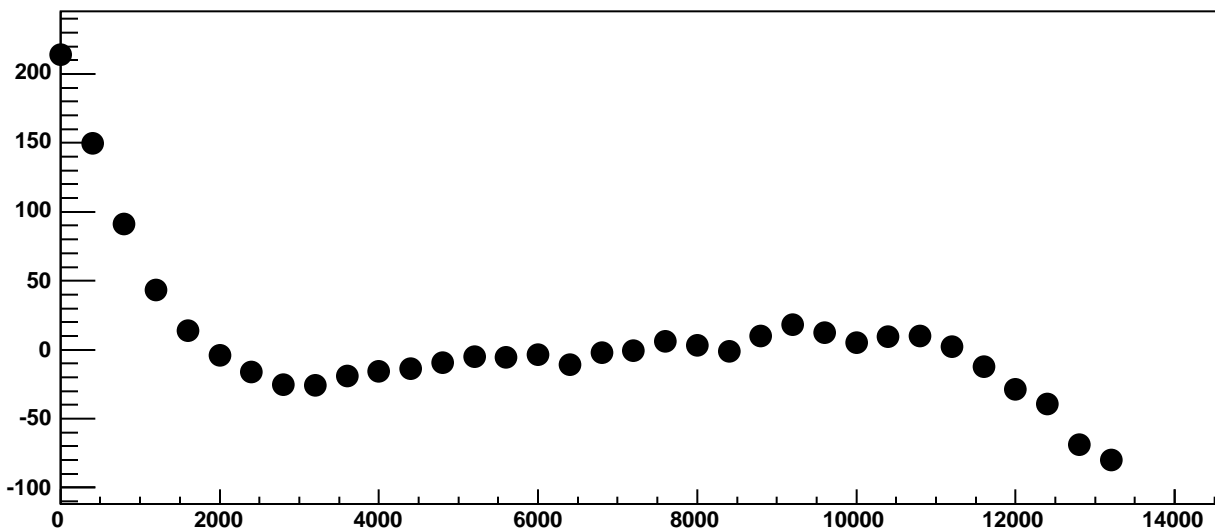


χ^2 / ndf 1340 / 23
p0 -565.6 ± 0.9726
p1 0.1634 ± 0.0001726

Chip 6, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

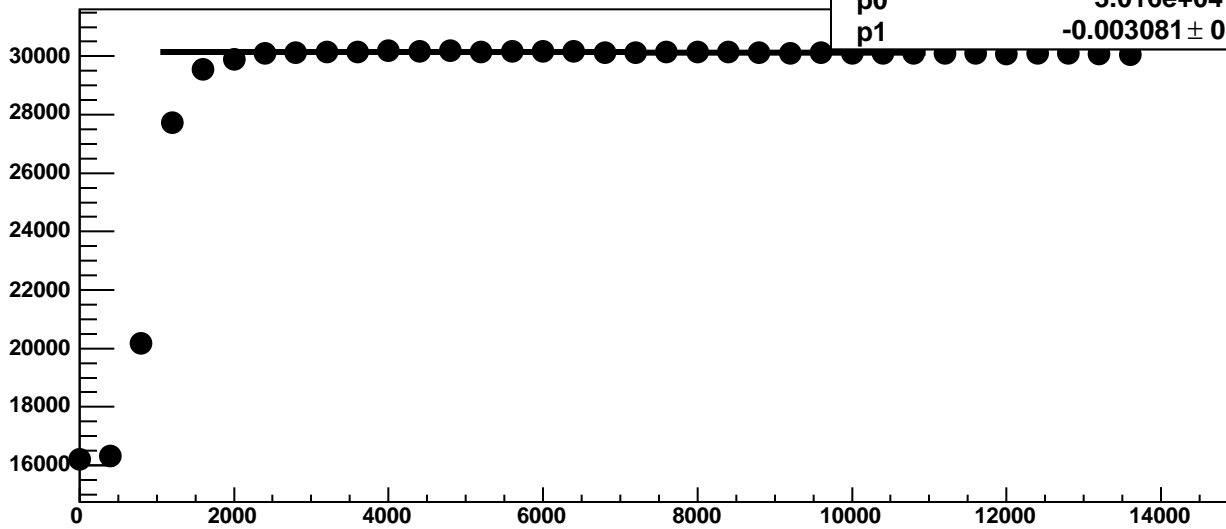


Chip 6, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

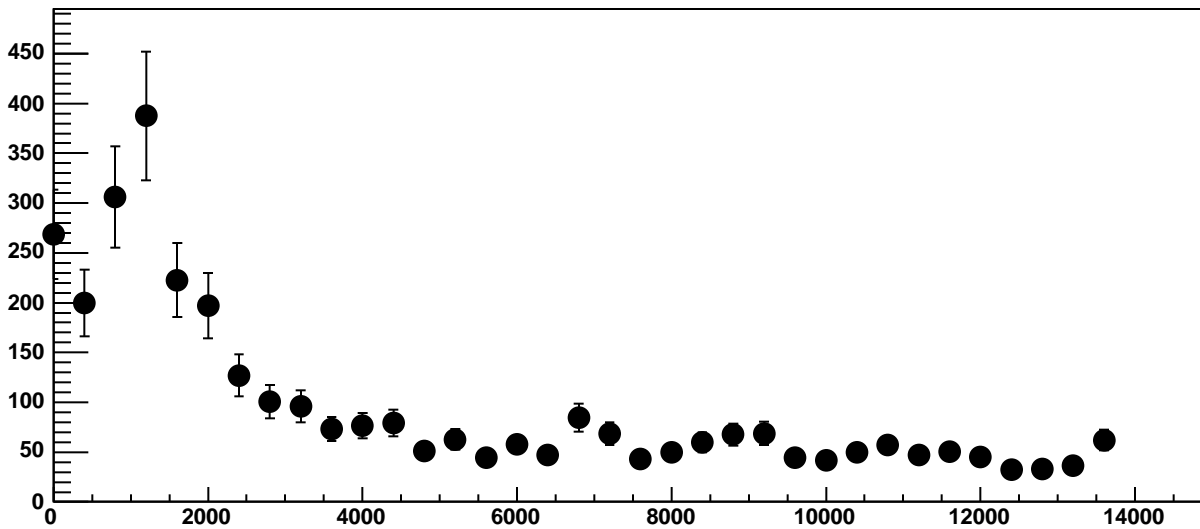


Chip 6, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC

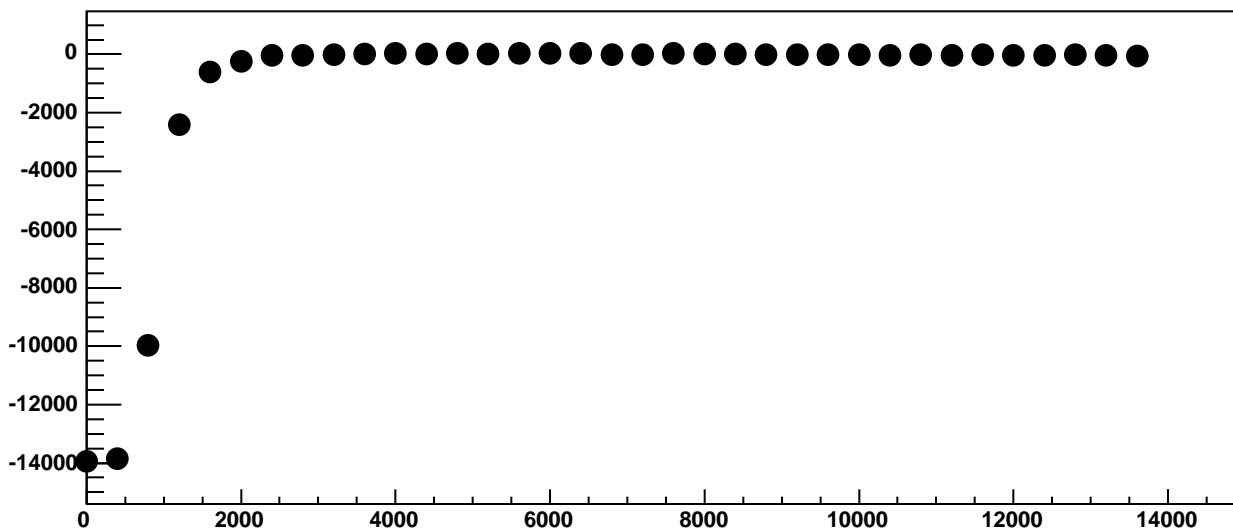
χ^2 / ndf 978.9 / 23
p0 $3.016\text{e}+04 \pm 9.407$
p1 -0.003081 ± 0.001226



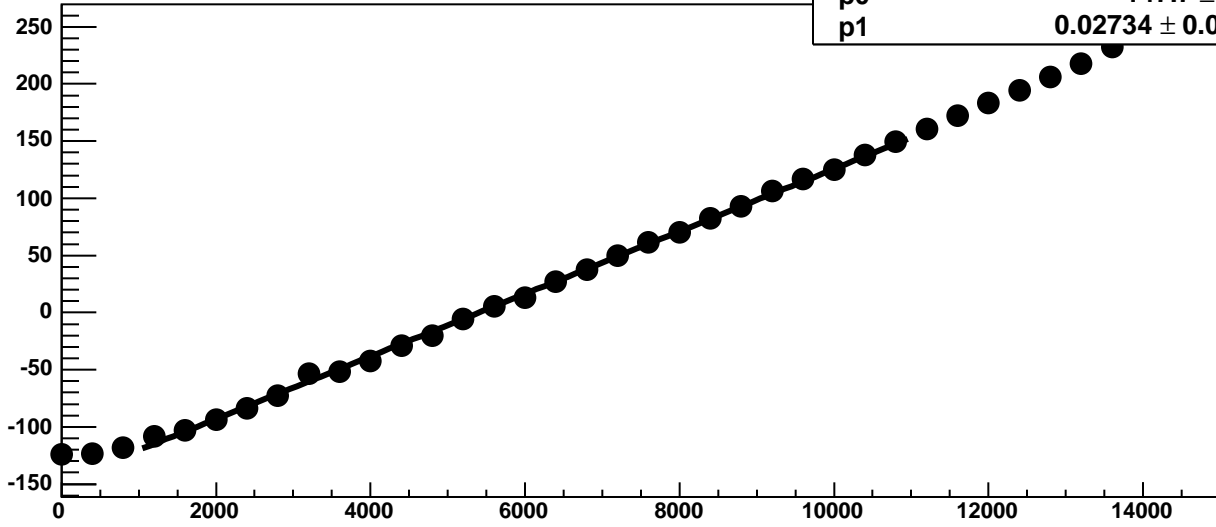
Chip 6, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC

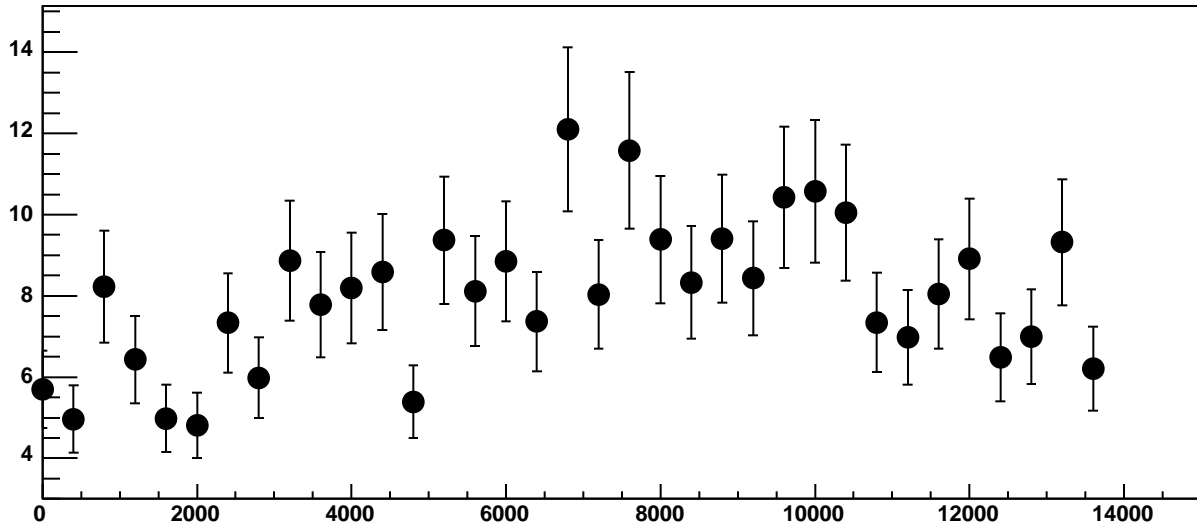


Chip 6, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

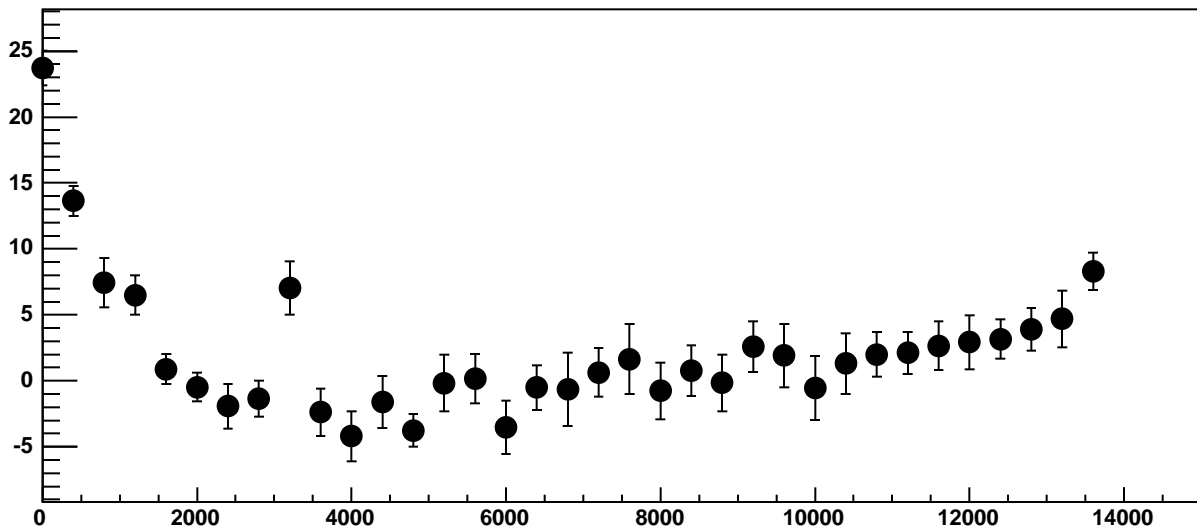


χ^2 / ndf 59.26 / 23
p0 -147.7 ± 0.6873
p1 0.02734 ± 0.0001181

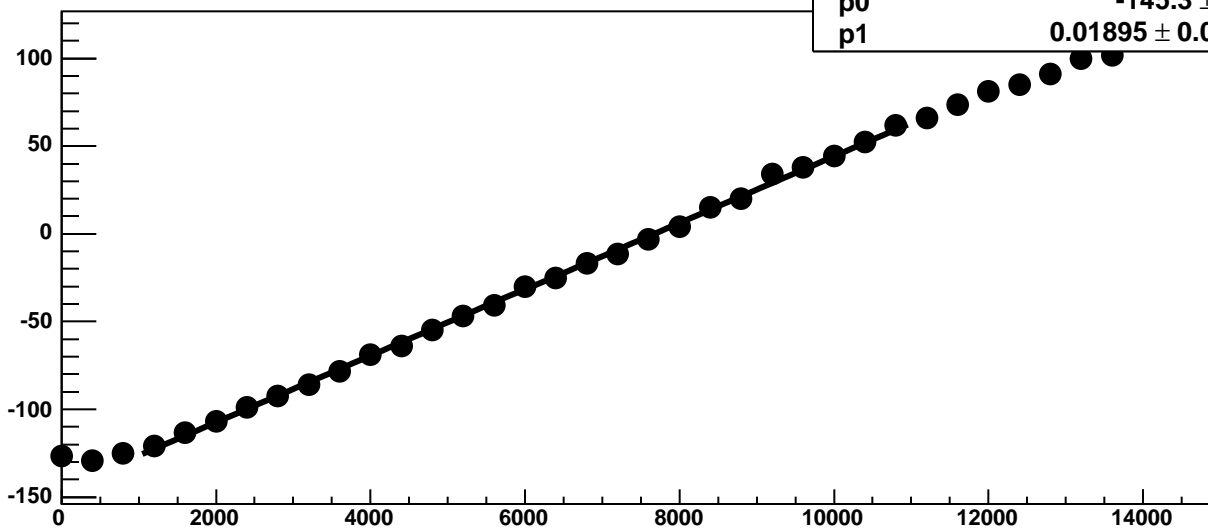
Chip 6, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 6, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

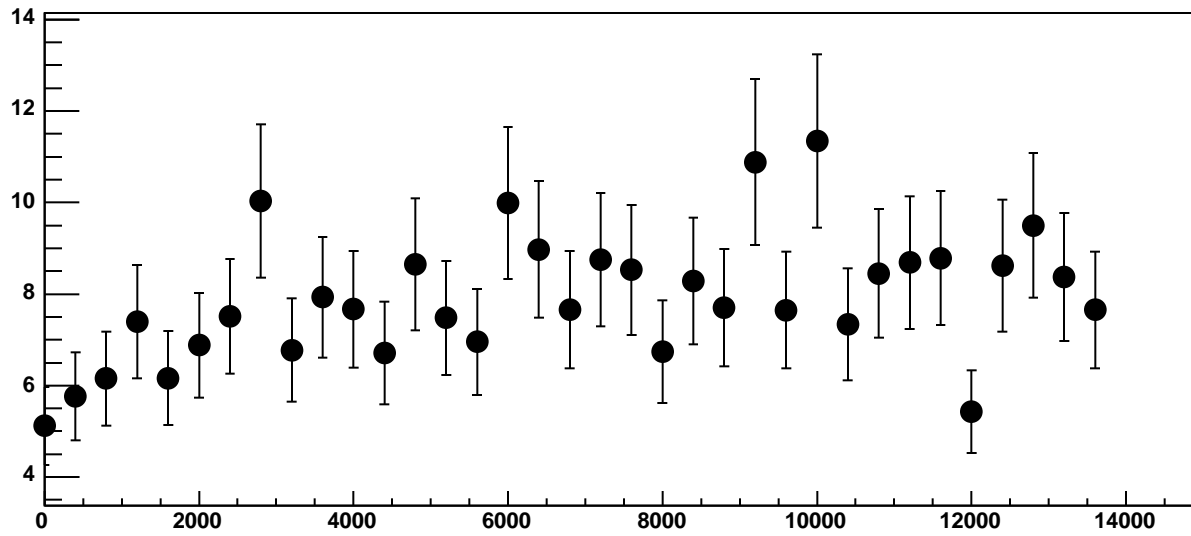


Chip 6, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

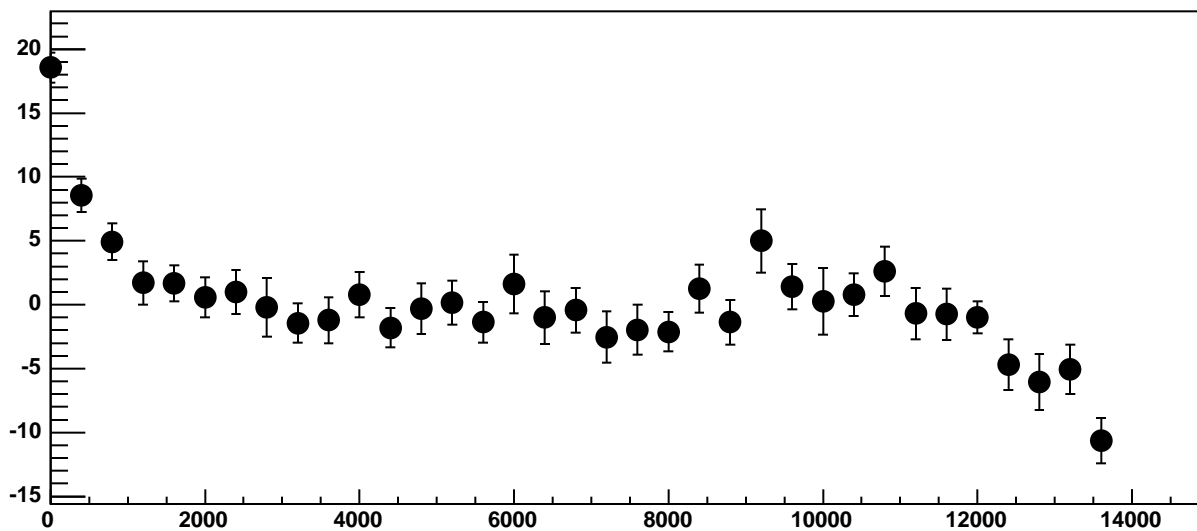


χ^2 / ndf 19.56 / 23
p0 -145.3 ± 0.7849
p1 0.01895 ± 0.0001234

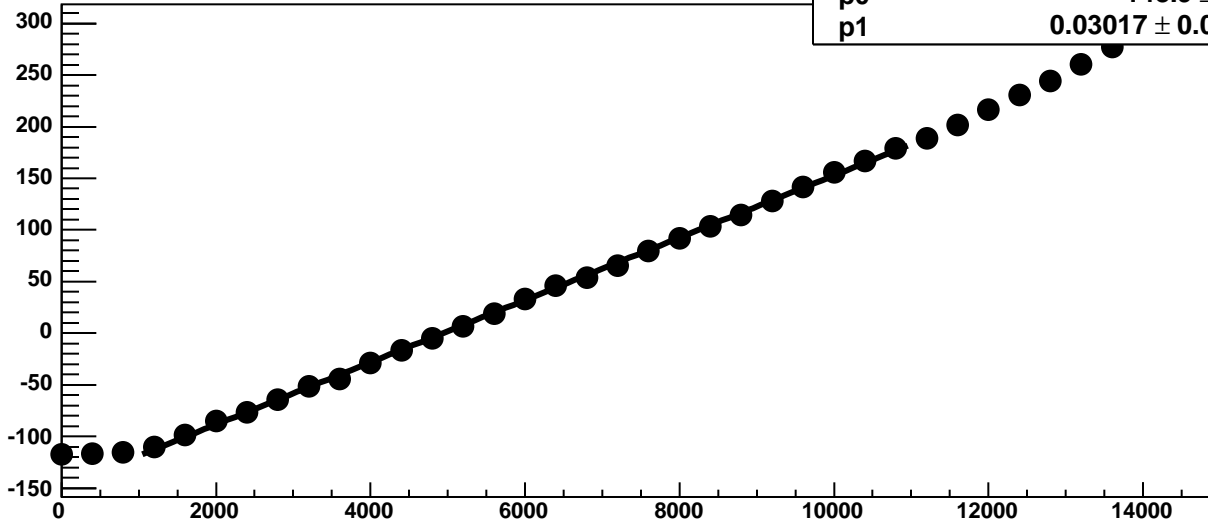
Chip 6, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

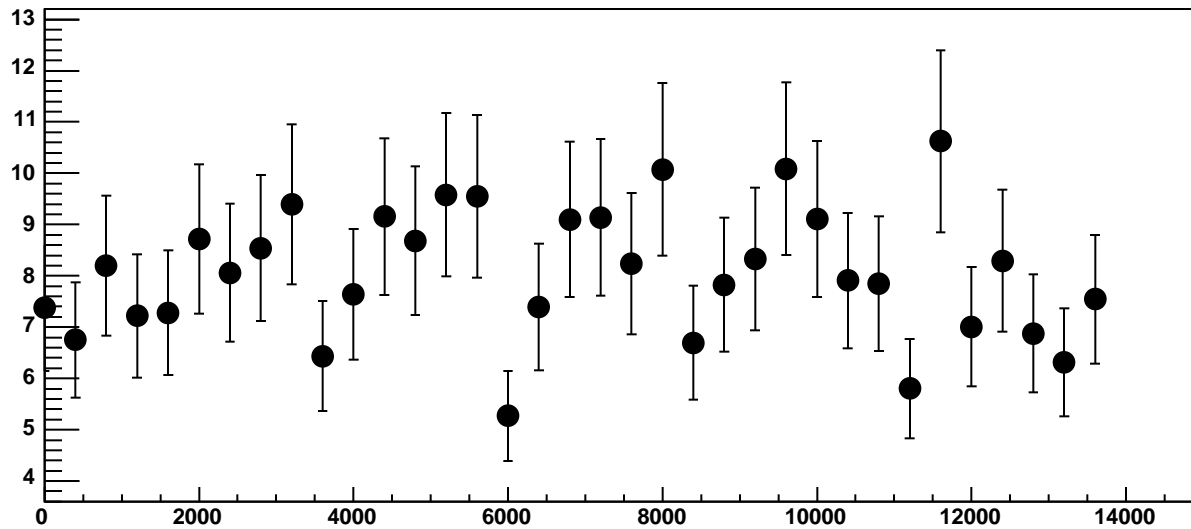


Chip 6, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

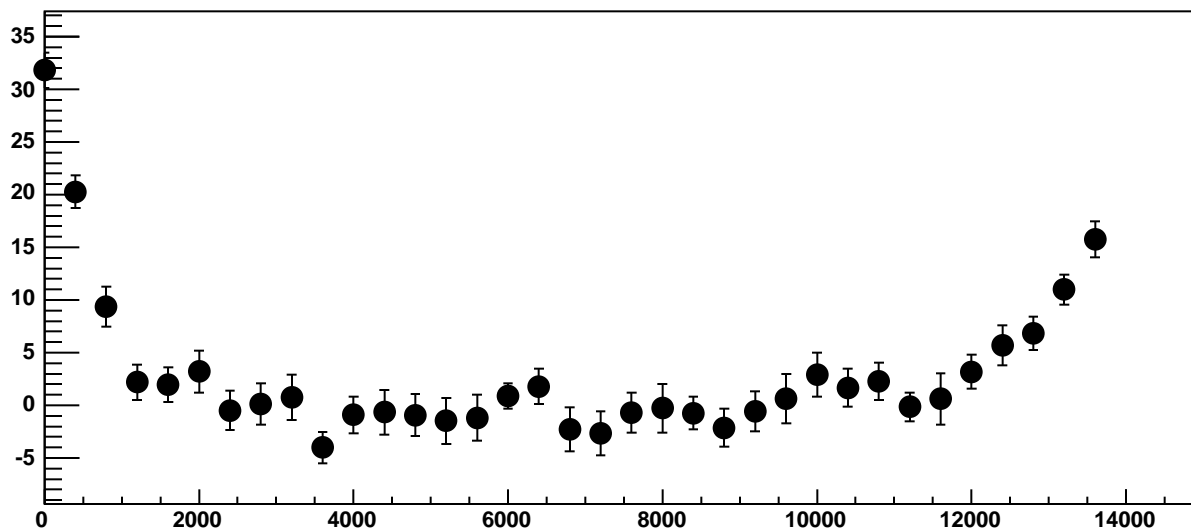


χ^2 / ndf 25.35 / 23
p0 -148.9 ± 0.8367
p1 0.03017 ± 0.0001279

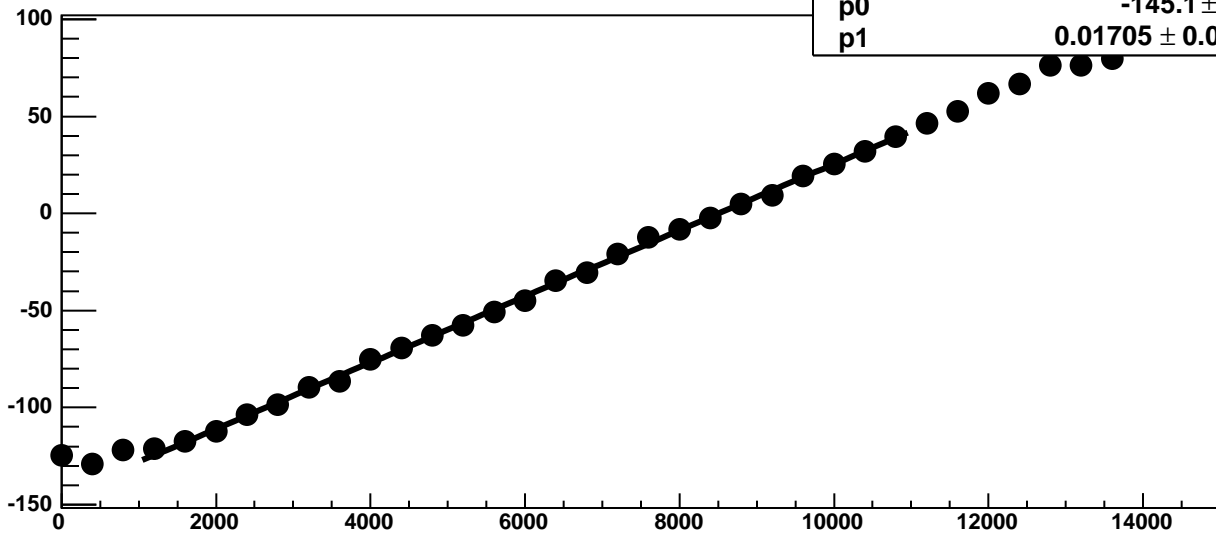
Chip 6, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

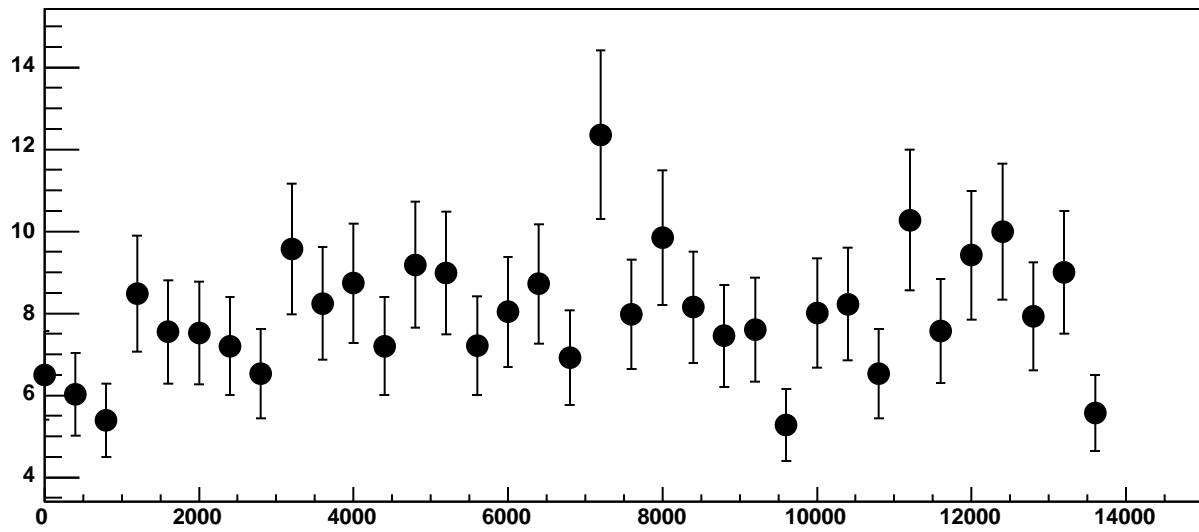


Chip 6, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

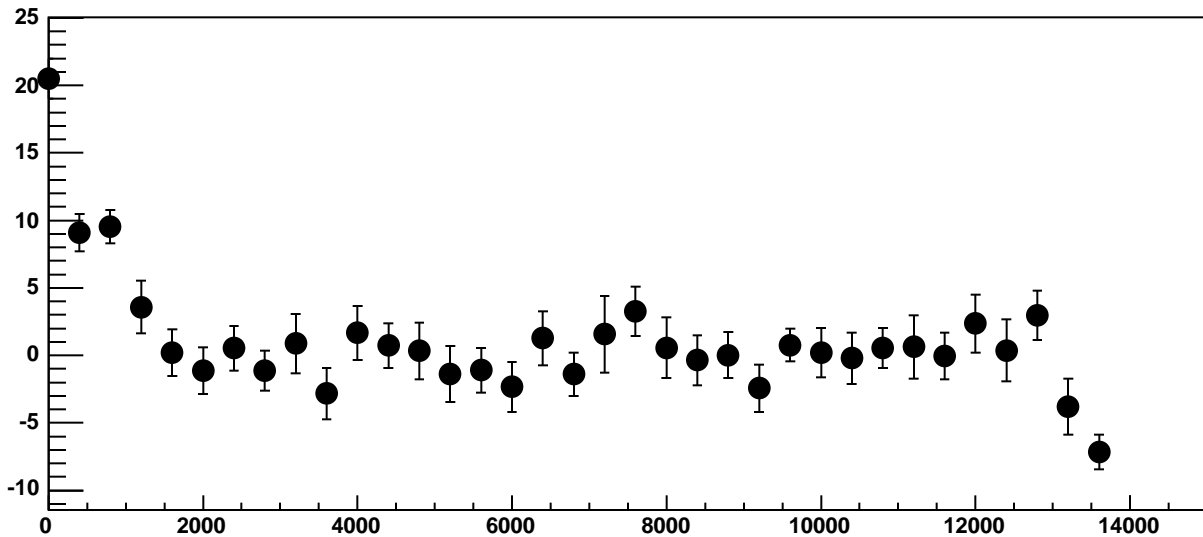


χ^2 / ndf 17.55 / 23
p0 -145.1 ± 0.8117
p1 0.01705 ± 0.0001181

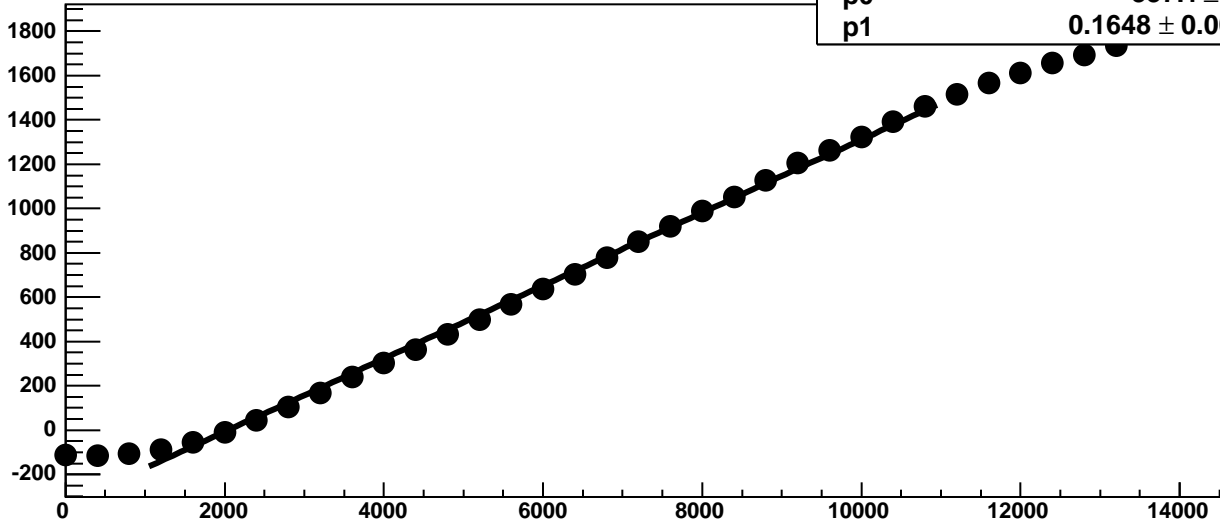
Chip 6, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



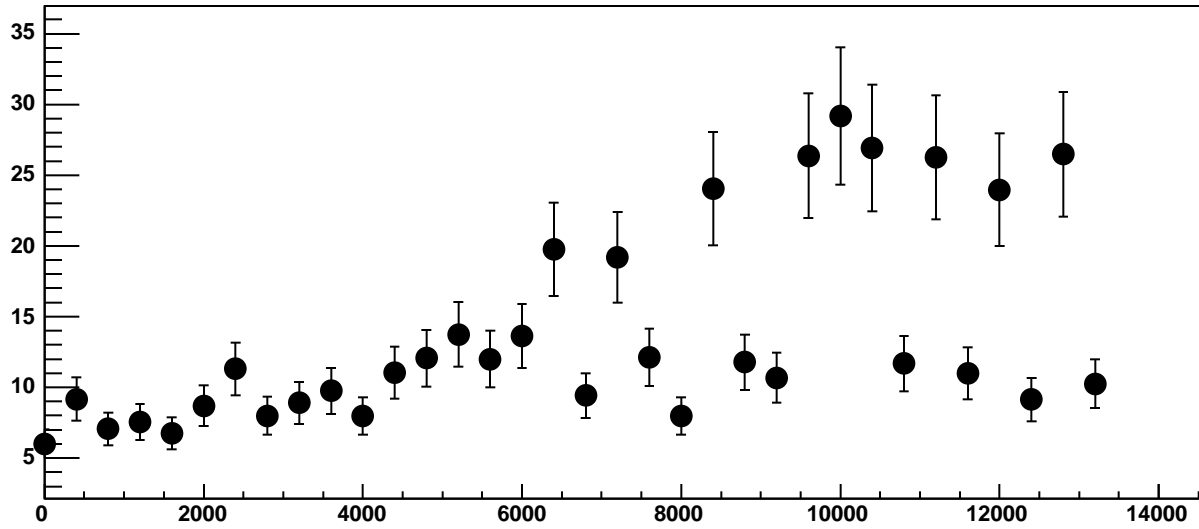
Chip 6, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



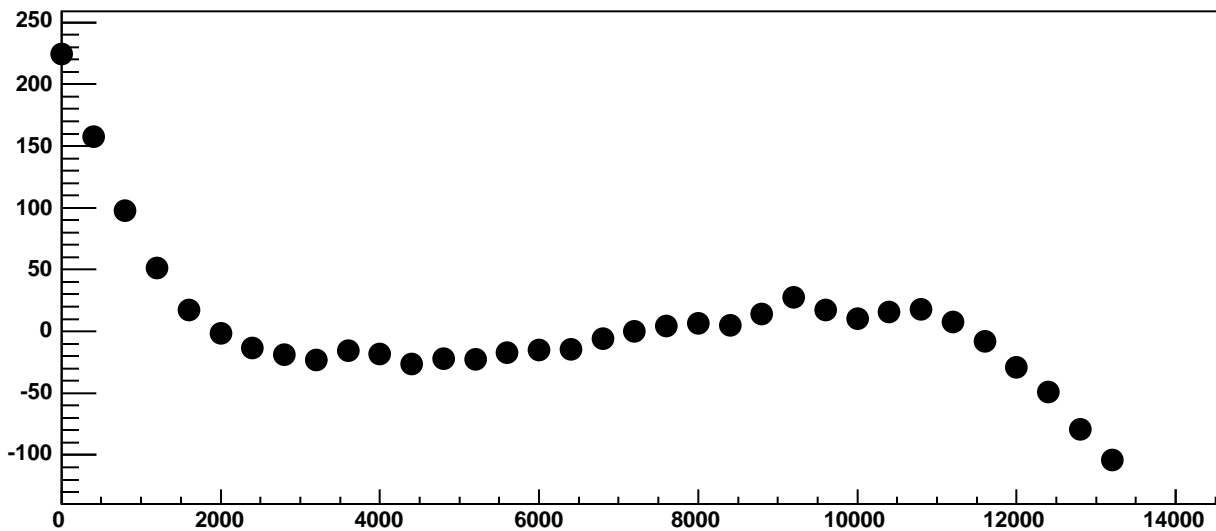
Chip 6, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



Chip 6, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC

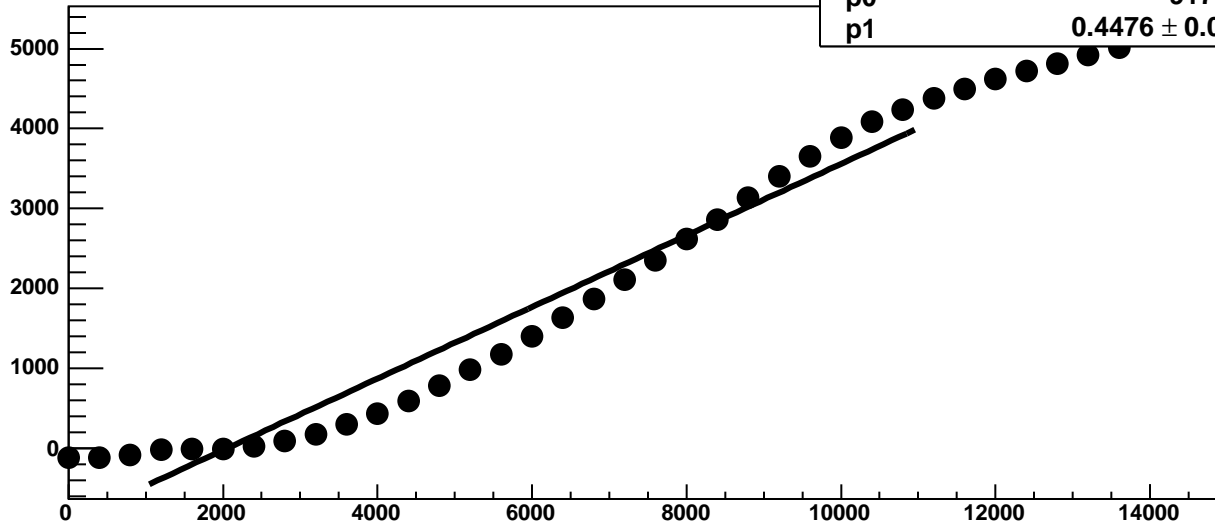


Chip 6, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

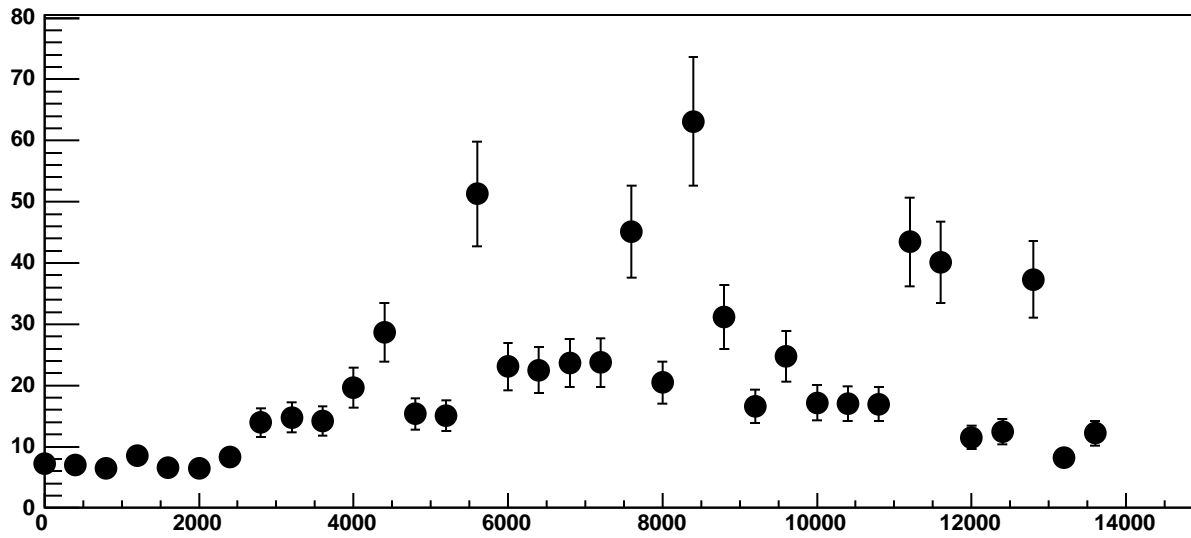


Chip 6, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

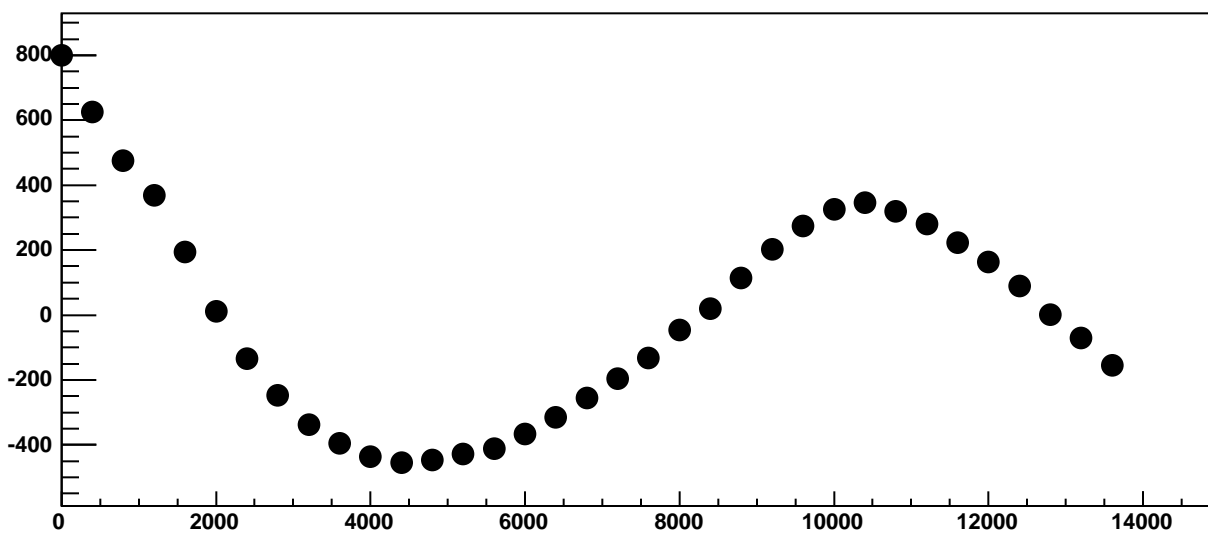
χ^2 / ndf 1.741e+05 / 23
p0 -917 ± 1.035
p1 0.4476 ± 0.0002194



Chip 6, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

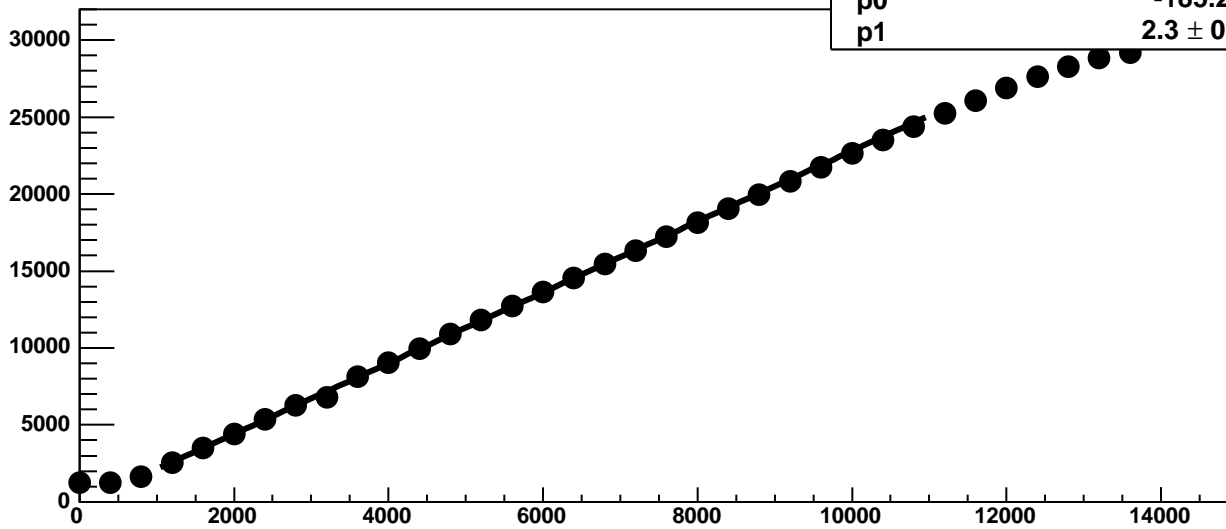


Chip 6, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

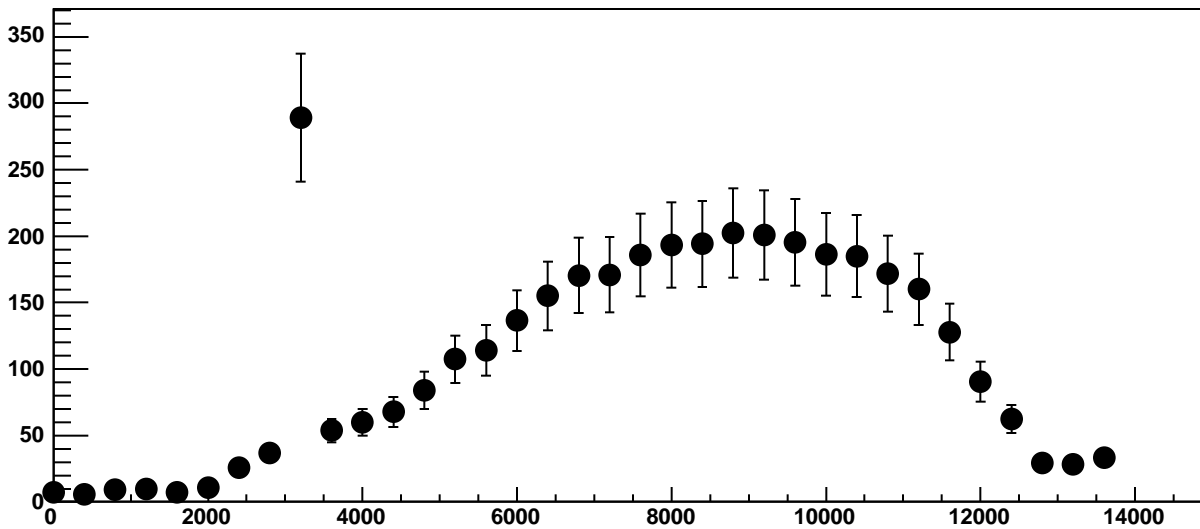


Chip 6, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC

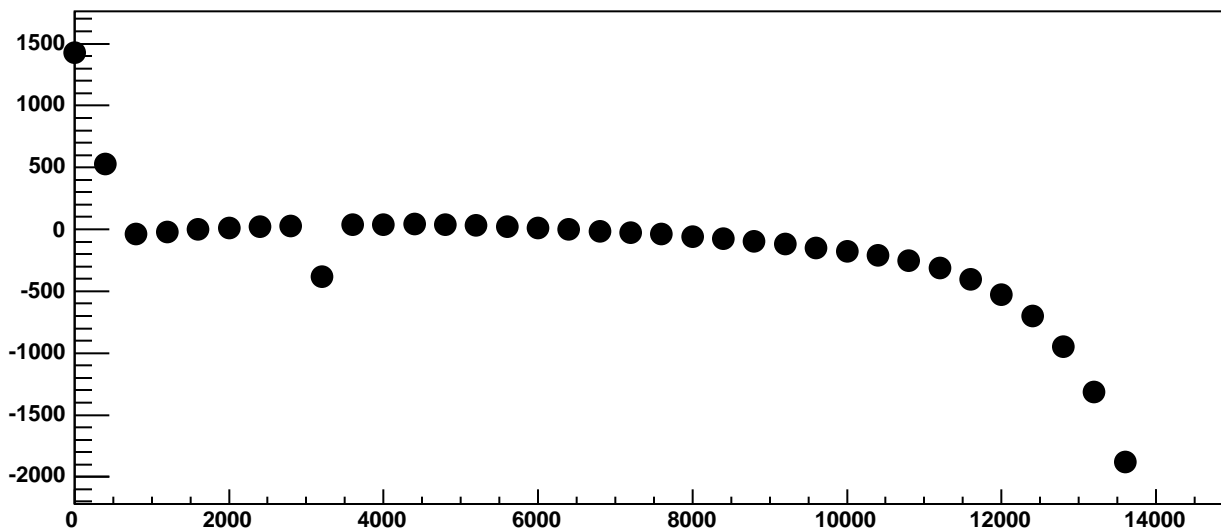
χ^2 / ndf 306.6 / 23
p0 -185.2 ± 2.551
p1 2.3 ± 0.001297



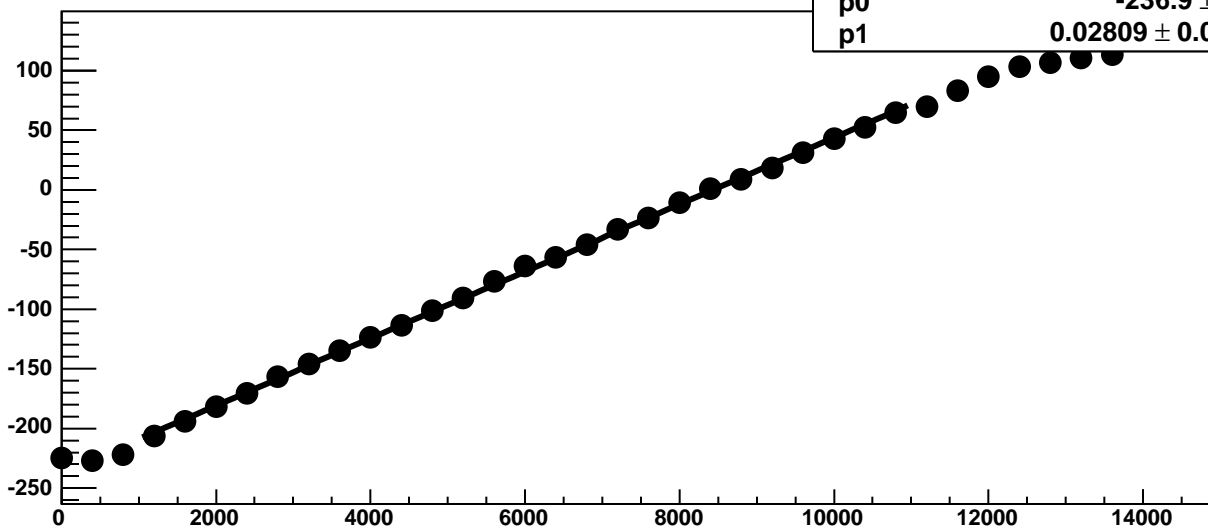
Chip 6, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC

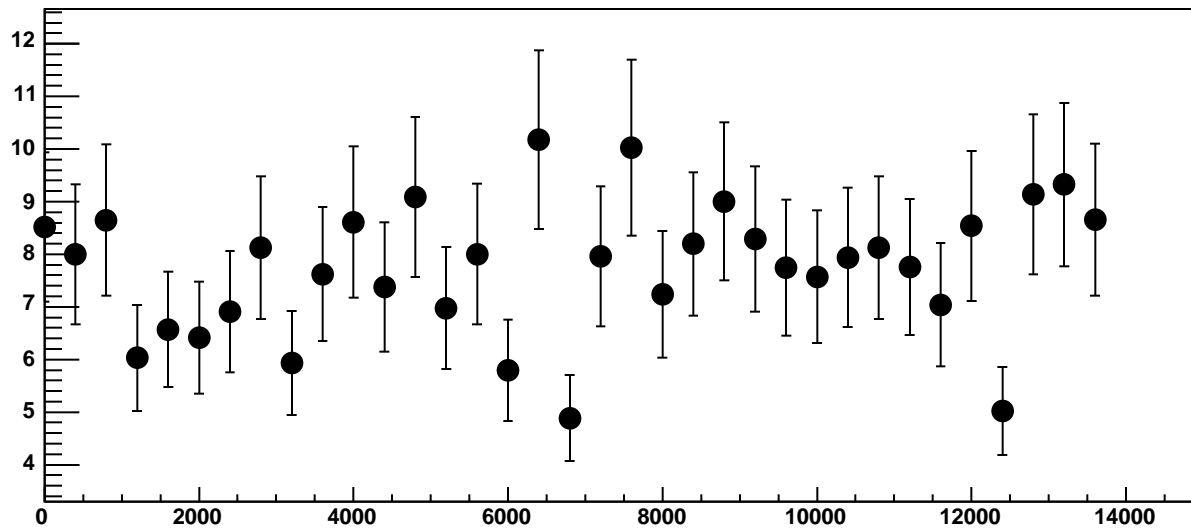


Chip 6, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

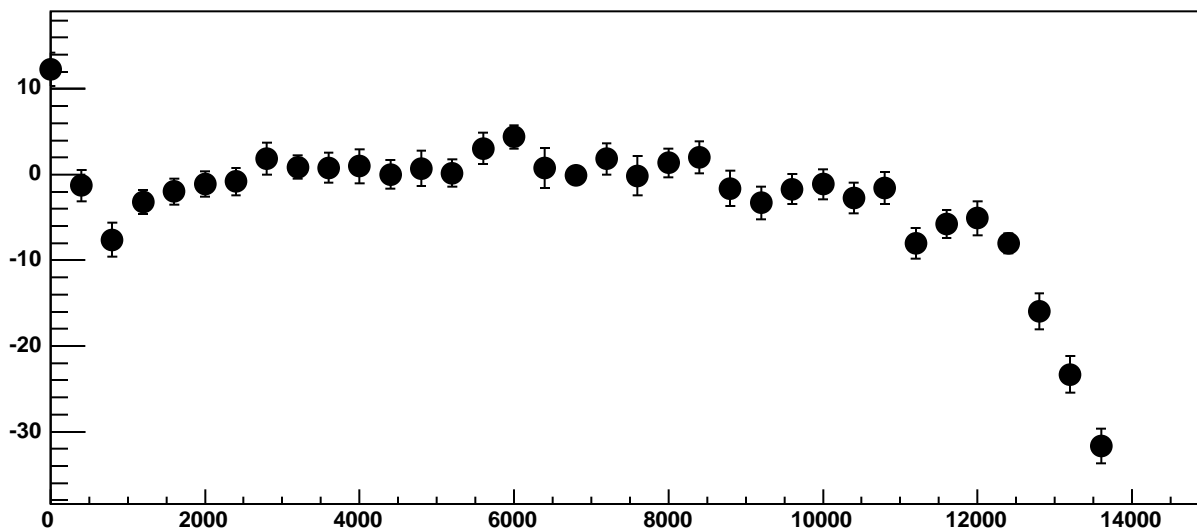


χ^2 / ndf 34.42 / 23
p0 -236.9 ± 0.7335
p1 0.02809 ± 0.0001158

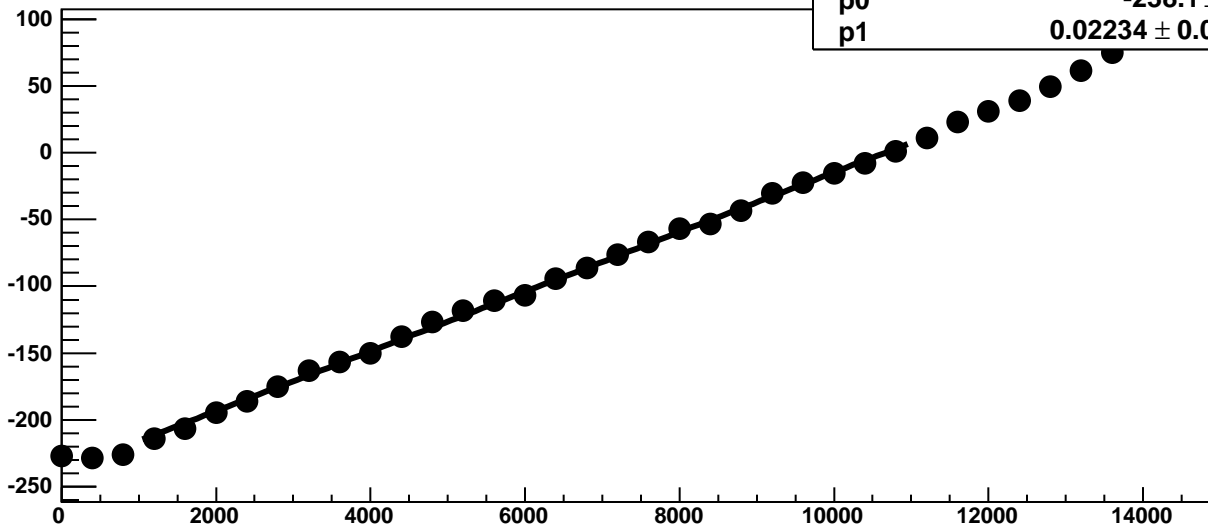
Chip 6, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

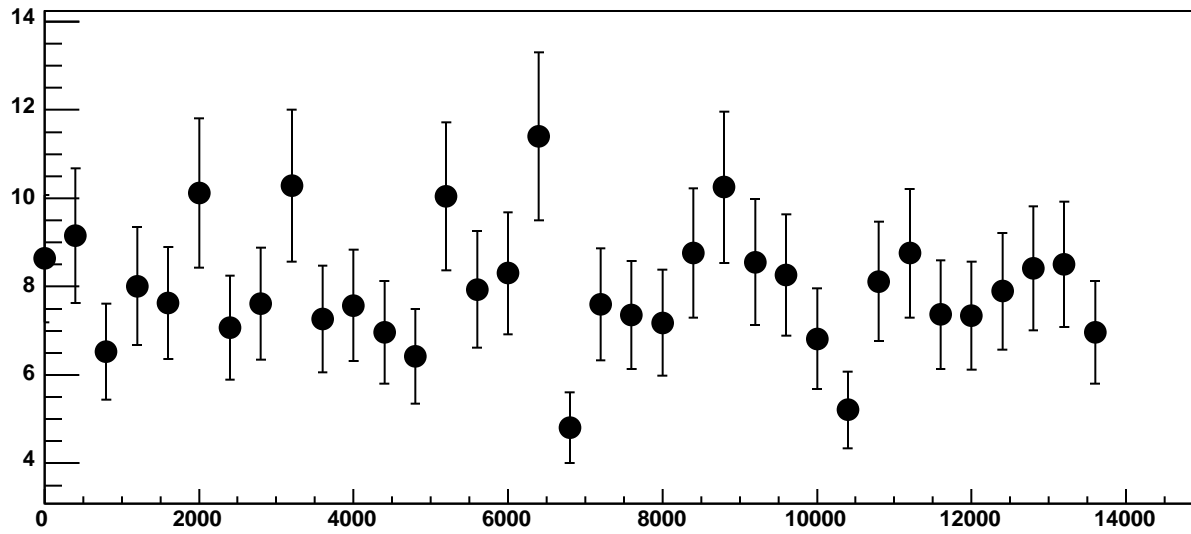


Chip 6, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

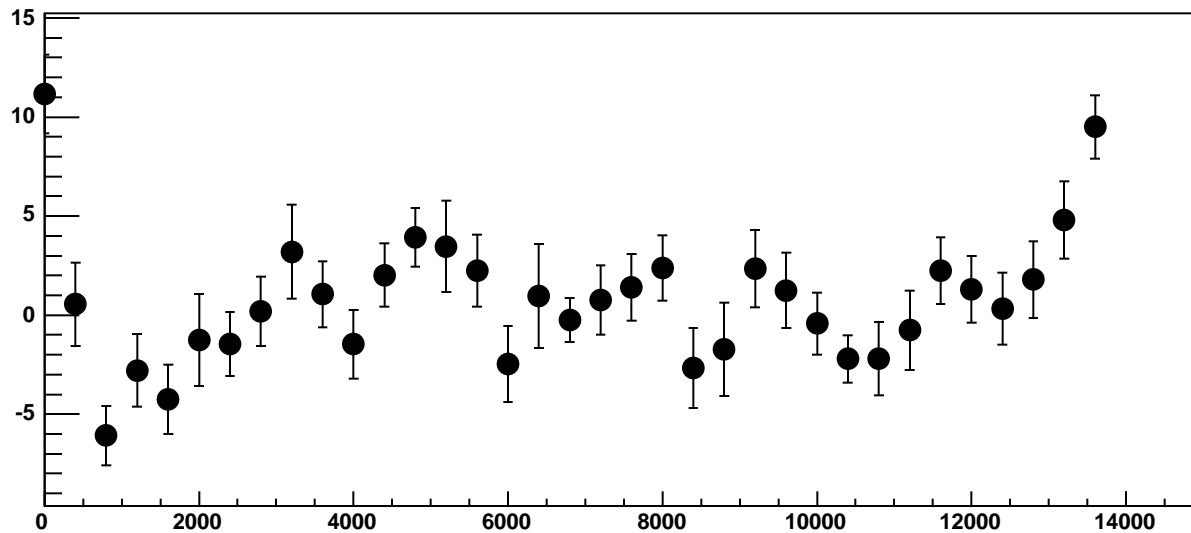


χ^2 / ndf 38.6 / 23
p0 -238.1 ± 0.8221
p1 0.02234 ± 0.0001197

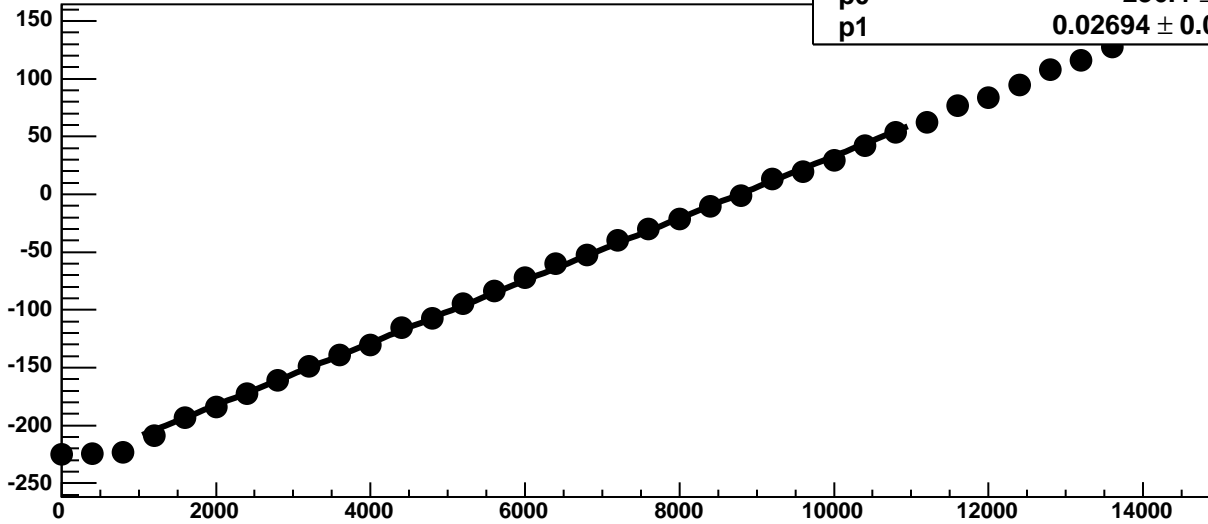
Chip 6, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

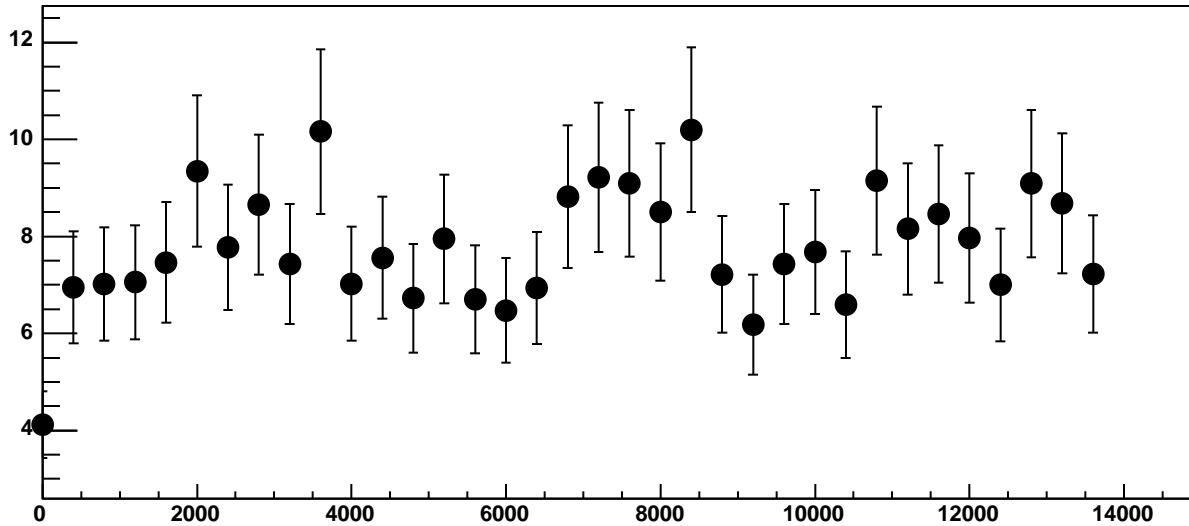


Chip 6, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

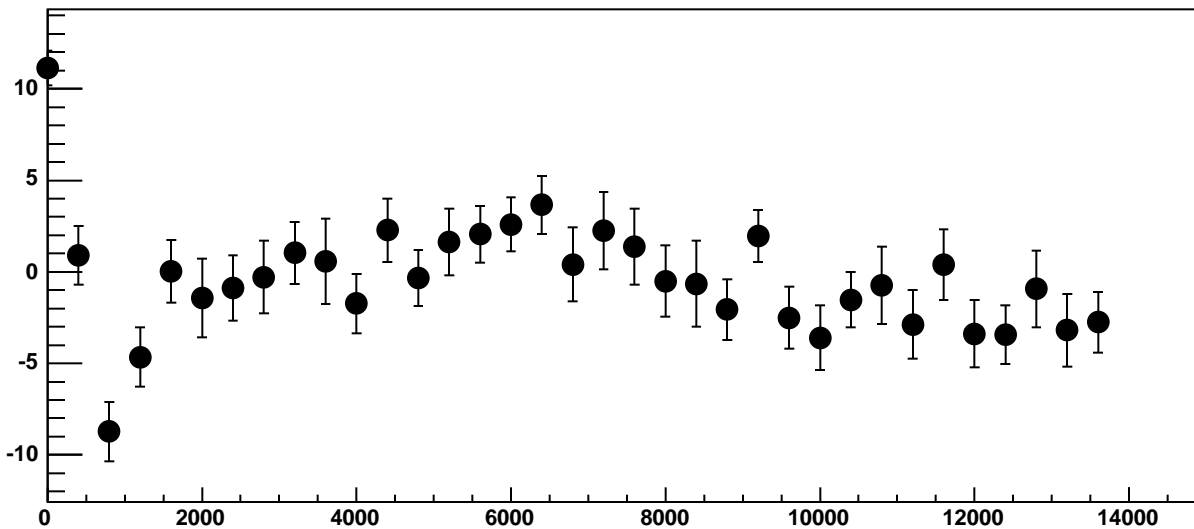


χ^2 / ndf 35.98 / 23
p0 -236.4 ± 0.8174
p1 0.02694 ± 0.0001221

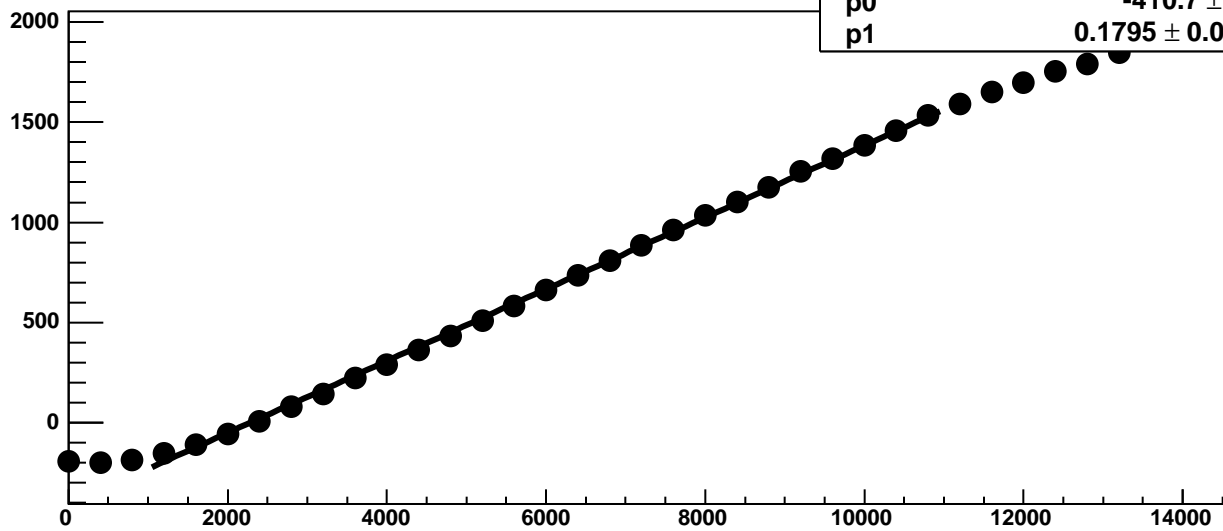
Chip 6, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



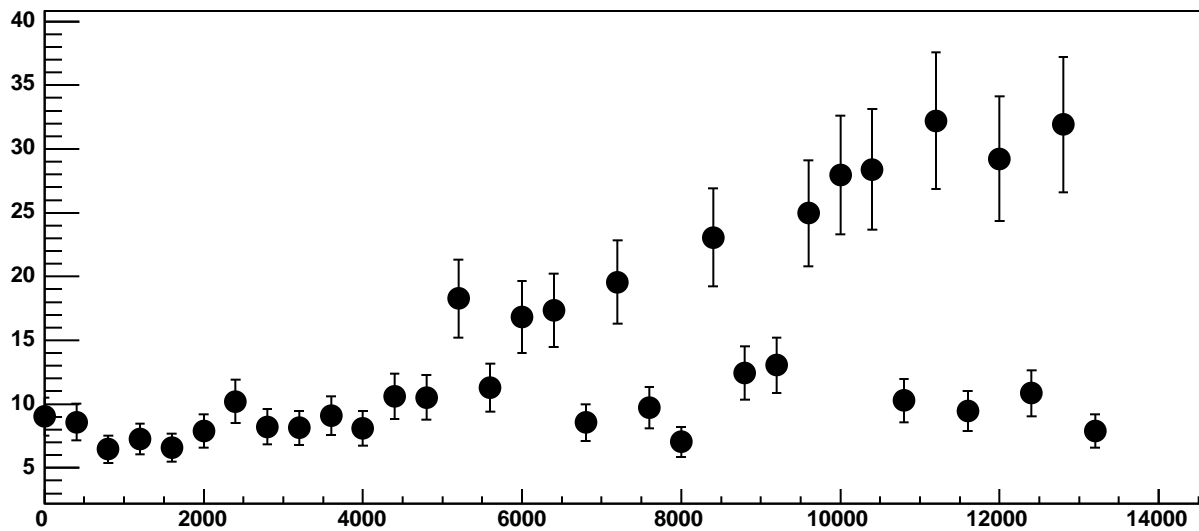
Chip 6, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC



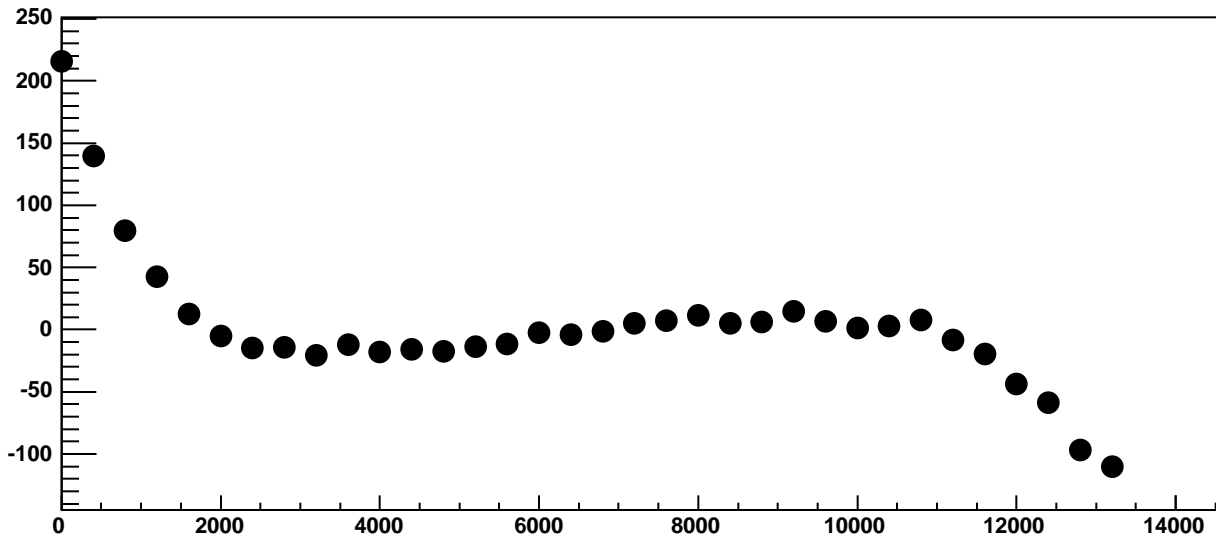
Chip 6, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



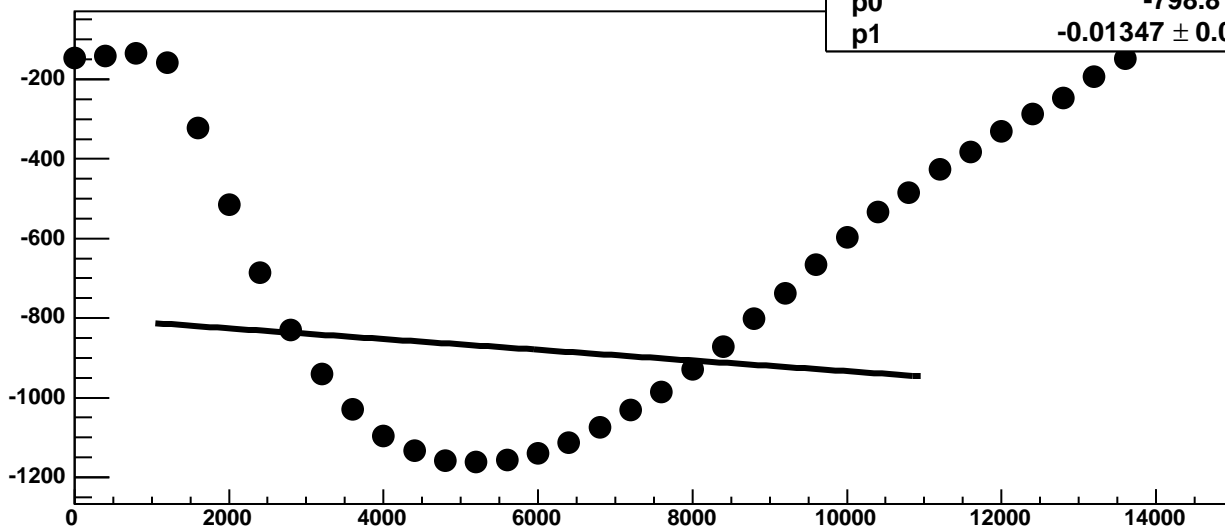
Chip 6, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

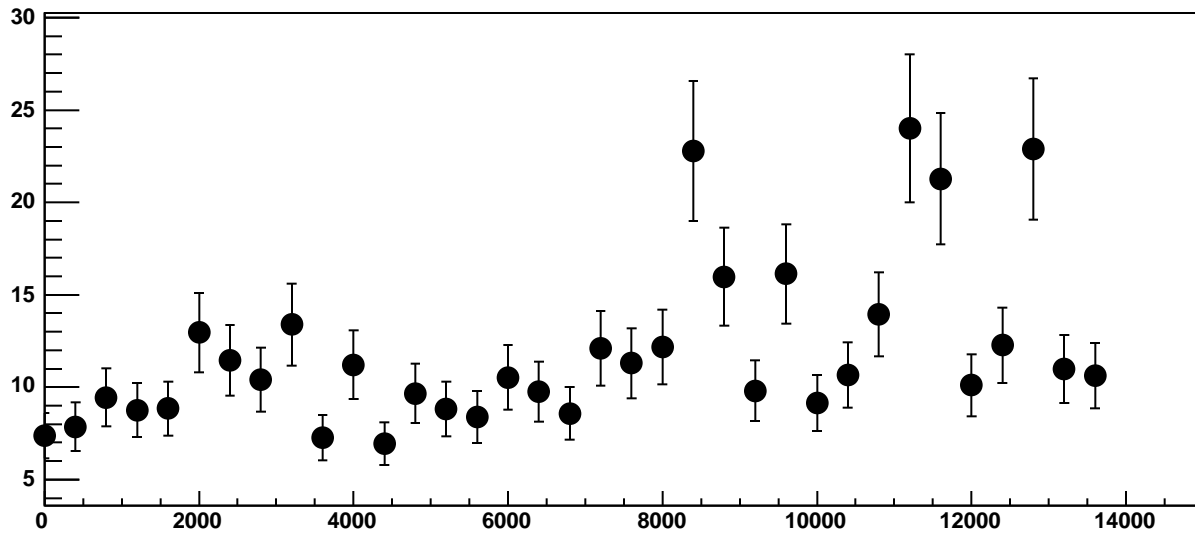


Chip 6, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

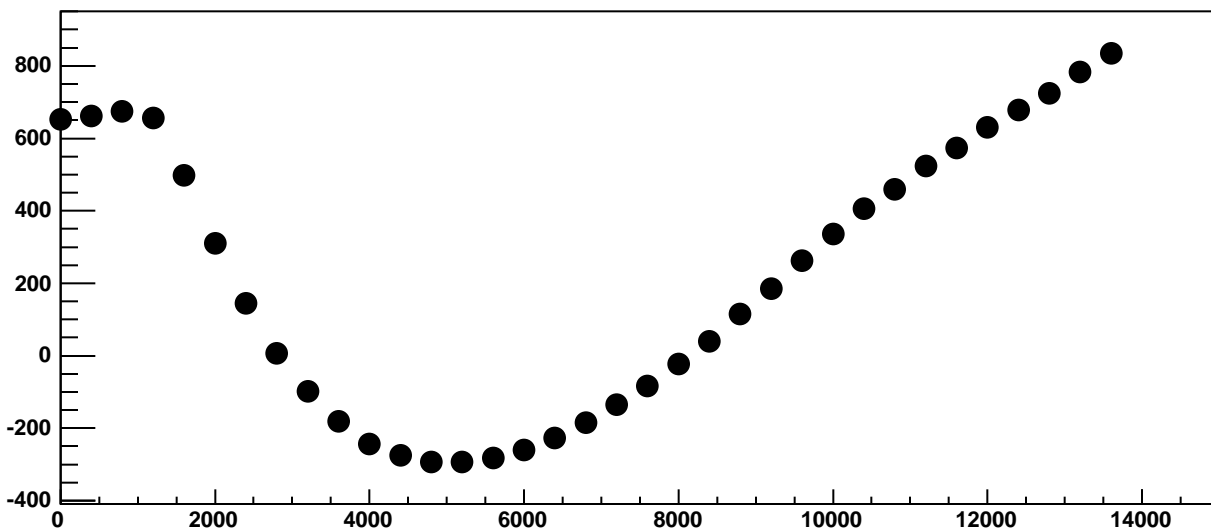


χ^2 / ndf 4.15e+05 / 23
p0 -798.8 ± 1.055
p1 -0.01347 ± 0.0001711

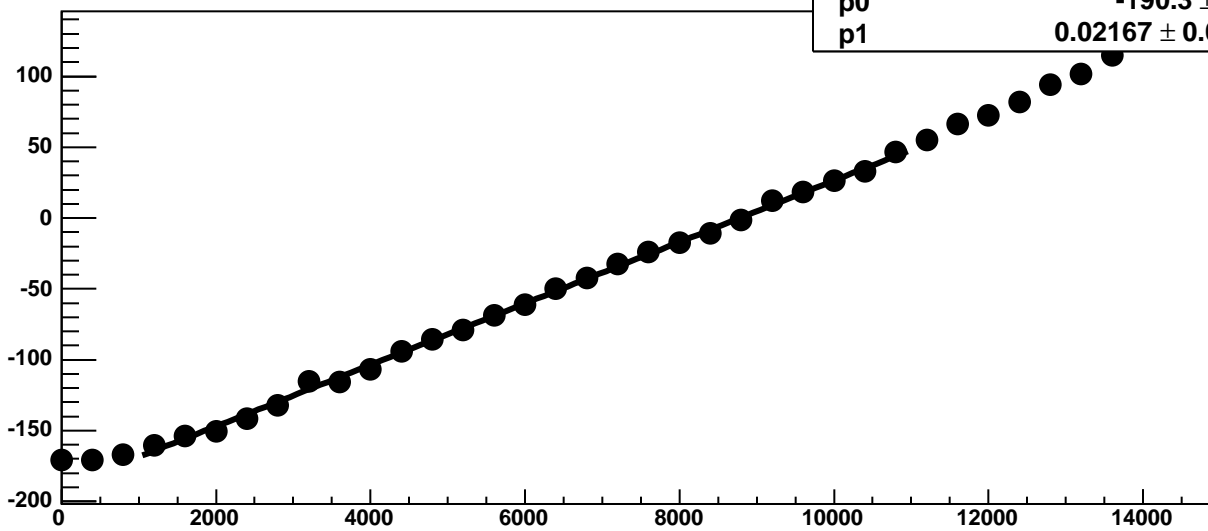
Chip 6, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



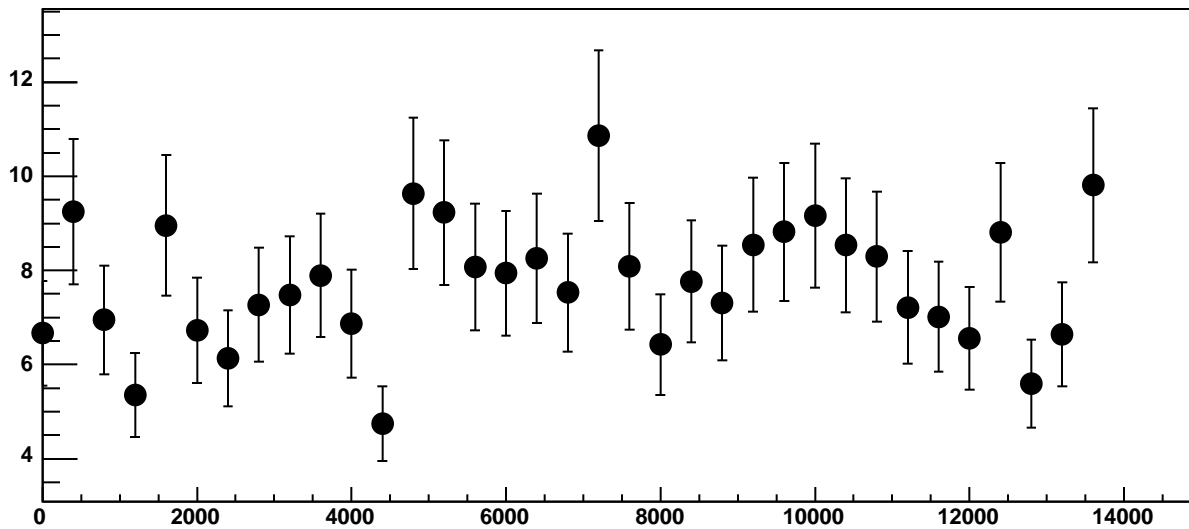
Chip 6, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



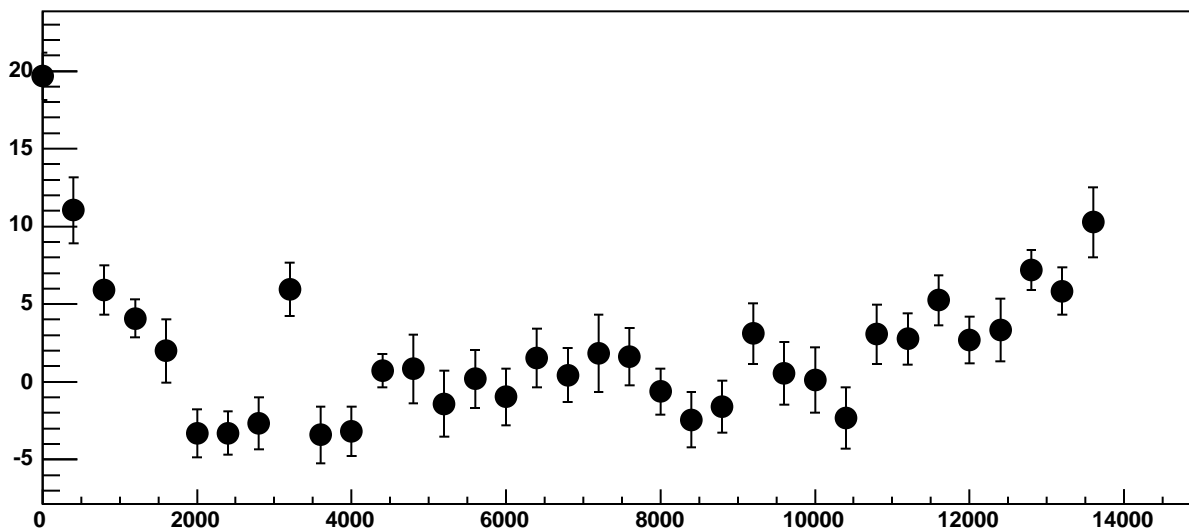
Chip 6, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC



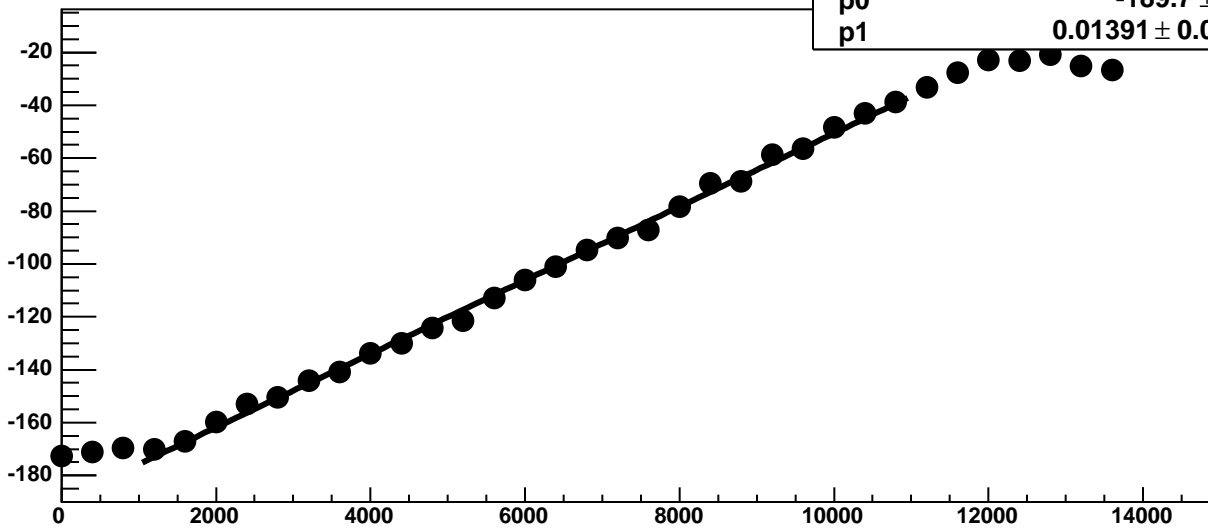
Chip 6, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



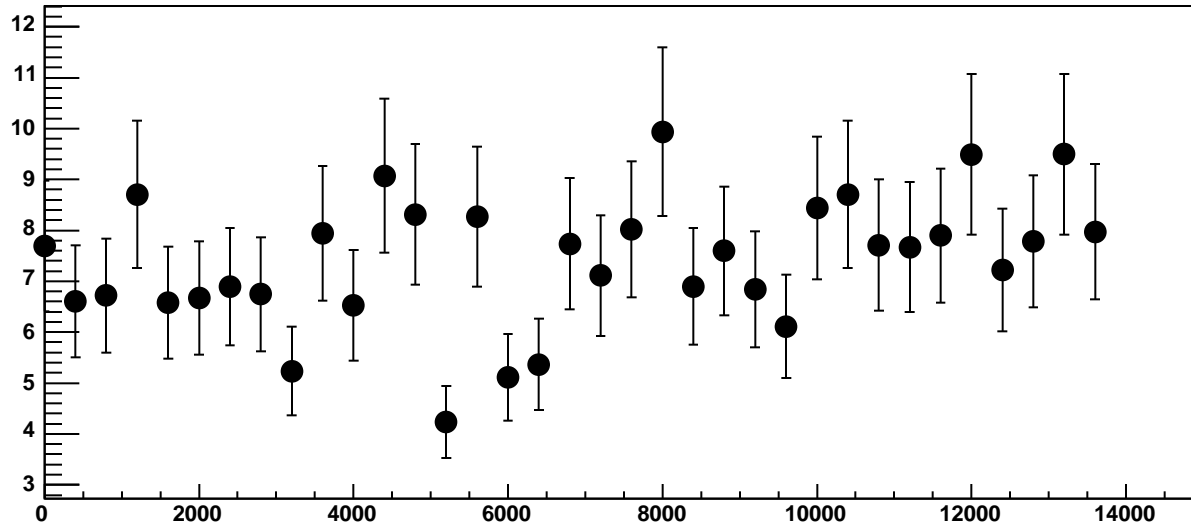
Chip 6, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC



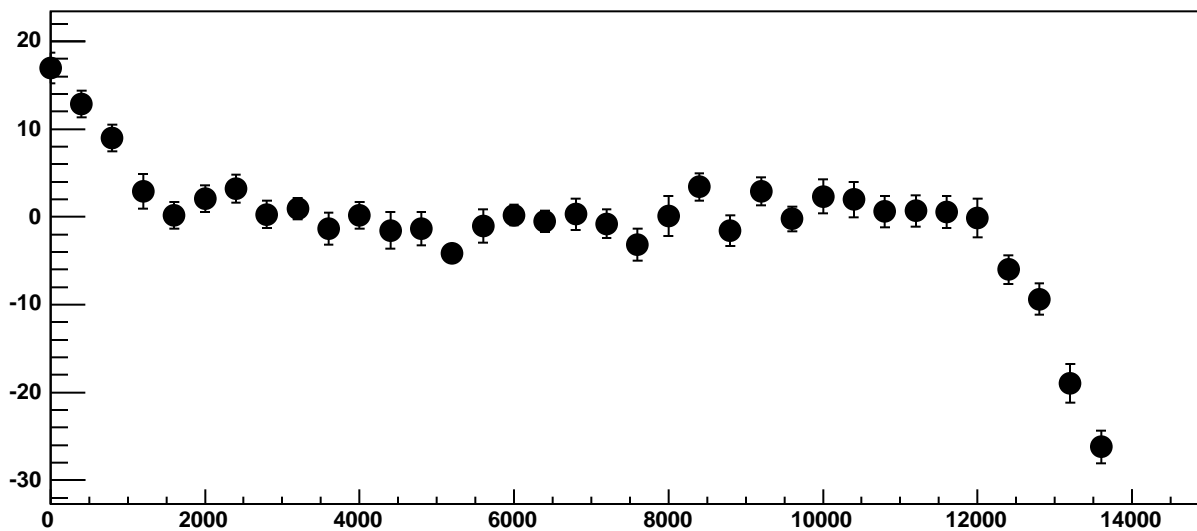
Chip 6, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



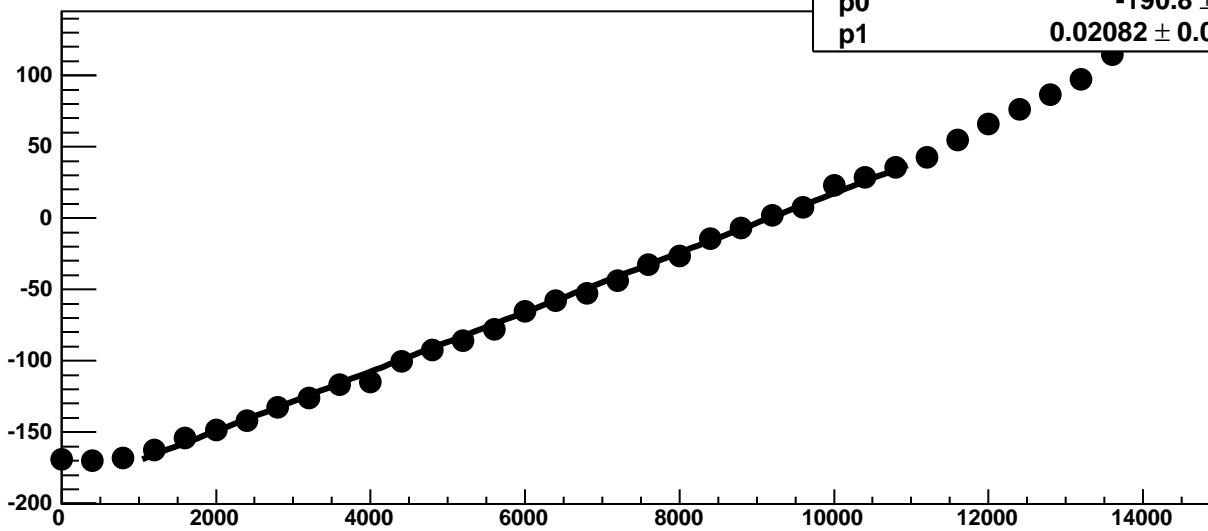
Chip 6, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

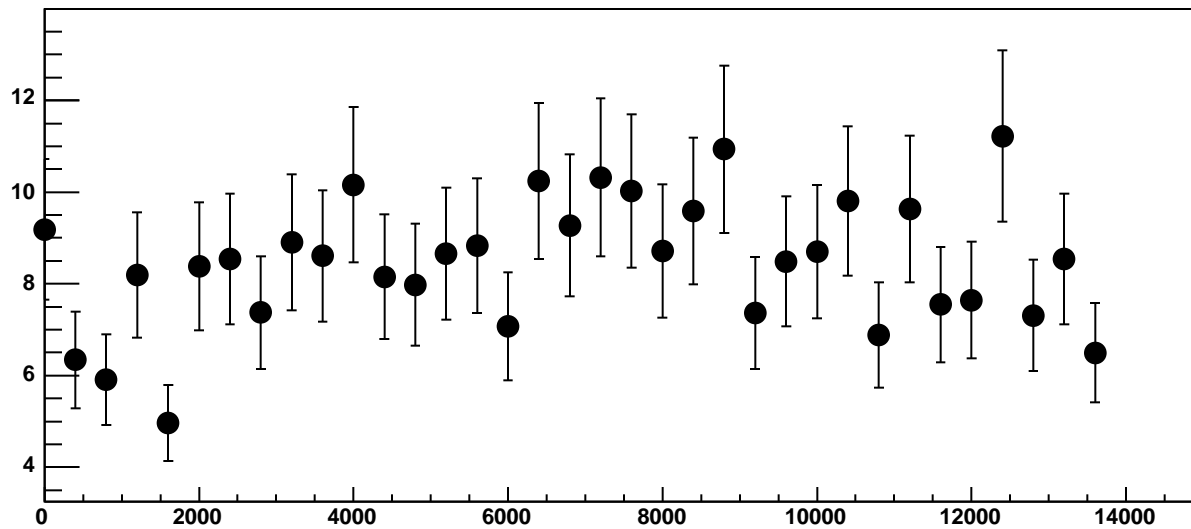


Chip 6, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

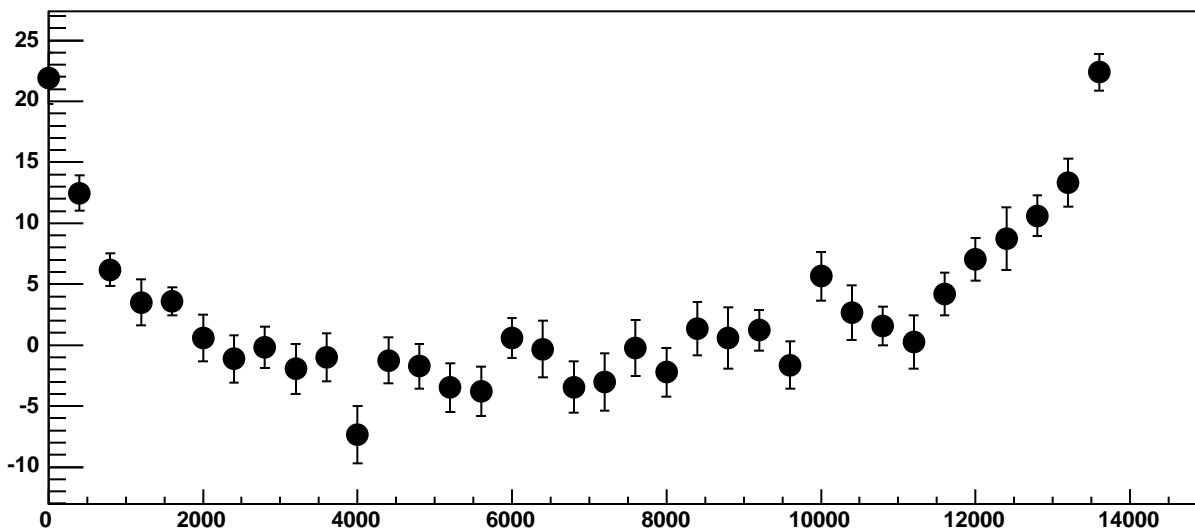


χ^2 / ndf 50.48 / 23
p0 -190.8 ± 0.7917
p1 0.02082 ± 0.0001234

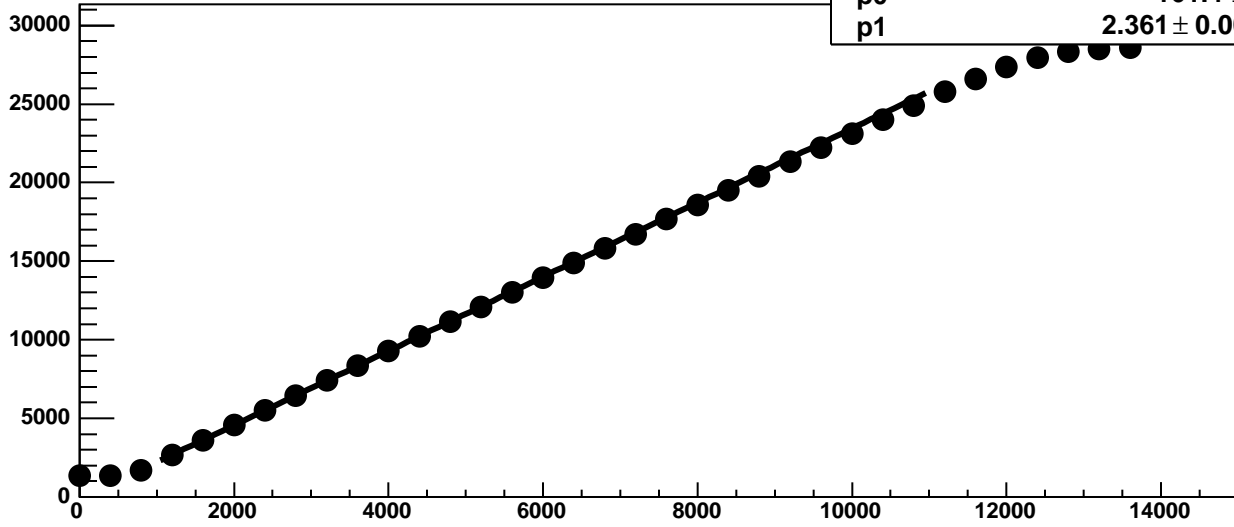
Chip 6, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



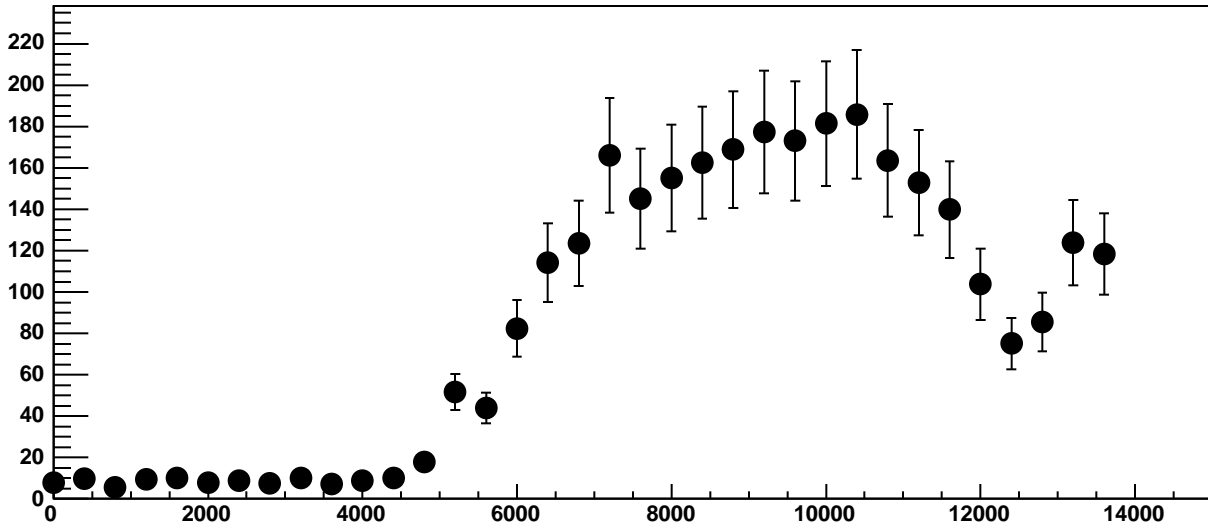
Chip 6, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



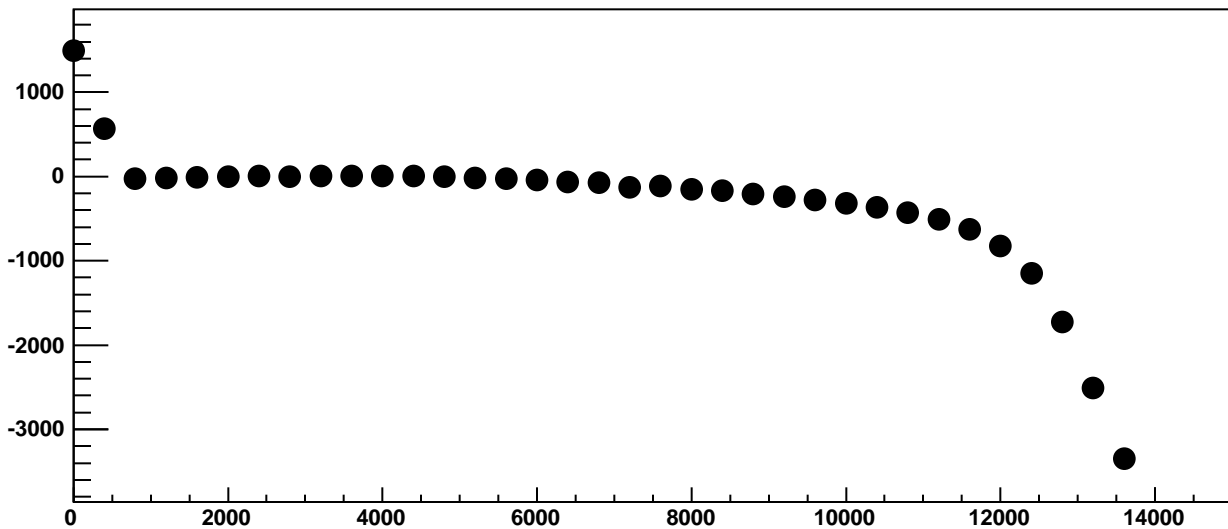
Chip 6, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC



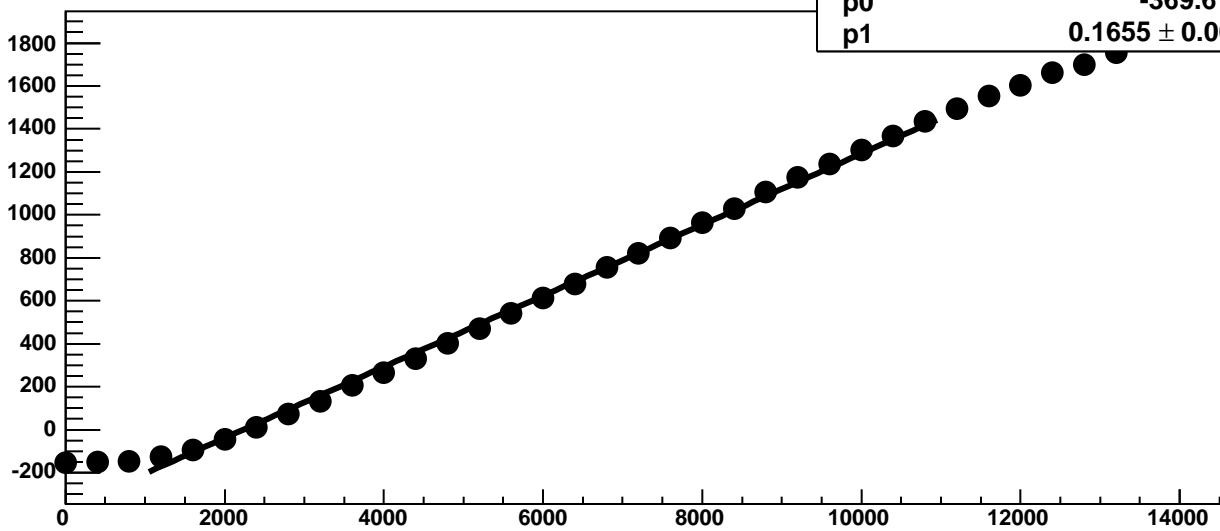
Chip 6, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



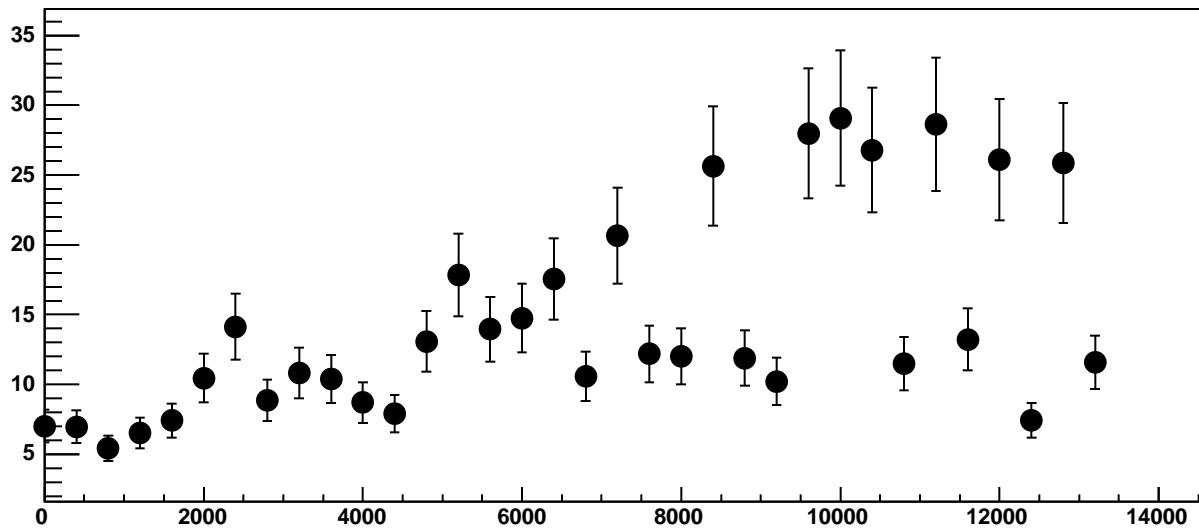
Chip 6, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC



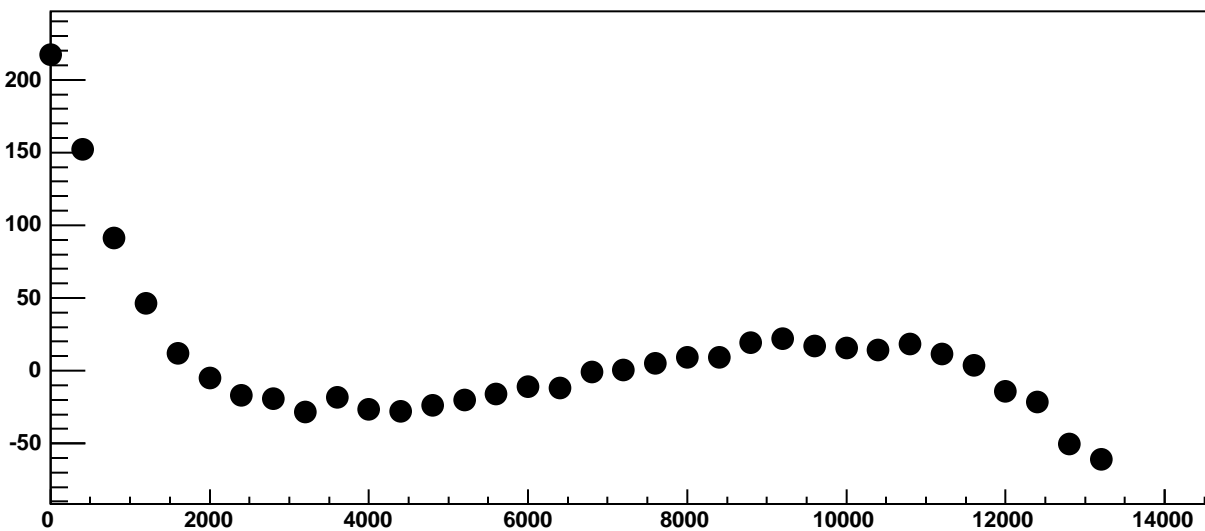
Chip 6, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



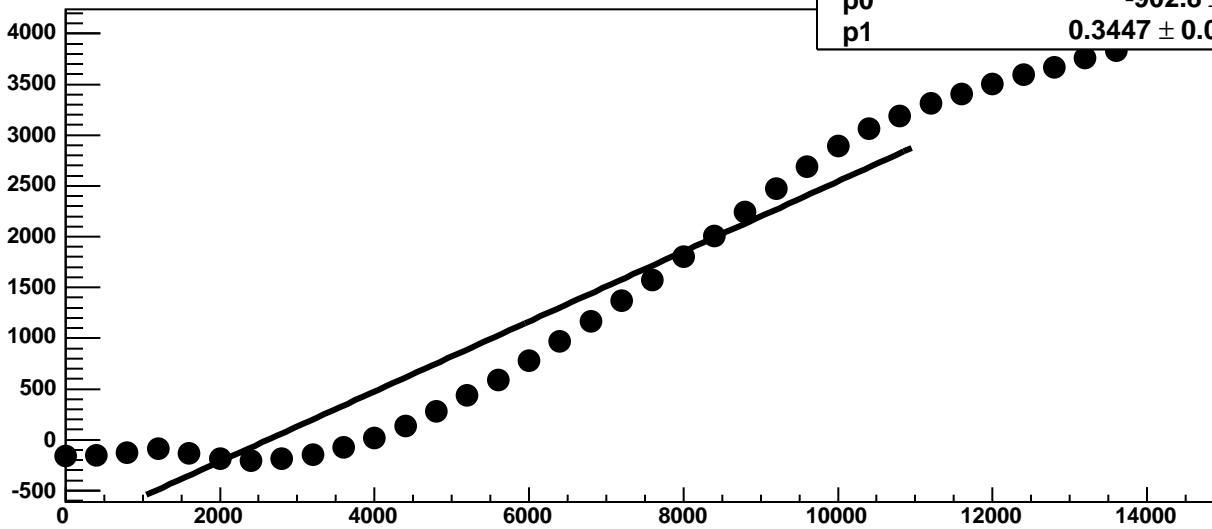
Chip 6, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

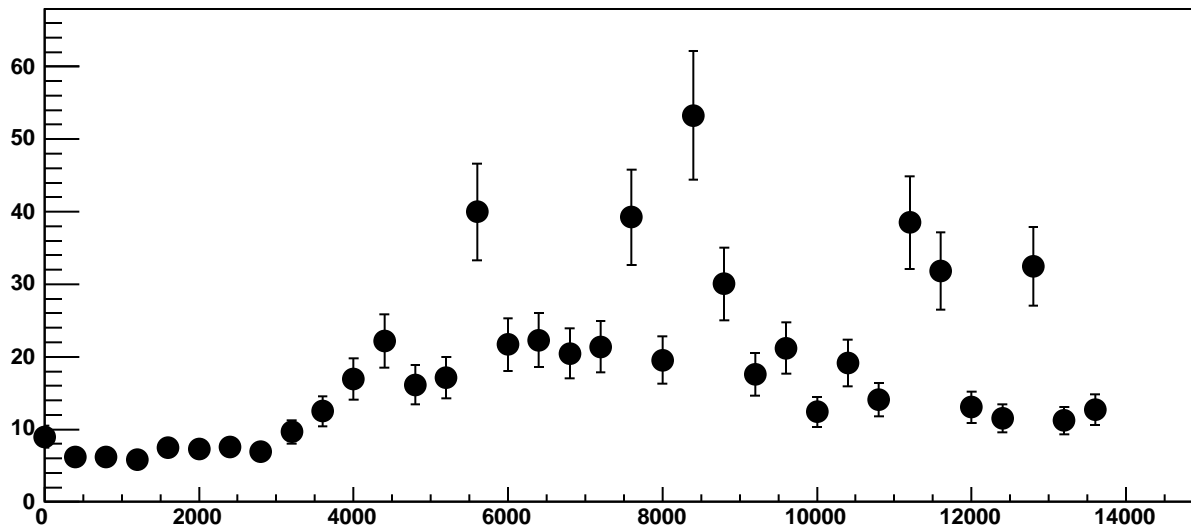


Chip 6, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

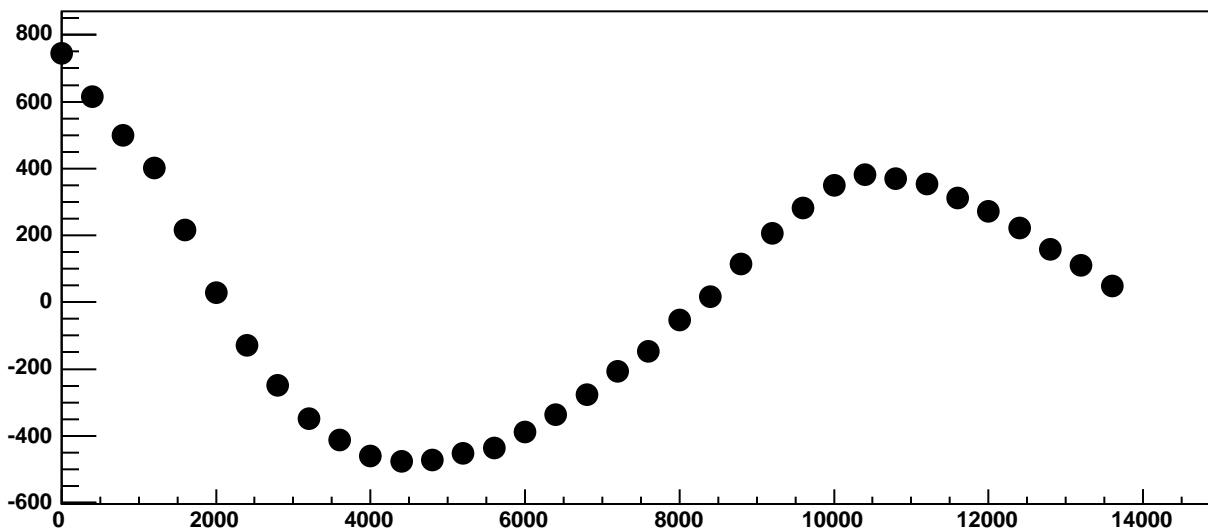


χ^2 / ndf 2.95e+05 / 23
p0 -902.8 ± 0.9131
p1 0.3447 ± 0.0001958

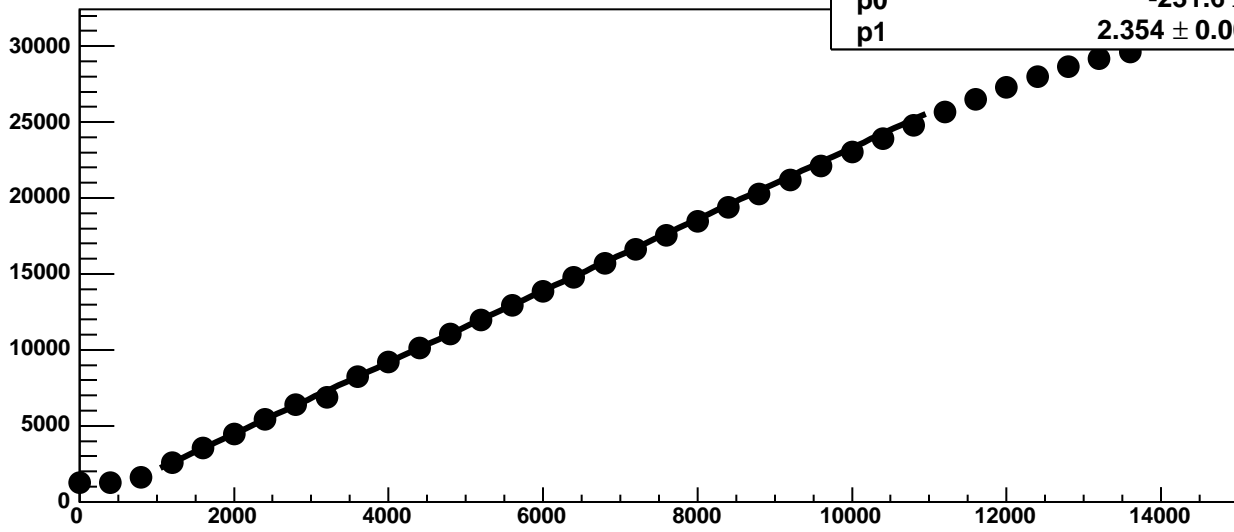
Chip 6, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



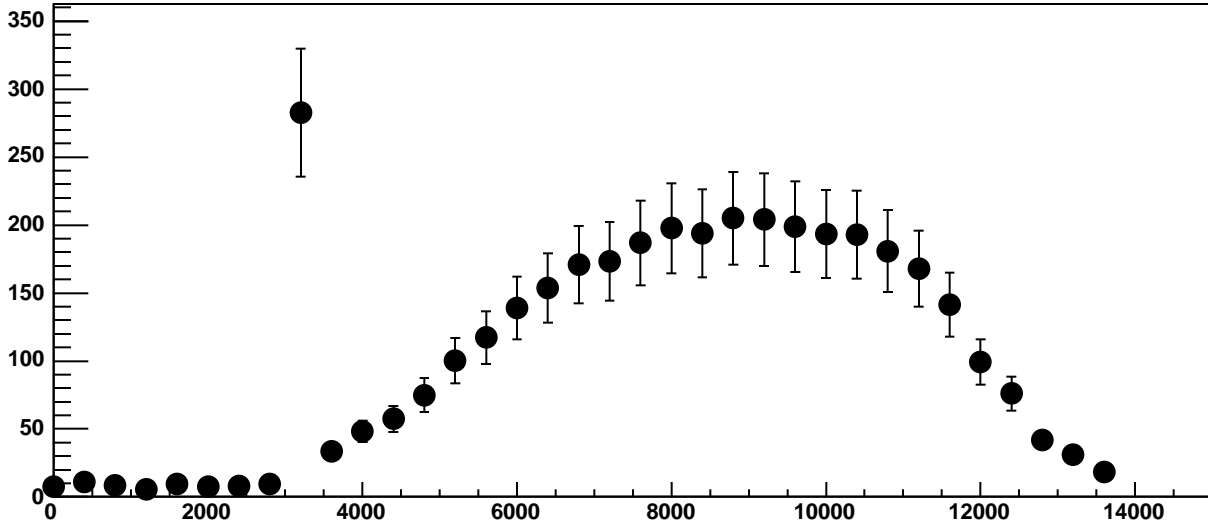
Chip 6, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



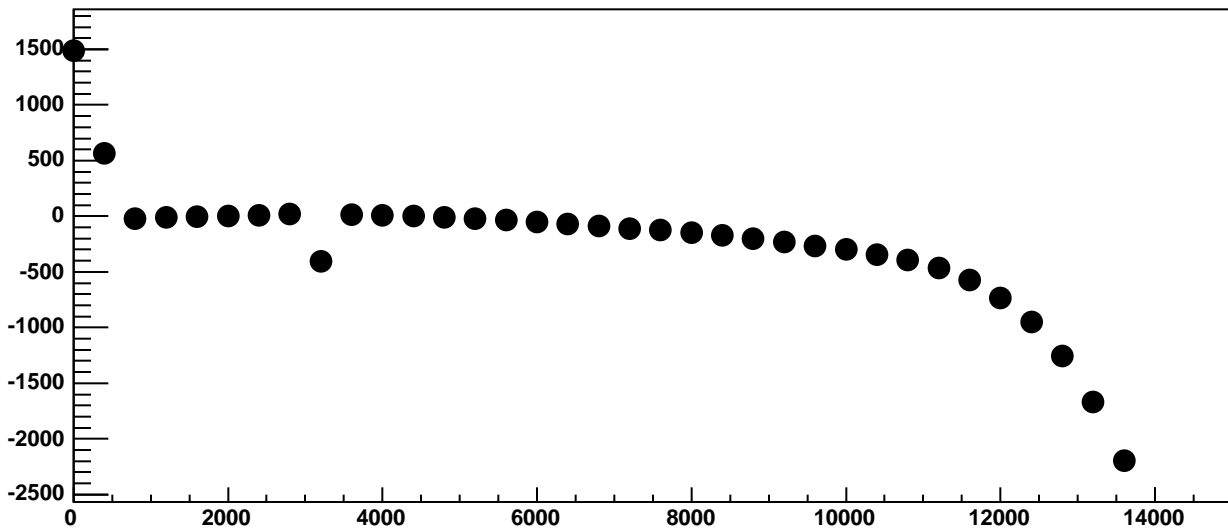
Chip 6, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC



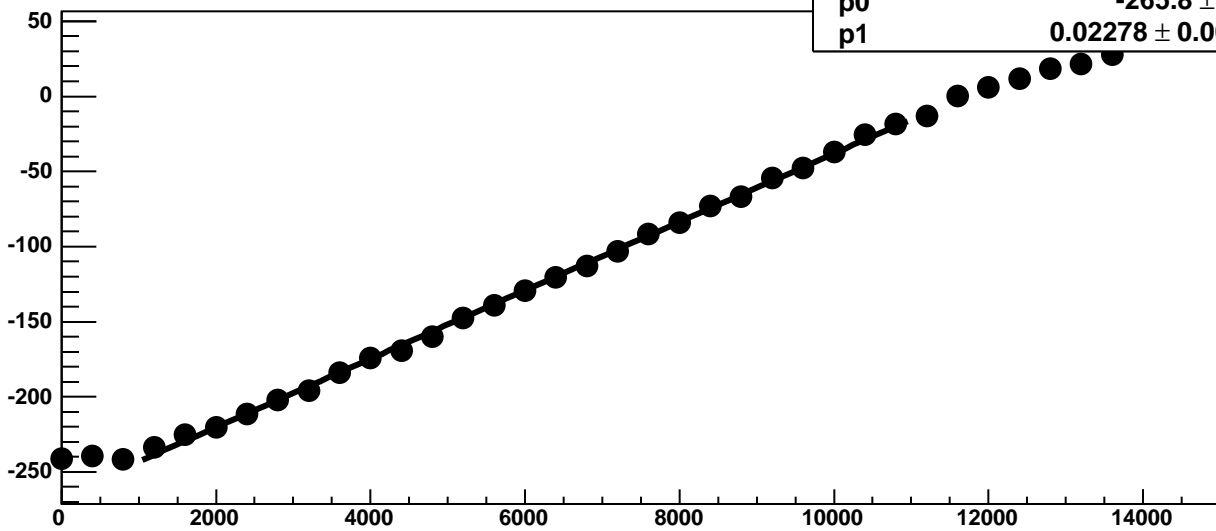
Chip 6, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC

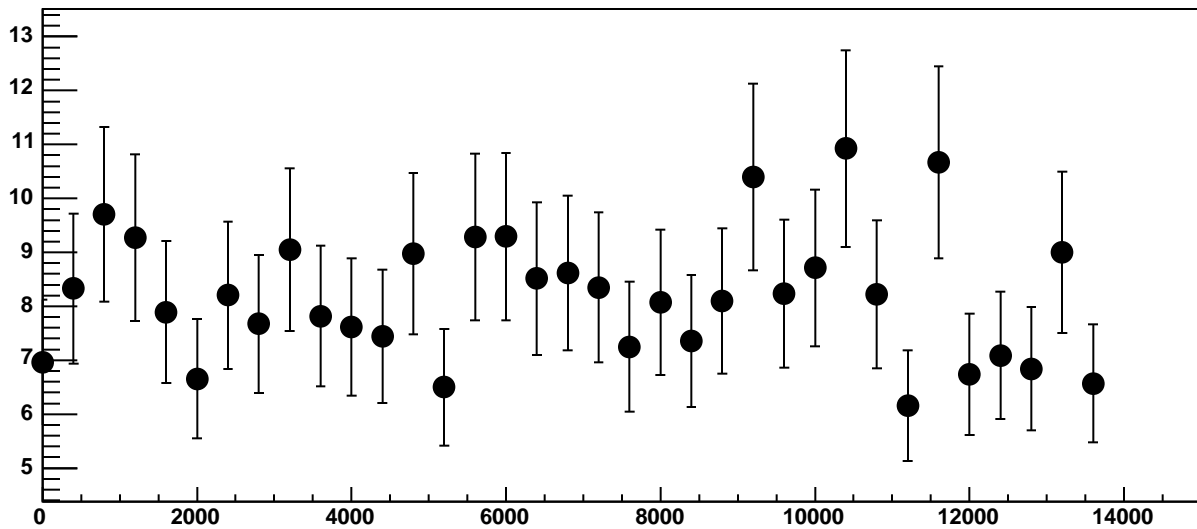


Chip 6, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

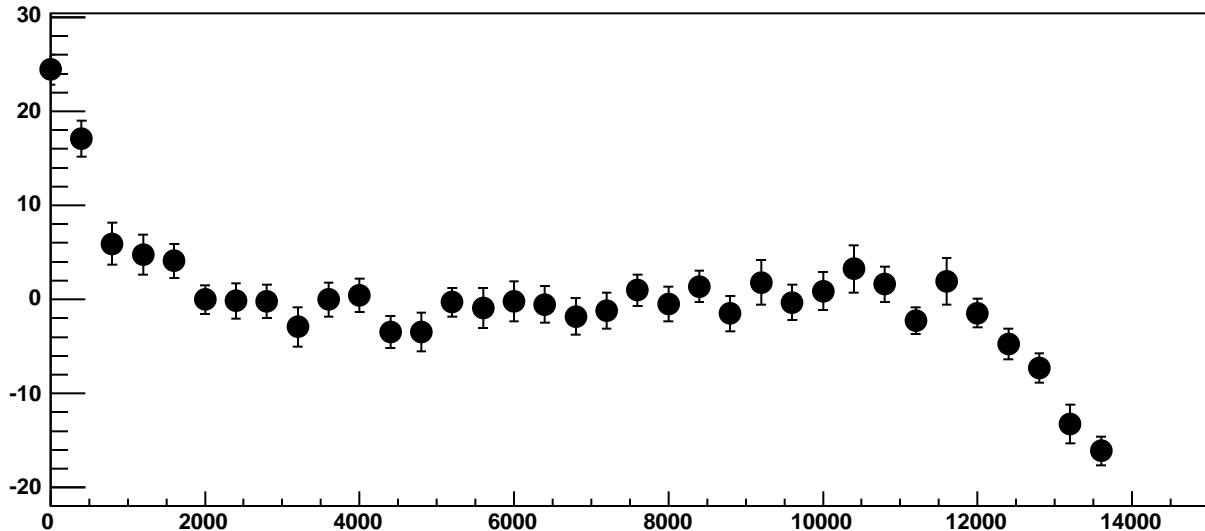


χ^2 / ndf 25.59 / 23
p0 -265.8 ± 0.8549
p1 0.02278 ± 0.0001325

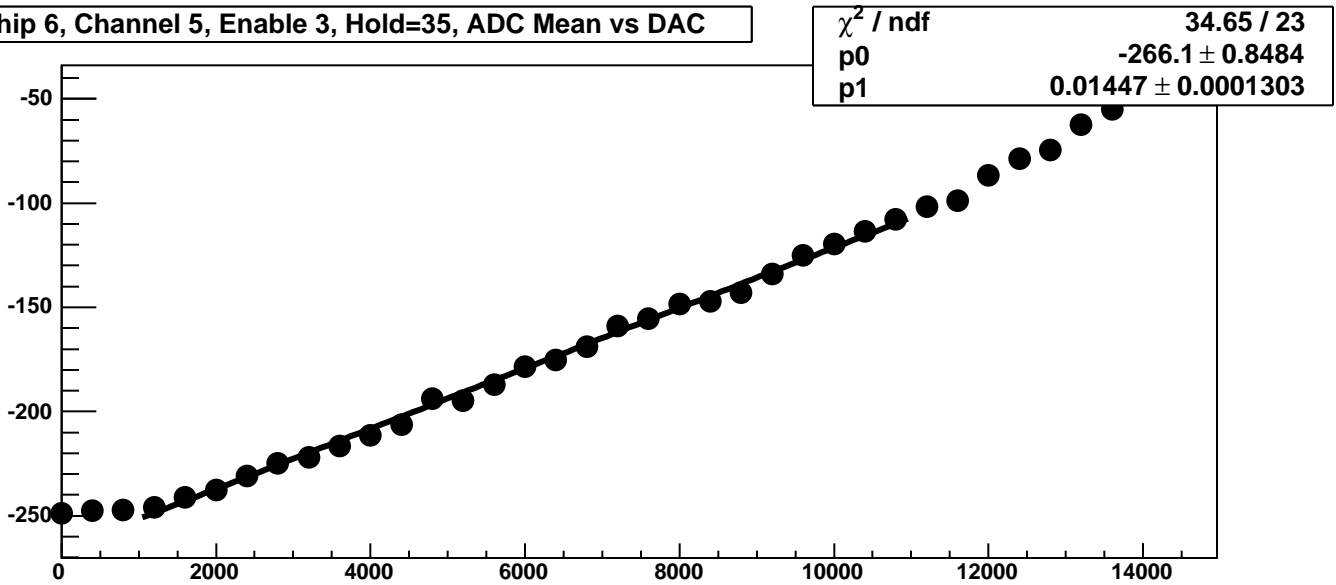
Chip 6, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



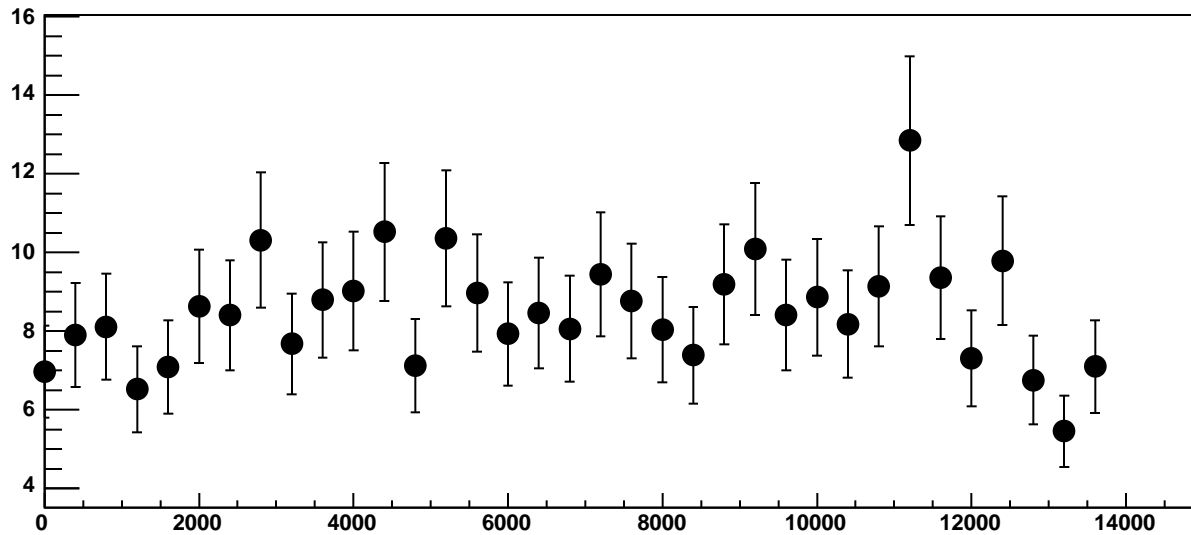
Chip 6, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



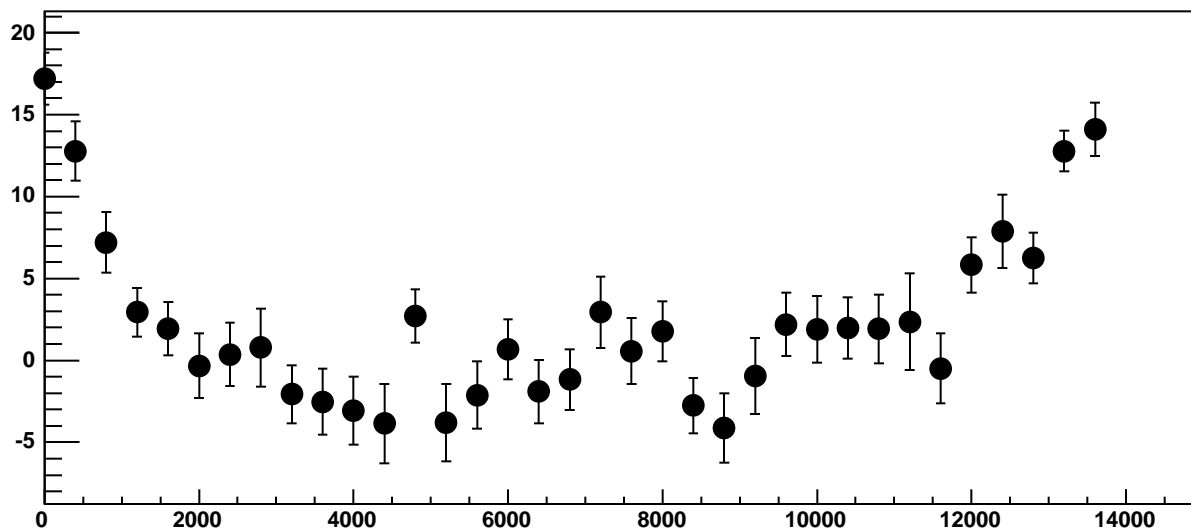
Chip 6, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



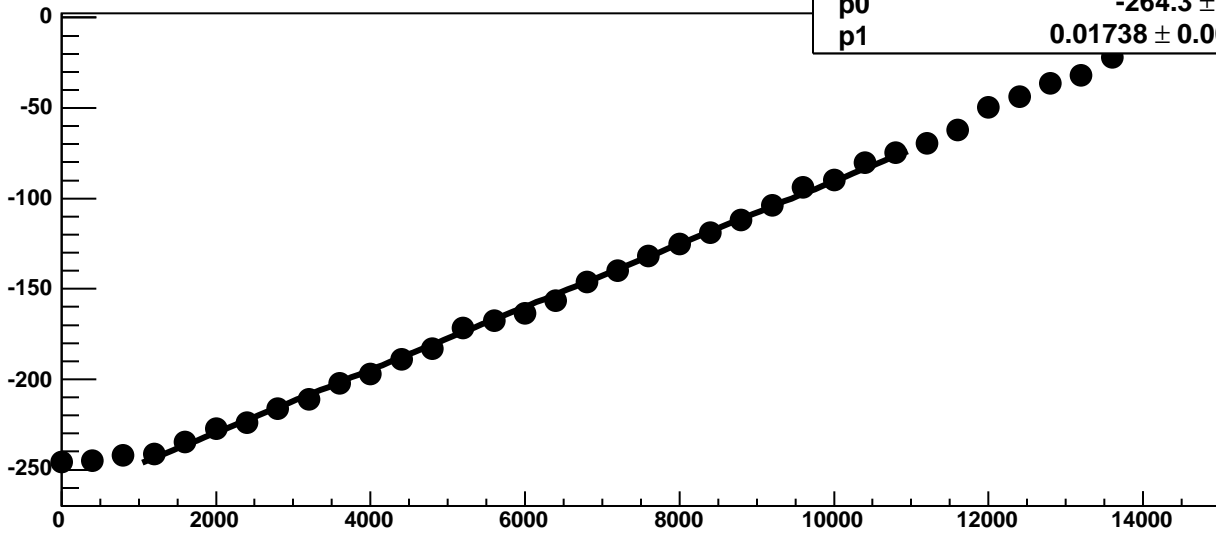
Chip 6, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



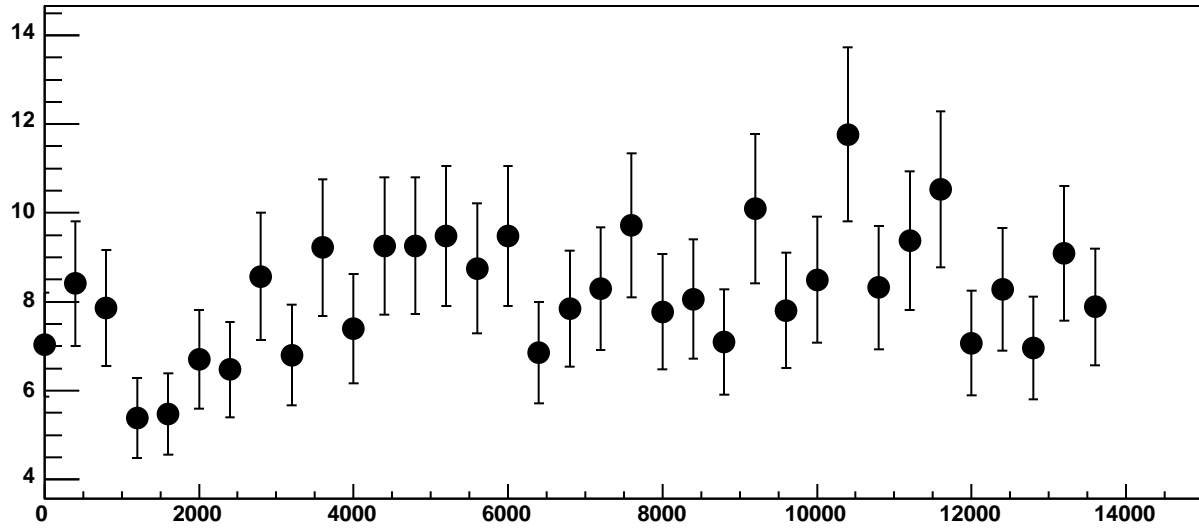
Chip 6, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



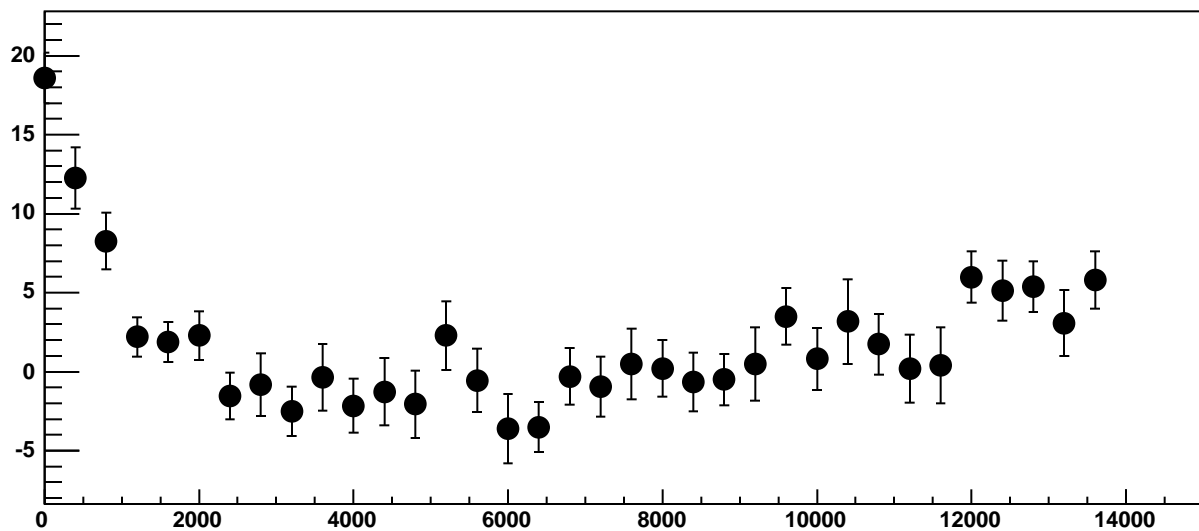
Chip 6, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC



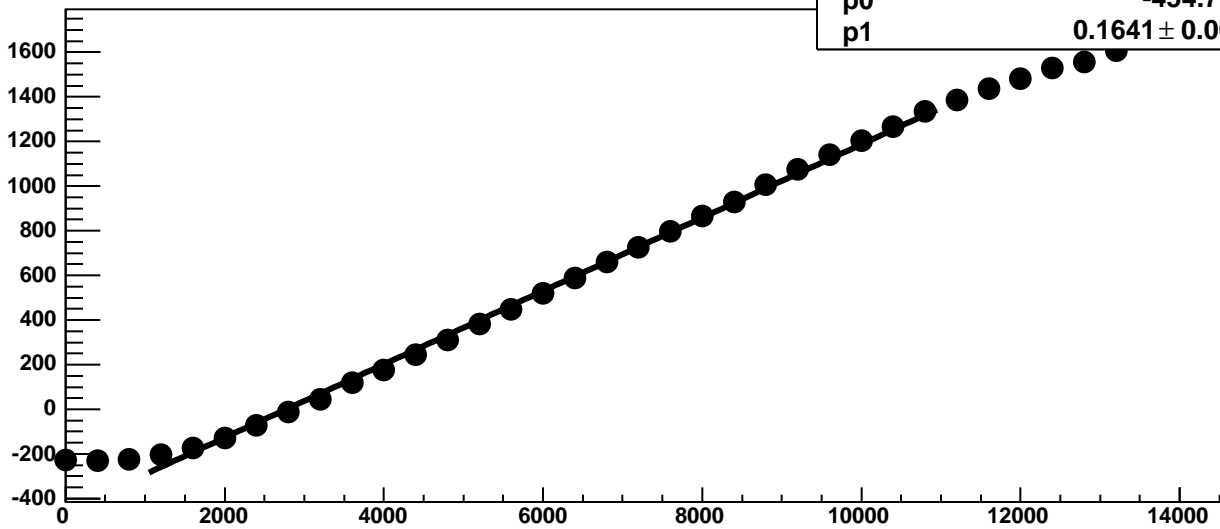
Chip 6, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



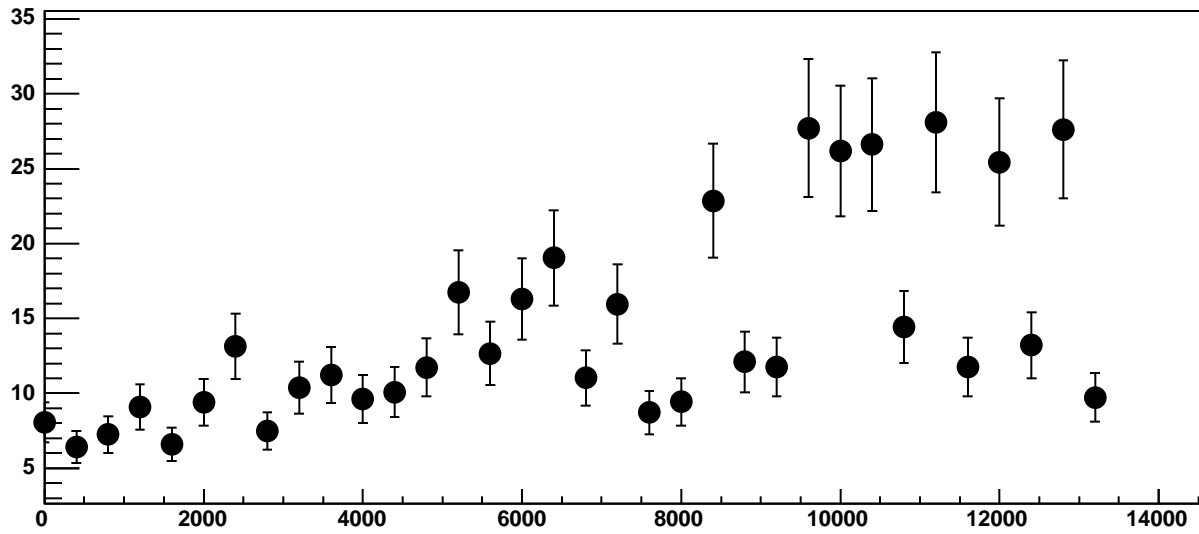
Chip 6, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



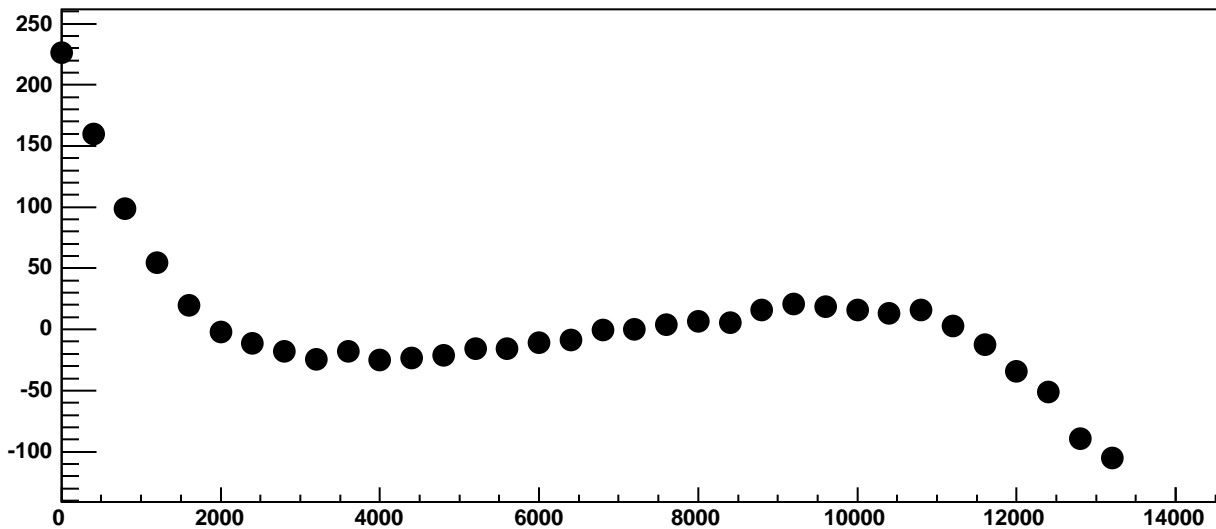
Chip 6, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



Chip 6, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC

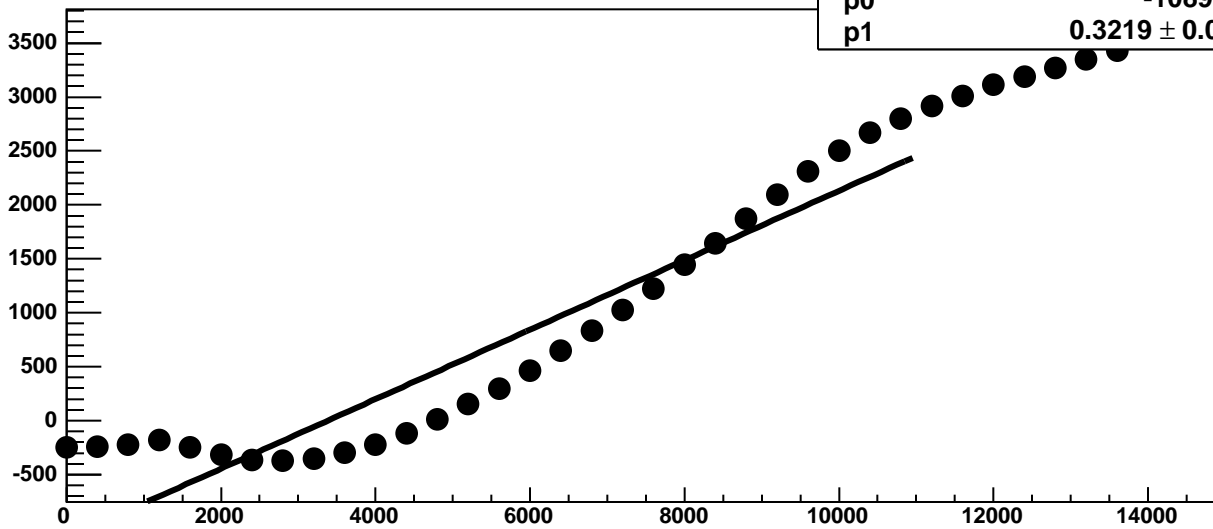


Chip 6, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

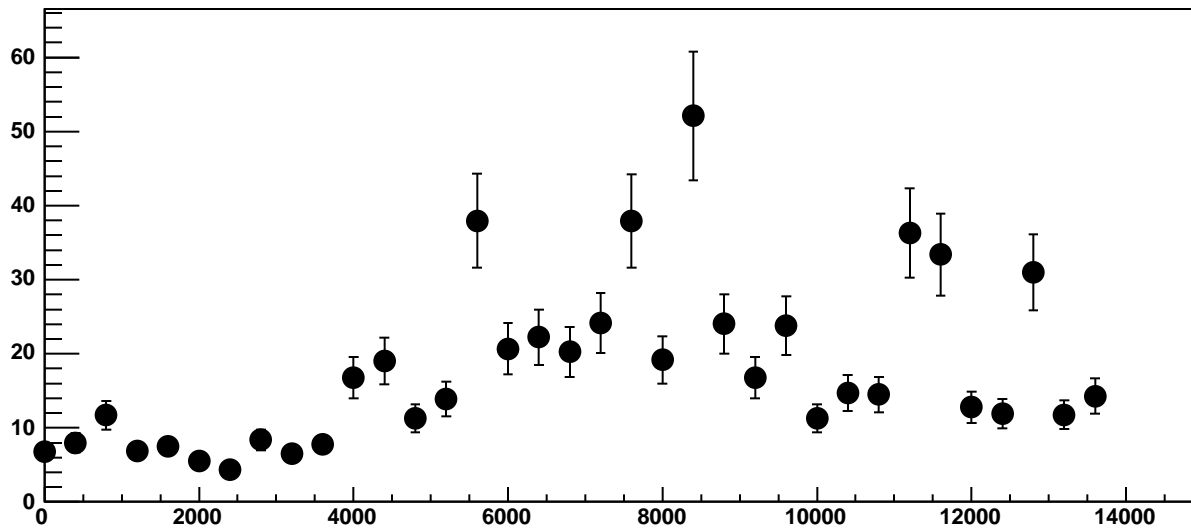


Chip 6, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

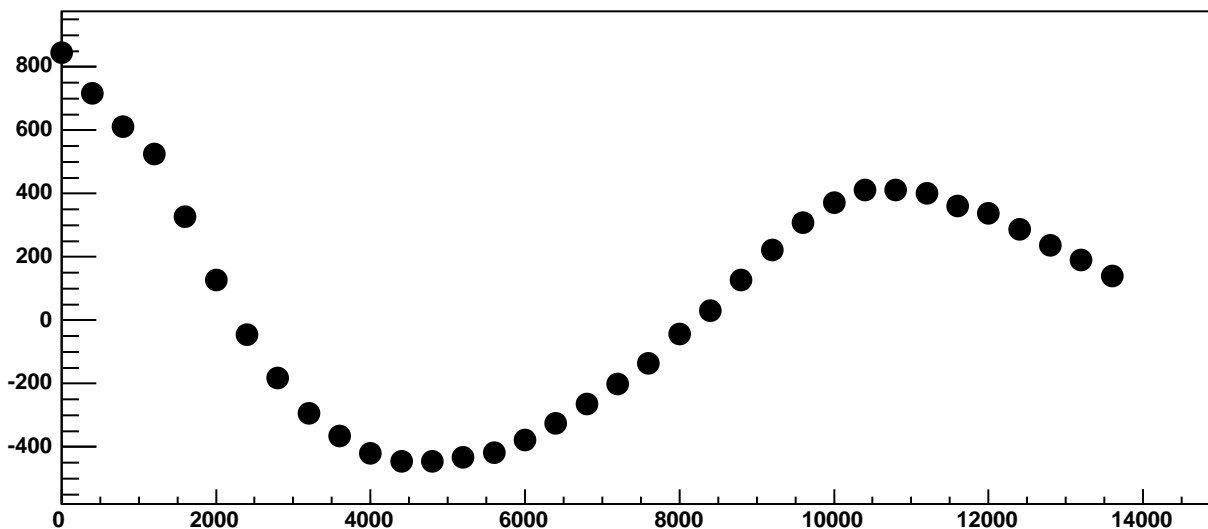
χ^2 / ndf 3.954e+05 / 23
p0 -1089 ± 0.814
p1 0.3219 ± 0.0001838



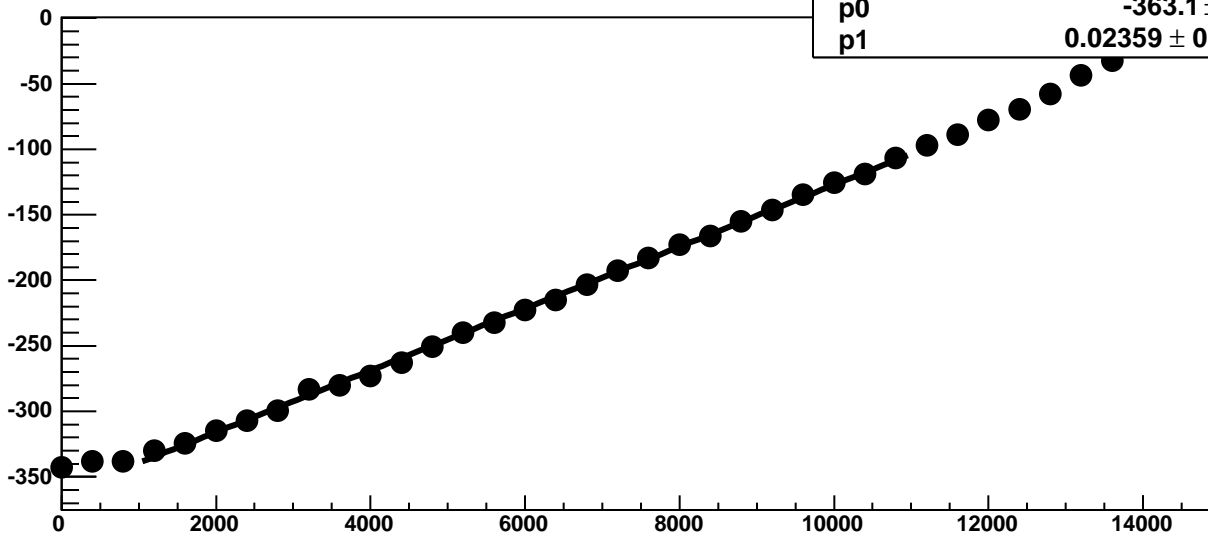
Chip 6, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 6, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

33.26 / 23

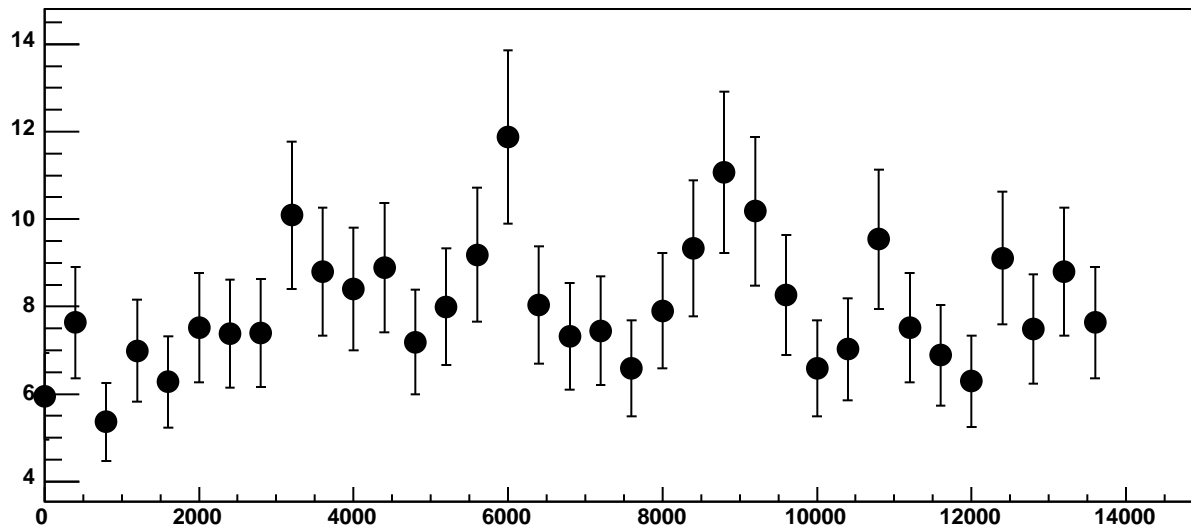
p0

-363.1 ± 0.7981

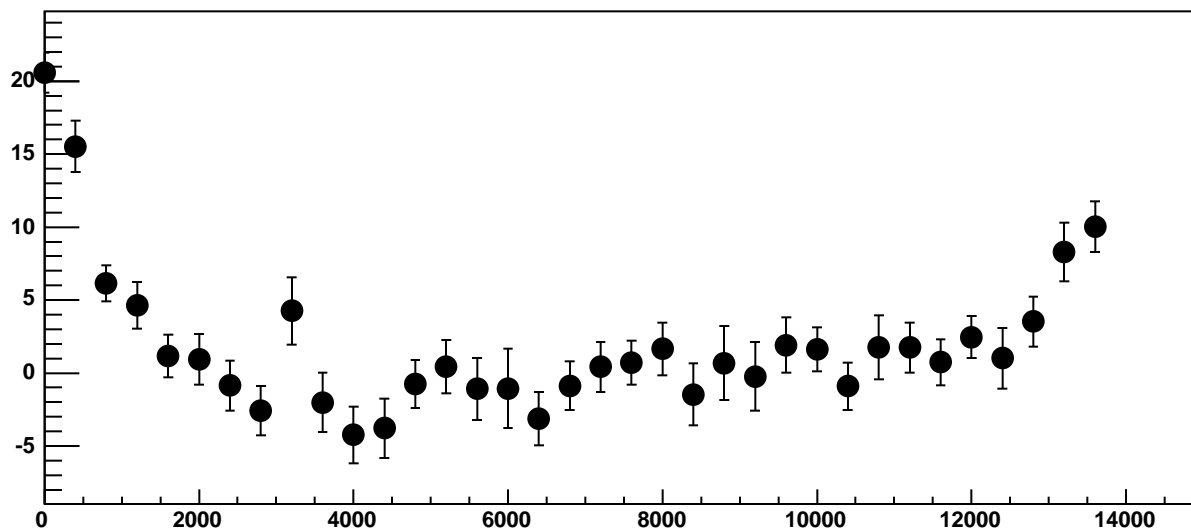
p1

0.02359 ± 0.000122

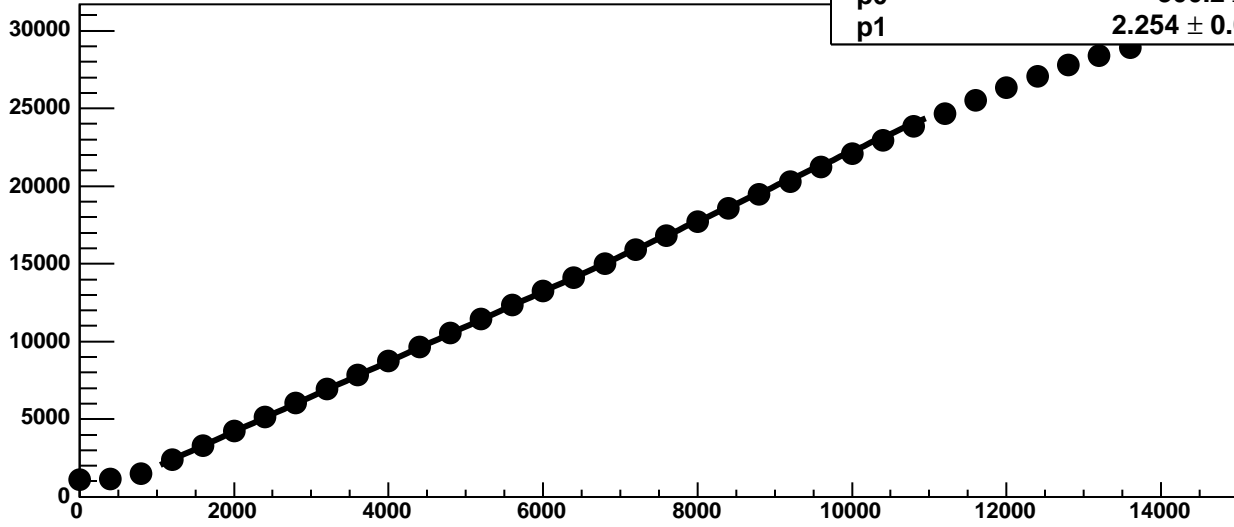
Chip 6, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



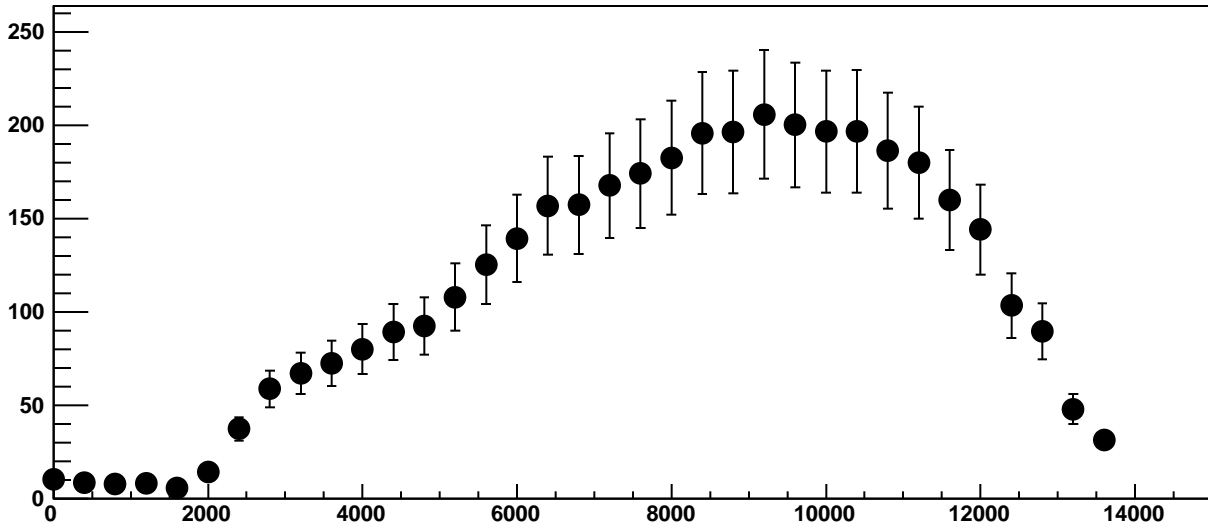
Chip 6, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



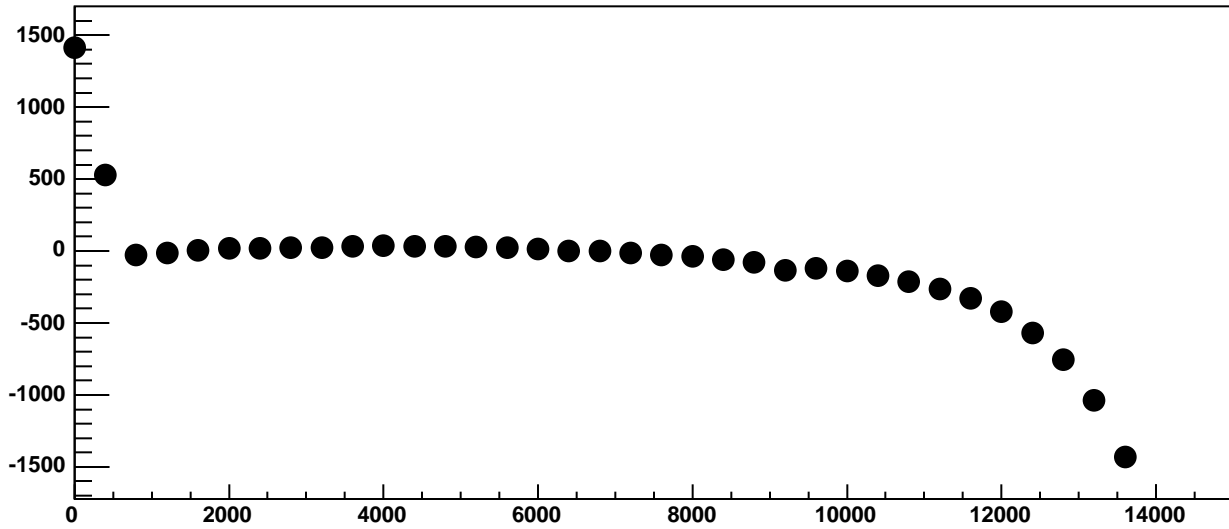
Chip 6, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



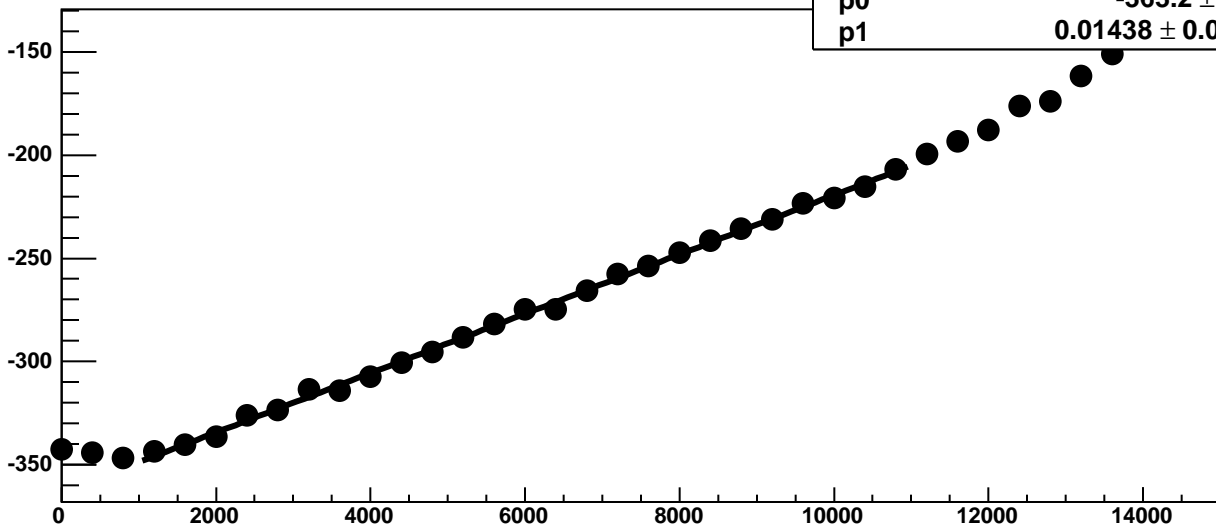
Chip 6, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC

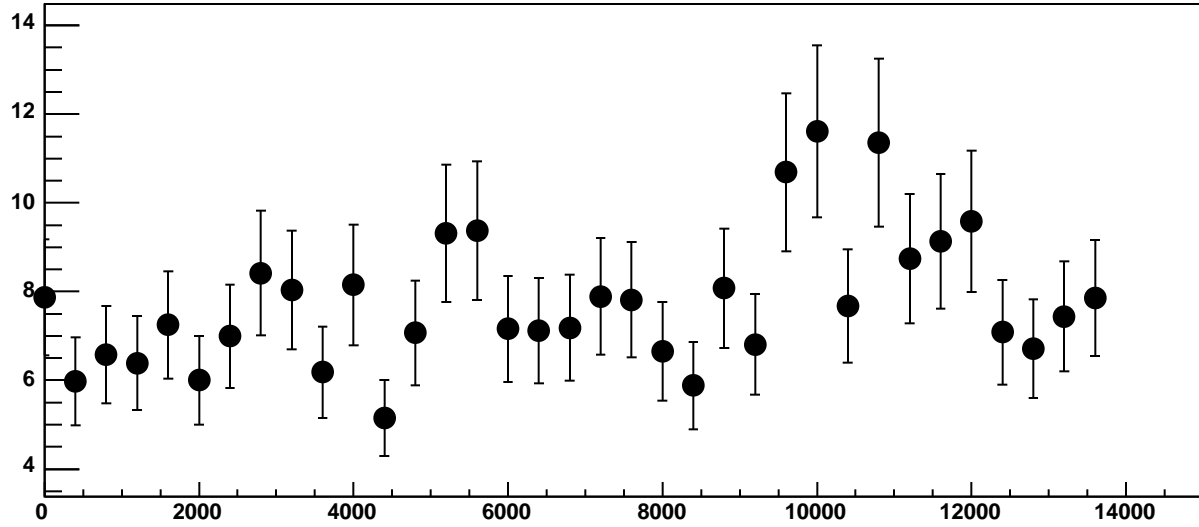


Chip 6, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

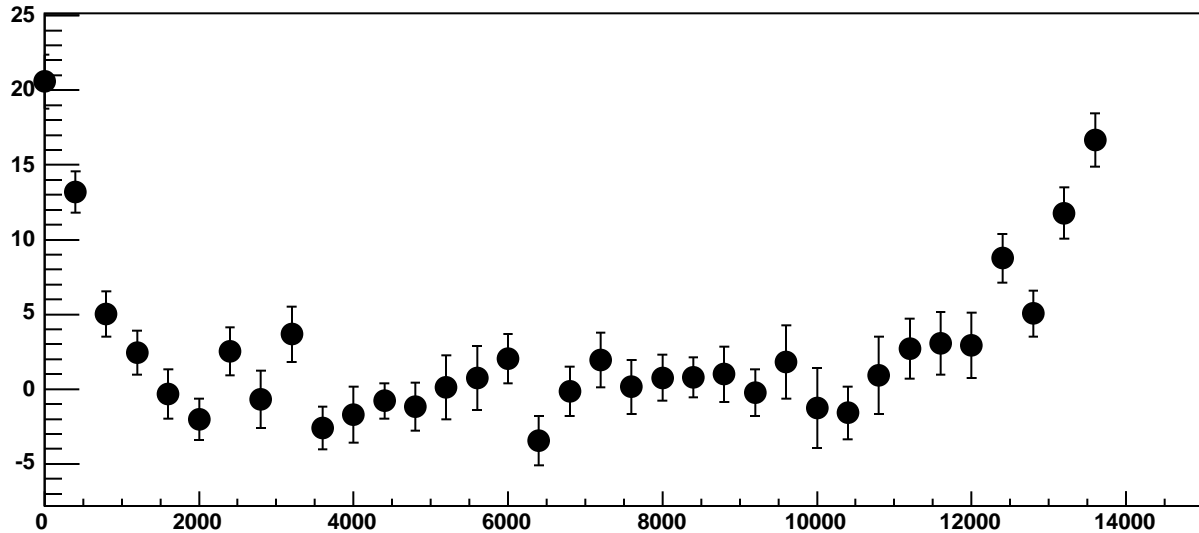


χ^2 / ndf 26.65 / 23
p0 -363.2 ± 0.7547
p1 0.01438 ± 0.0001211

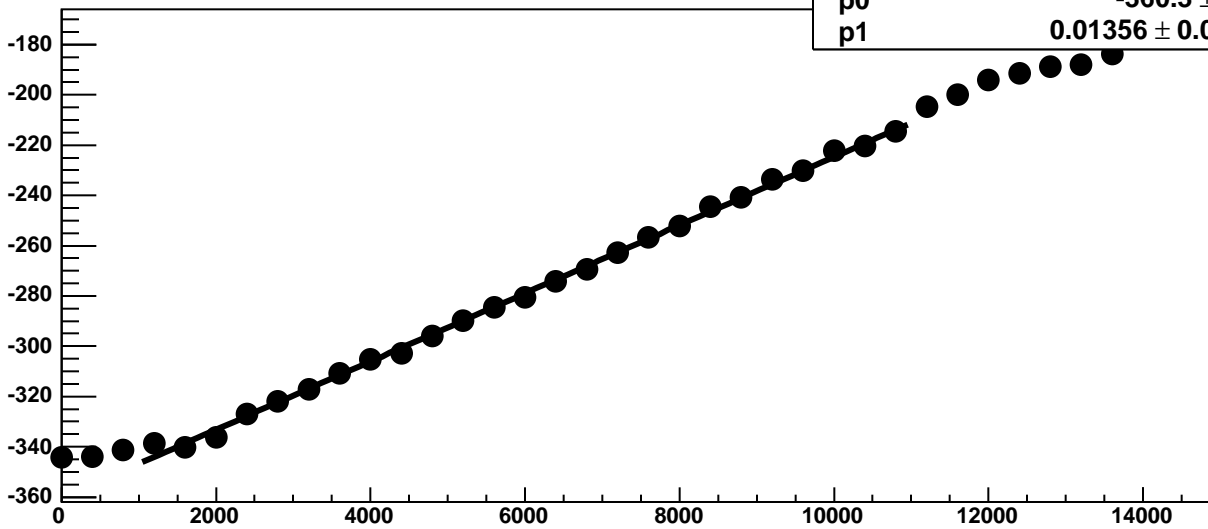
Chip 6, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



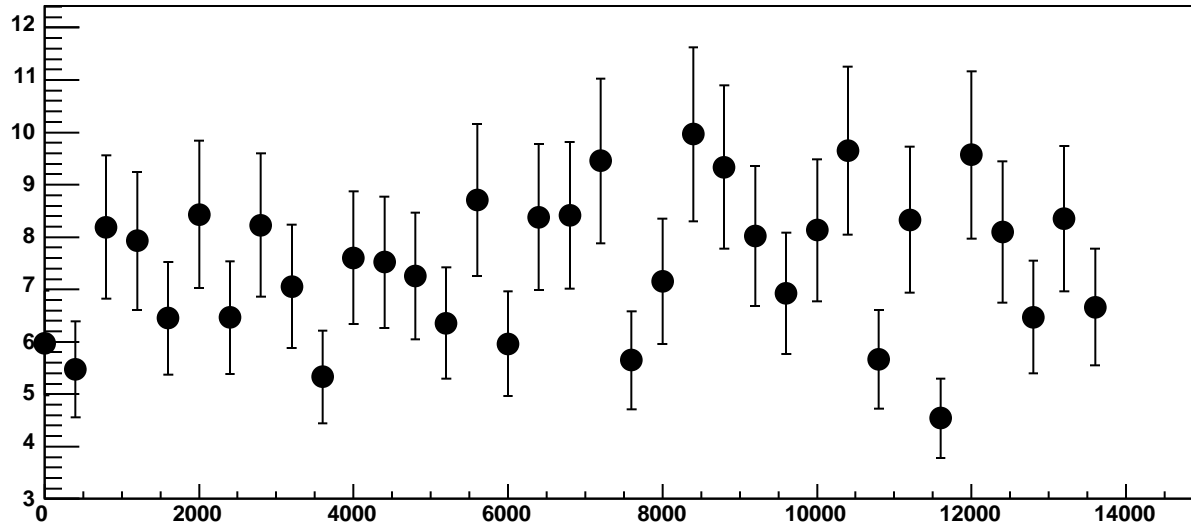
Chip 6, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



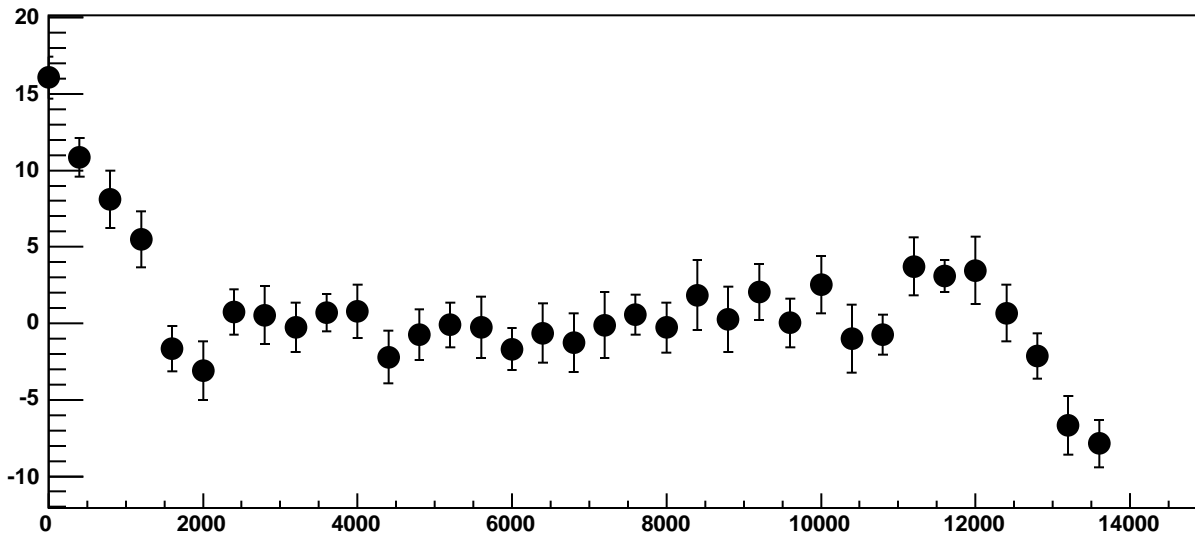
Chip 6, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



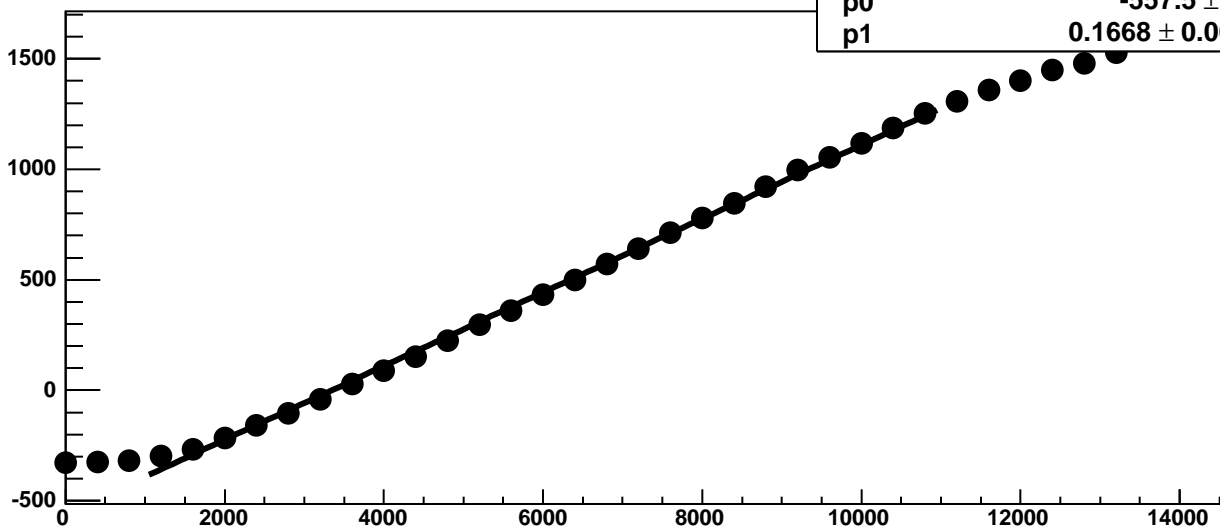
Chip 6, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



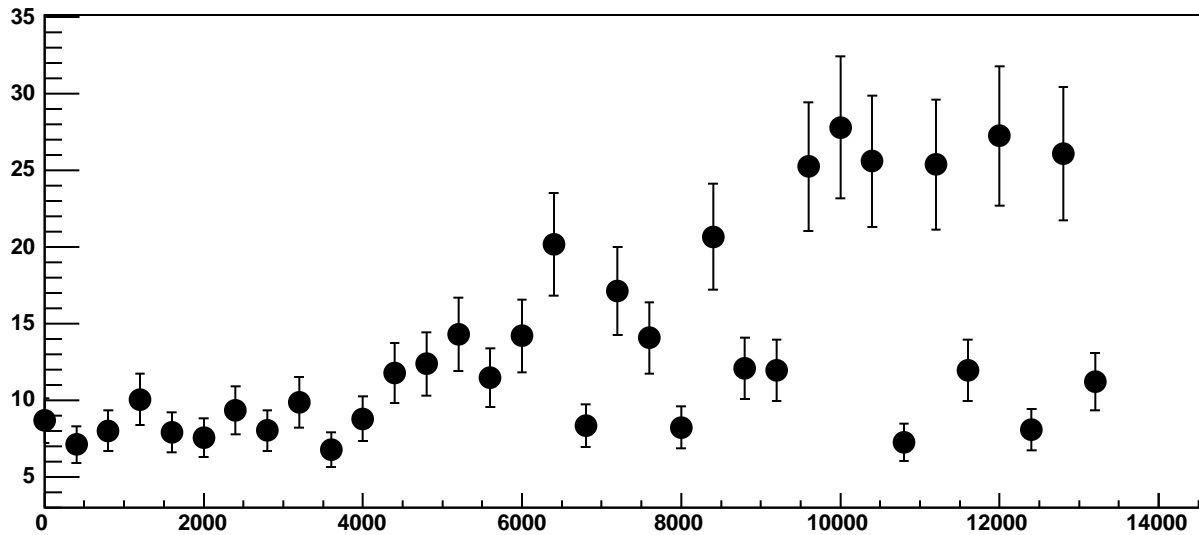
Chip 6, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



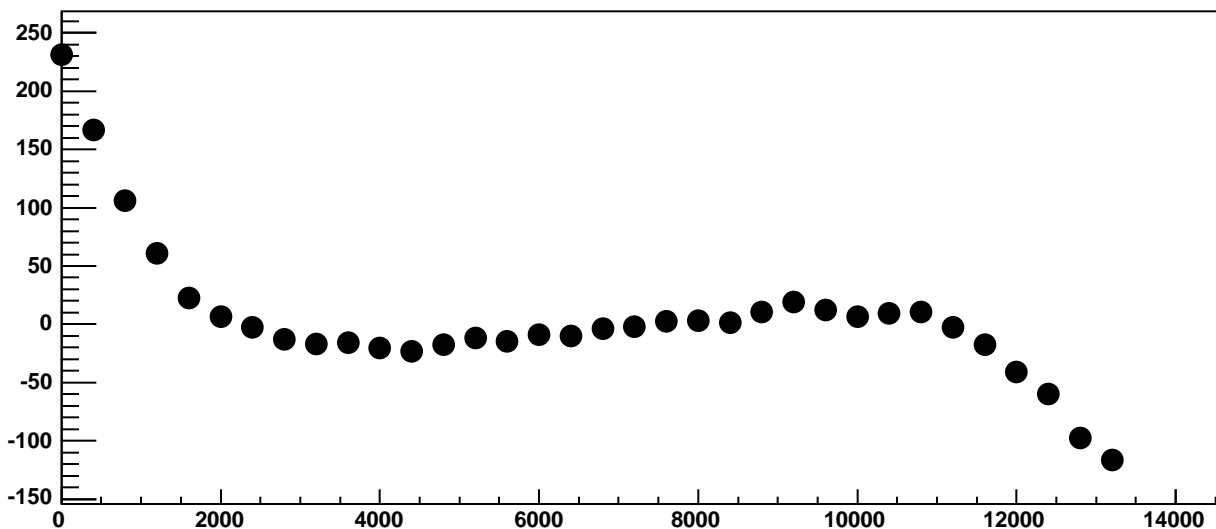
Chip 6, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC



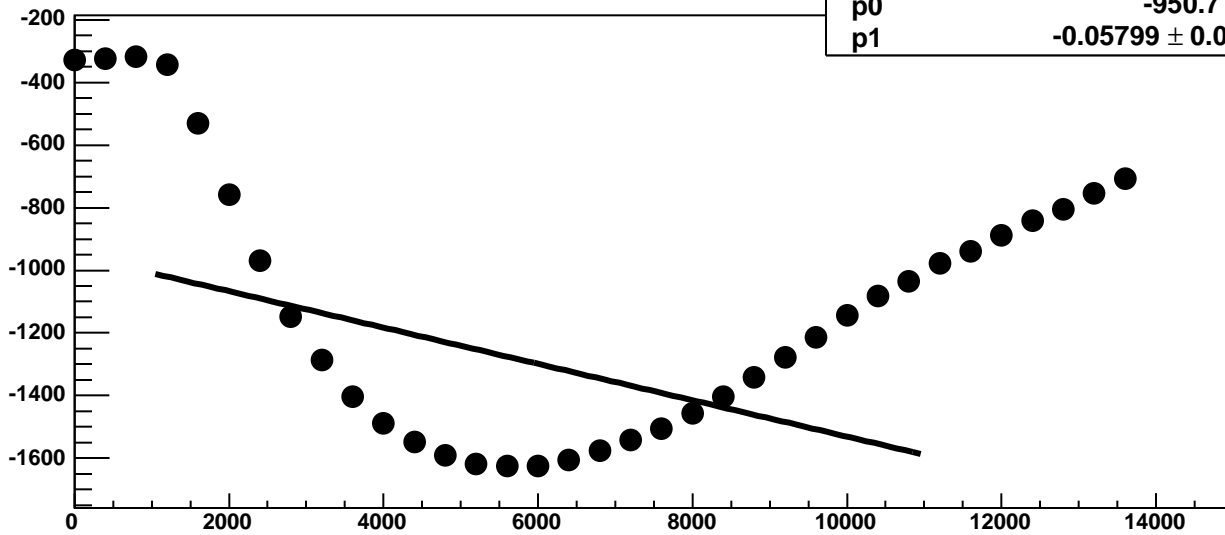
Chip 6, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC

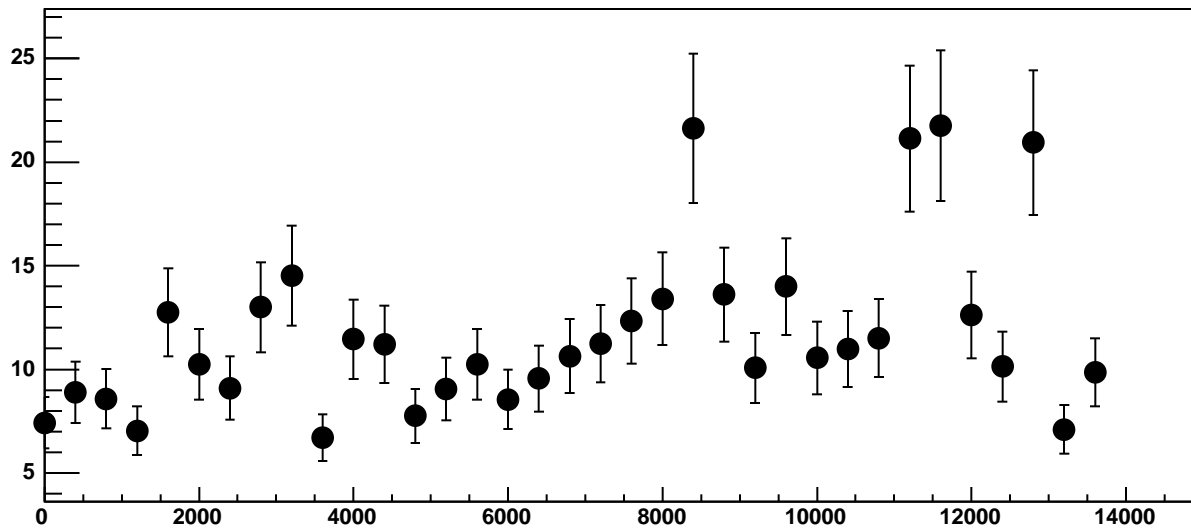


Chip 6, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

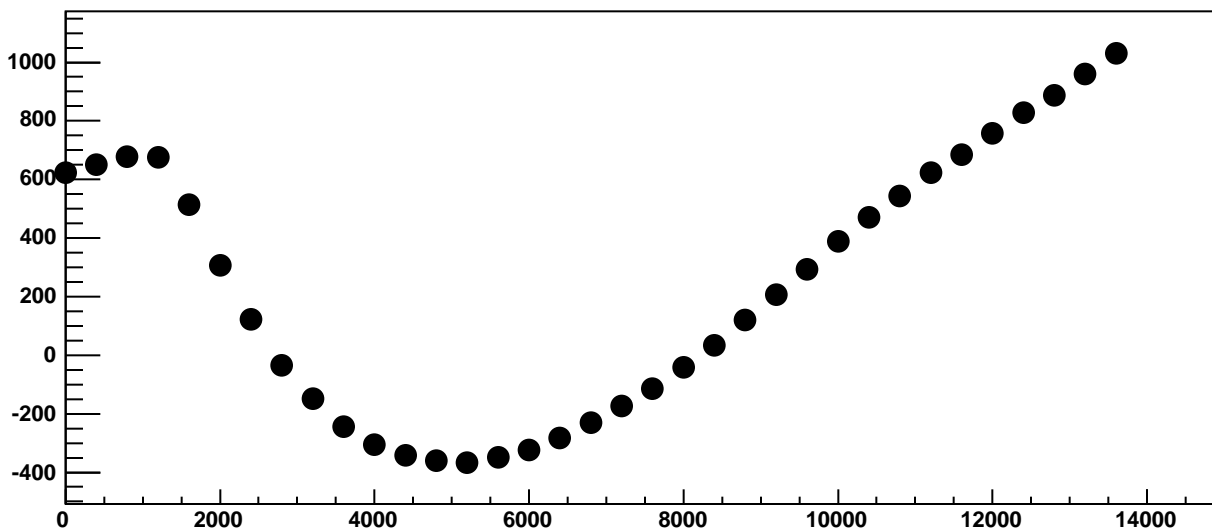


χ^2 / ndf 5.62e+05 / 23
p0 -950.7 ± 1.023
p1 -0.05799 ± 0.0001665

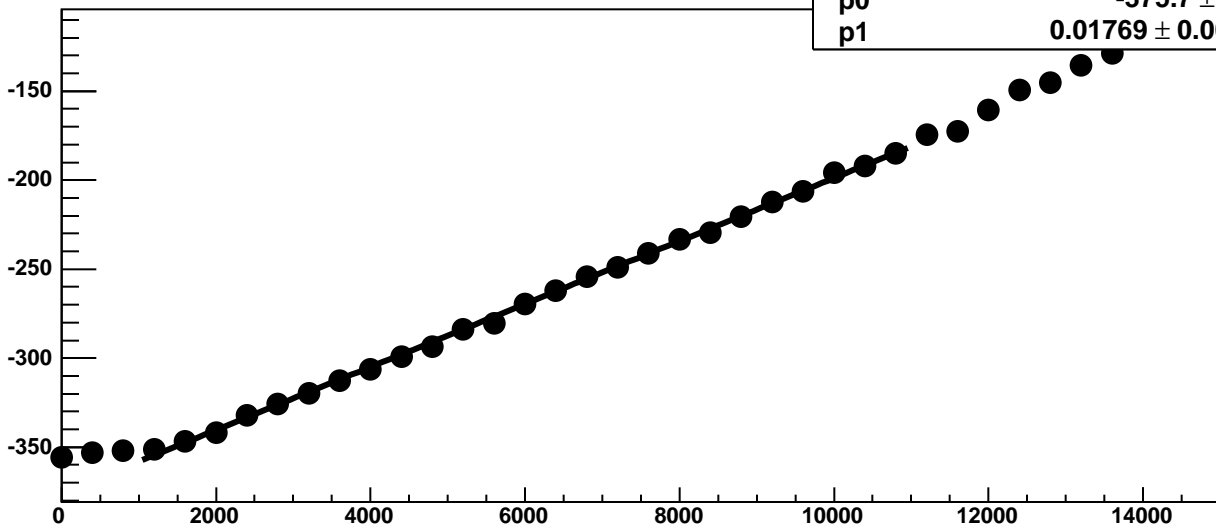
Chip 6, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

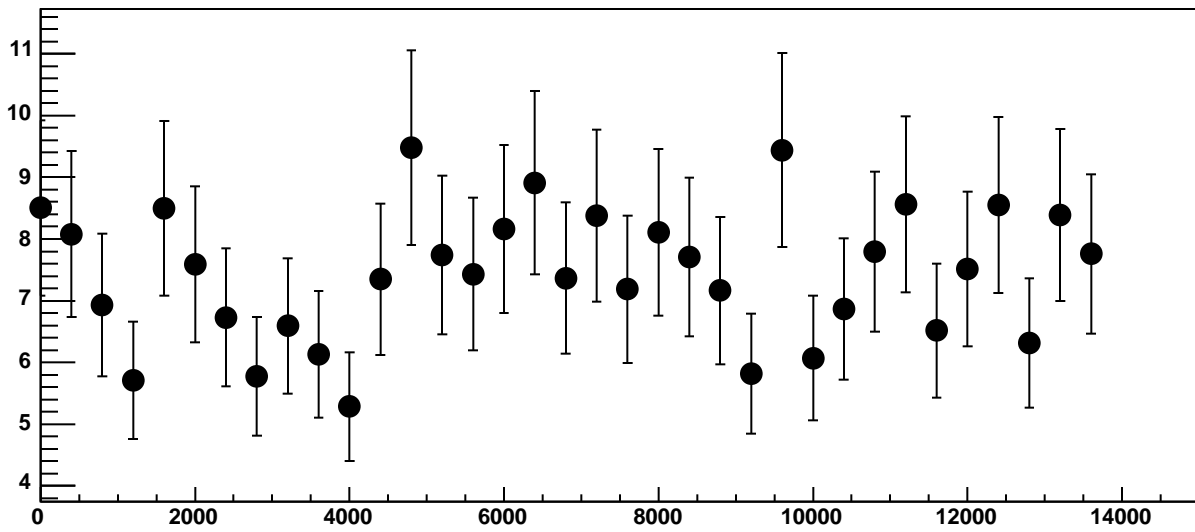


Chip 6, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

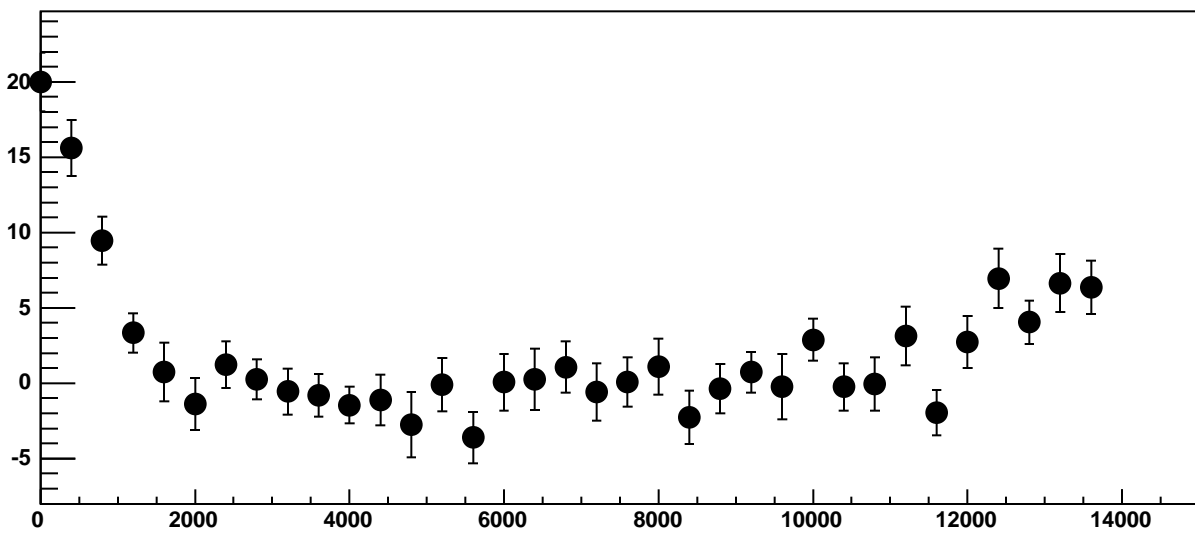


χ^2 / ndf 23.56 / 23
p0 -375.7 ± 0.7082
p1 0.01769 ± 0.0001088

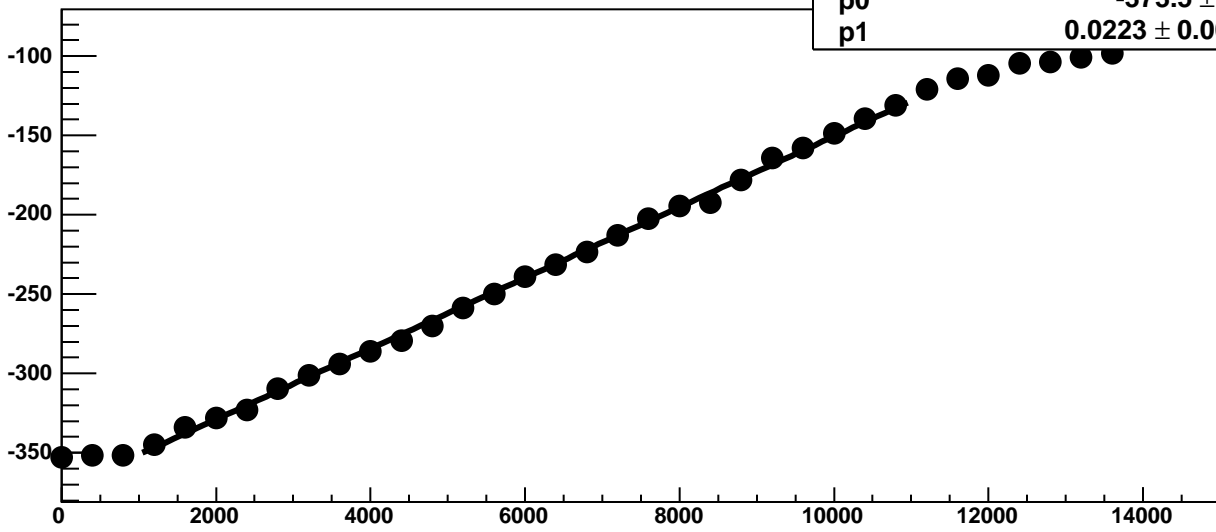
Chip 6, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



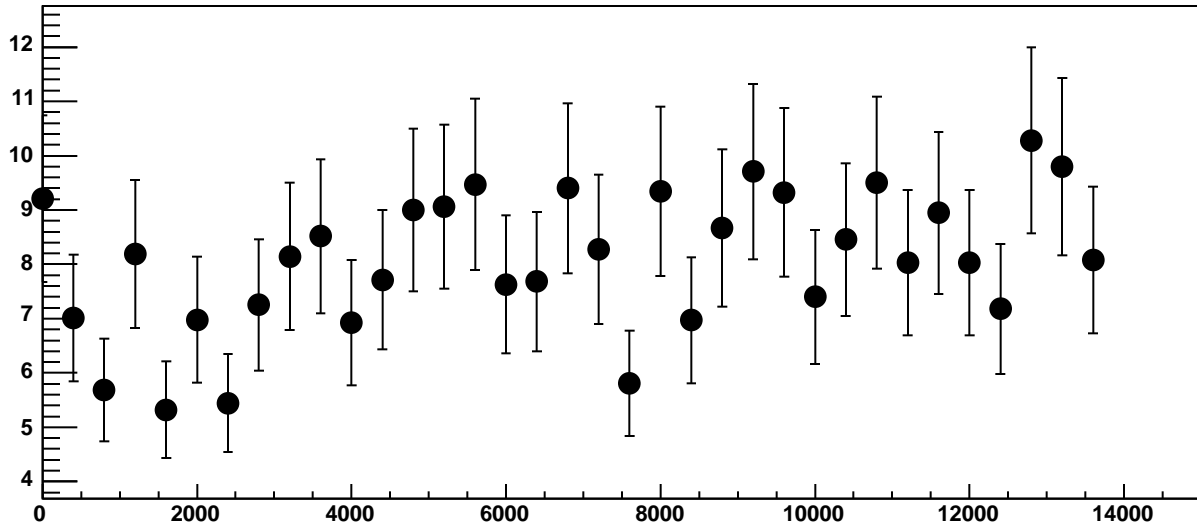
Chip 6, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



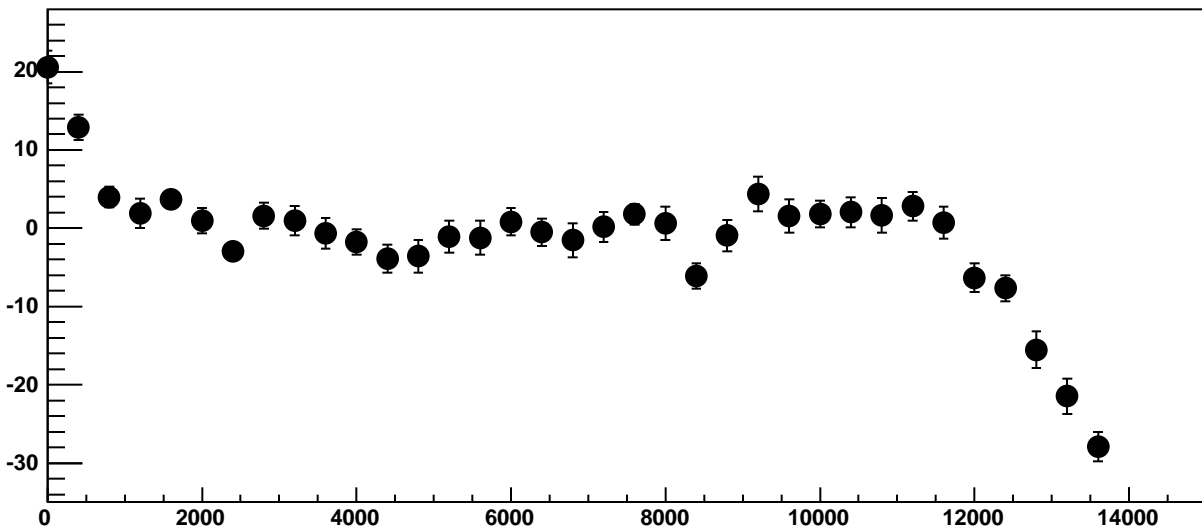
Chip 6, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC



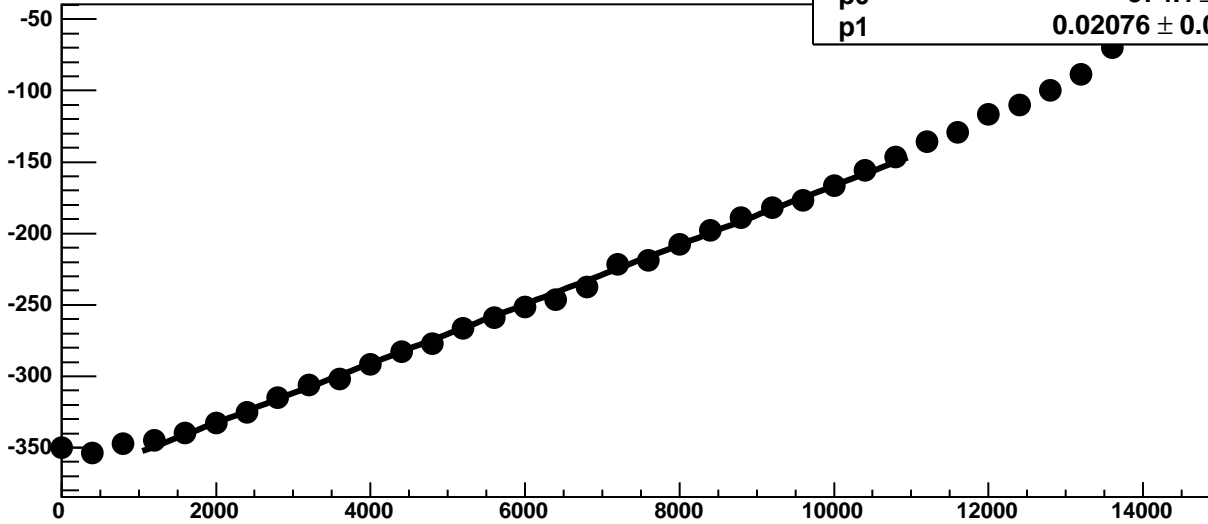
Chip 6, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

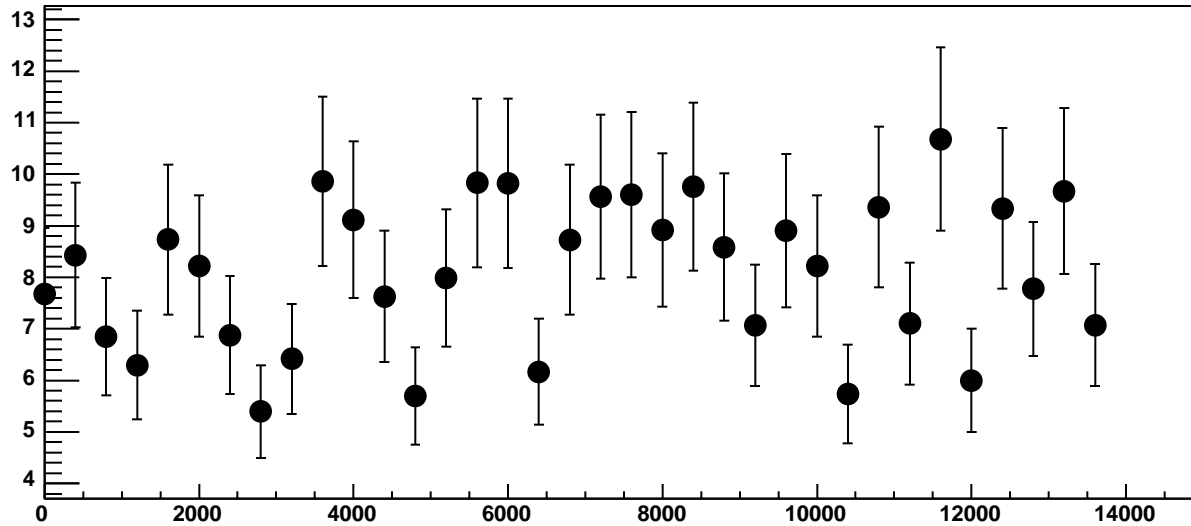


Chip 6, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

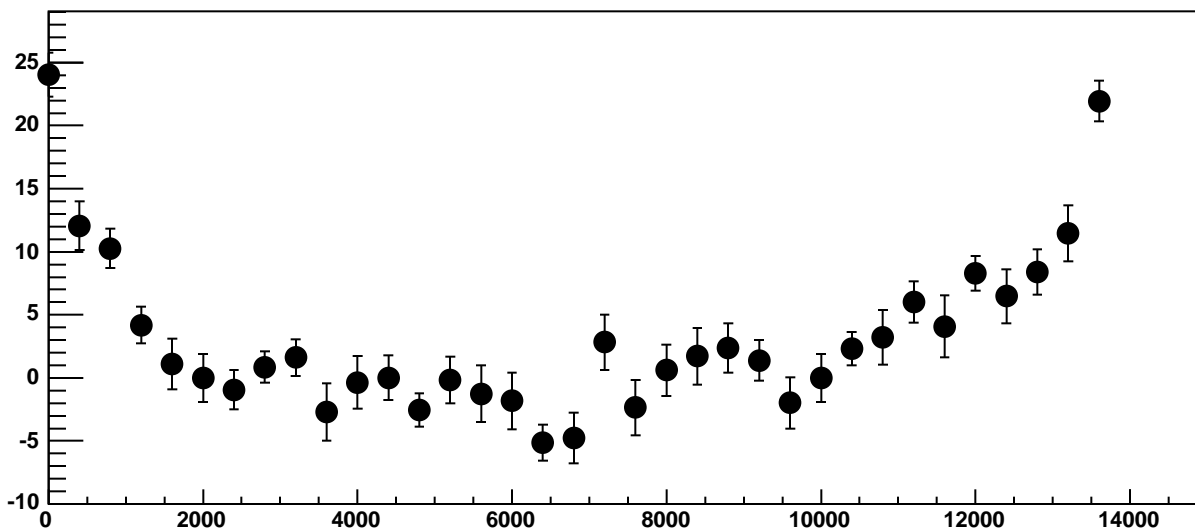


χ^2 / ndf 47.65 / 23
p0 -374.1 ± 0.7588
p1 0.02076 ± 0.0001179

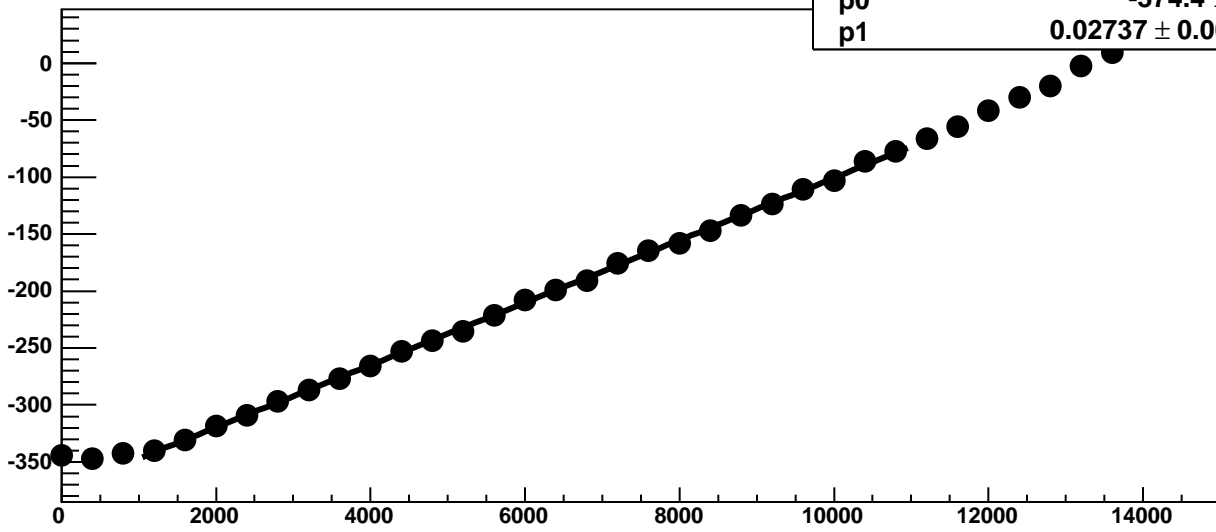
Chip 6, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



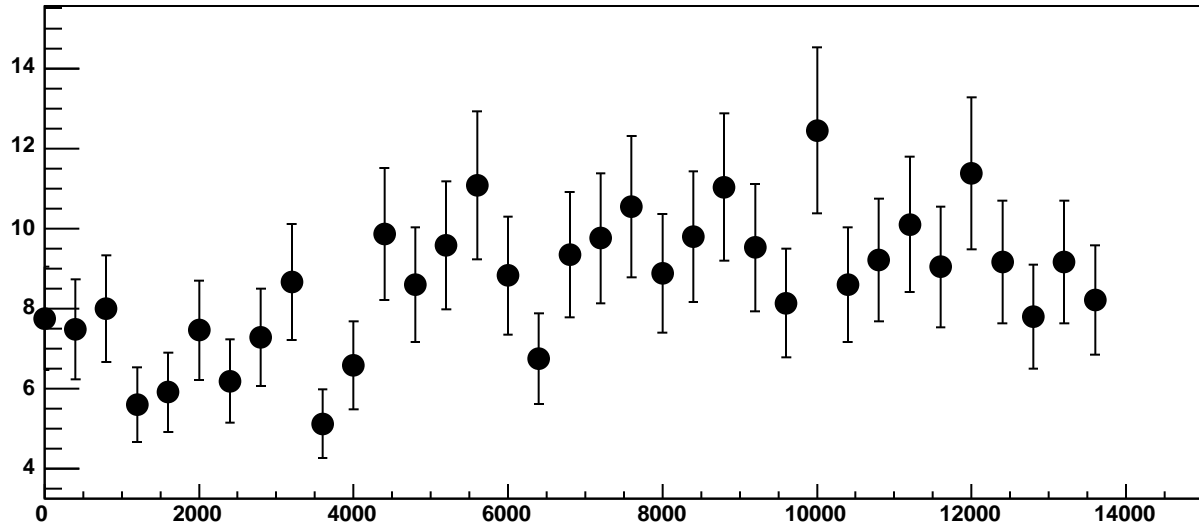
Chip 6, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



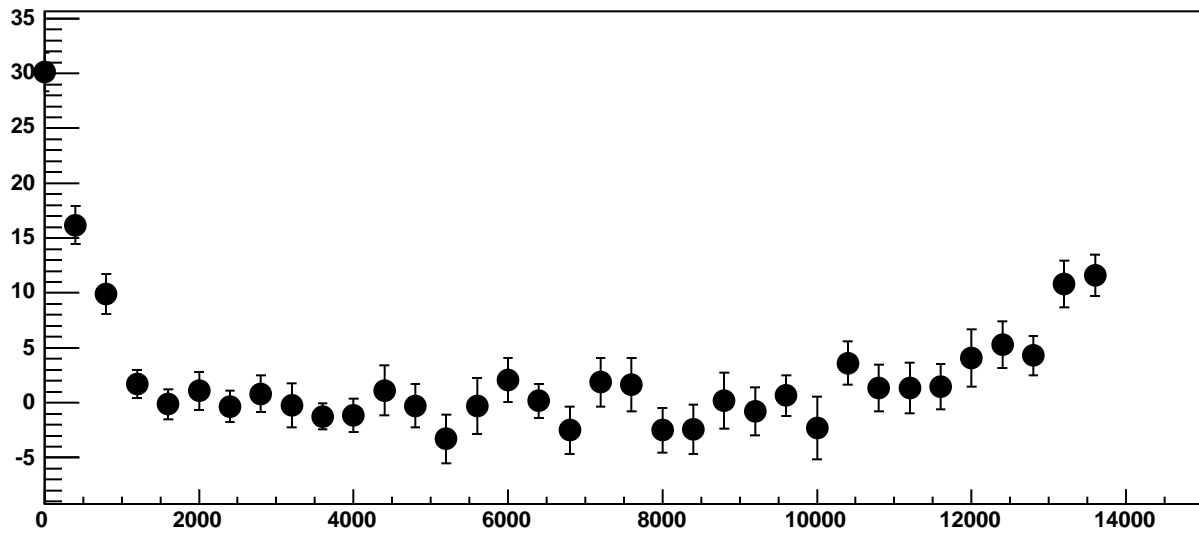
Chip 6, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



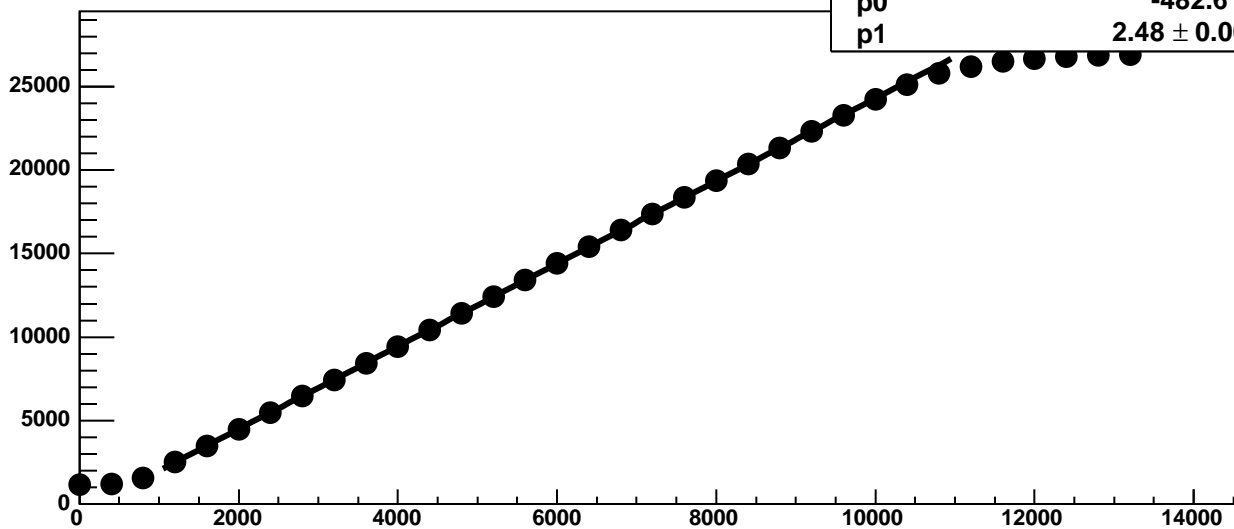
Chip 6, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



Chip 6, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

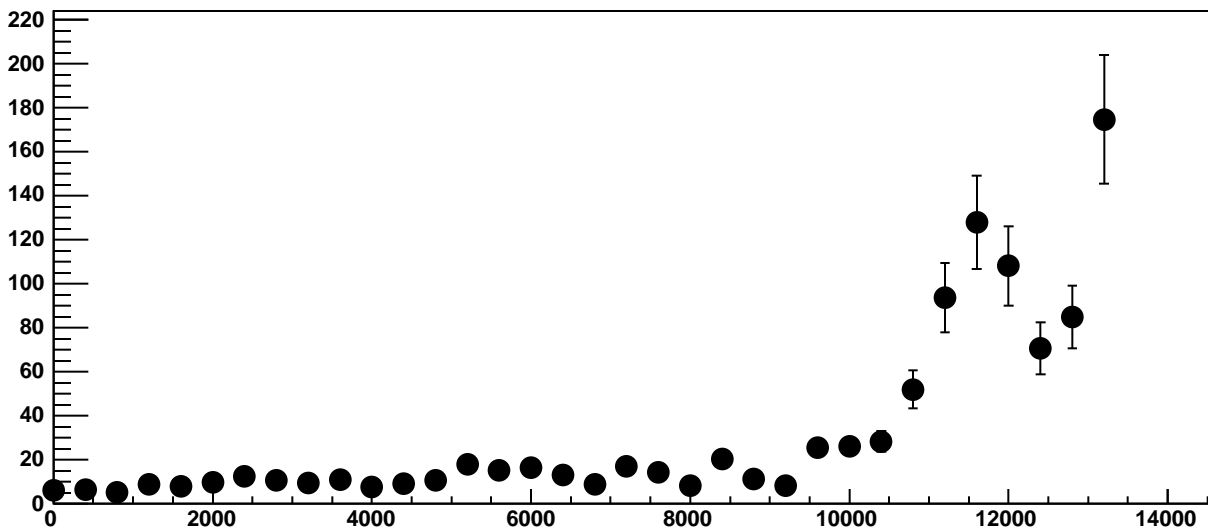


Chip 6, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC

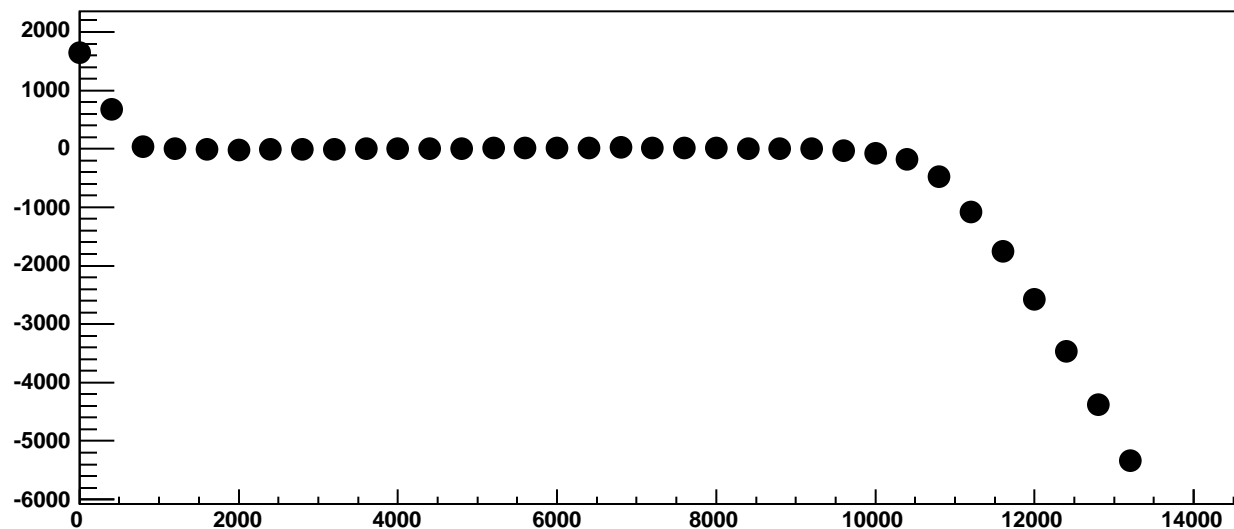


χ^2 / ndf 2963 / 23
p0 -482.6 ± 1.118
p1 2.48 ± 0.0001978

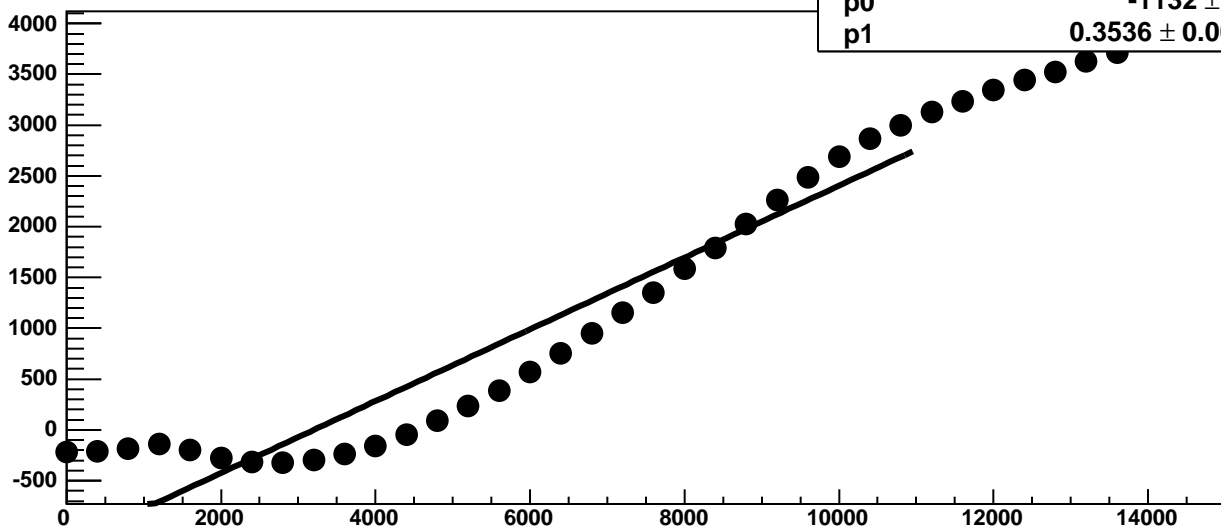
Chip 6, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC

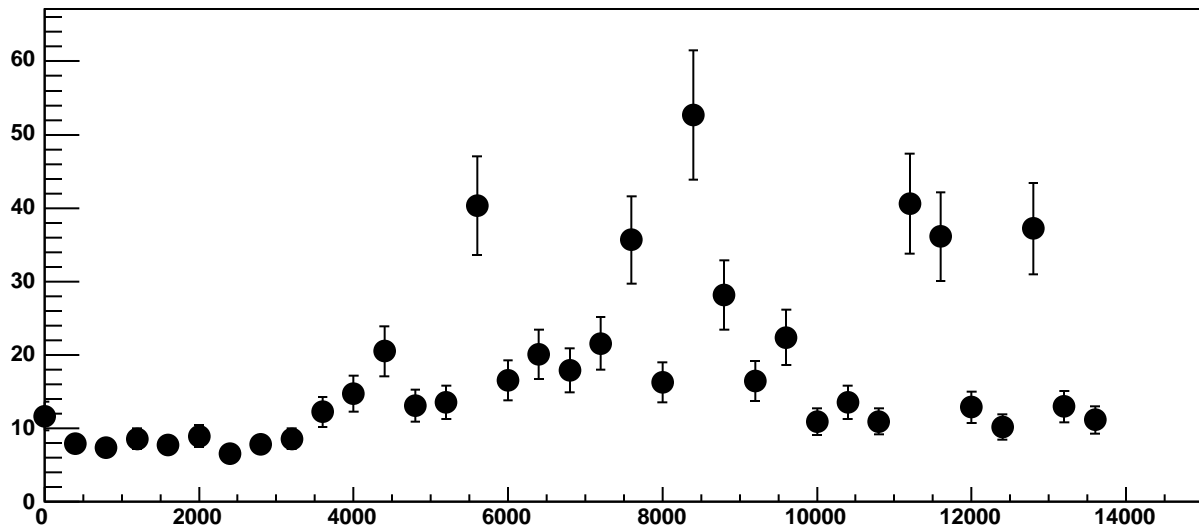


Chip 6, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

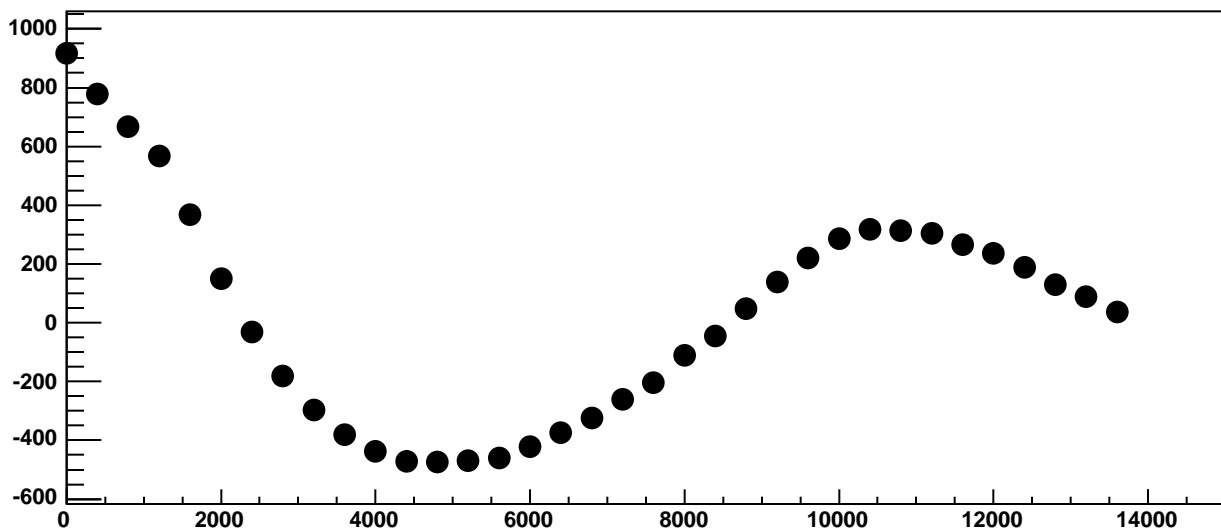


χ^2 / ndf 3.316e+05 / 23
p0 -1132 ± 0.9773
p1 0.3536 ± 0.0001813

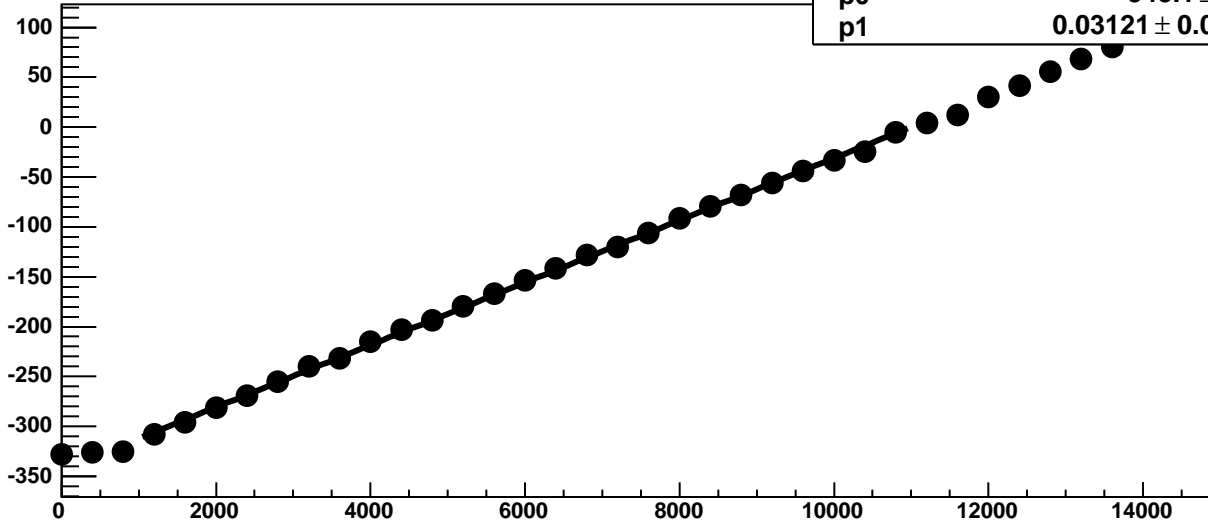
Chip 6, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

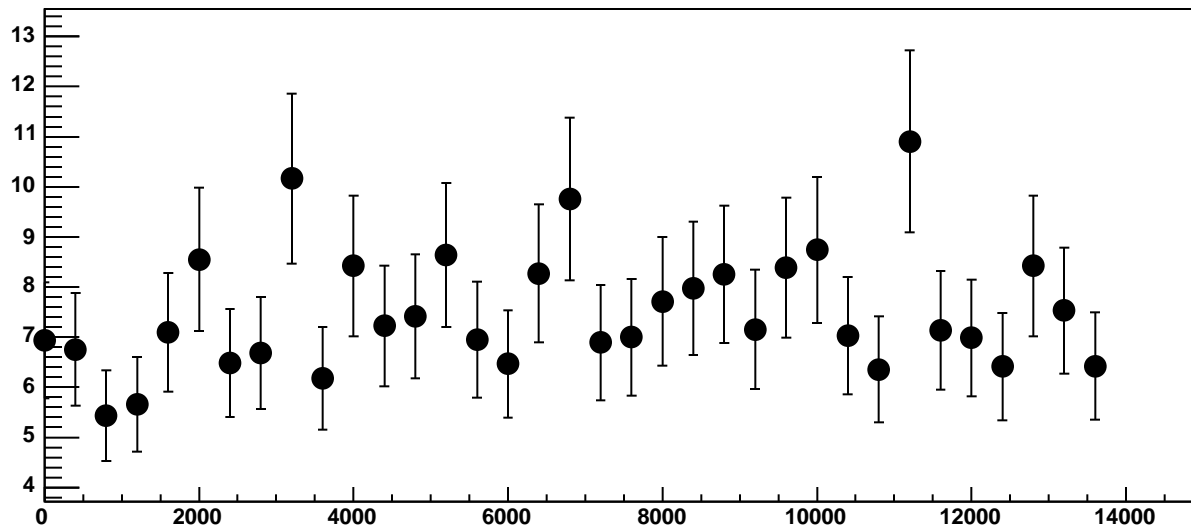


Chip 6, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

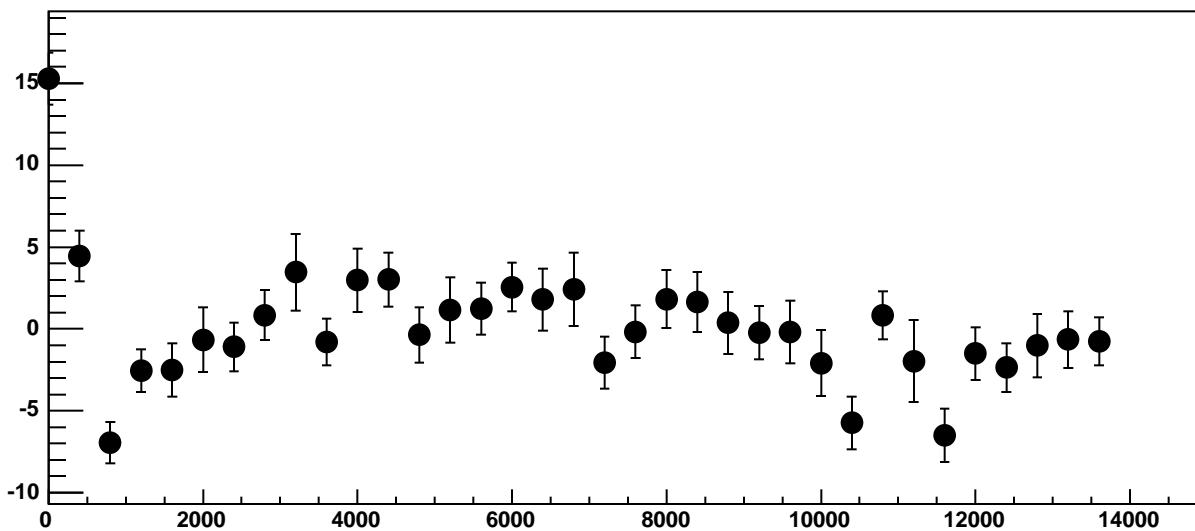


χ^2 / ndf 38.98 / 23
p0 -343.1 ± 0.7388
p1 0.03121 ± 0.0001128

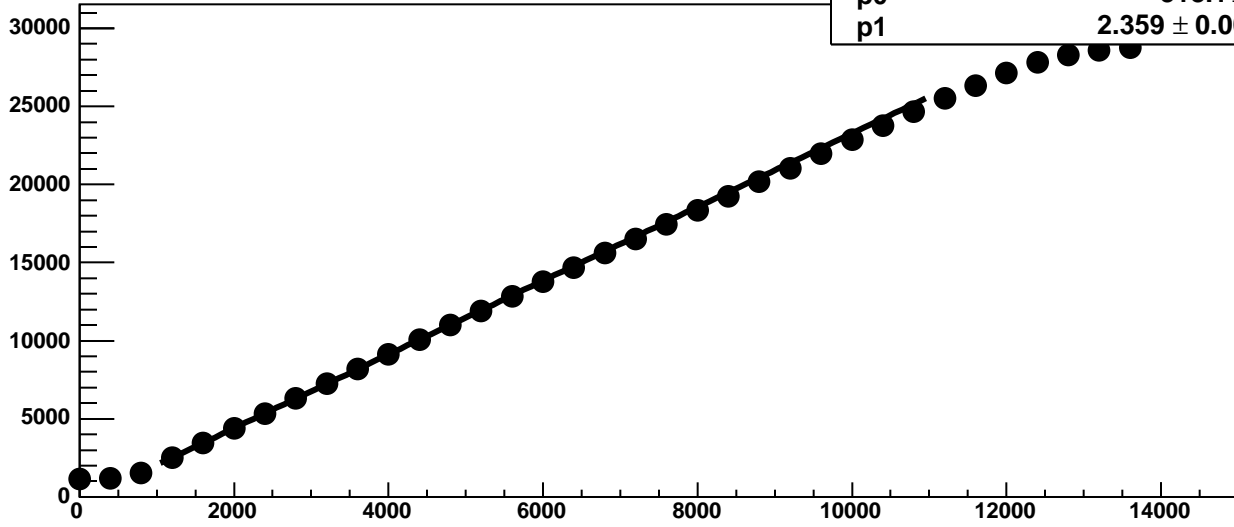
Chip 6, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



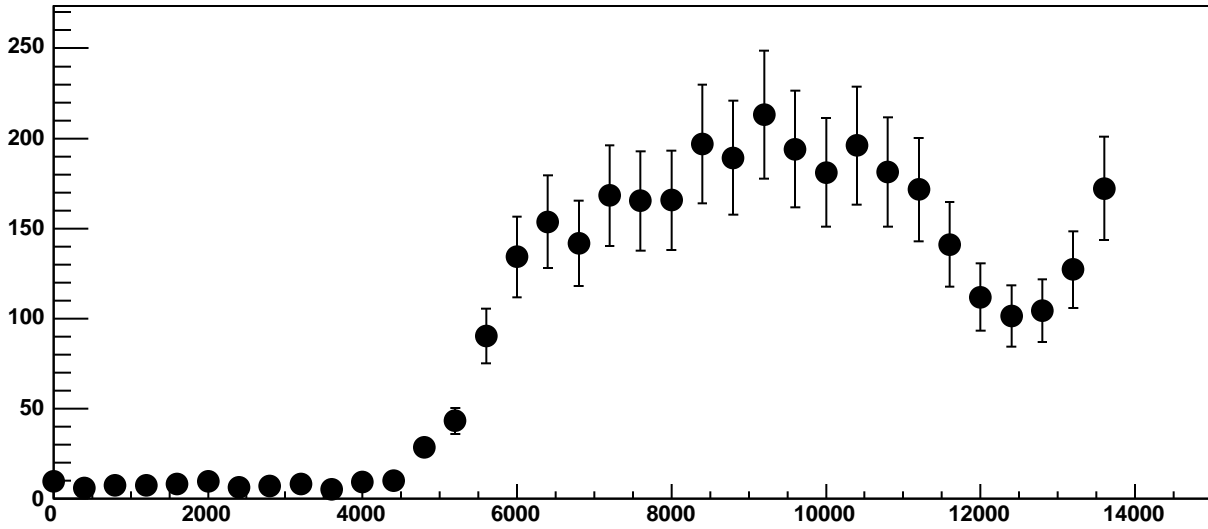
Chip 6, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



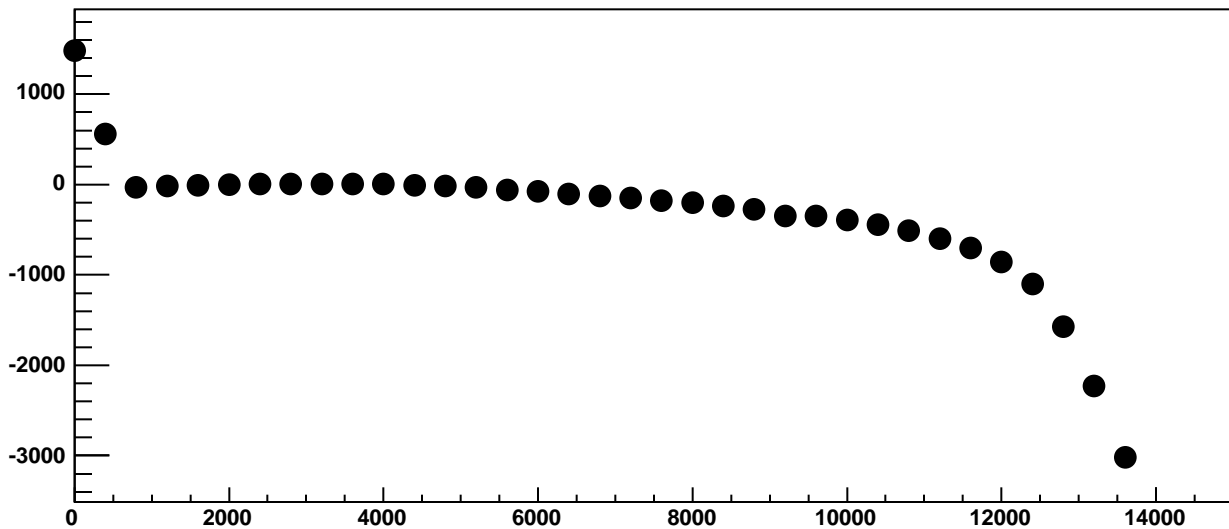
Chip 6, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC



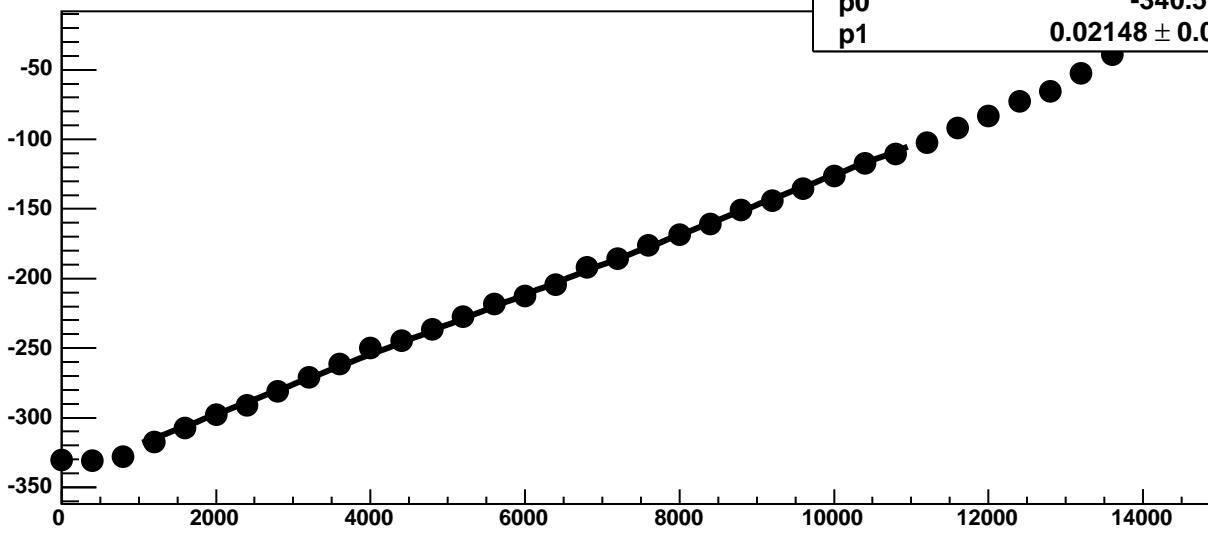
Chip 6, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC

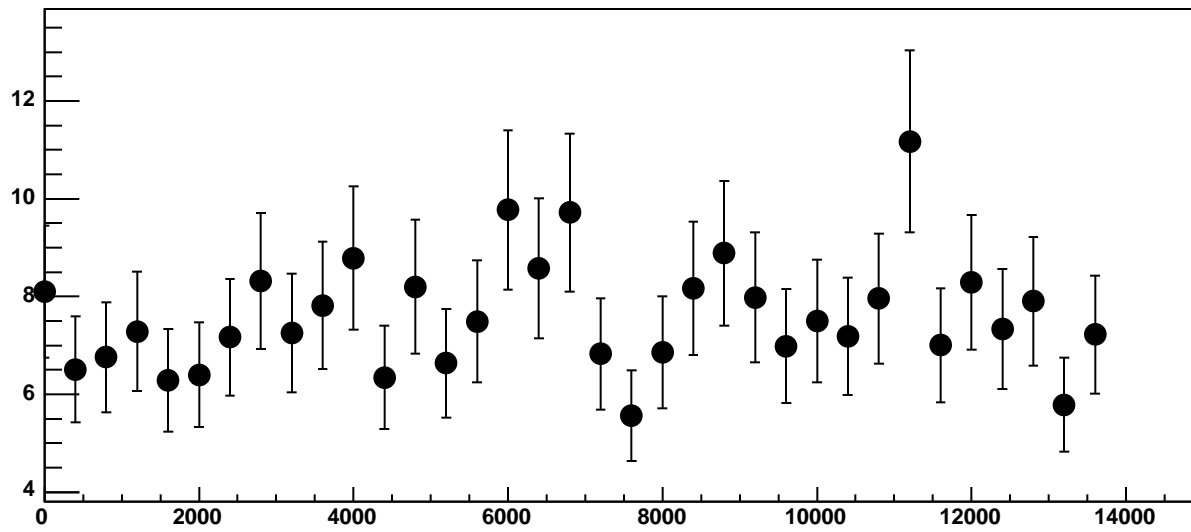


Chip 6, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

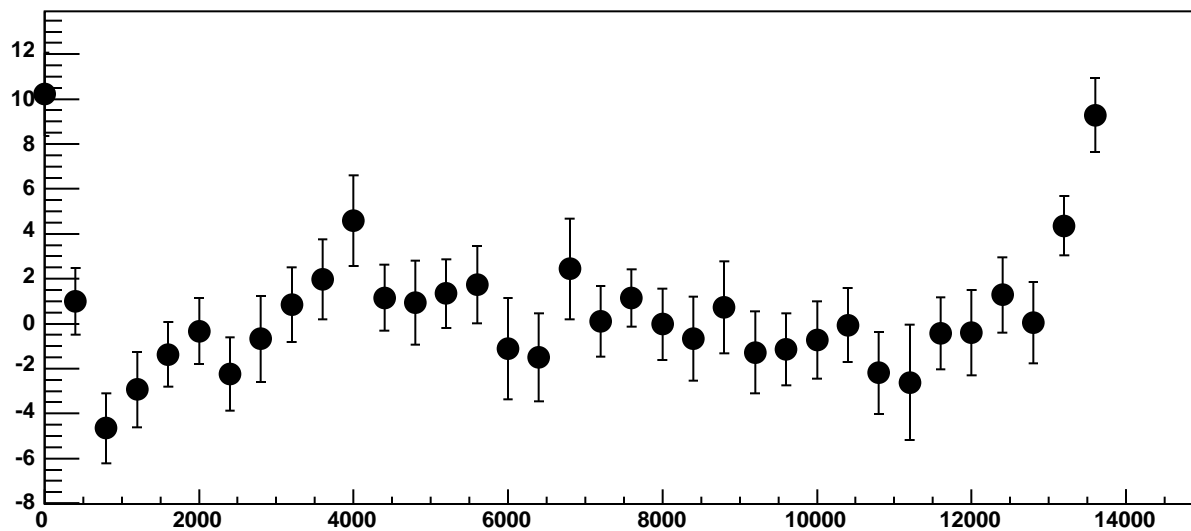


χ^2 / ndf 21.05 / 23
p0 -340.5 ± 0.759
p1 0.02148 ± 0.0001155

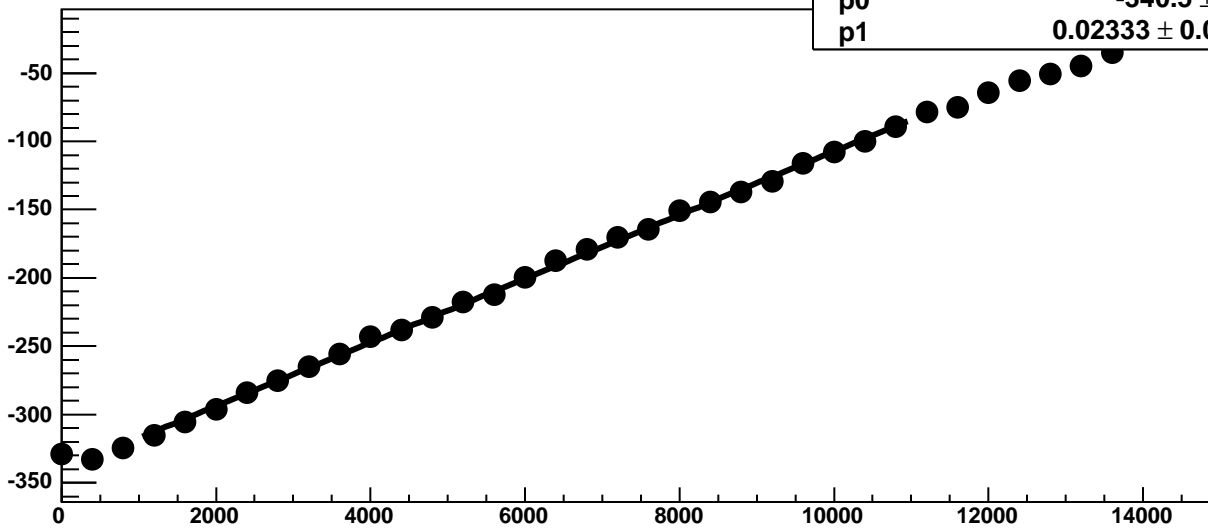
Chip 6, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

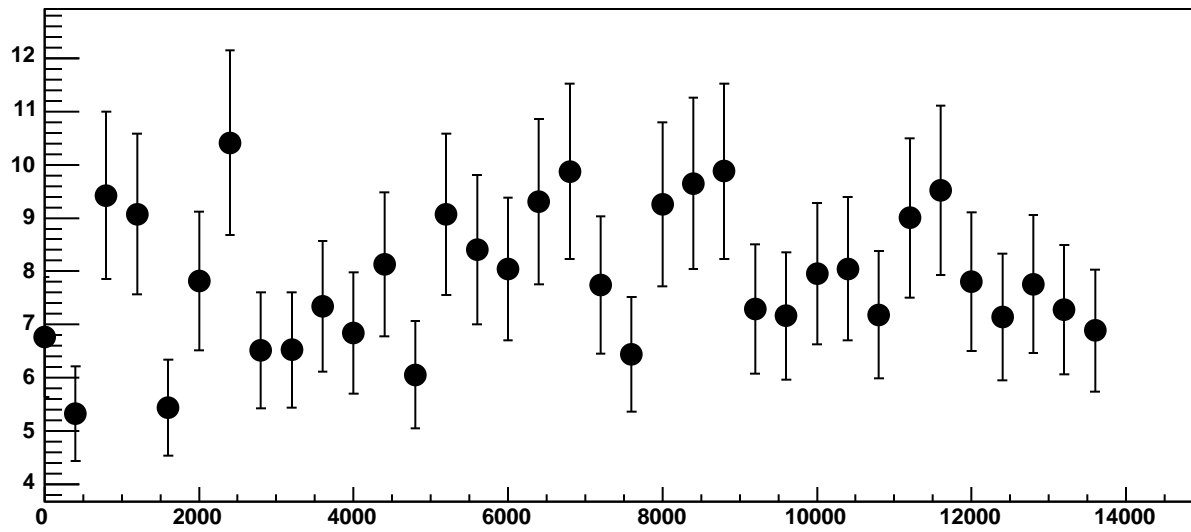


Chip 6, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

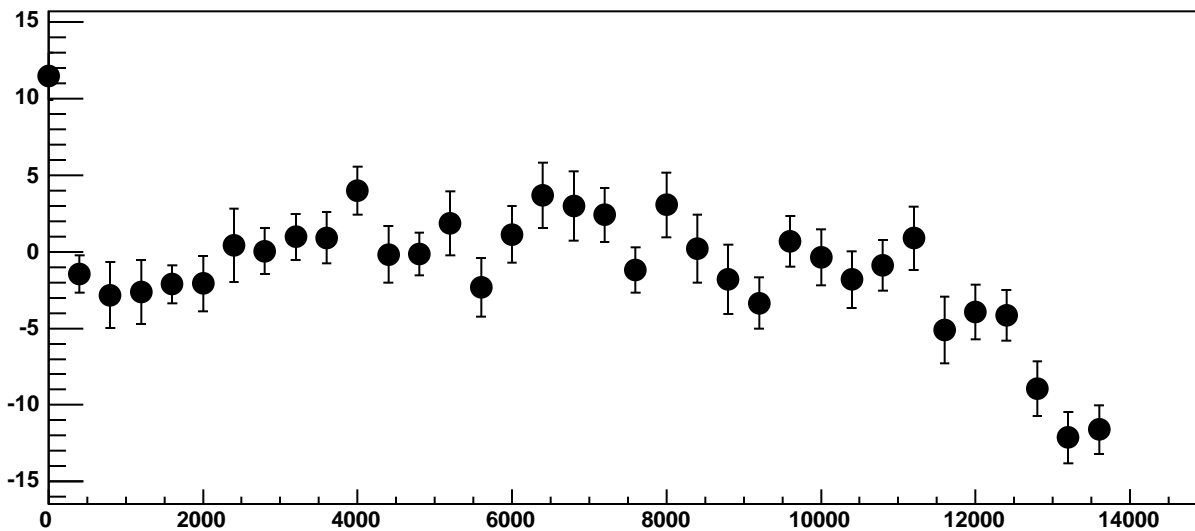


χ^2 / ndf 31.18 / 23
p0 -340.5 ± 0.7696
p1 0.02333 ± 0.0001189

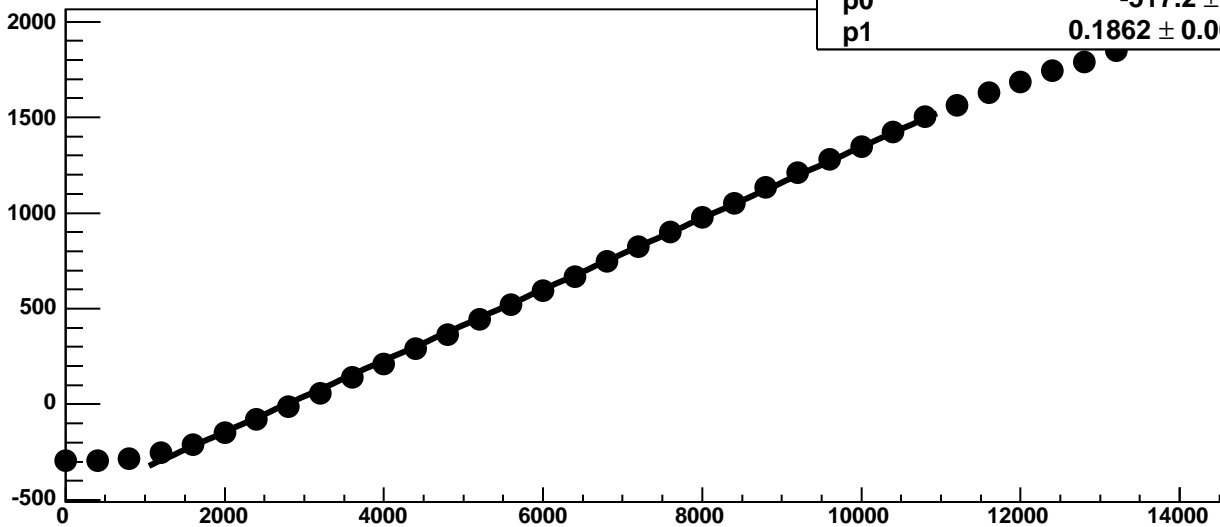
Chip 6, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



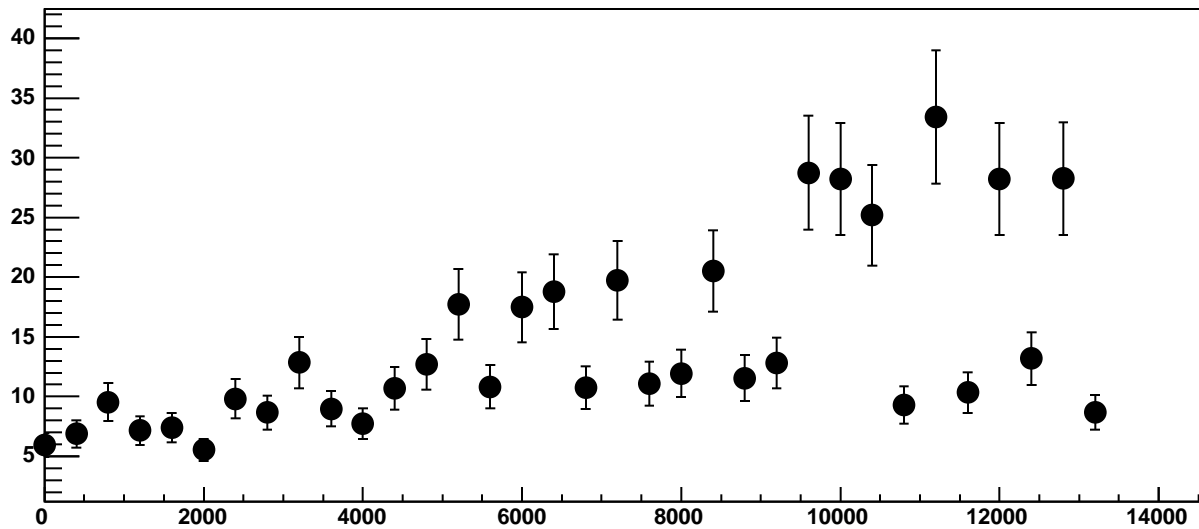
Chip 6, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



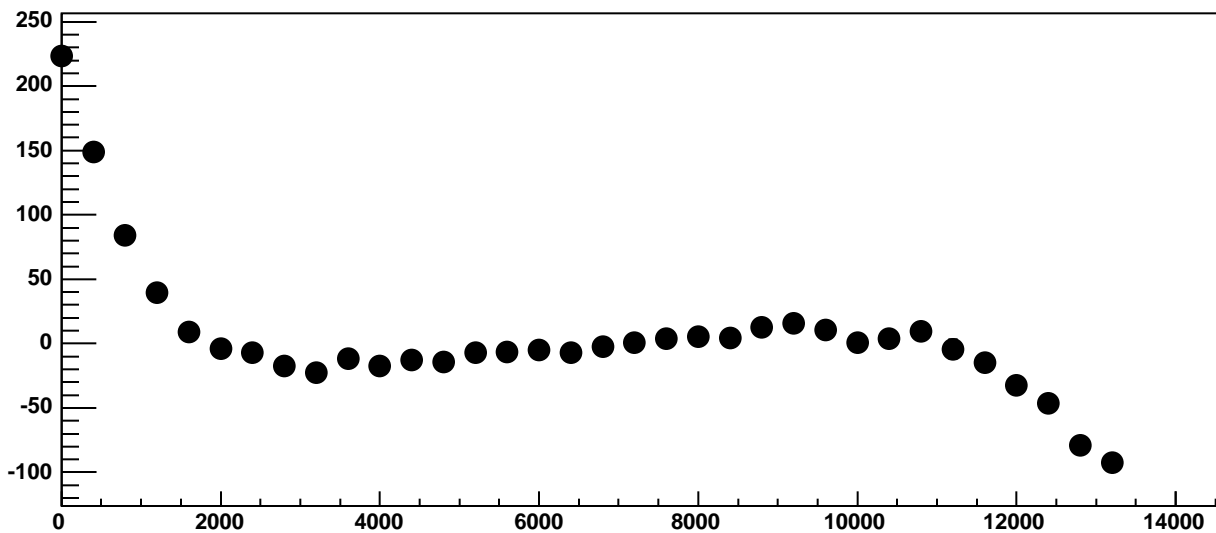
Chip 6, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC



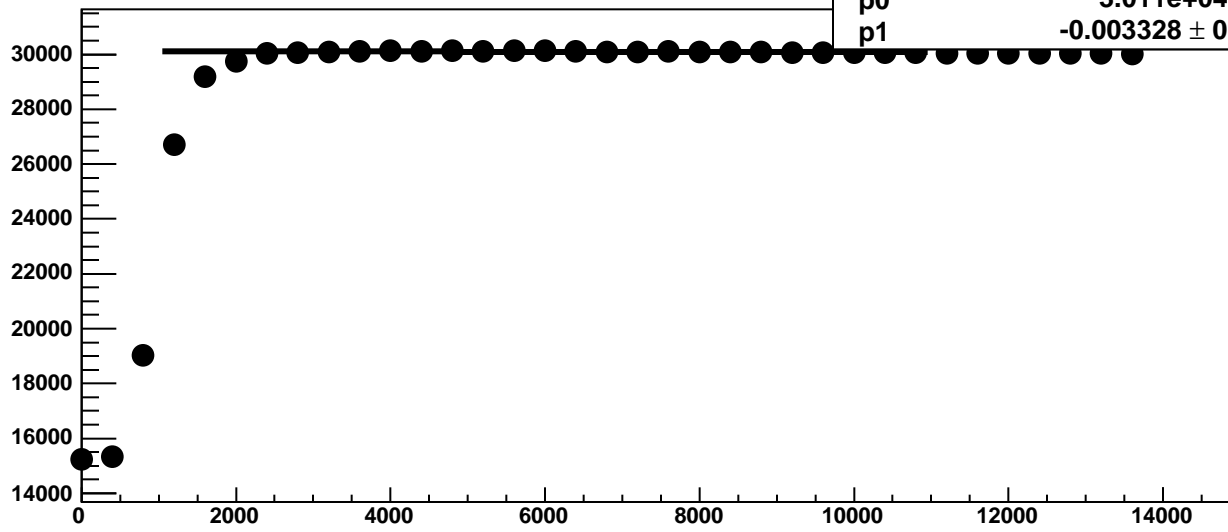
Chip 6, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



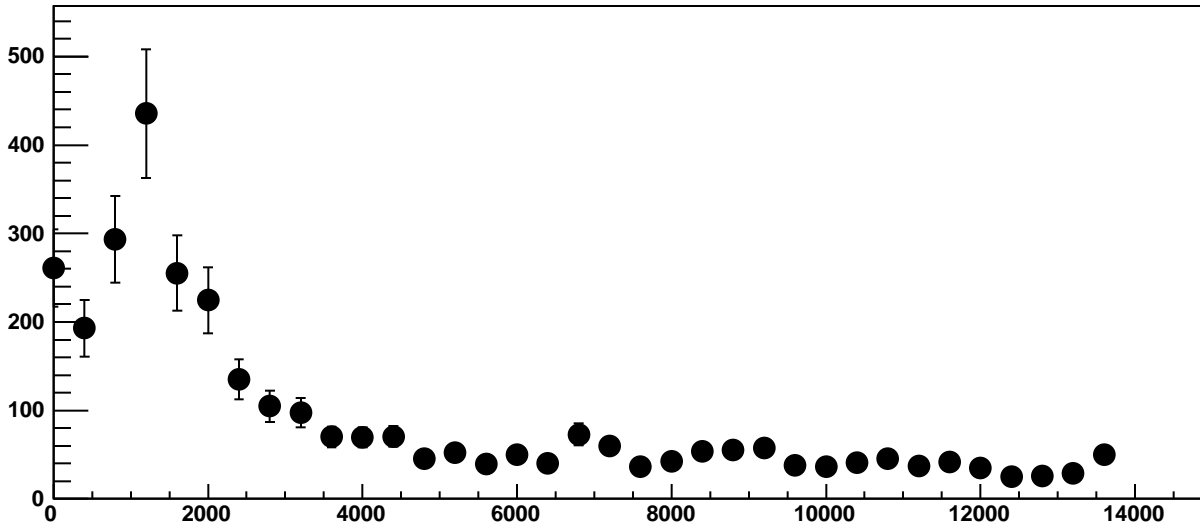
Chip 6, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



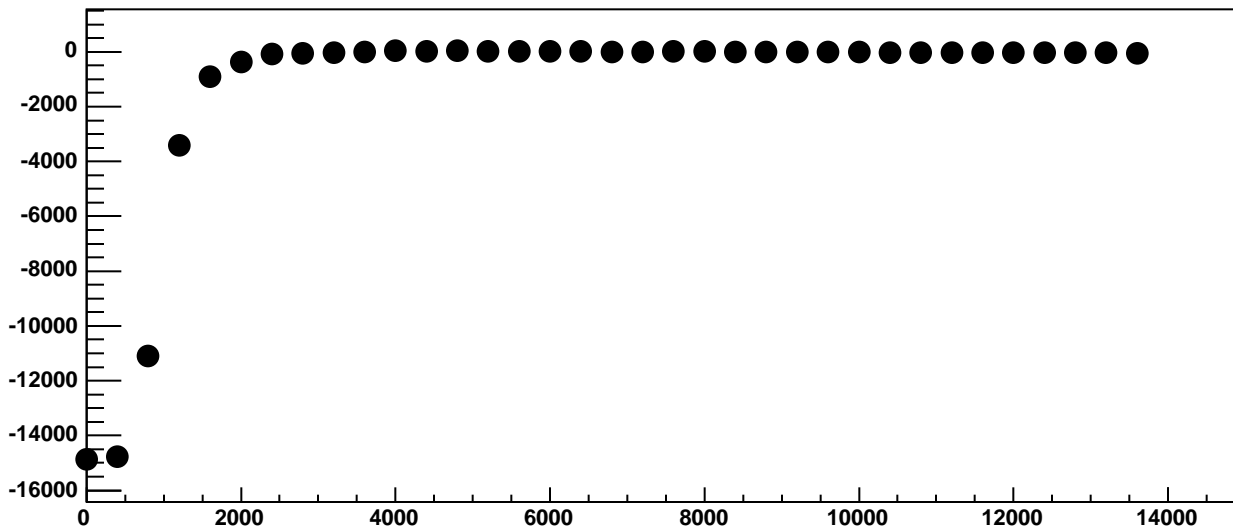
Chip 6, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



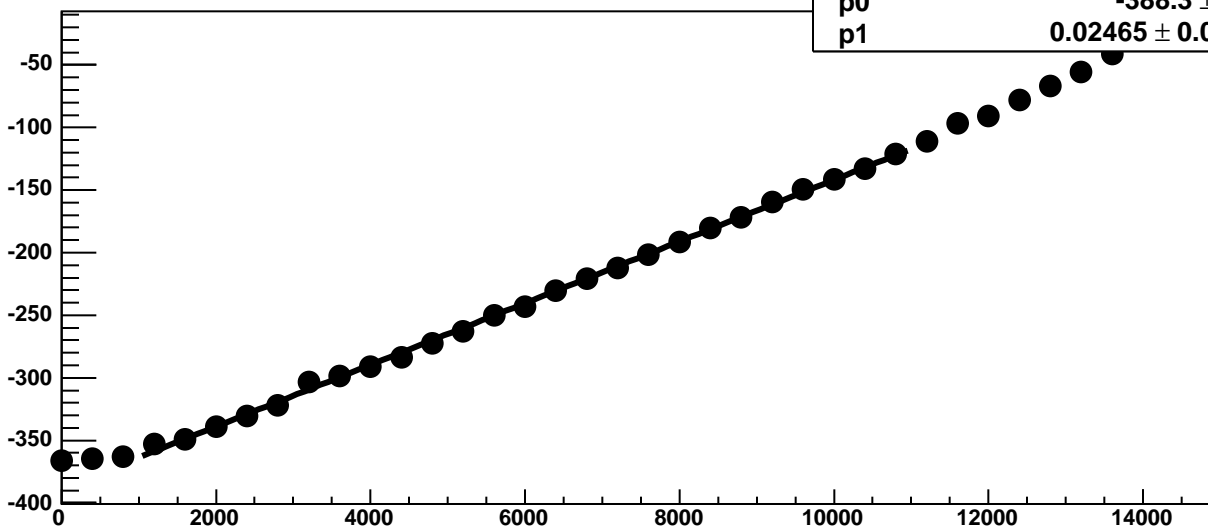
Chip 6, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



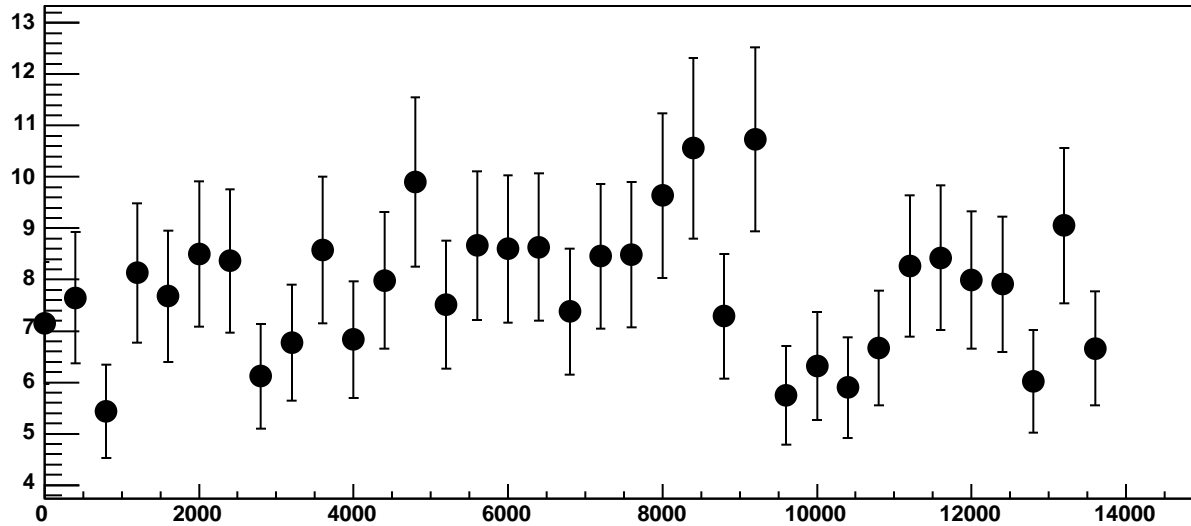
Chip 6, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC



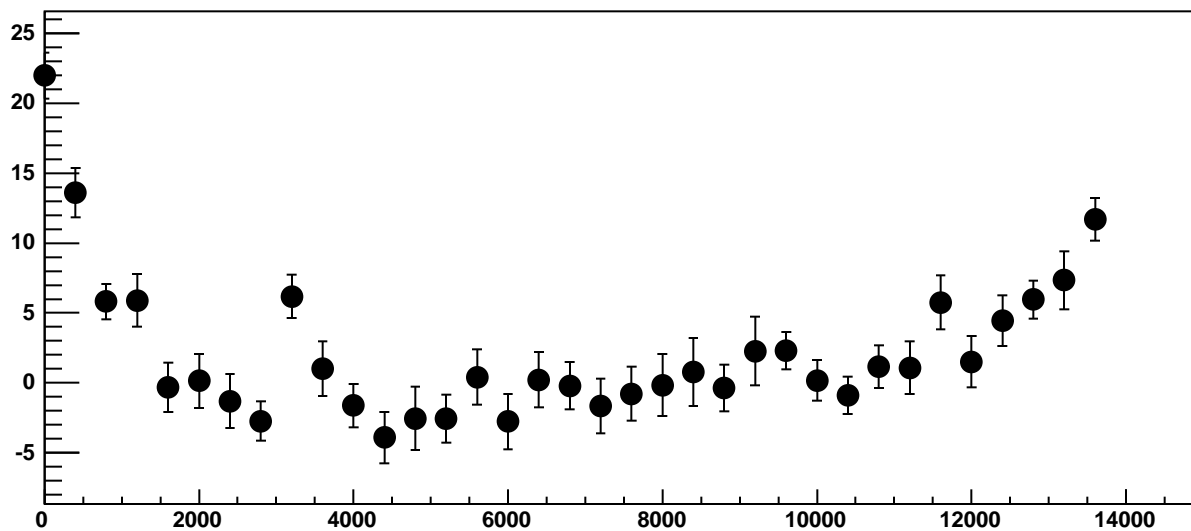
Chip 6, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



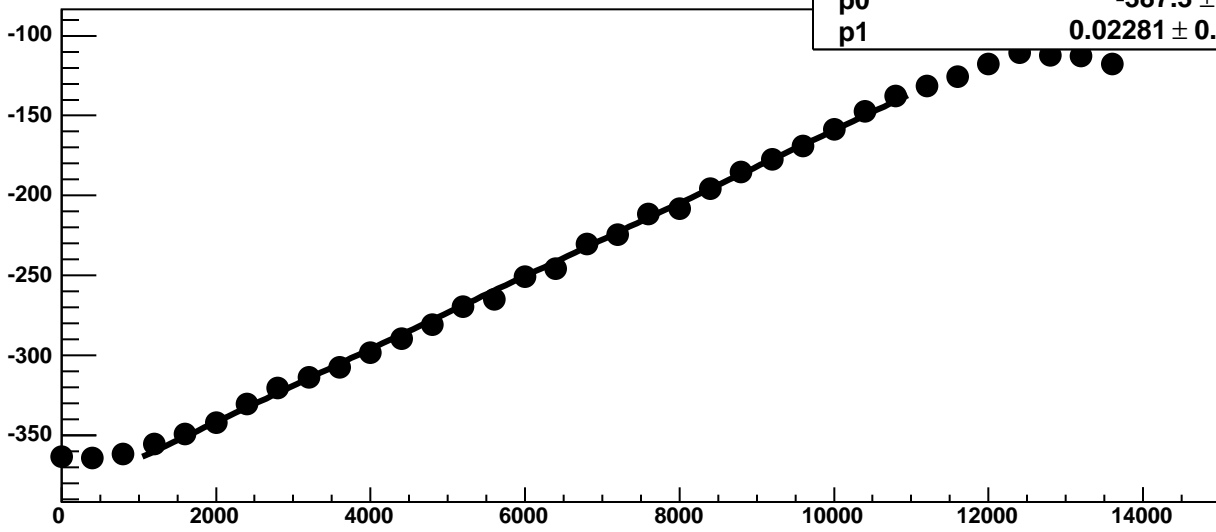
Chip 6, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



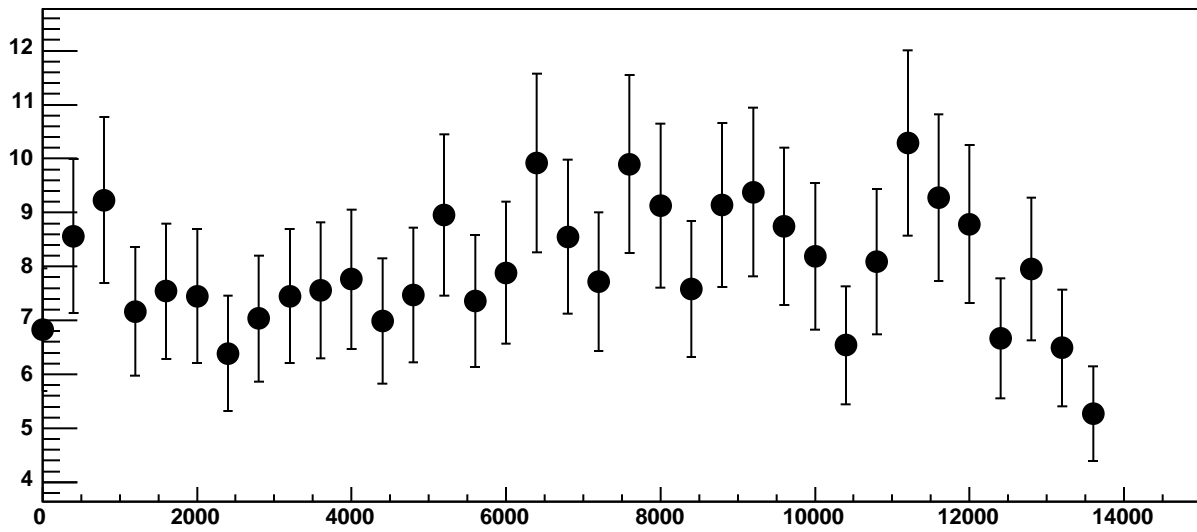
Chip 6, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



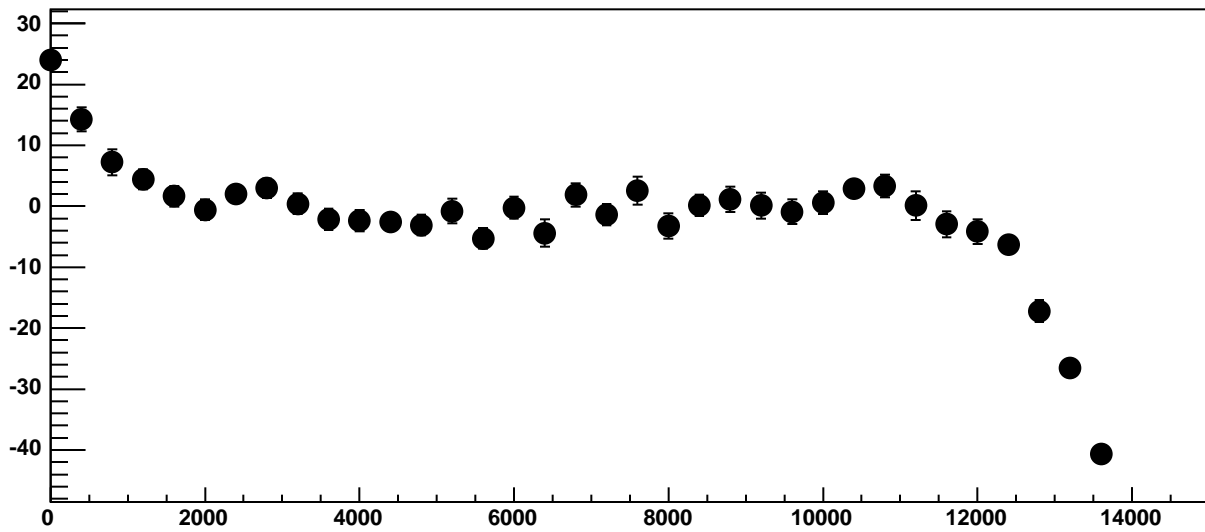
Chip 6, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



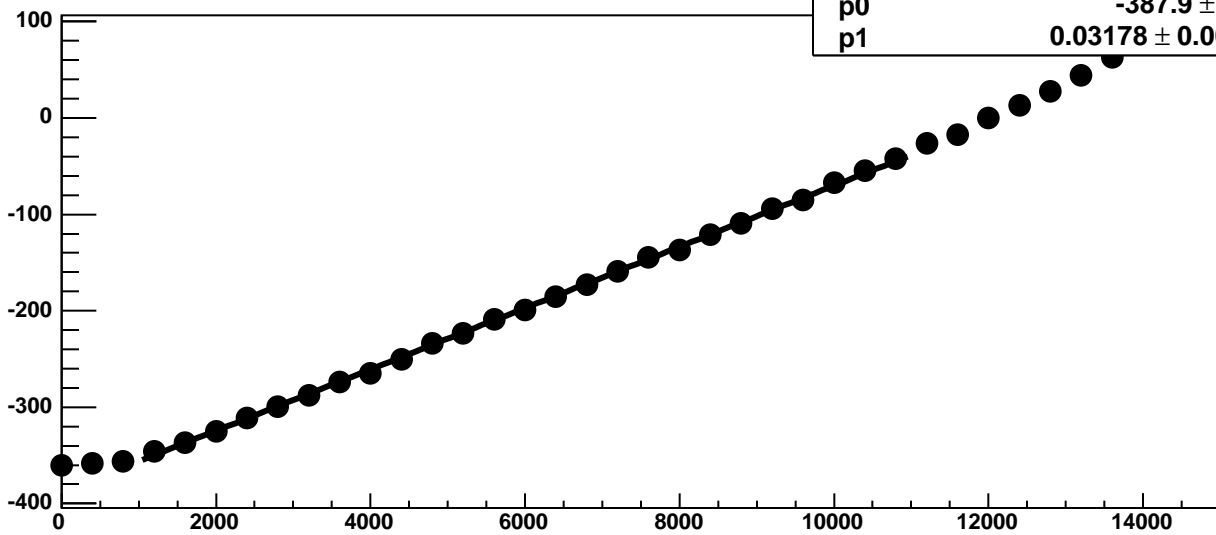
Chip 6, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

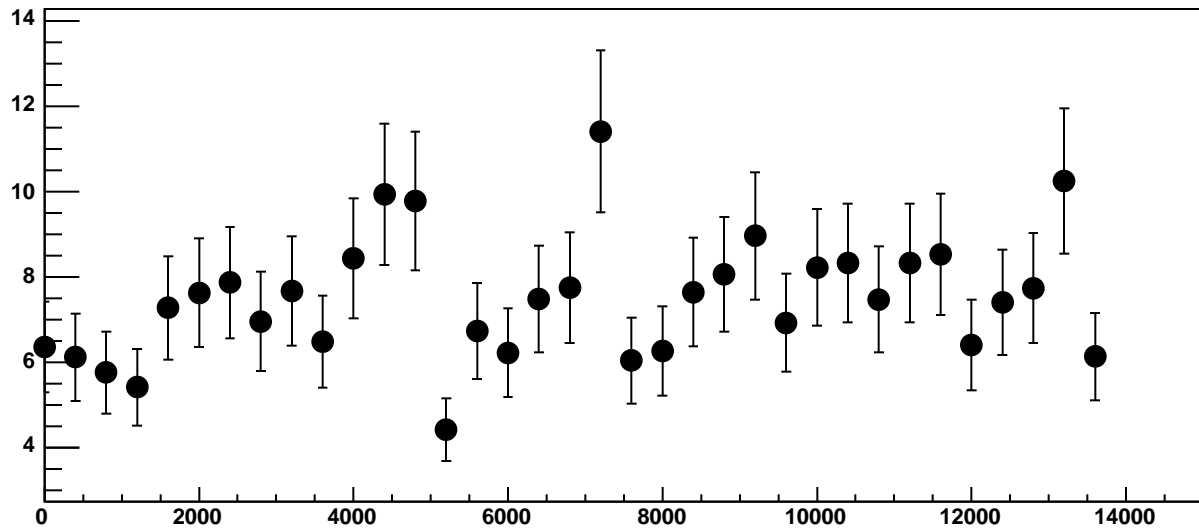


Chip 6, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

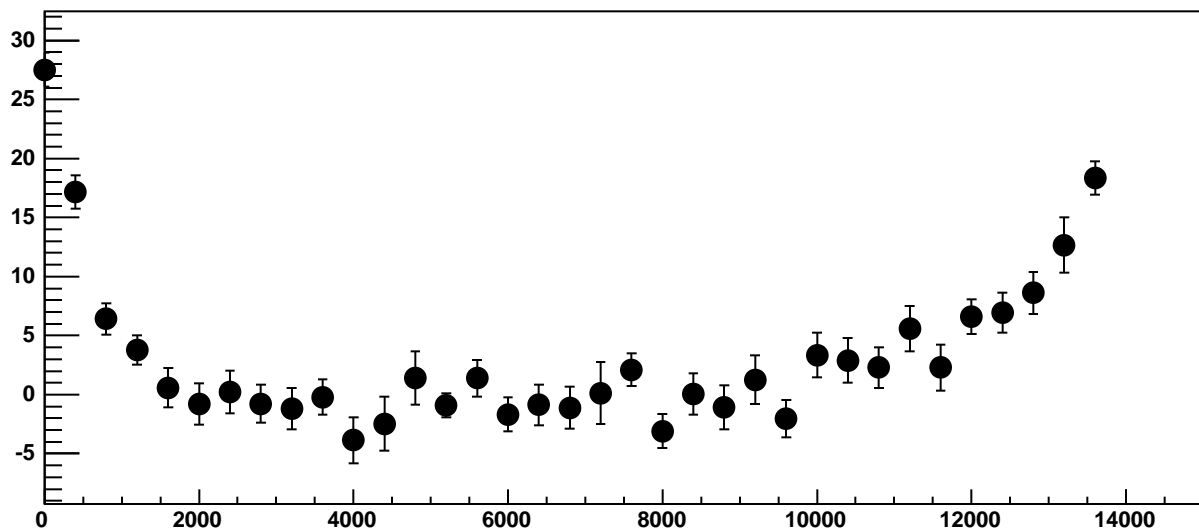


χ^2 / ndf 35.84 / 23
p0 -387.9 ± 0.7402
p1 0.03178 ± 0.0001153

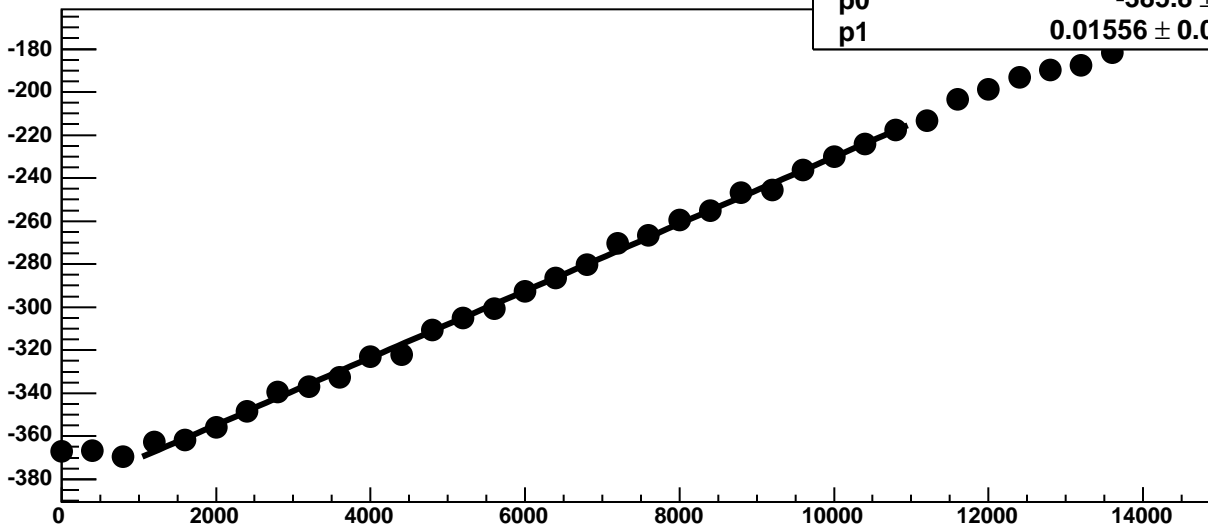
Chip 6, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

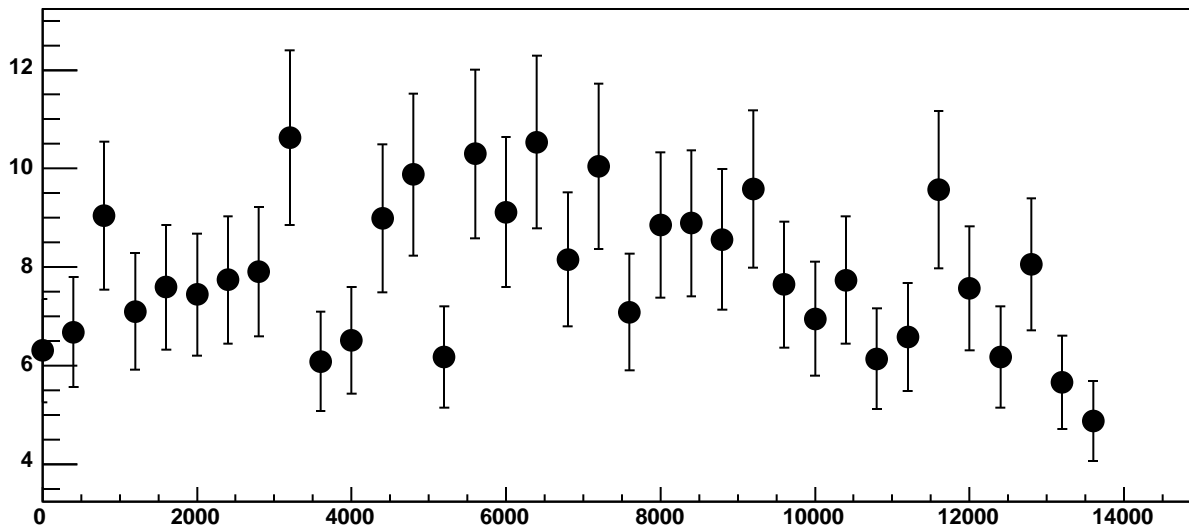


Chip 6, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

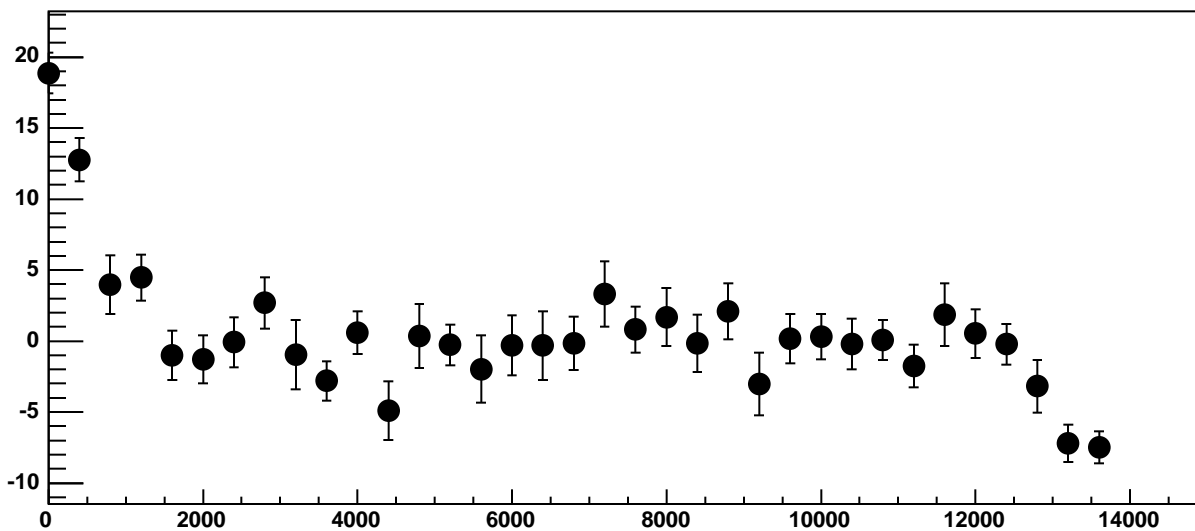


χ^2 / ndf 27.52 / 23
p0 -385.8 ± 0.7933
p1 0.01556 ± 0.0001184

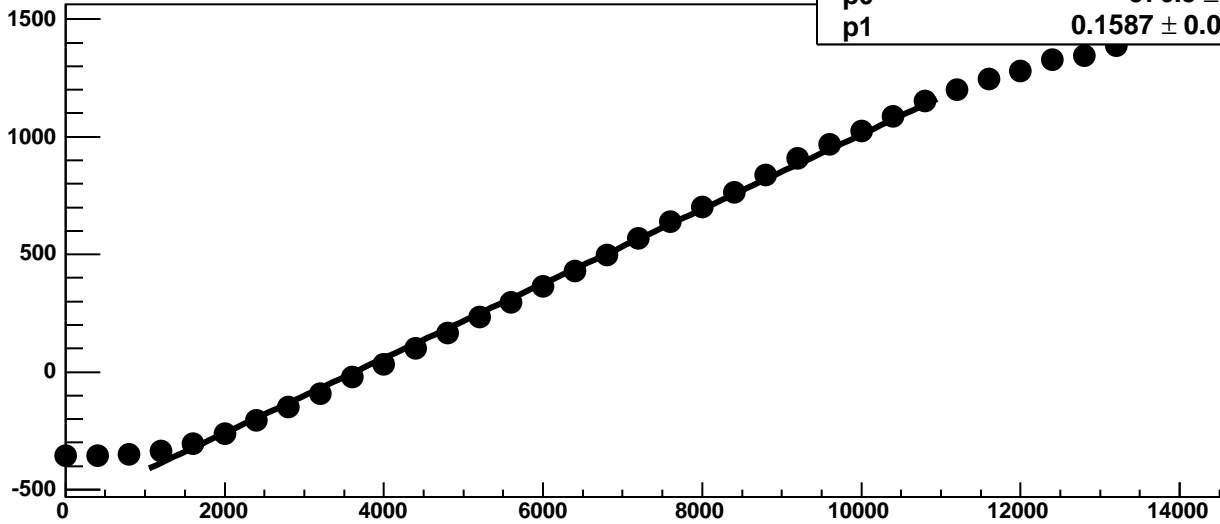
Chip 6, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



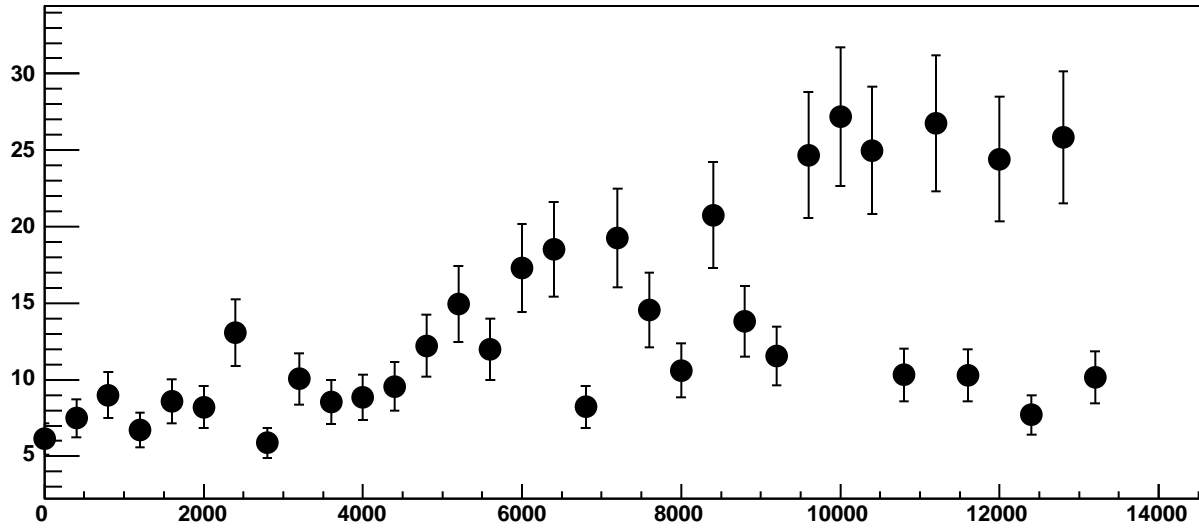
Chip 6, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



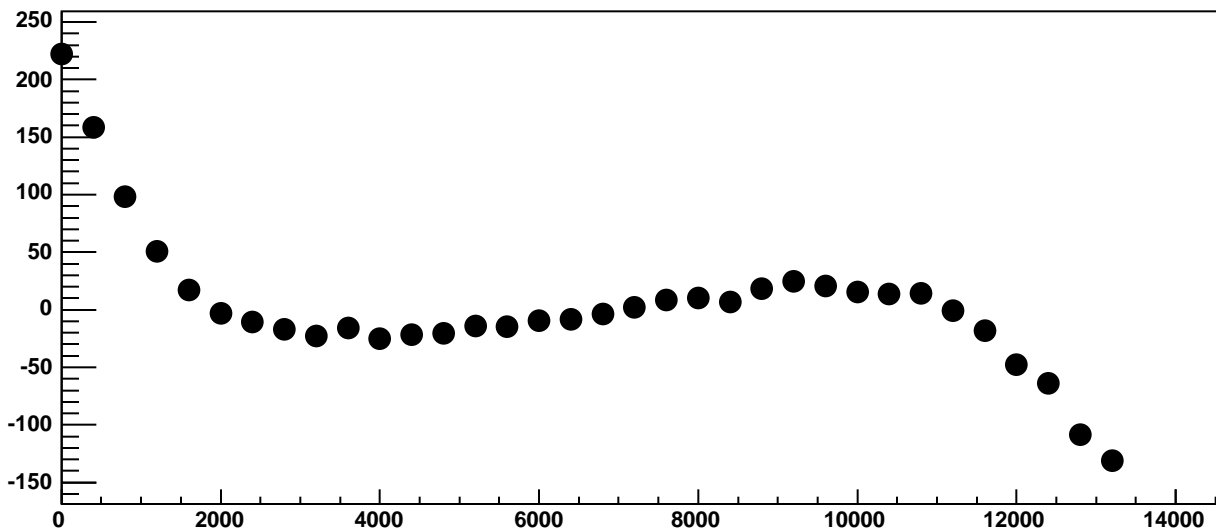
Chip 6, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



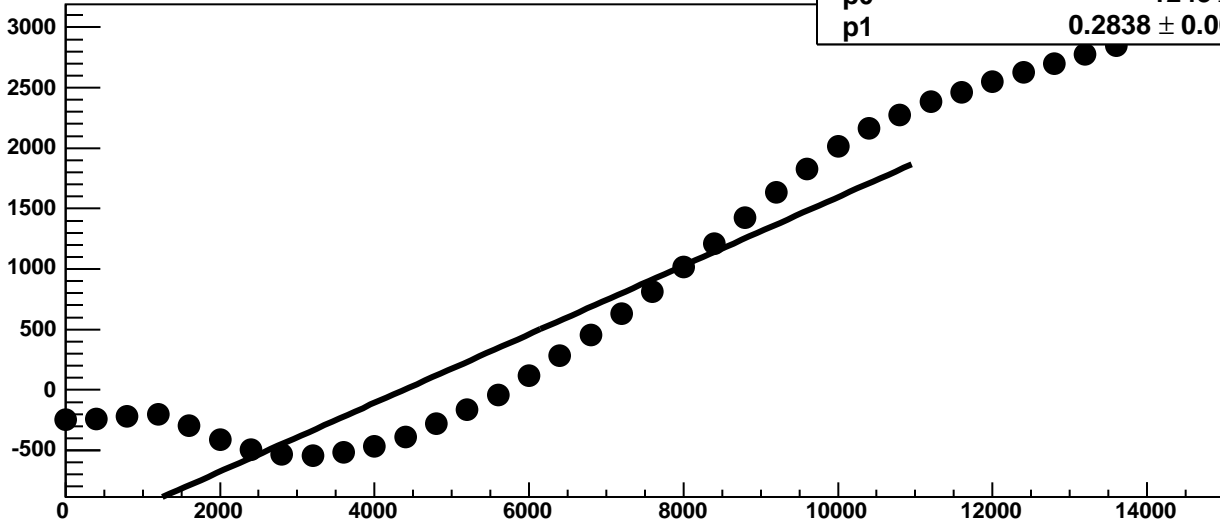
Chip 6, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

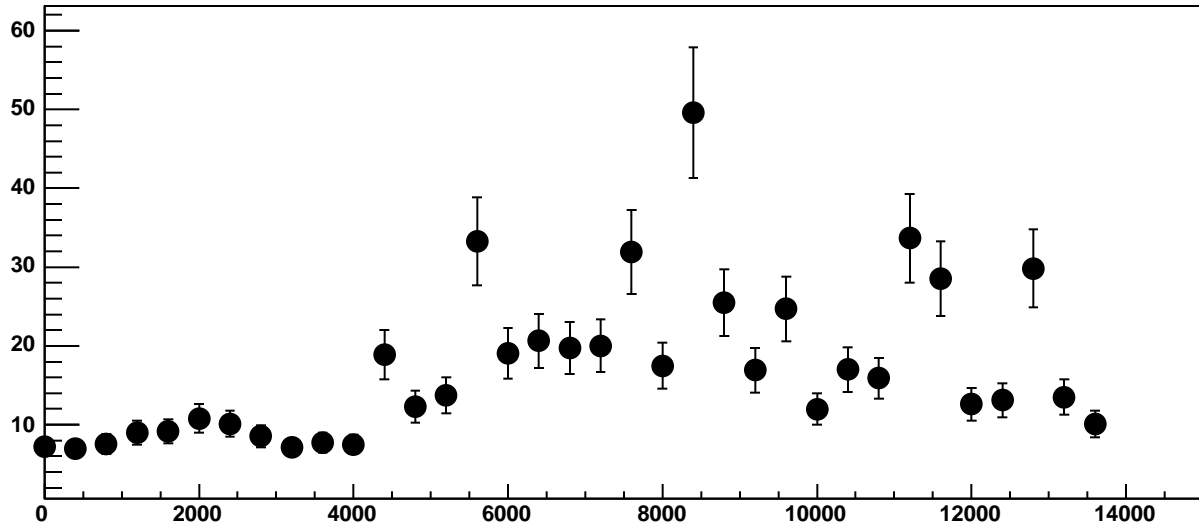


Chip 6, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC

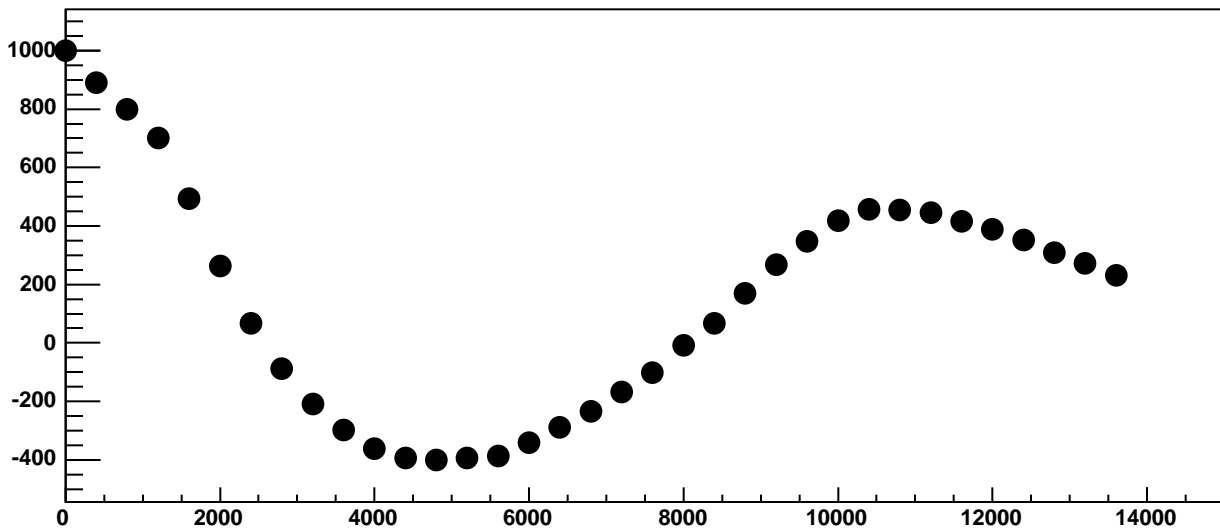


χ^2 / ndf 3.97e+05 / 23
p0 -1243 ± 1.078
p1 0.2838 ± 0.0002122

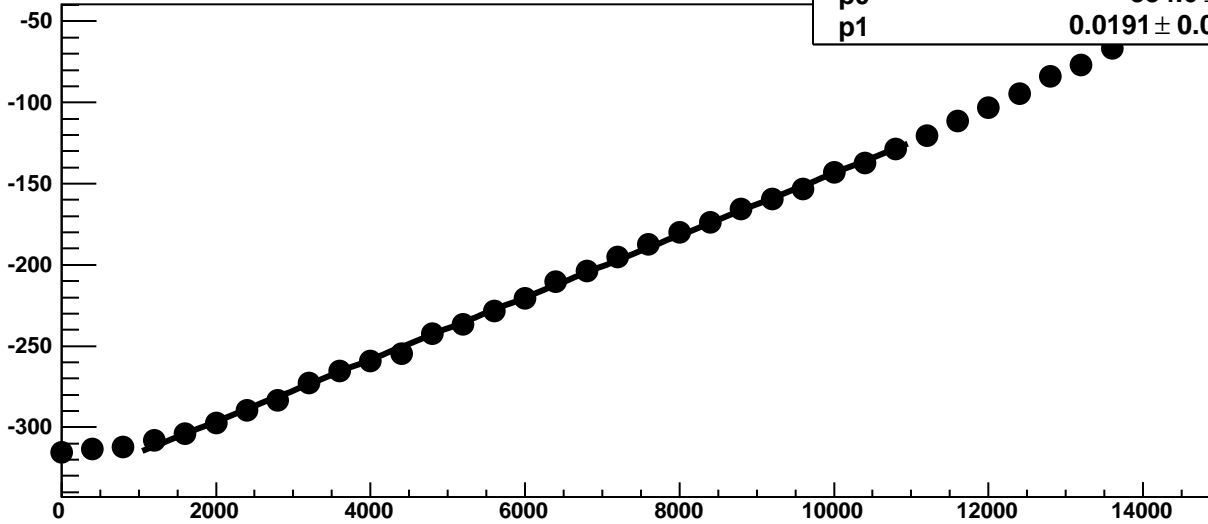
Chip 6, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC

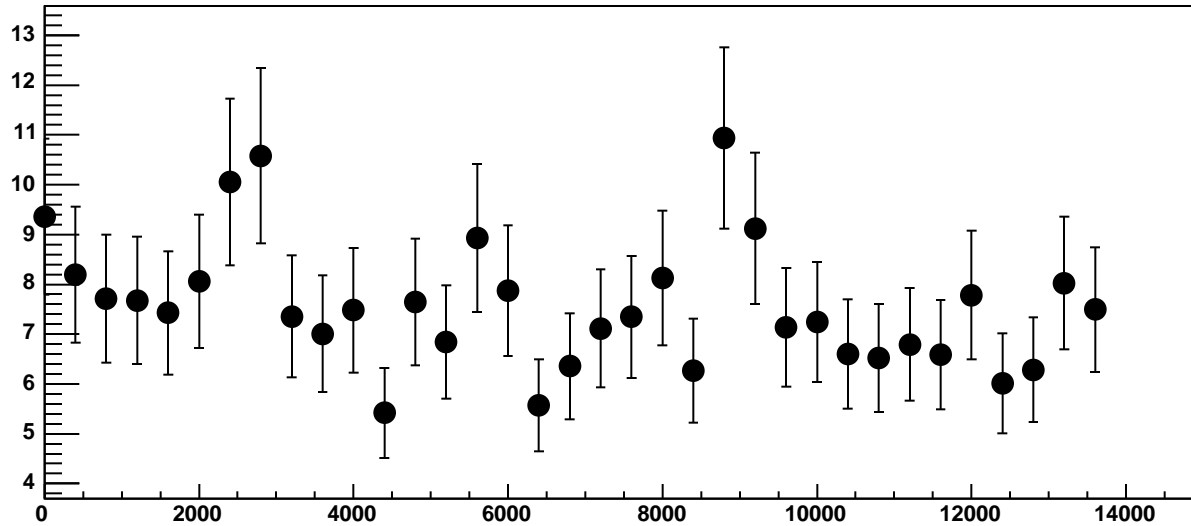


Chip 6, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

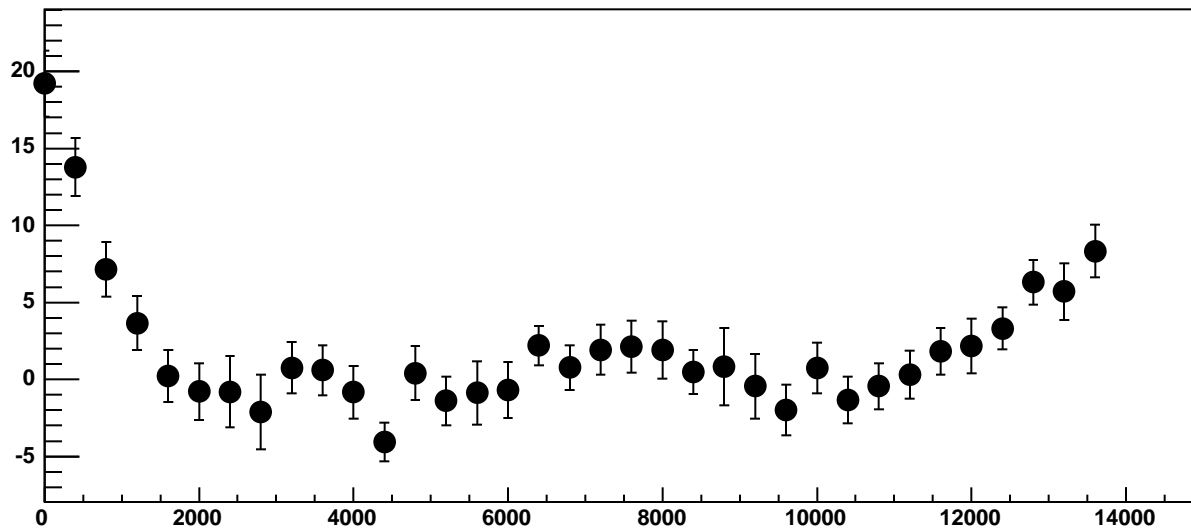


χ^2 / ndf 27.84 / 23
p0 -334.6 ± 0.8091
p1 0.0191 ± 0.0001194

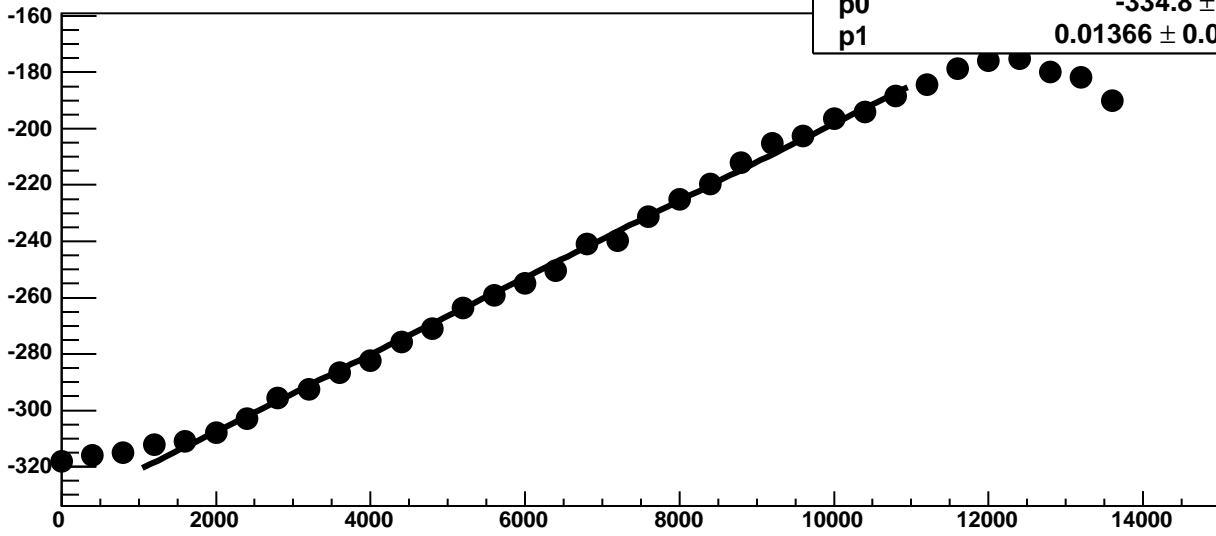
Chip 6, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



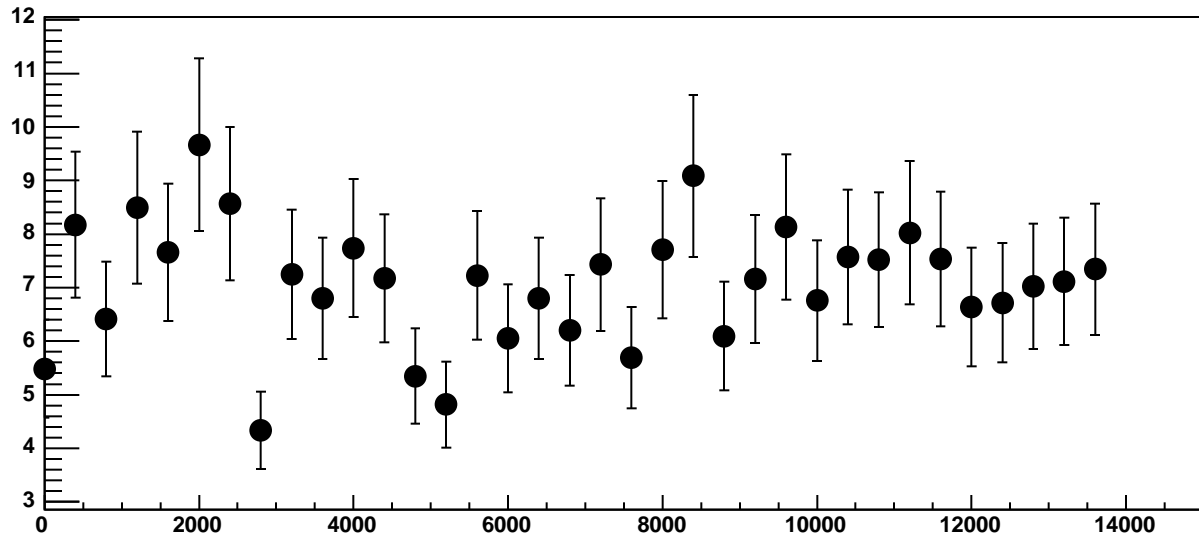
Chip 6, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



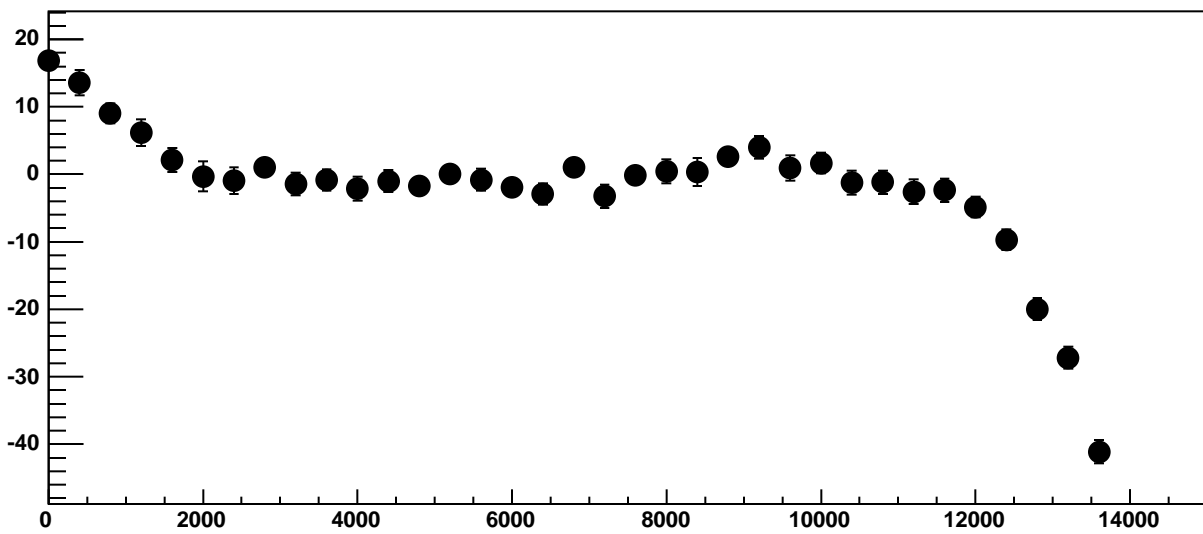
Chip 6, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



Chip 6, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

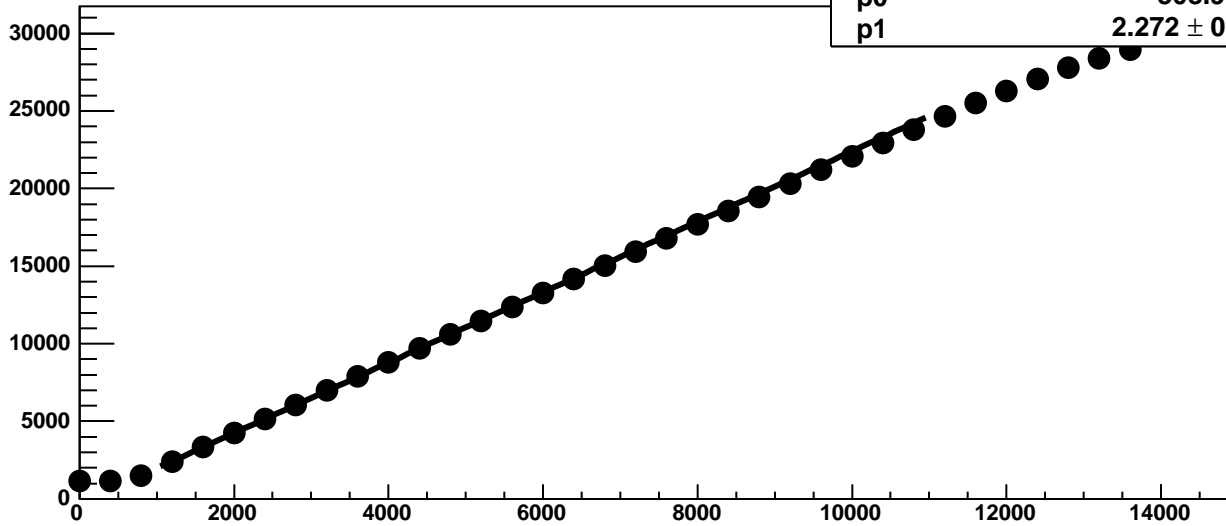


Chip 6, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

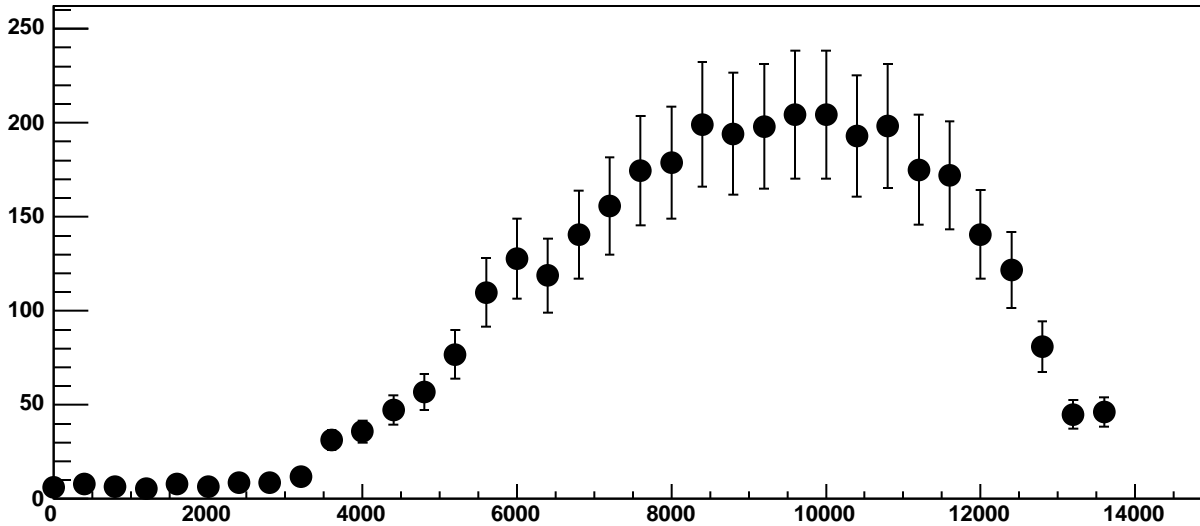


Chip 6, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC

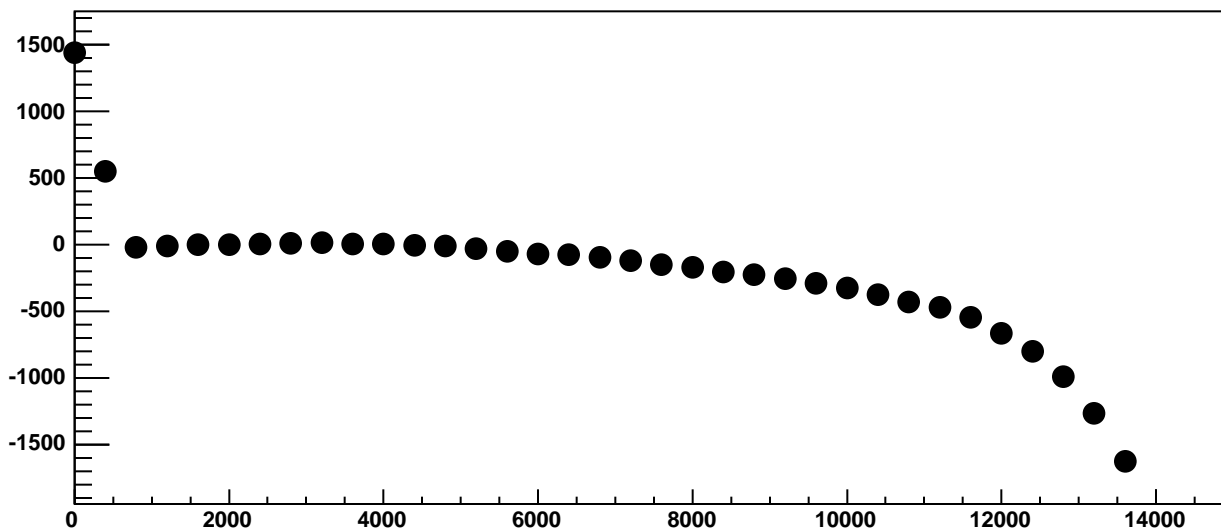
χ^2 / ndf 551.9 / 23
p0 -308.9 ± 1.783
p1 2.272 ± 0.000829



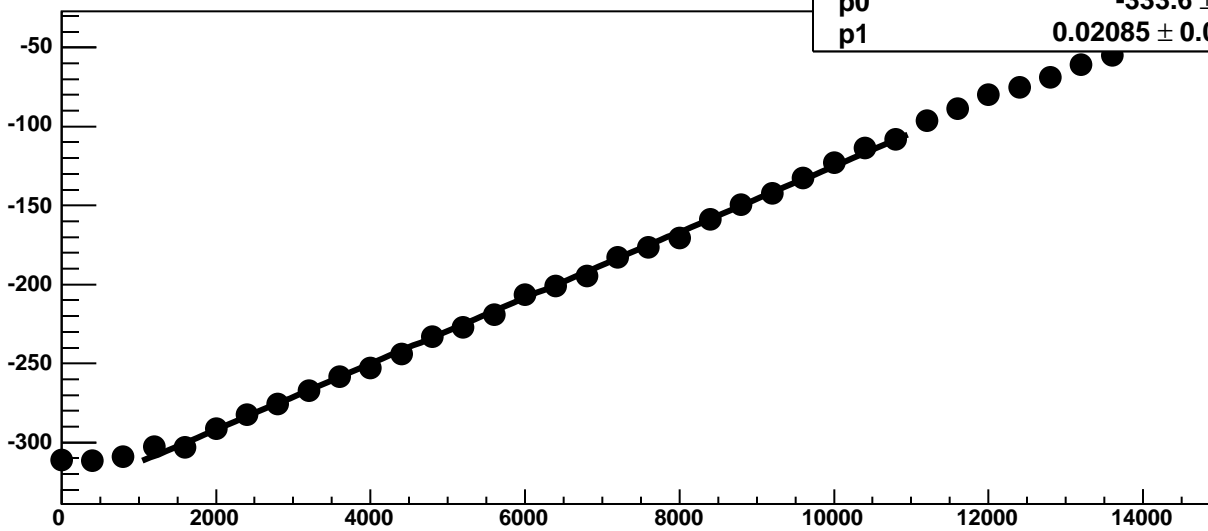
Chip 6, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



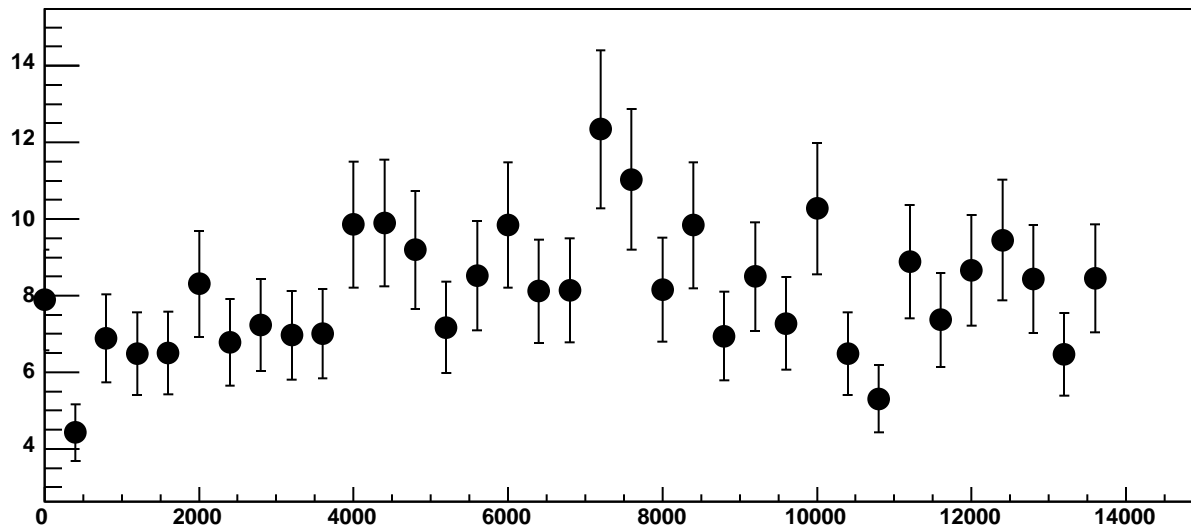
Chip 6, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC



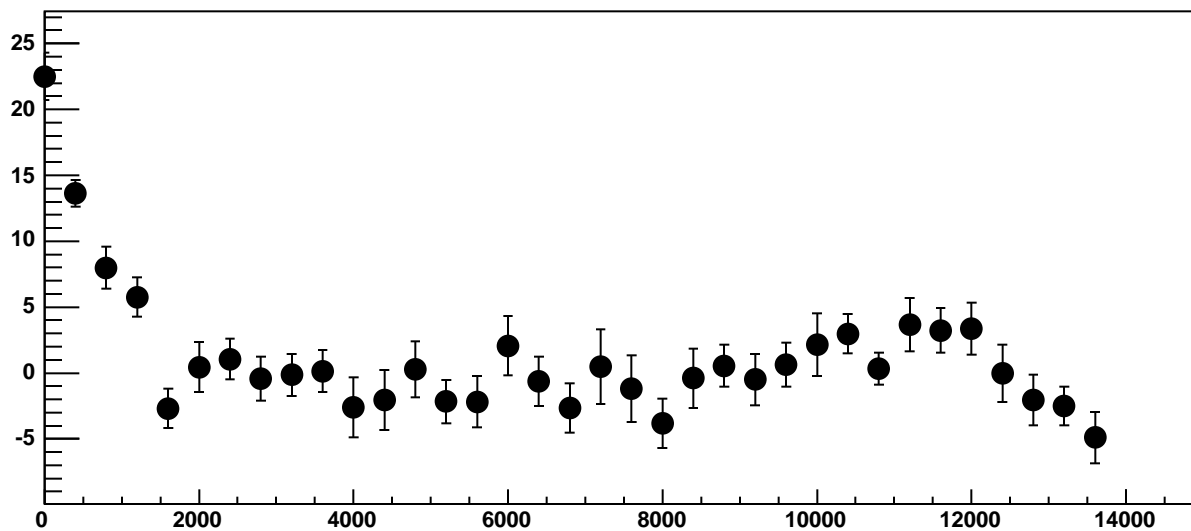
Chip 6, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



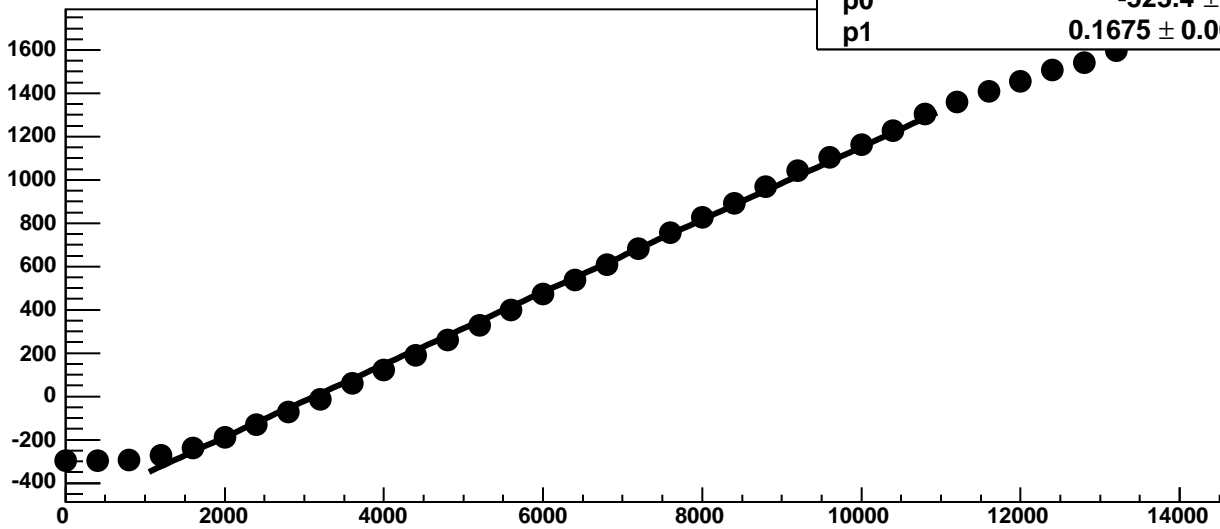
Chip 6, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



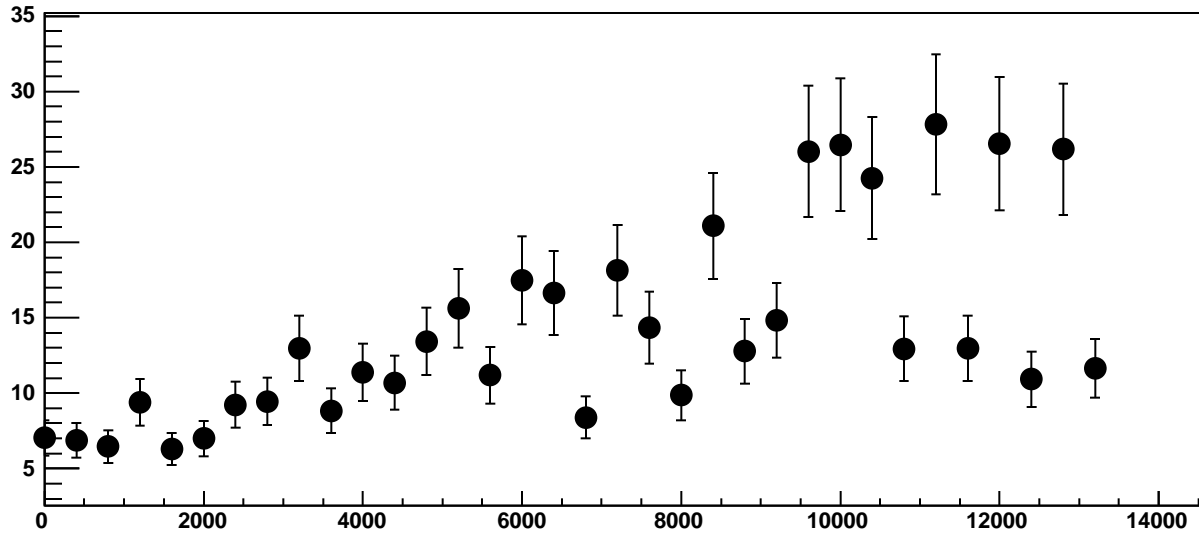
Chip 6, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



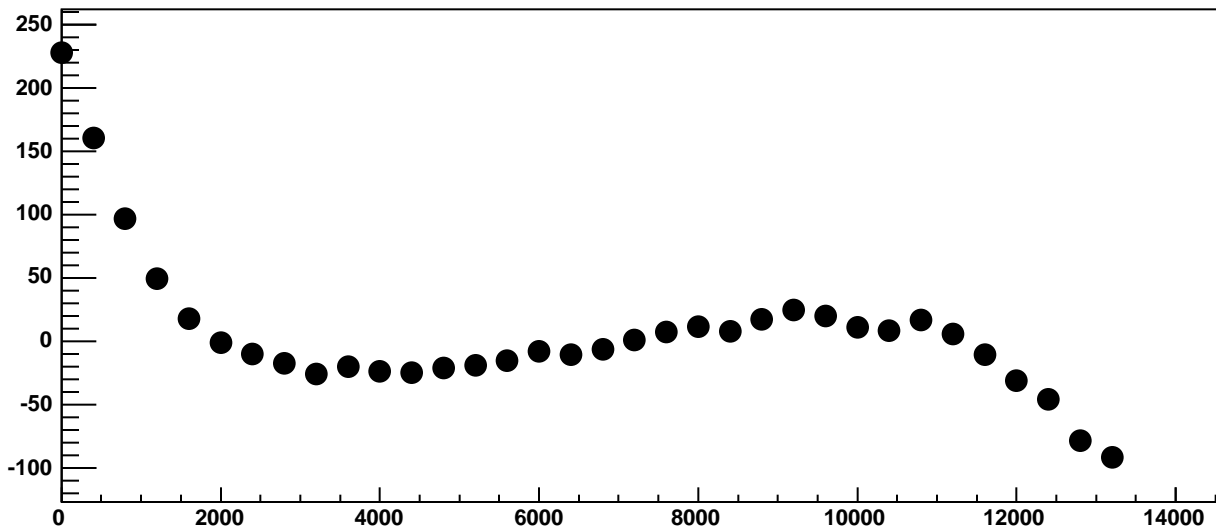
Chip 6, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



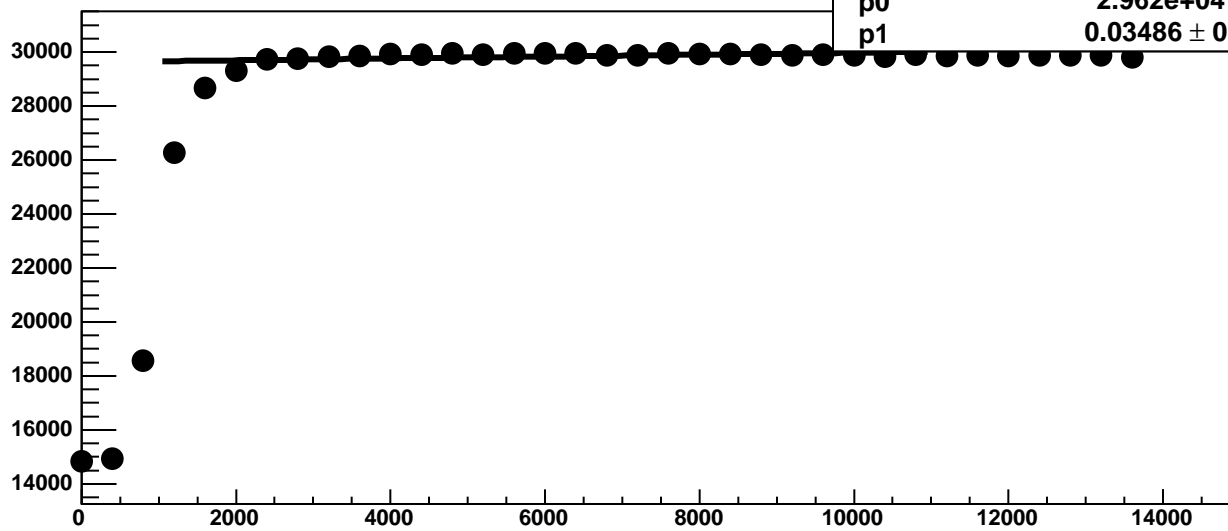
Chip 6, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



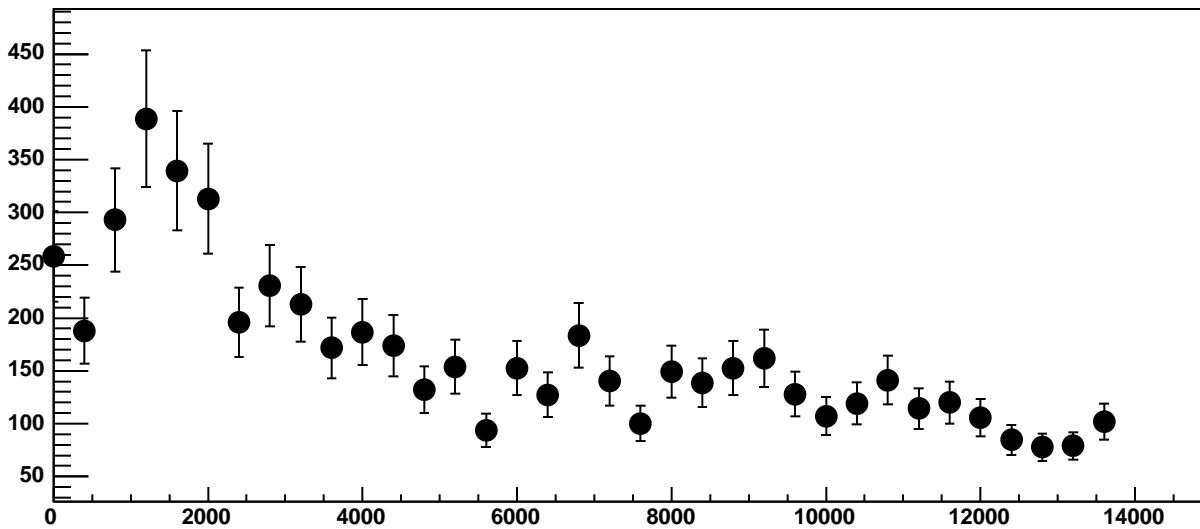
Chip 6, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



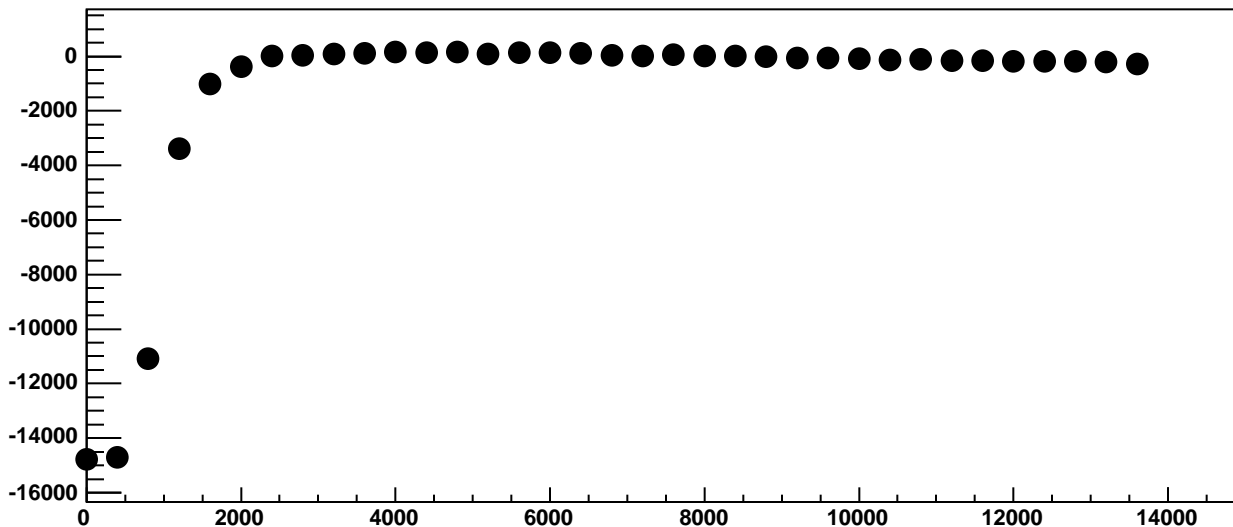
Chip 6, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC



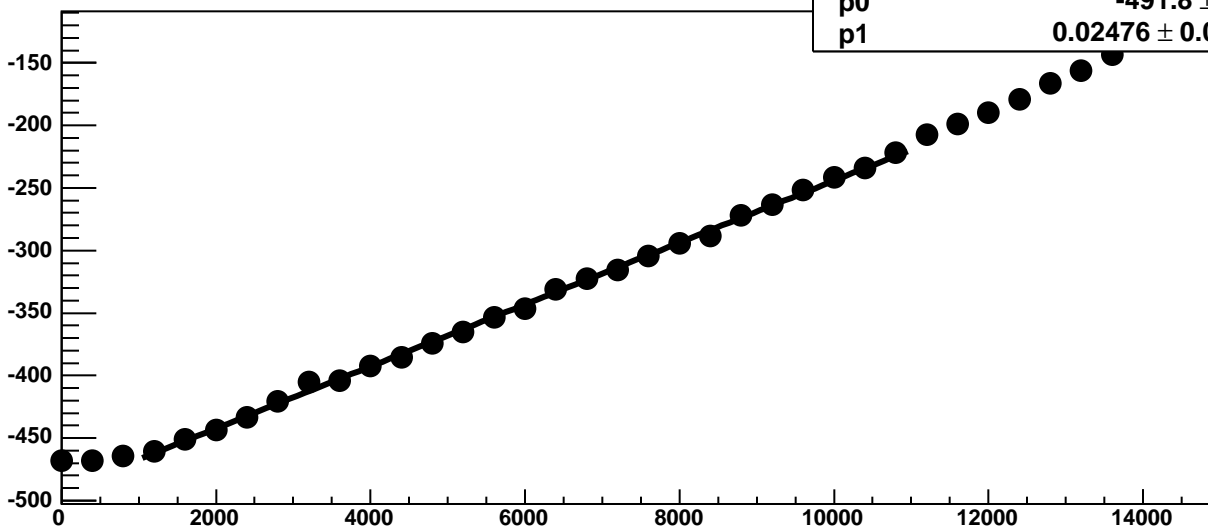
Chip 6, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC

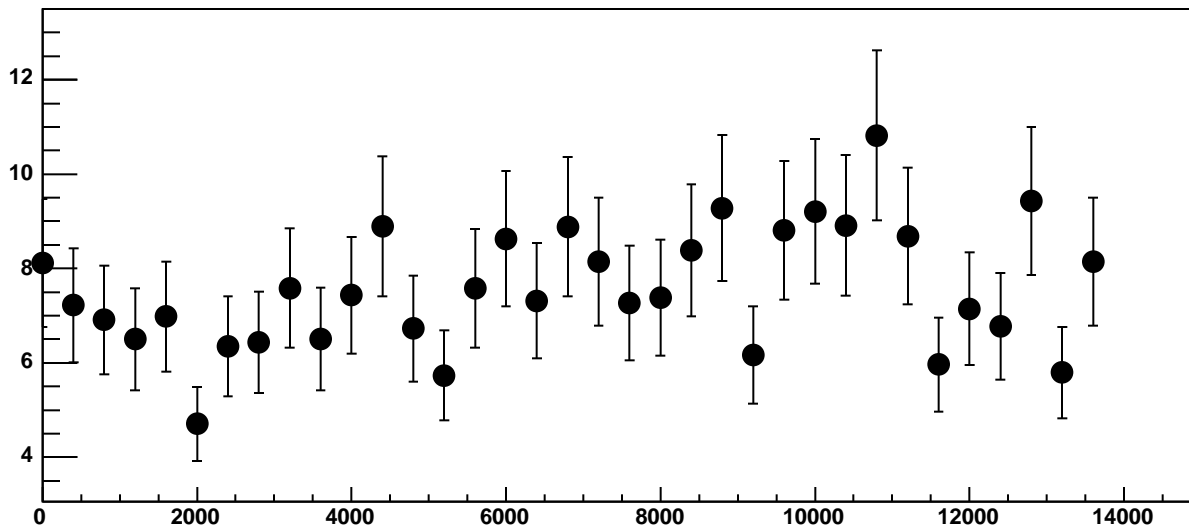


Chip 6, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

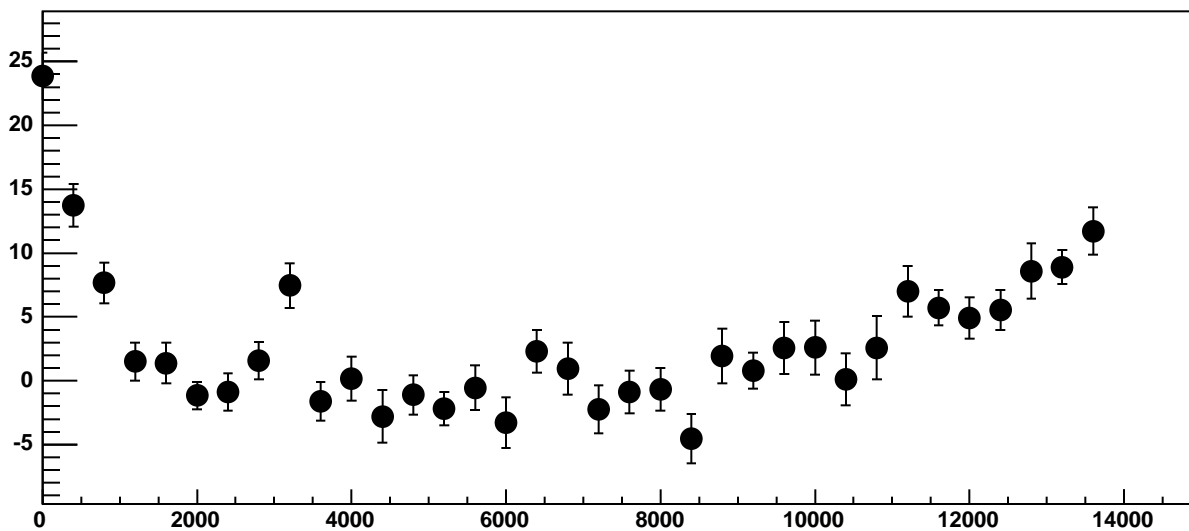


χ^2 / ndf 46.74 / 23
p0 -491.8 ± 0.7058
p1 0.02476 ± 0.0001172

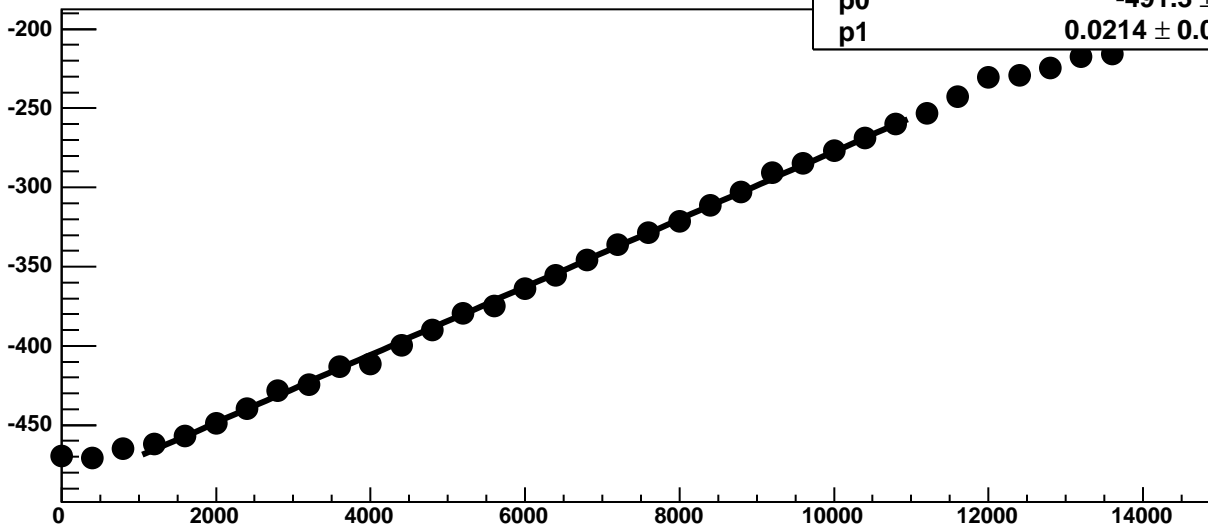
Chip 6, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



Chip 6, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

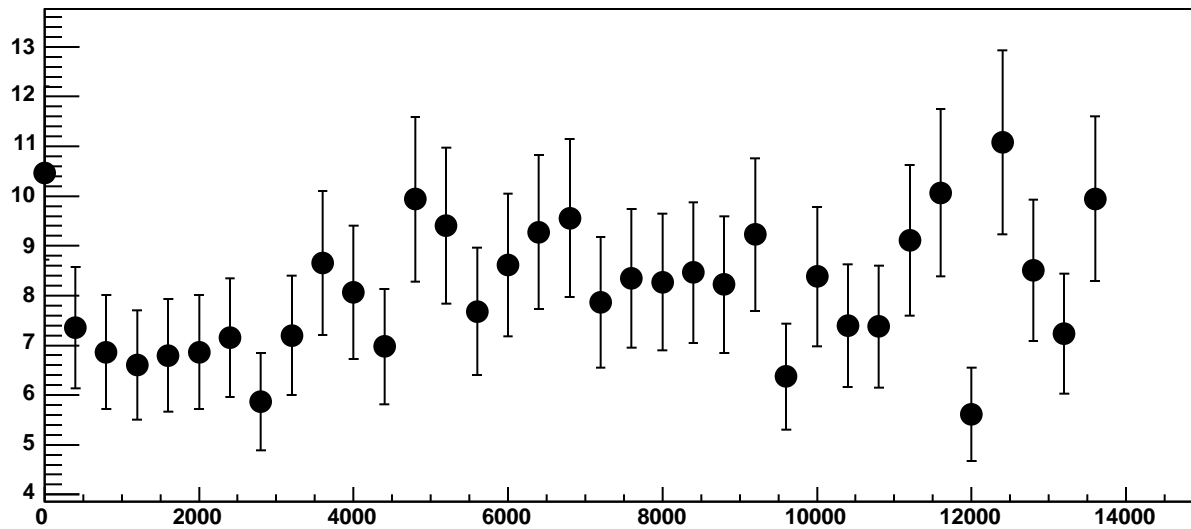


Chip 6, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

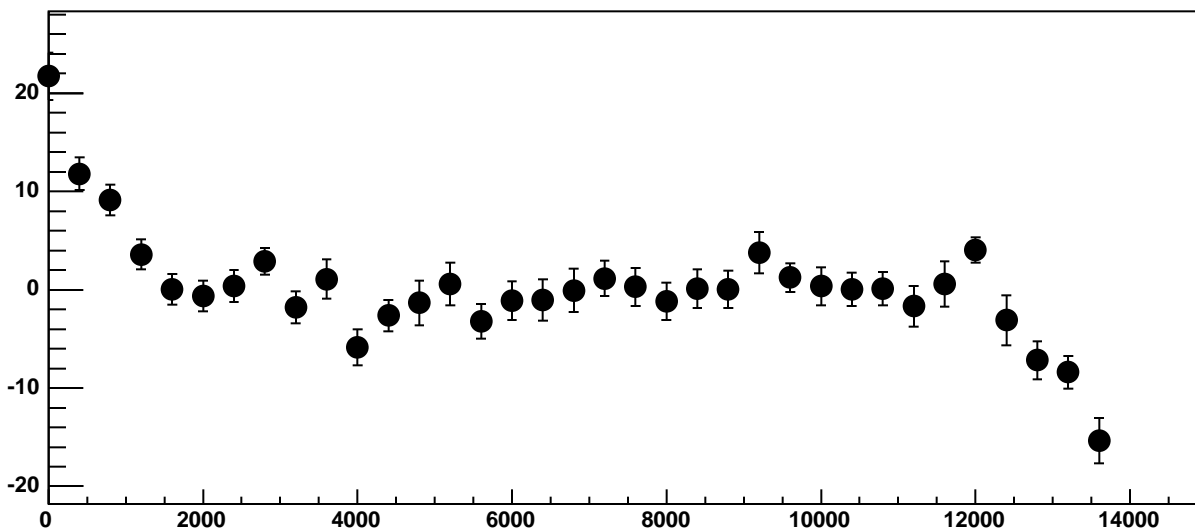


χ^2 / ndf 33.53 / 23
p0 -491.3 ± 0.7498
p1 0.0214 ± 0.0001156

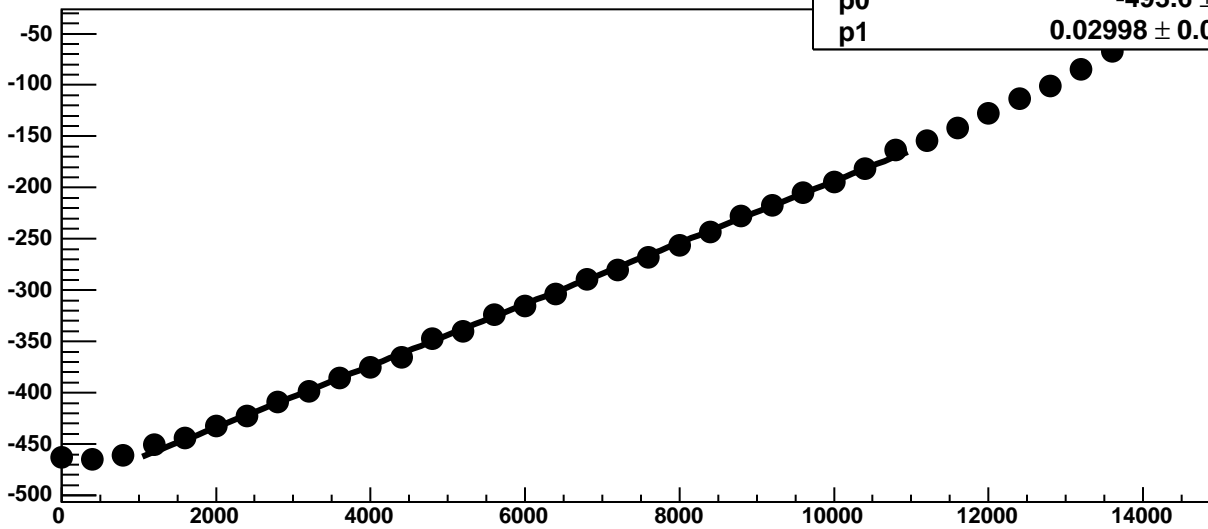
Chip 6, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

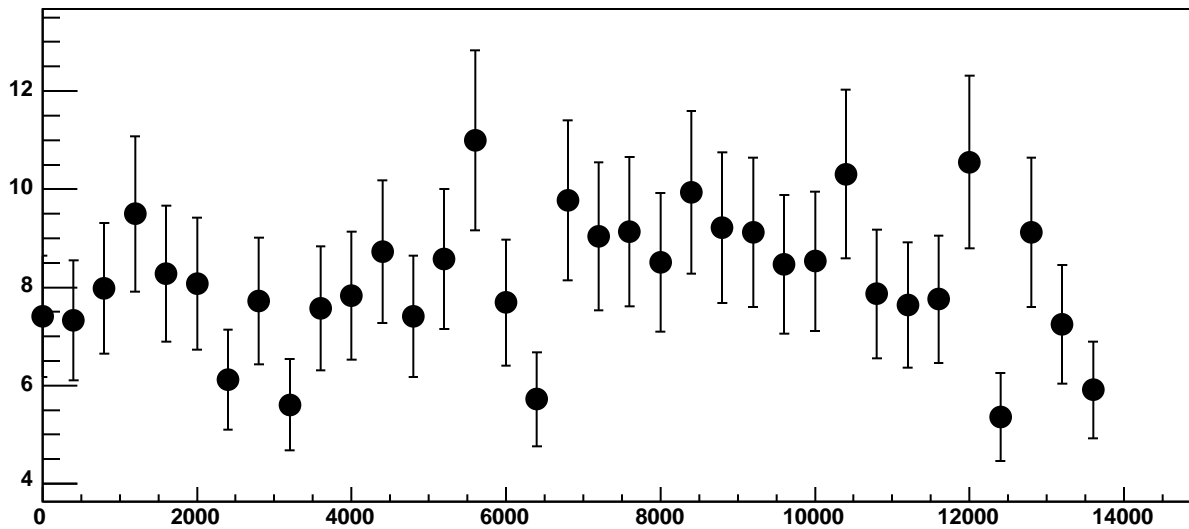


Chip 6, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

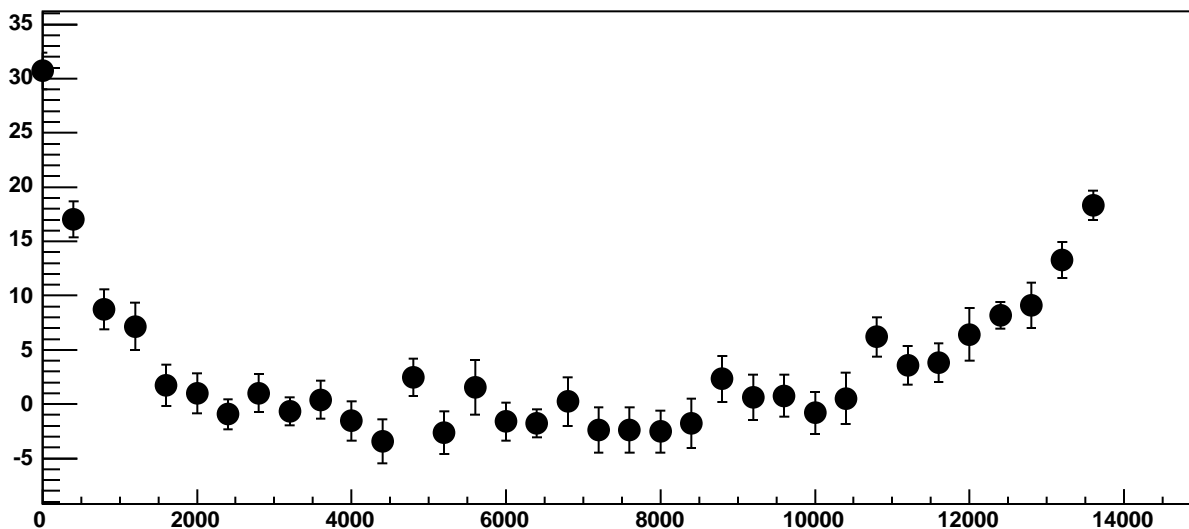


χ^2 / ndf 41.96 / 23
p0 -493.6 ± 0.8227
p1 0.02998 ± 0.0001309

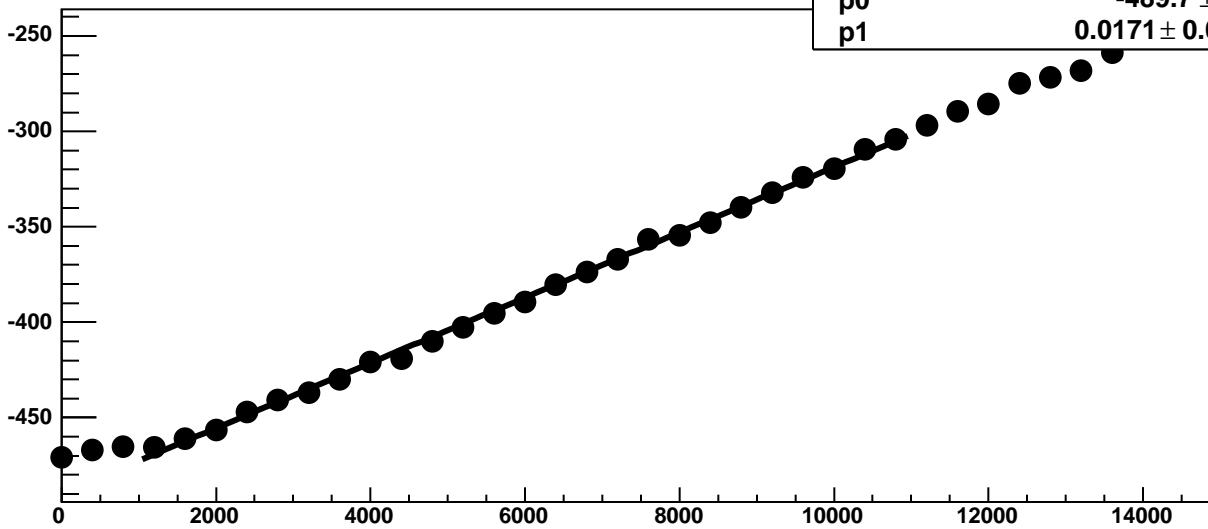
Chip 6, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

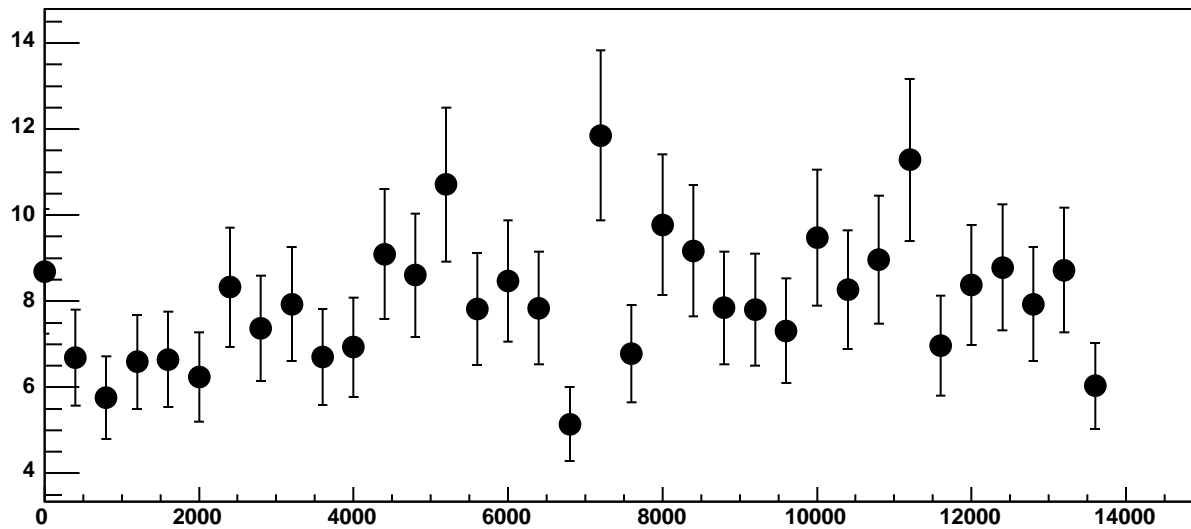


Chip 6, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

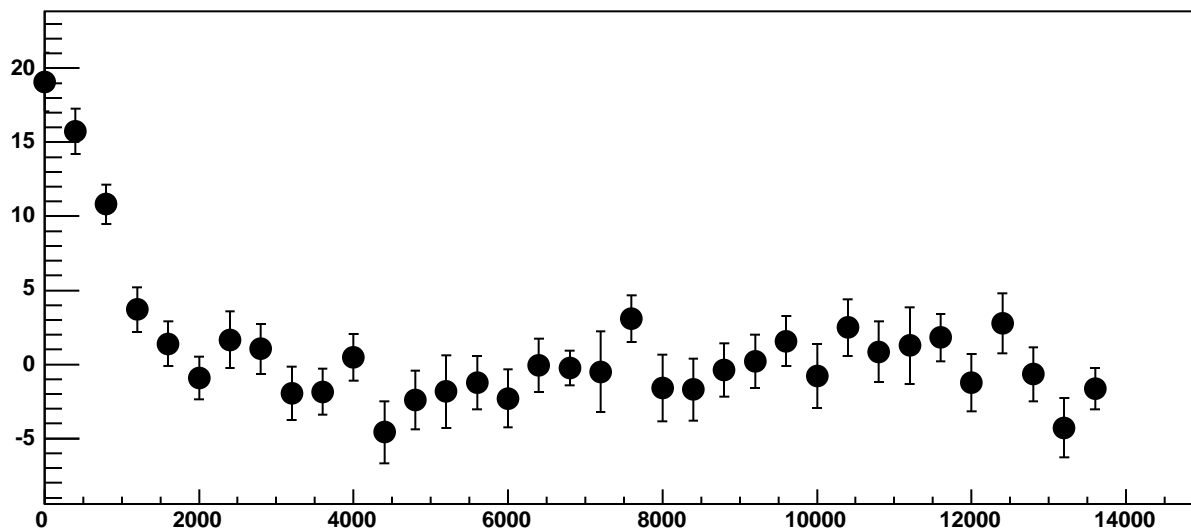


χ^2 / ndf 27.97 / 23
p0 -489.7 ± 0.7695
p1 0.0171 ± 0.0001211

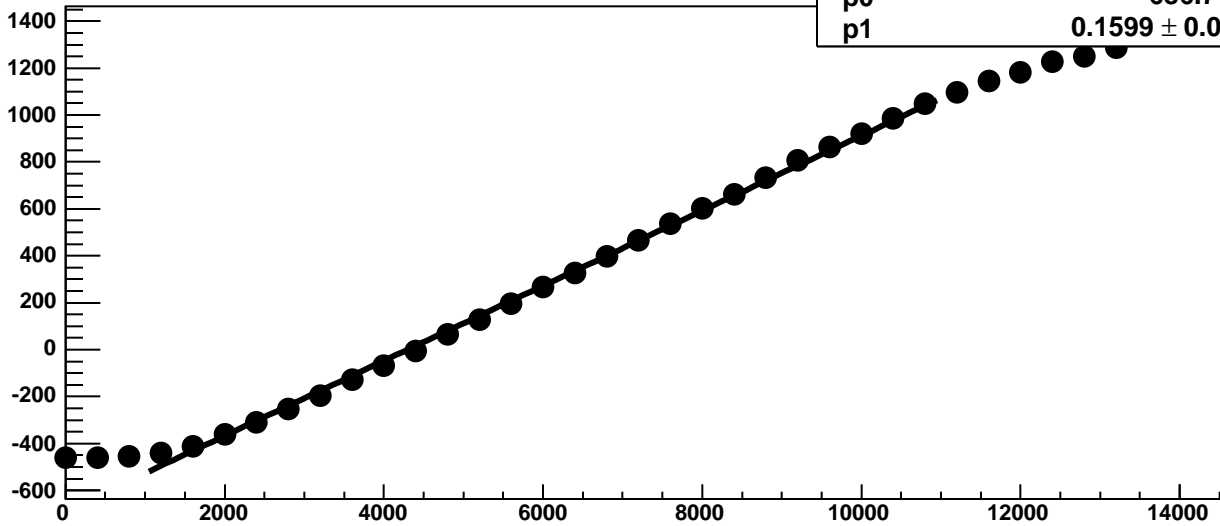
Chip 6, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



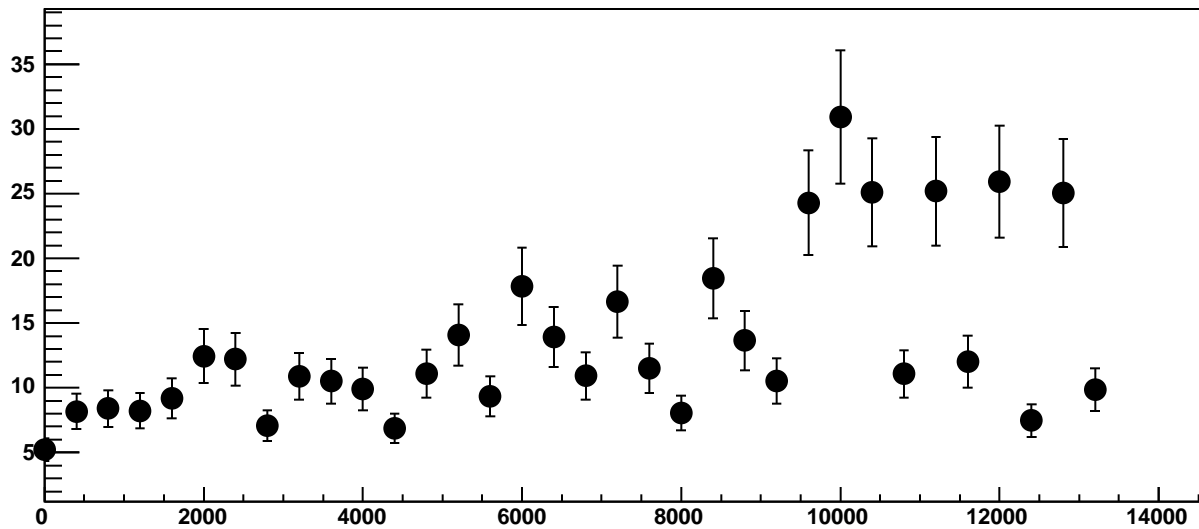
Chip 6, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



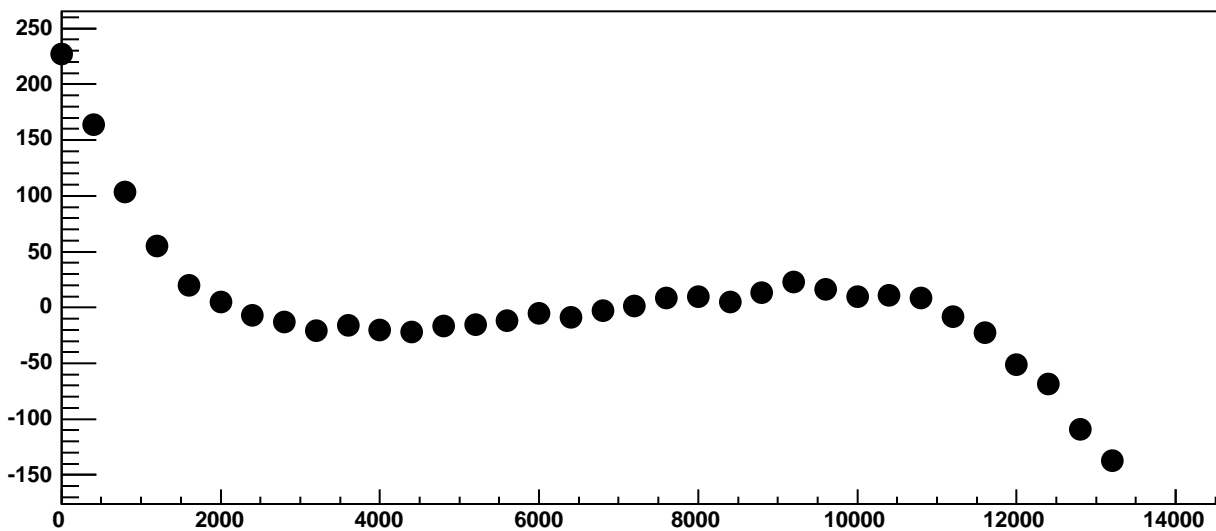
Chip 6, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



Chip 6, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

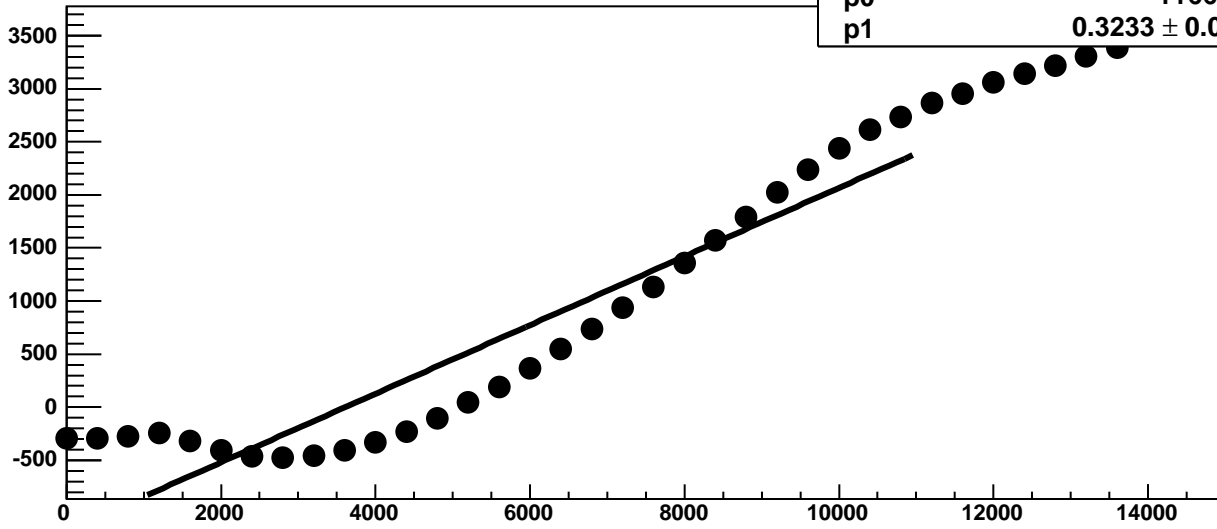


Chip 6, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

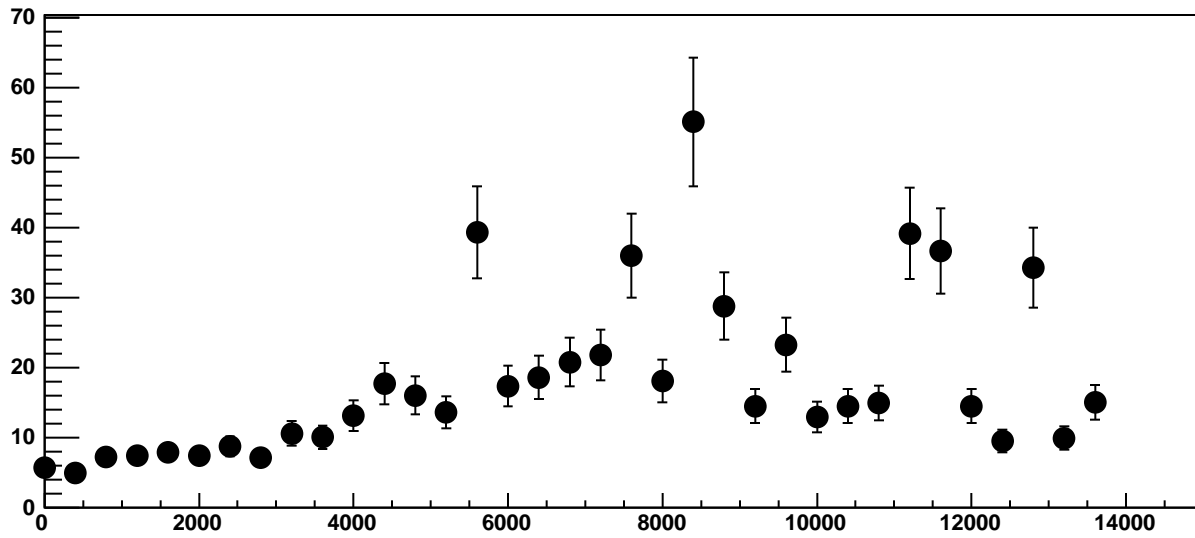


Chip 6, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

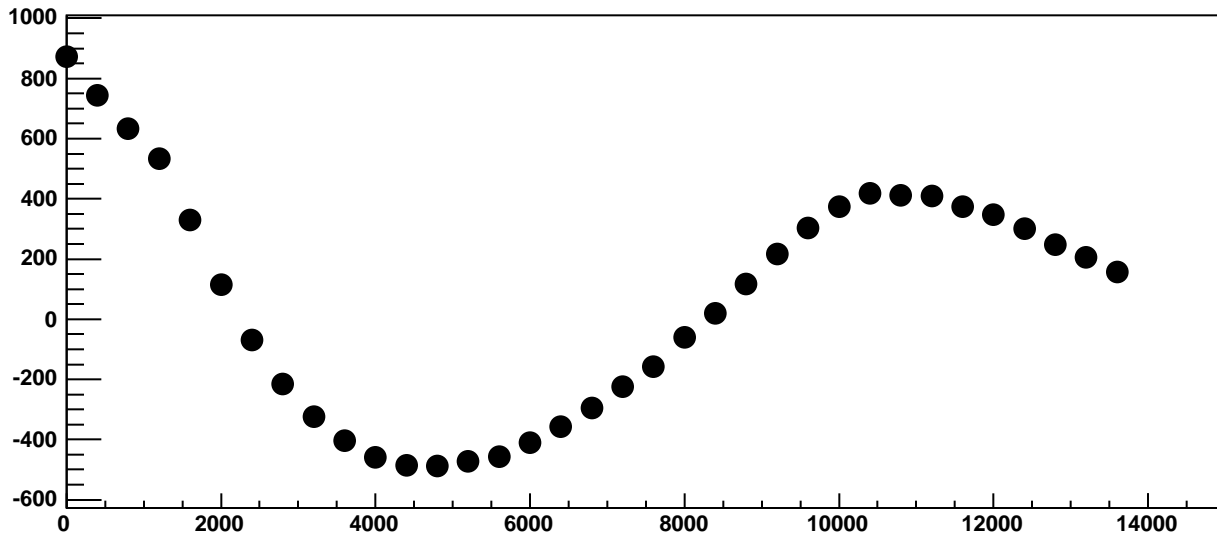
χ^2 / ndf 3.58e+05 / 23
p0 -1166 ± 0.991
p1 0.3233 ± 0.0001971



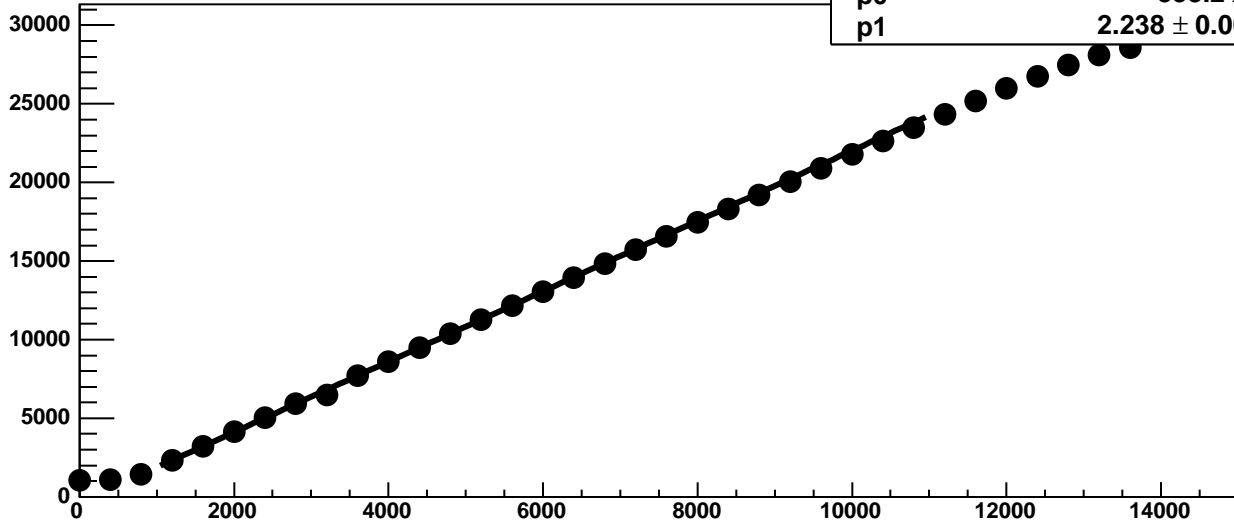
Chip 6, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



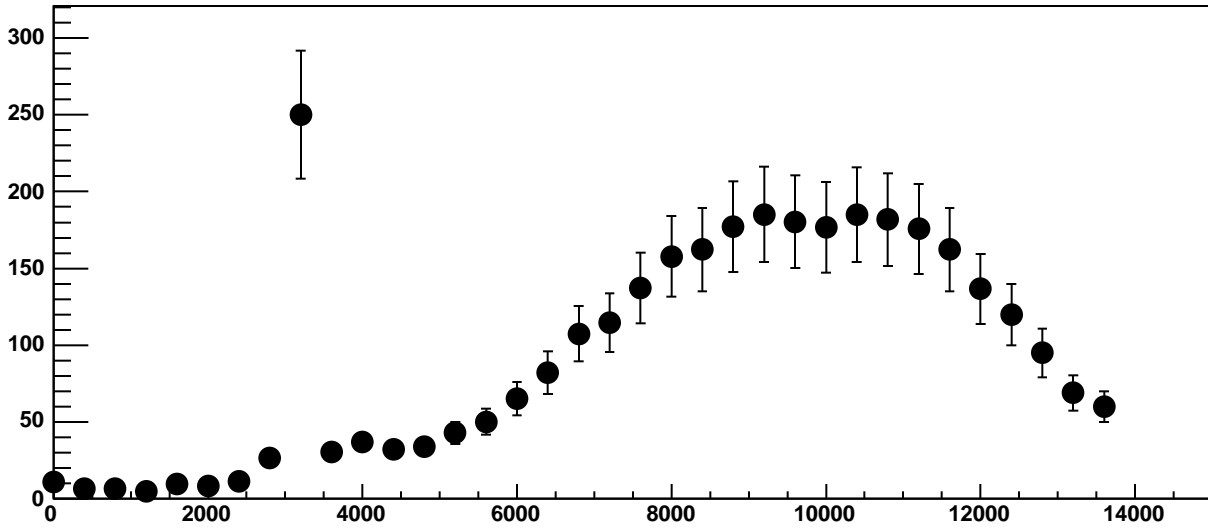
Chip 6, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



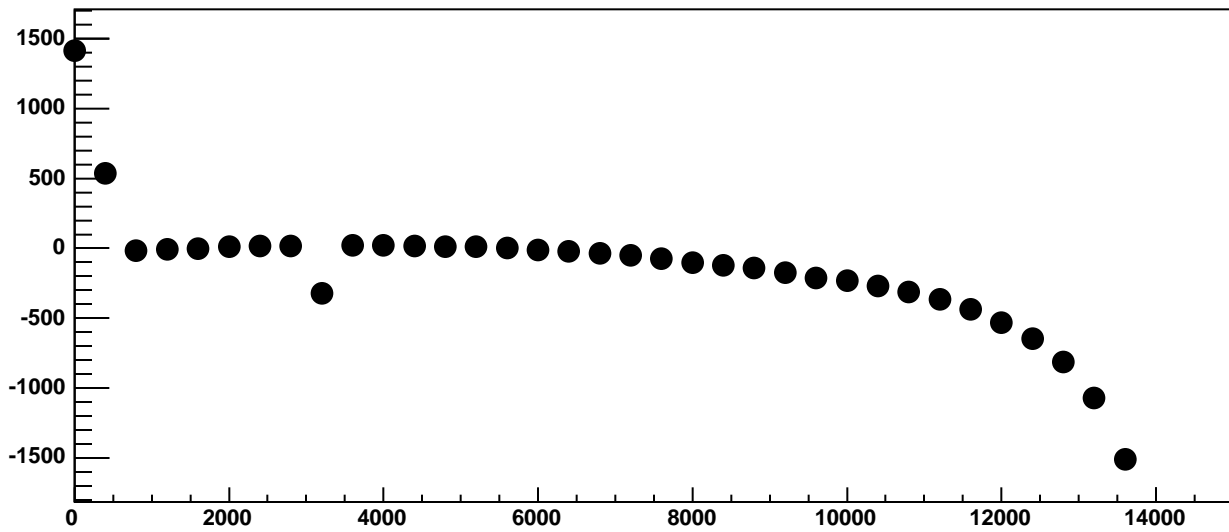
Chip 6, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC



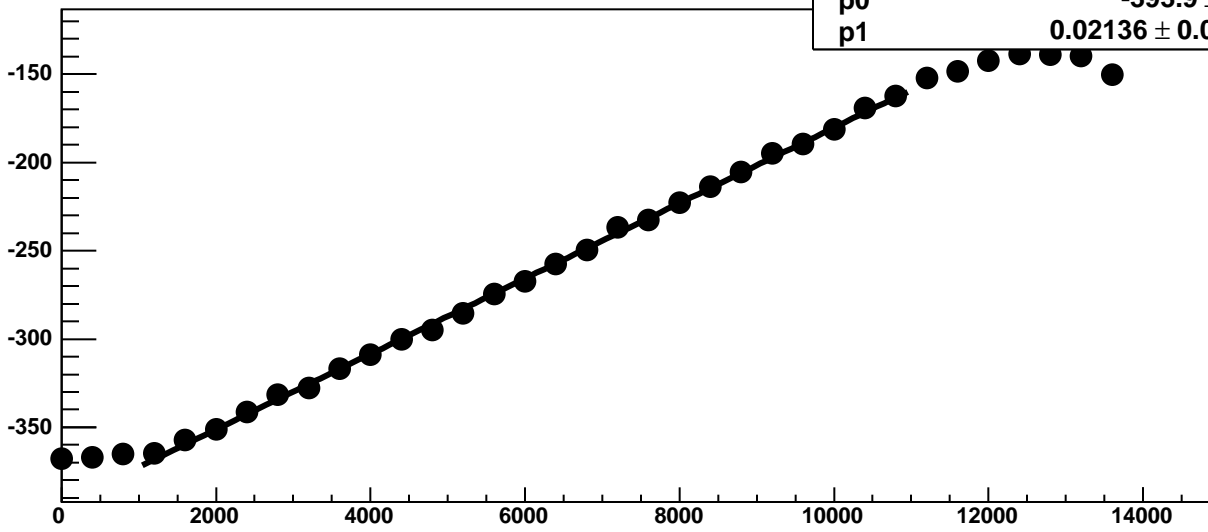
Chip 6, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



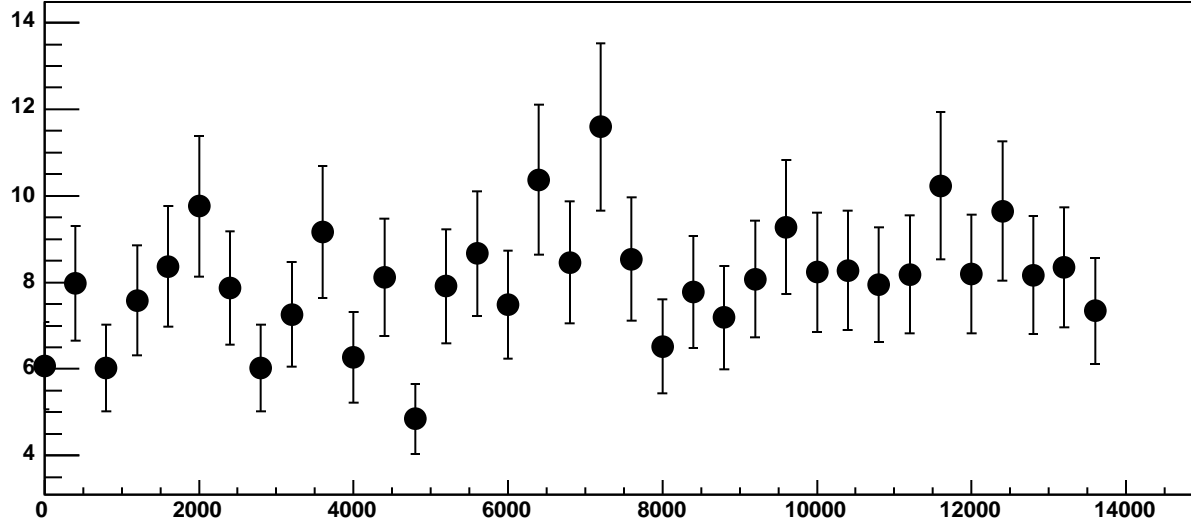
Chip 6, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC



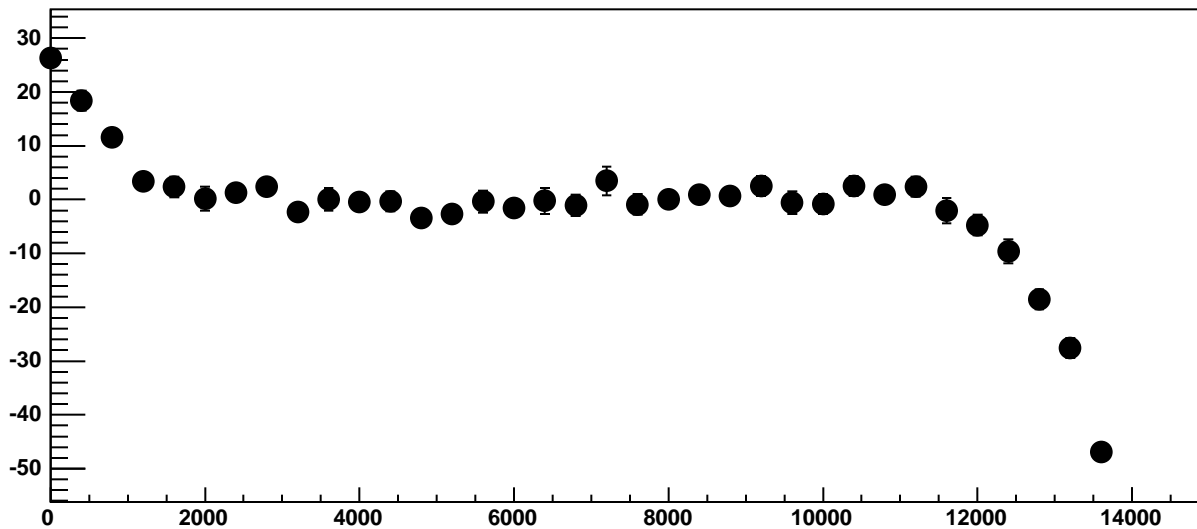
Chip 6, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



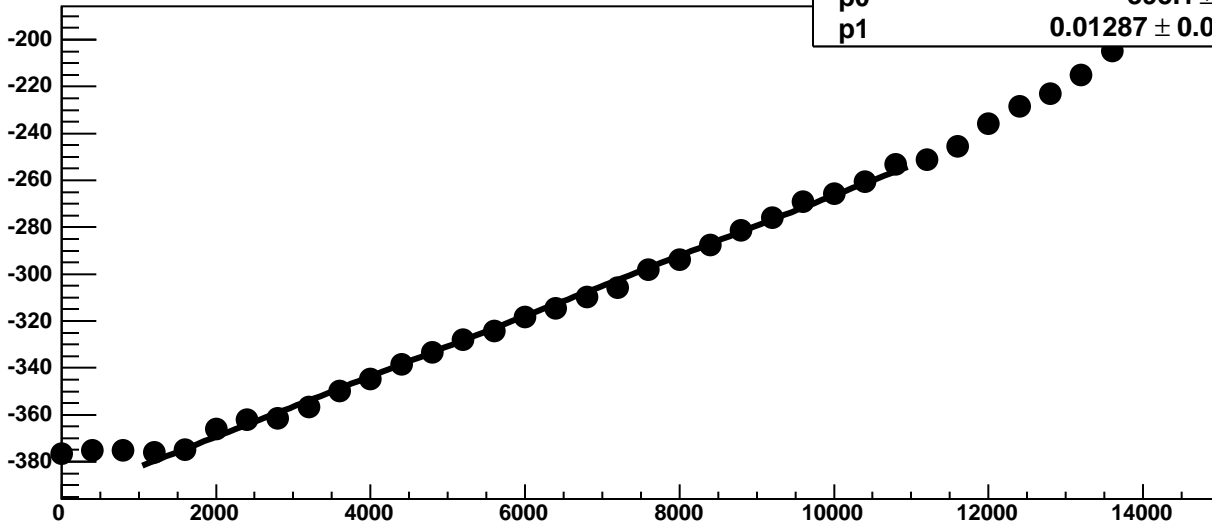
Chip 6, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

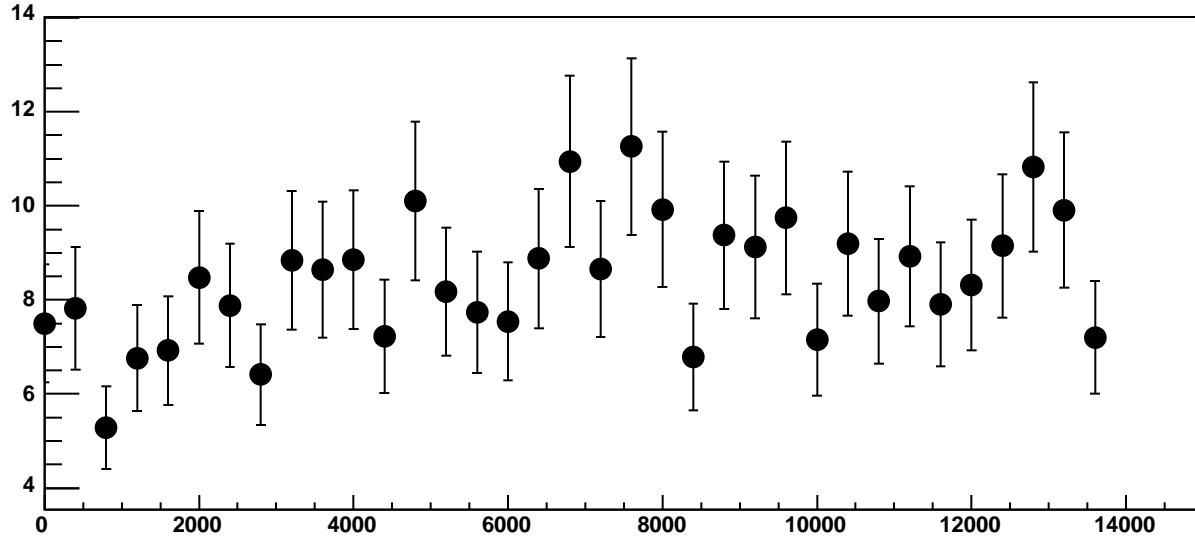


Chip 6, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

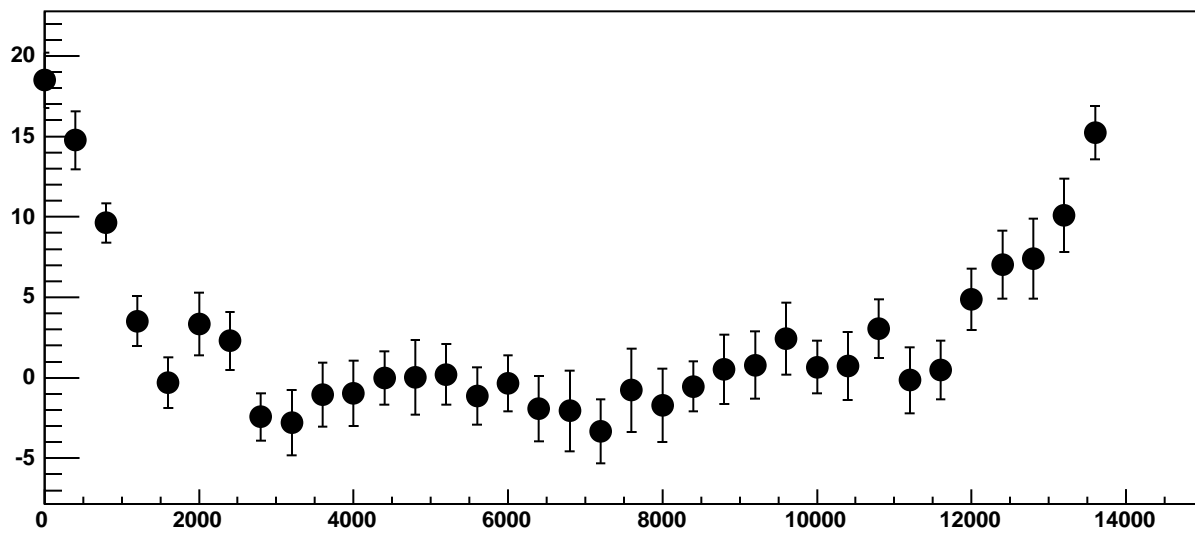


χ^2 / ndf 24.92 / 23
p0 -395.1 ± 0.8092
p1 0.01287 ± 0.0001259

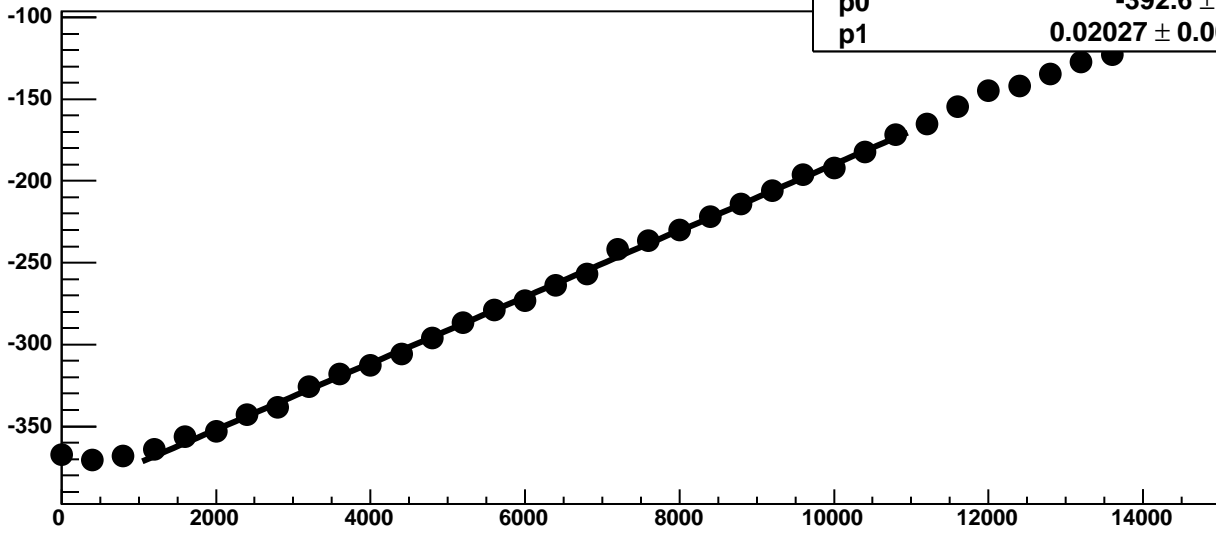
Chip 6, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

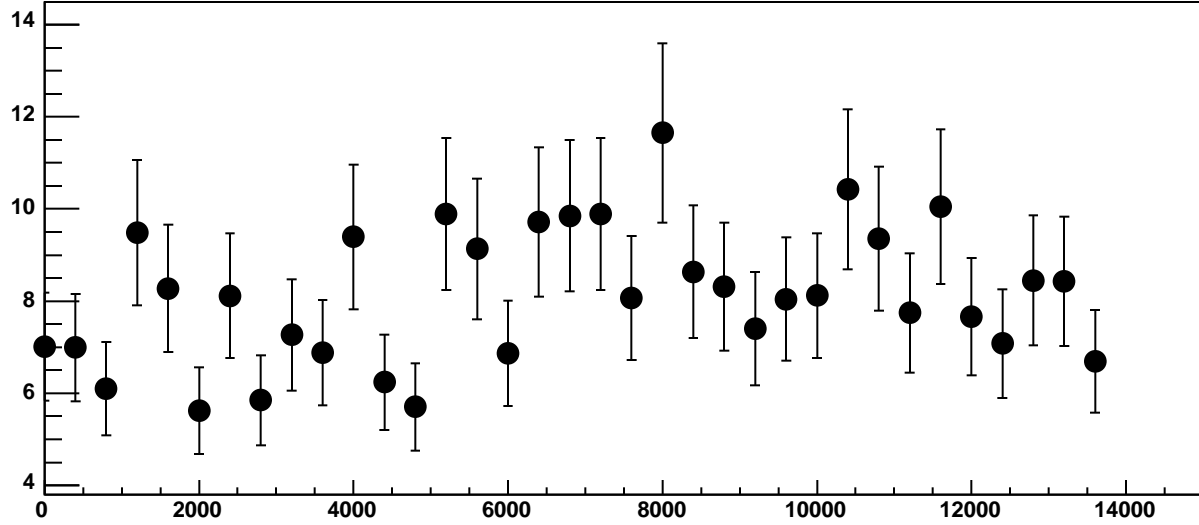


Chip 6, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC

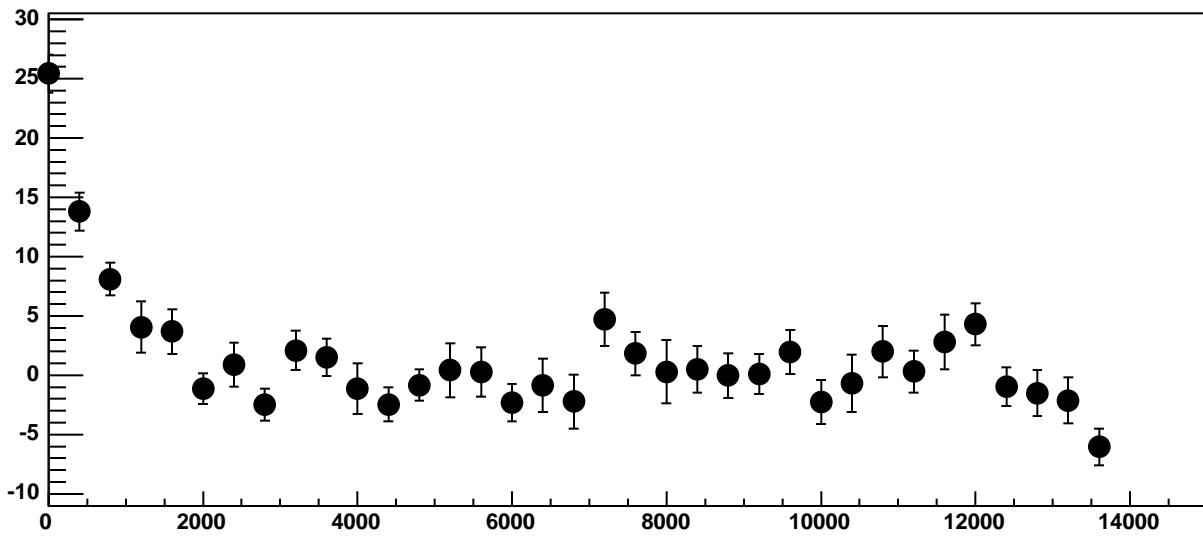


χ^2 / ndf 29.98 / 23
p0 -392.6 ± 0.7894
p1 0.02027 ± 0.0001277

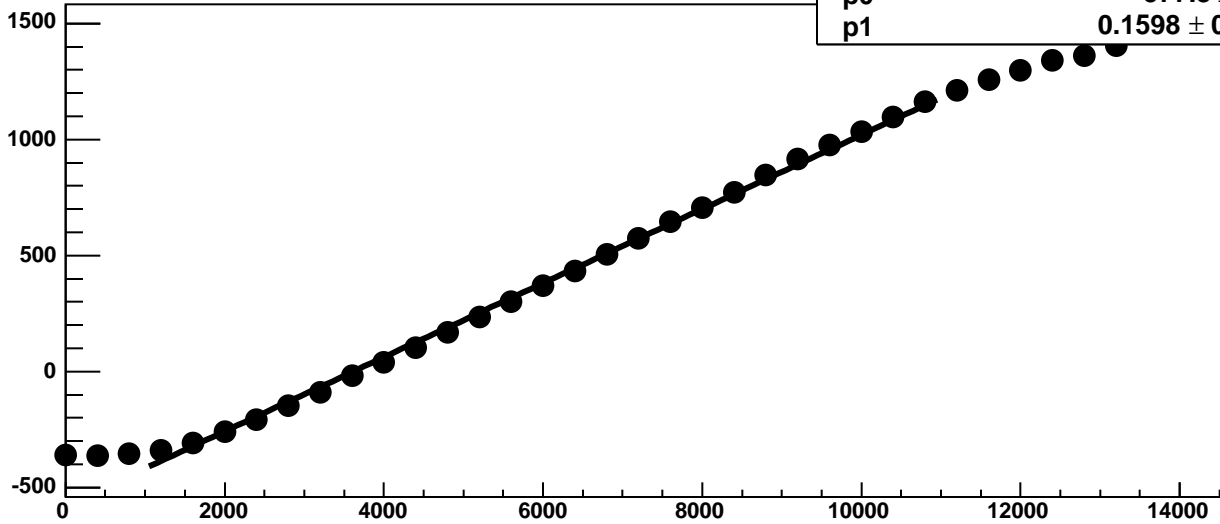
Chip 6, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



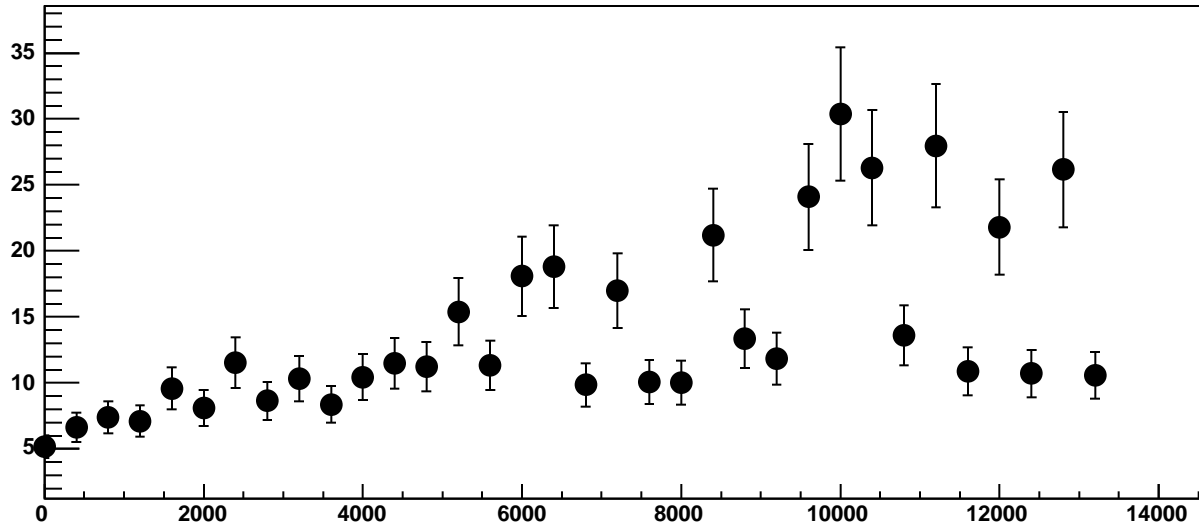
Chip 6, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC



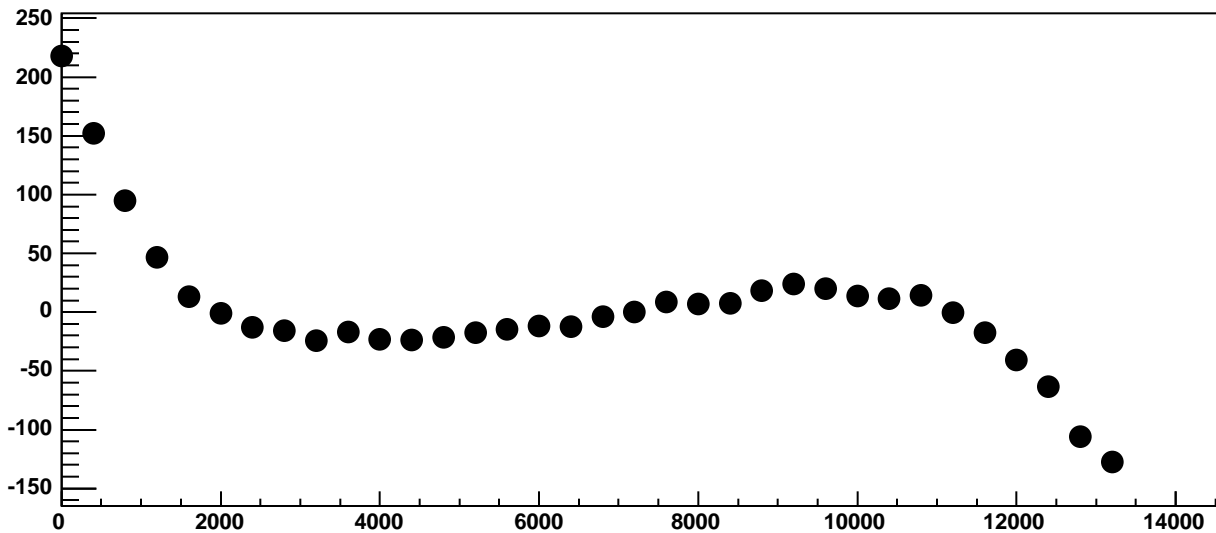
Chip 6, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC



Chip 6, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

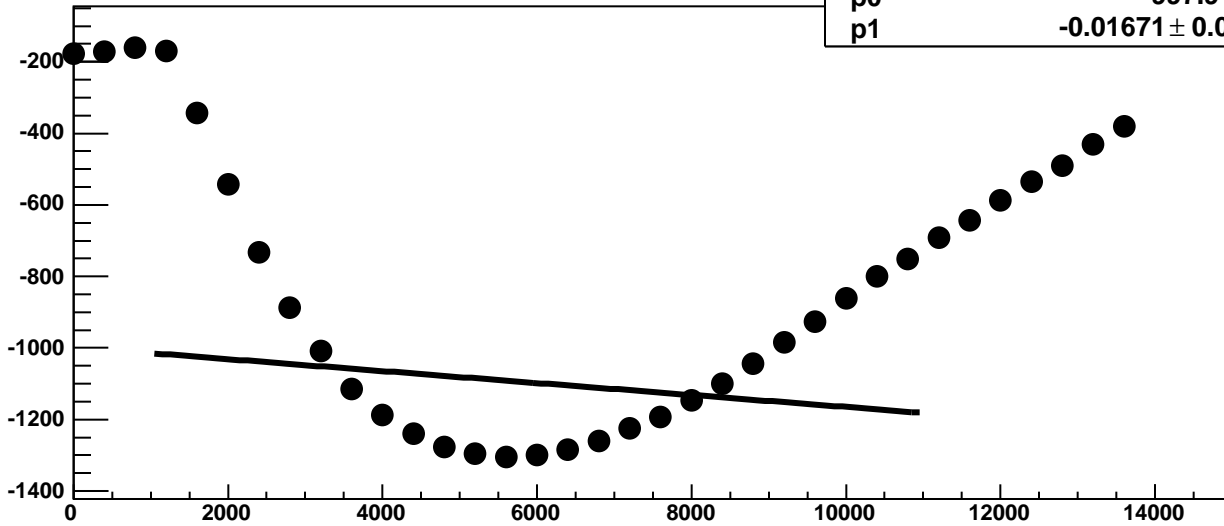


Chip 6, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

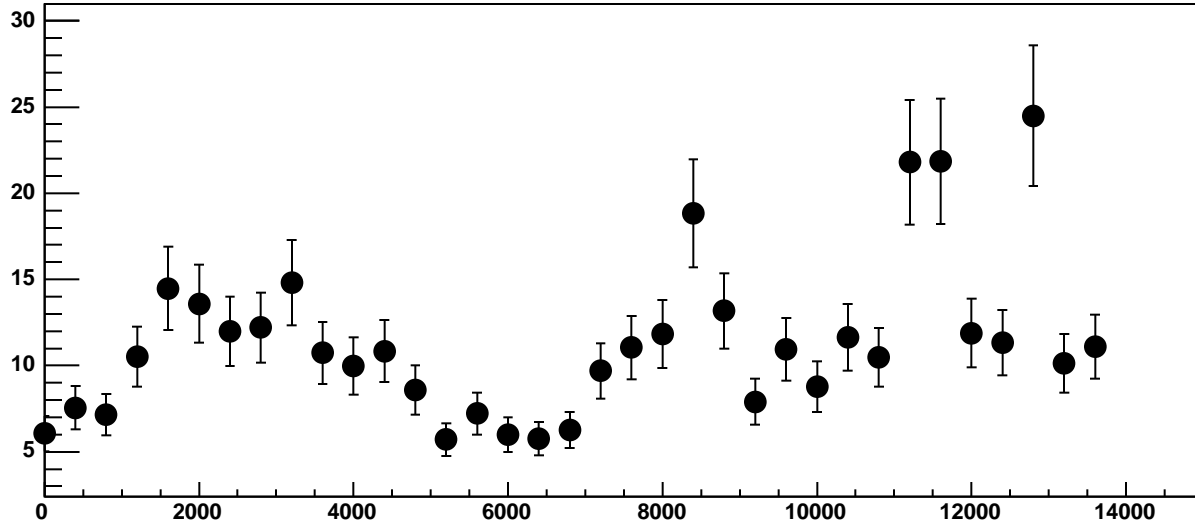


Chip 6, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

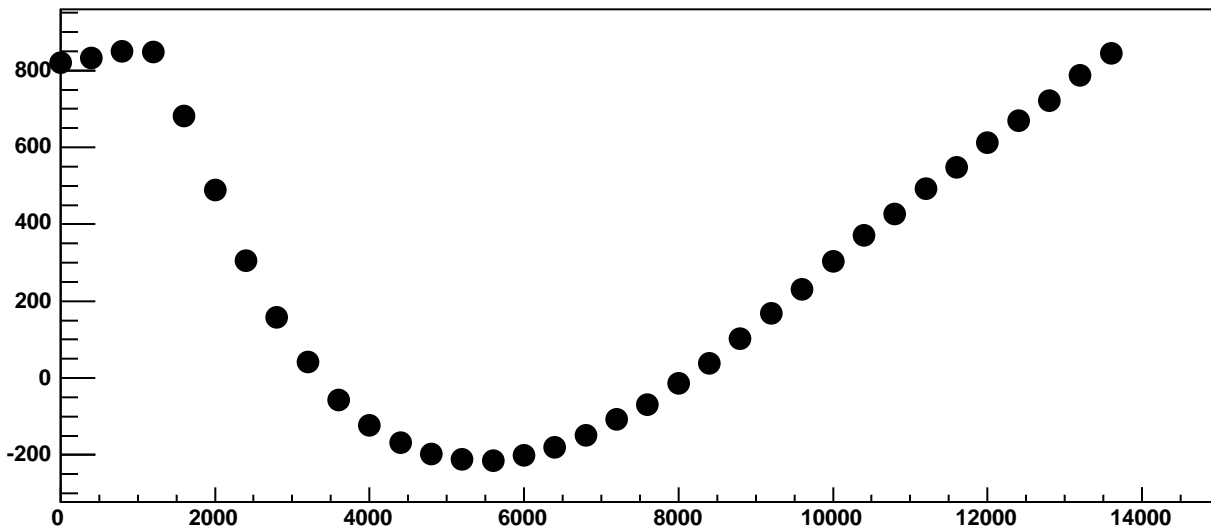
χ^2 / ndf 4.146e+05 / 23
p0 -997.9 ± 1.163
p1 -0.01671 ± 0.0001751



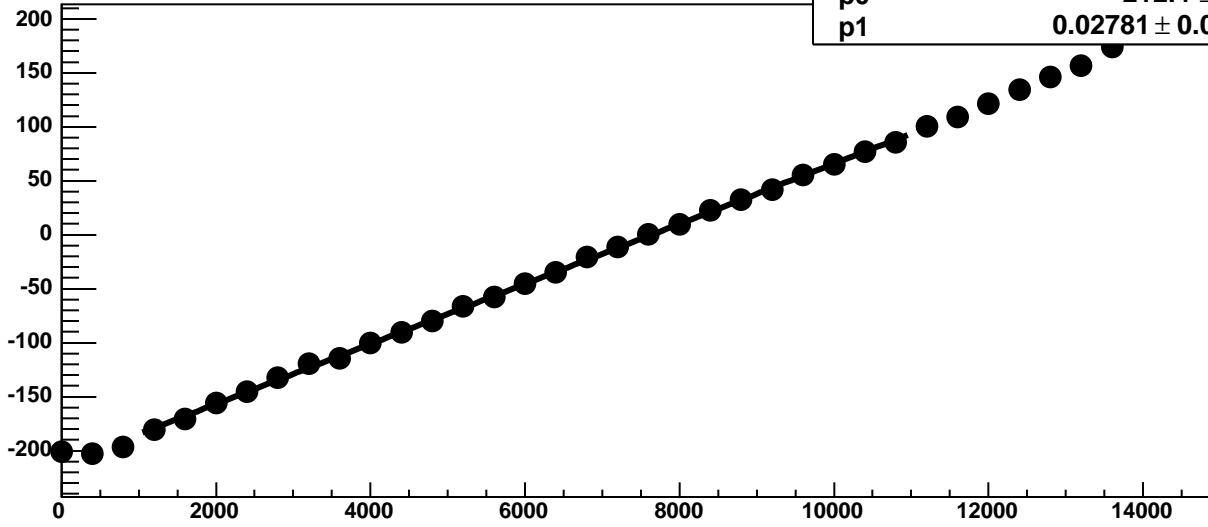
Chip 6, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

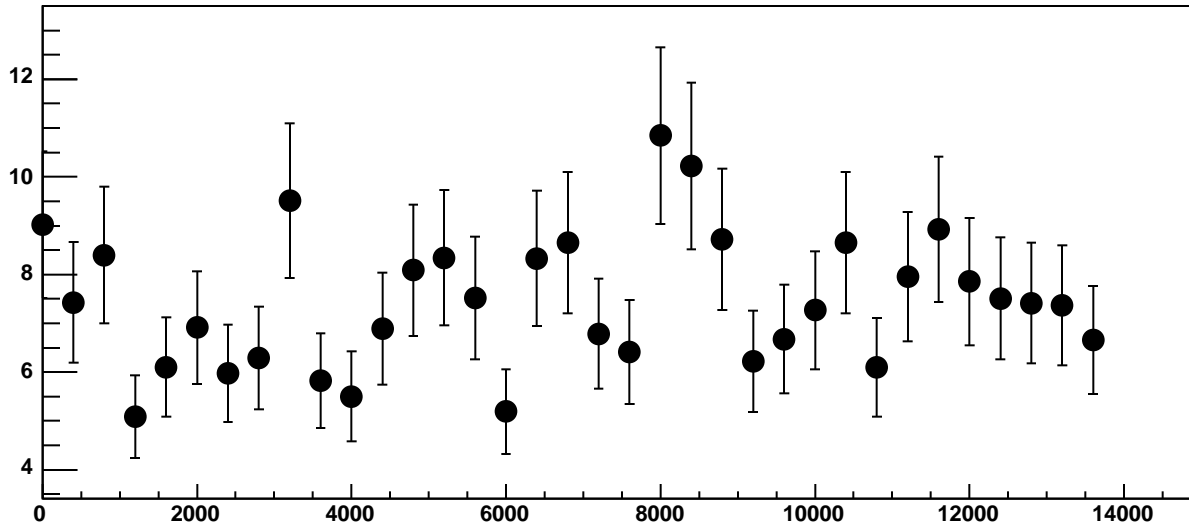


Chip 6, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC

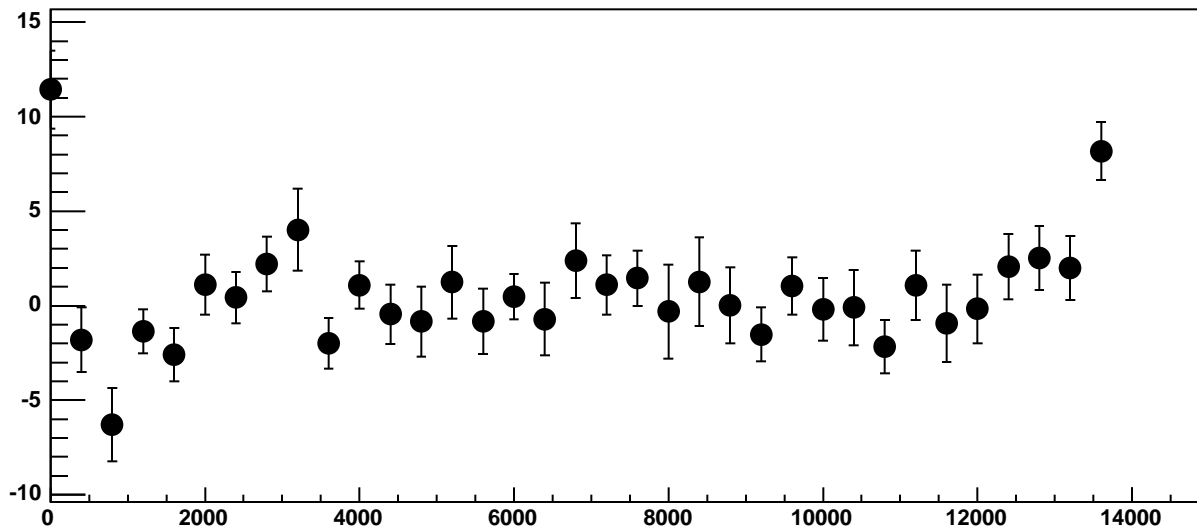


χ^2 / ndf 22.53 / 23
p0 -212.4 ± 0.6649
p1 0.02781 ± 0.0001049

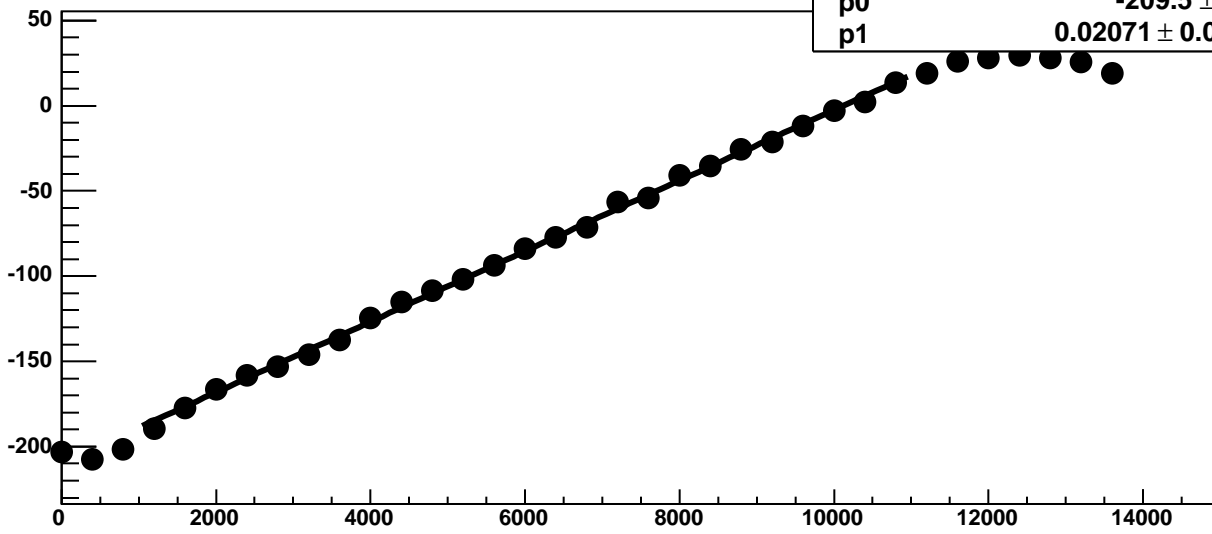
Chip 6, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC



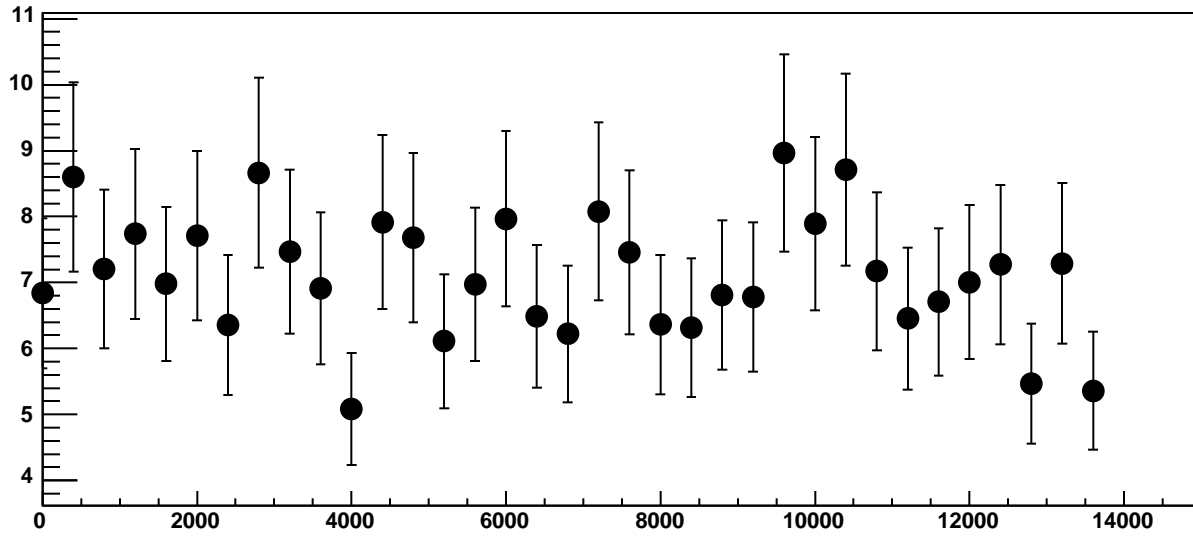
Chip 6, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC



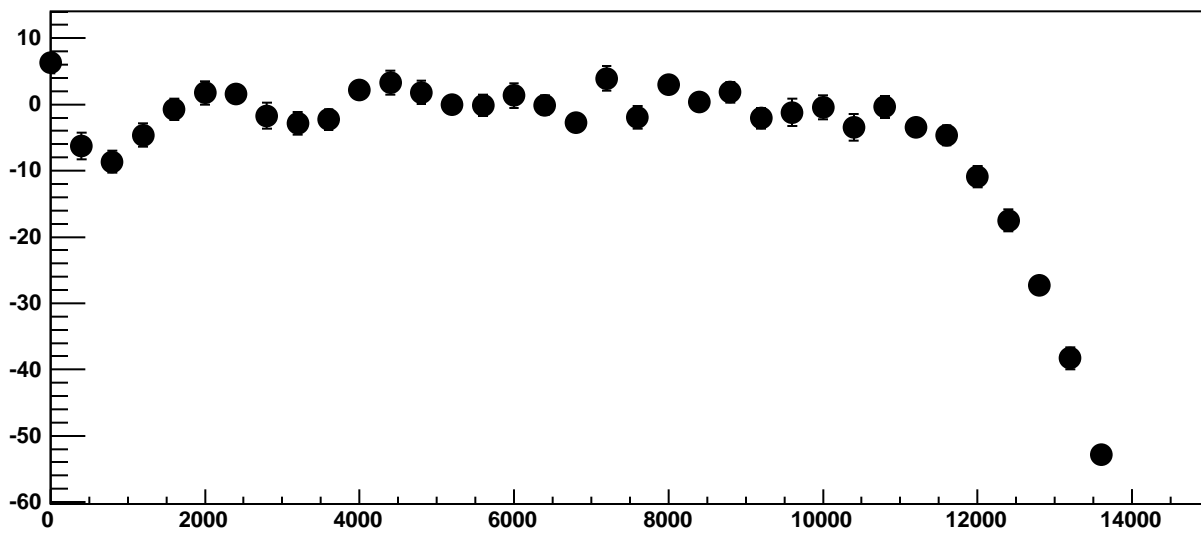
Chip 6, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



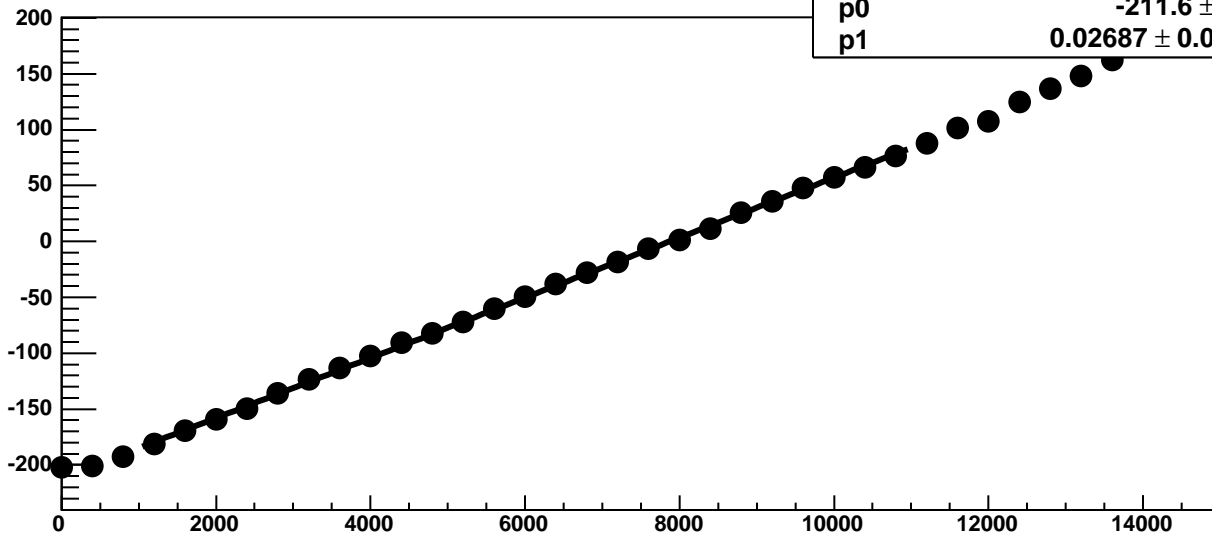
Chip 6, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

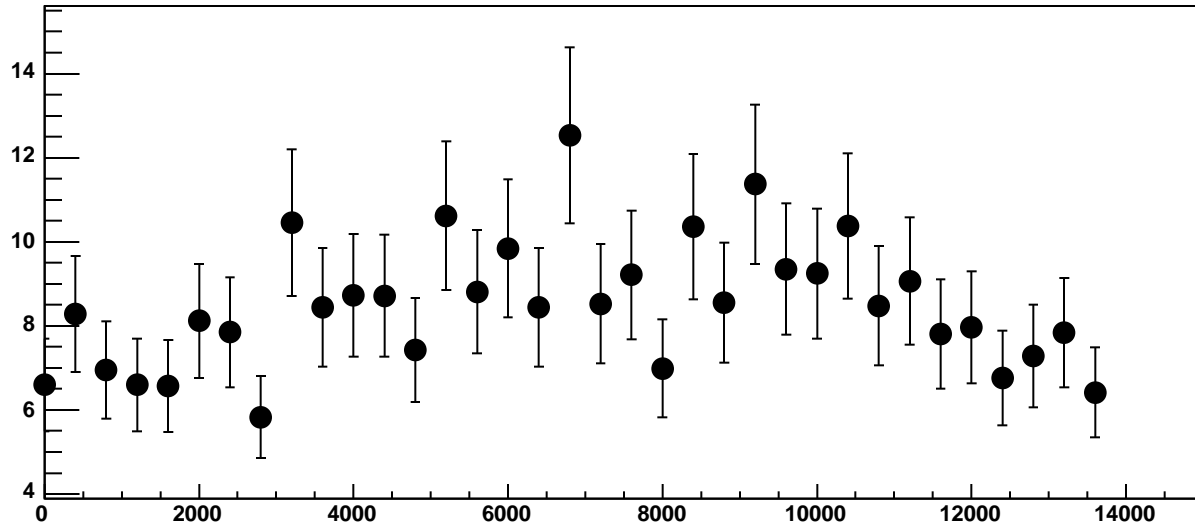


Chip 6, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

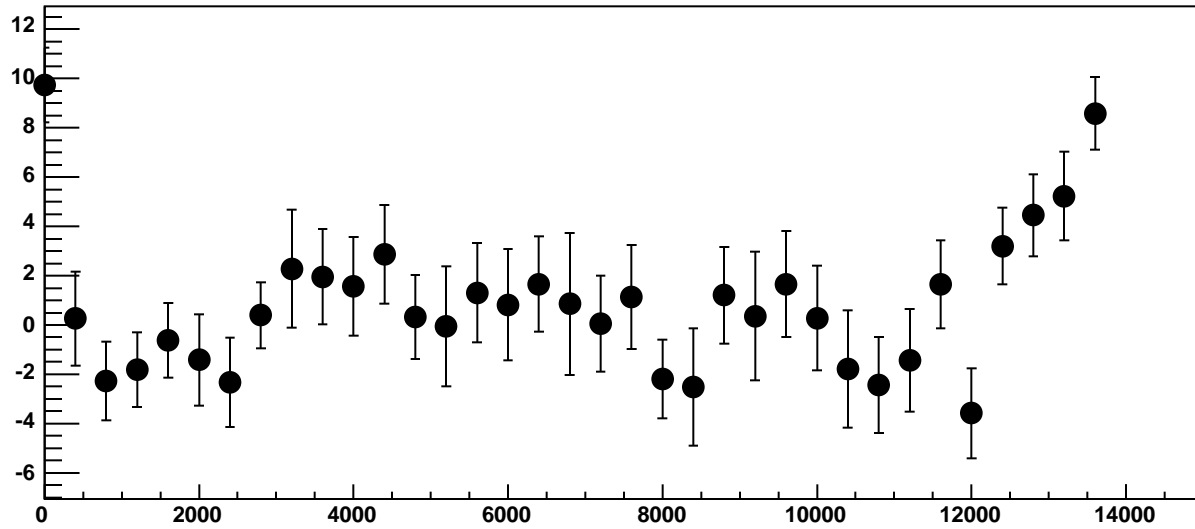


χ^2 / ndf 16.37 / 23
p0 -211.6 ± 0.8066
p1 0.02687 ± 0.0001302

Chip 6, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

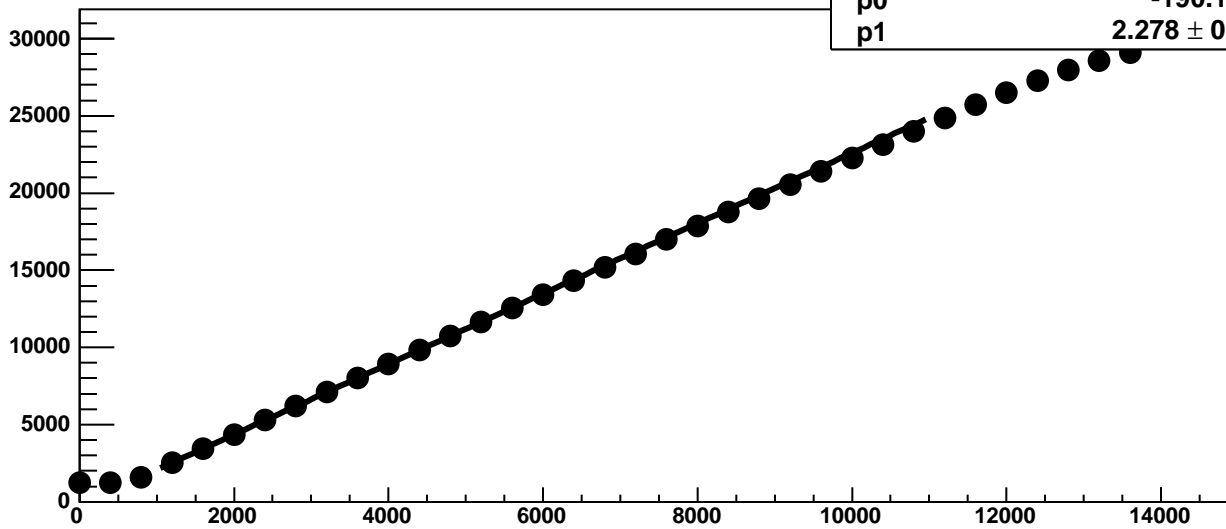


Chip 6, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

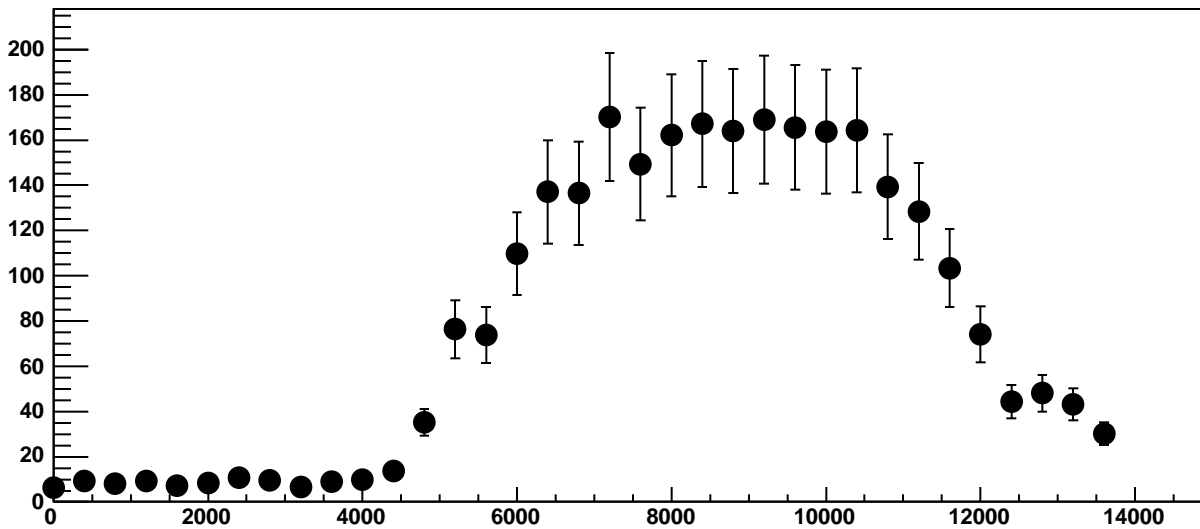


Chip 6, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

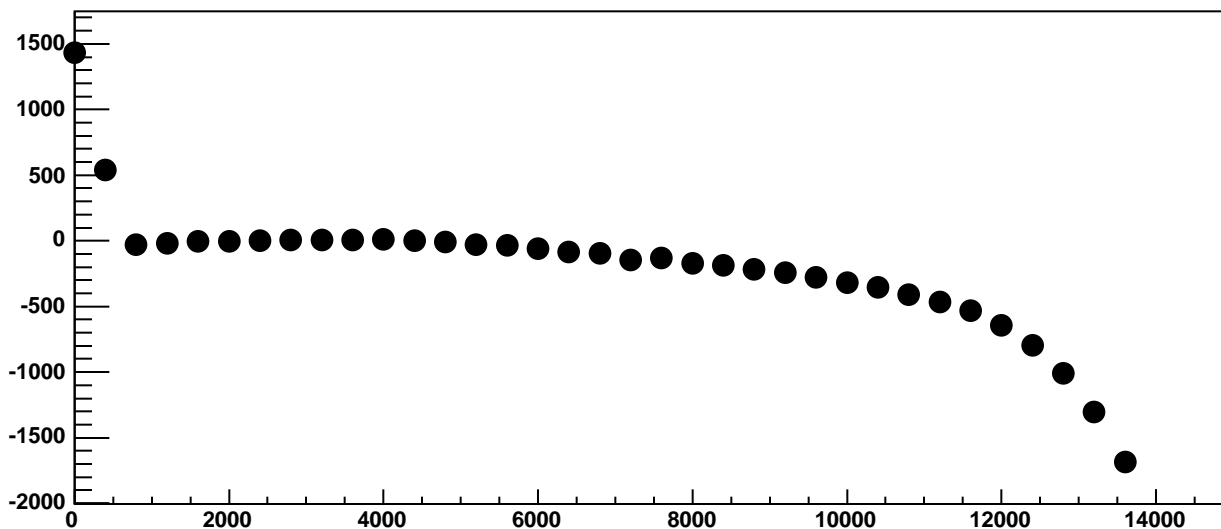
χ^2 / ndf 707.4 / 23
p0 -190.1 ± 1.842
p1 2.278 ± 0.000635



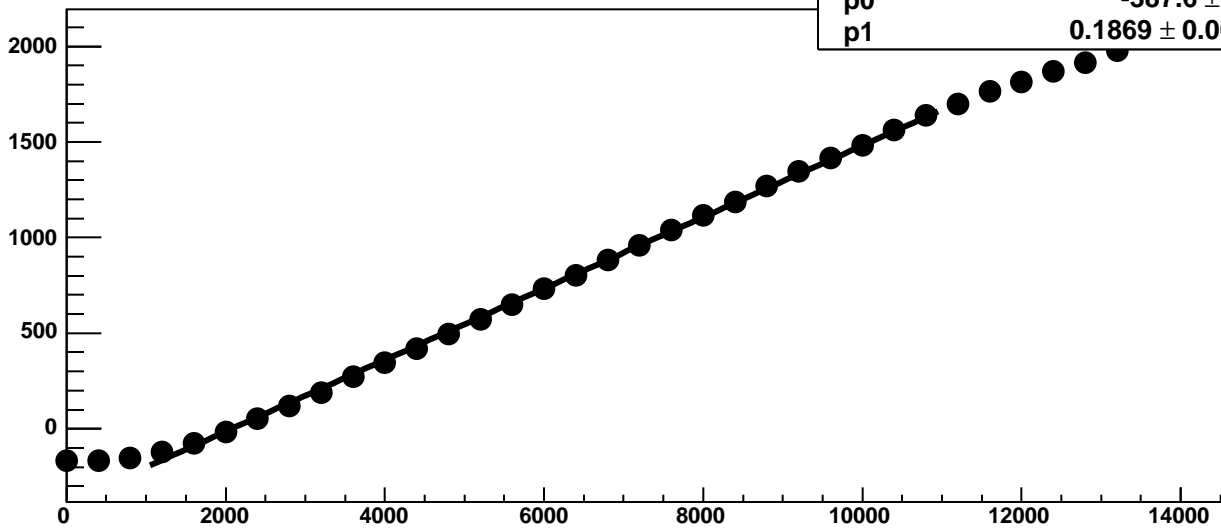
Chip 6, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC



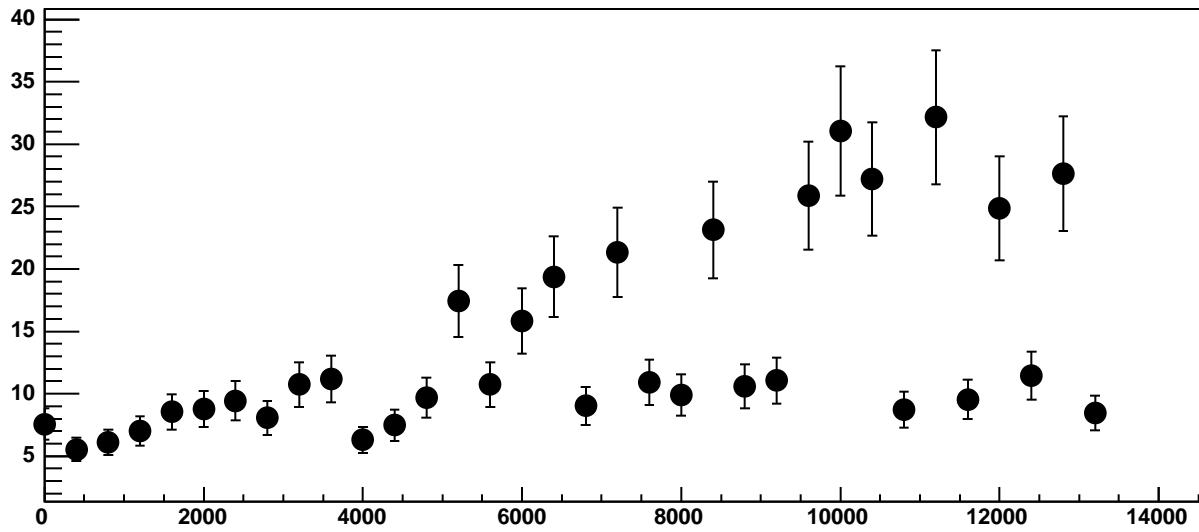
Chip 6, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC



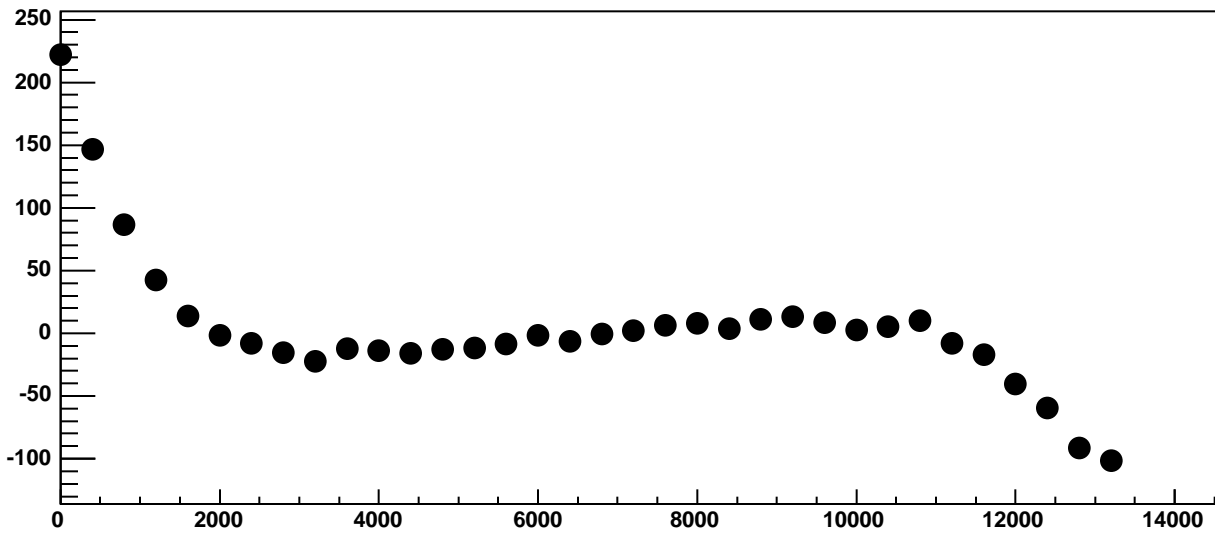
Chip 6, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



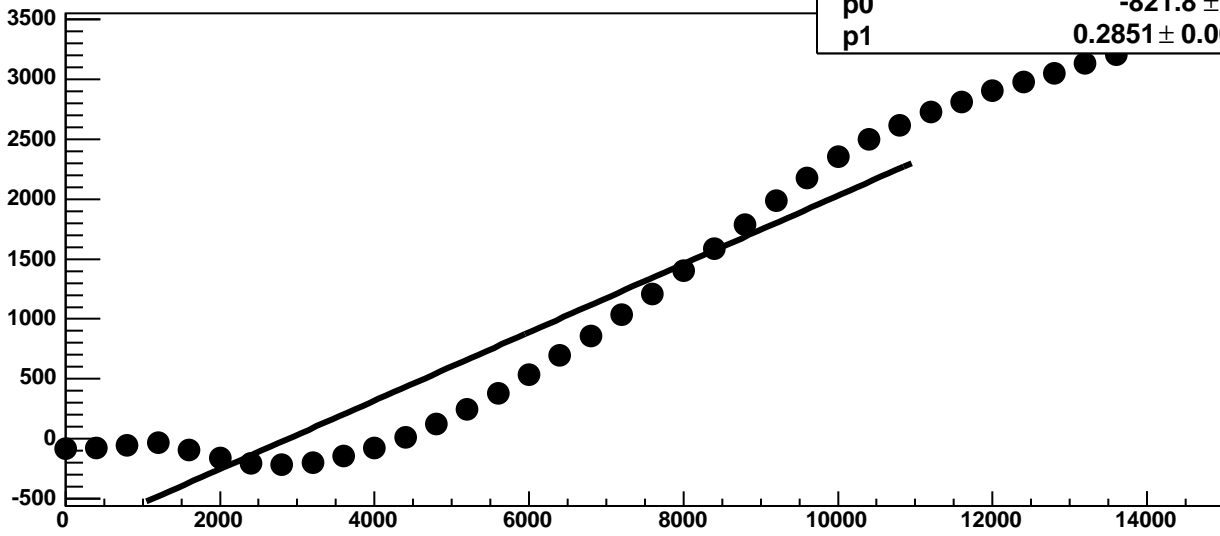
Chip 6, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



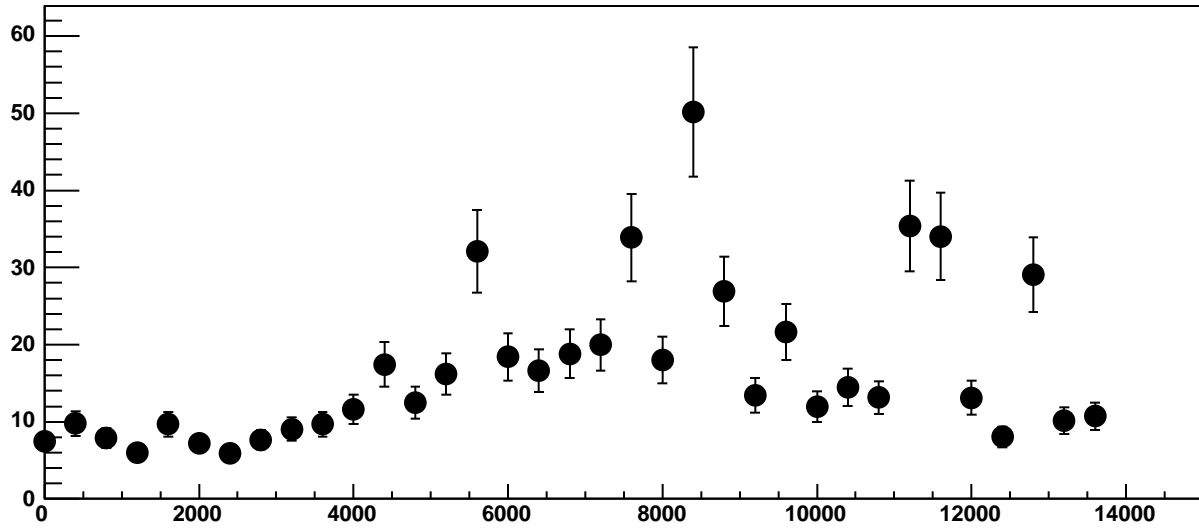
Chip 6, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



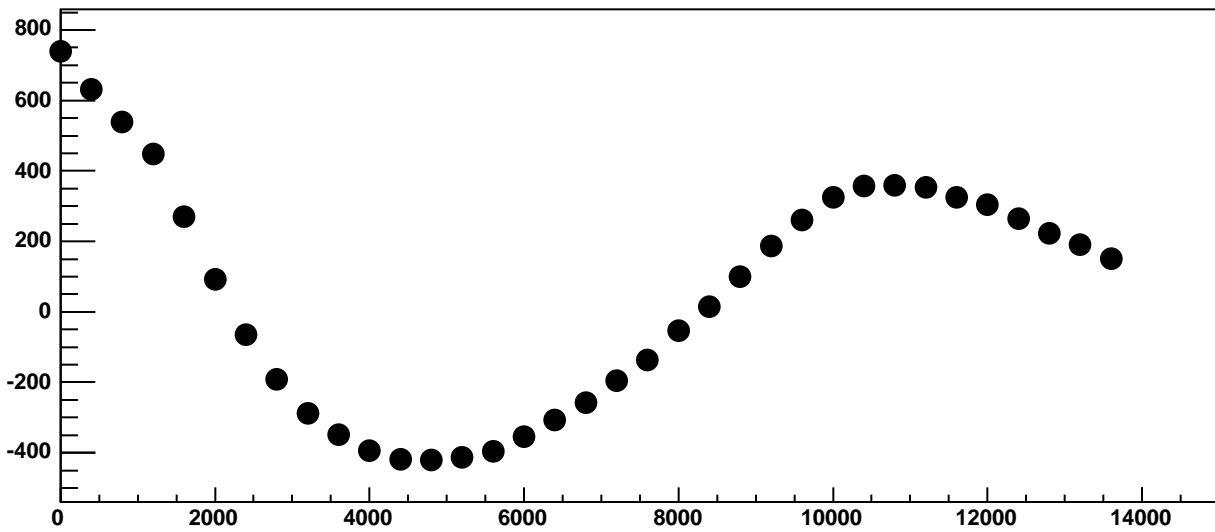
Chip 6, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



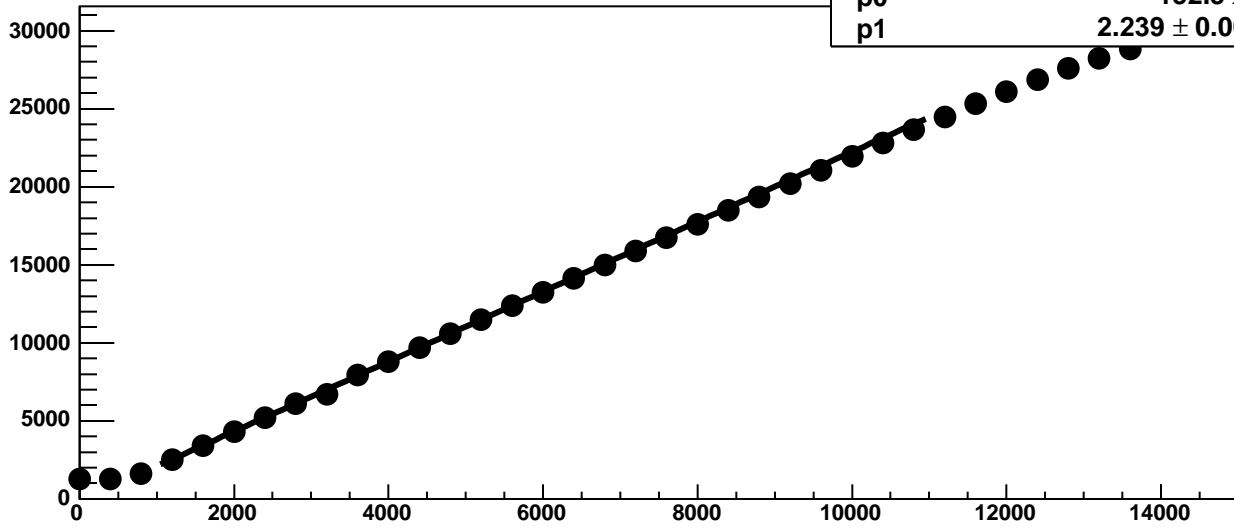
Chip 6, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

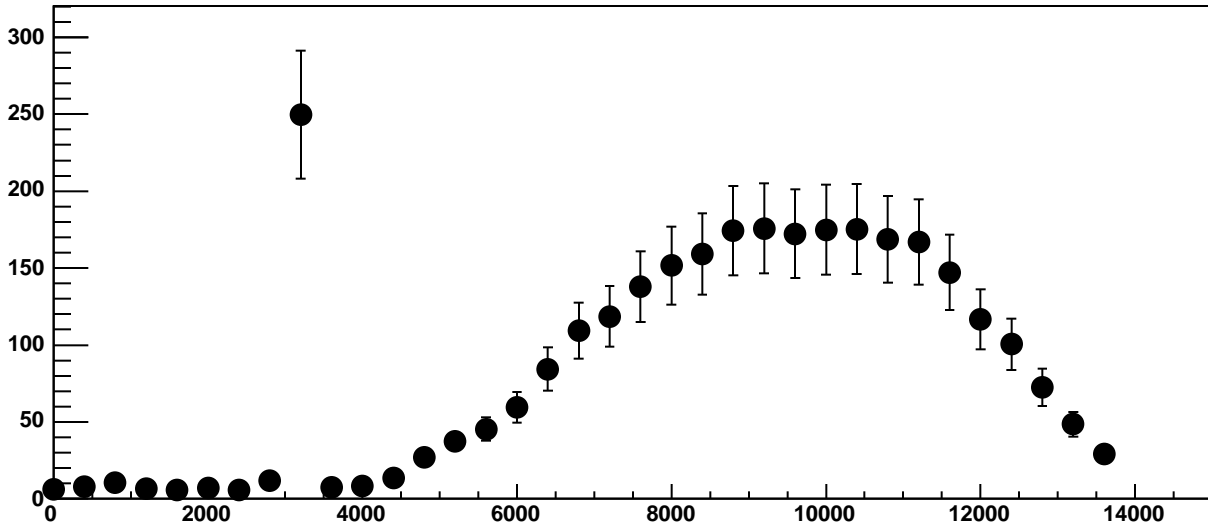


Chip 6, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC

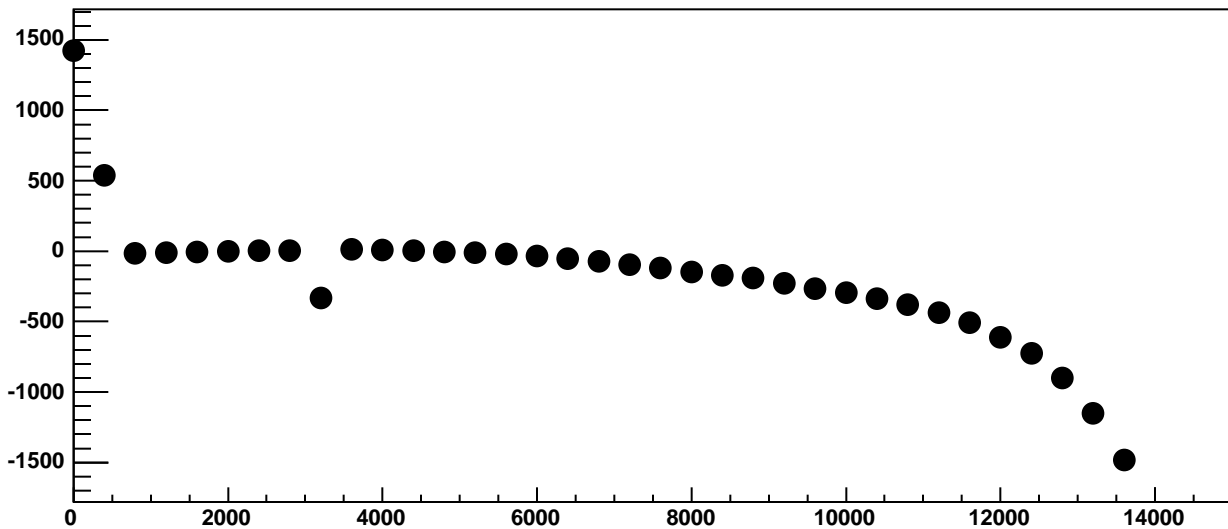


χ^2 / ndf 553.6 / 23
p0 -152.5 ± 1.454
p1 2.239 ± 0.0005394

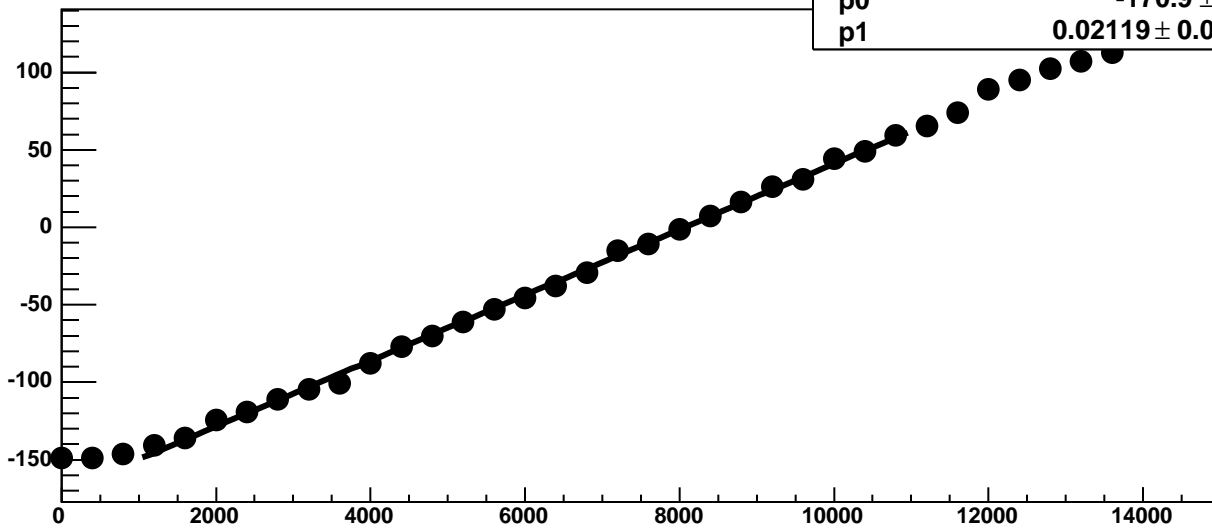
Chip 6, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



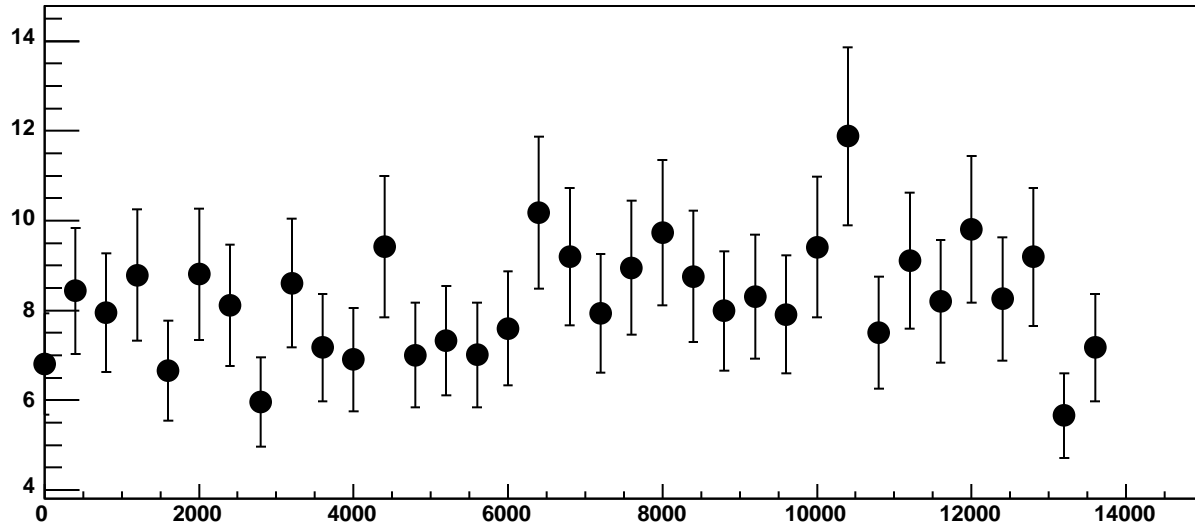
Chip 6, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC



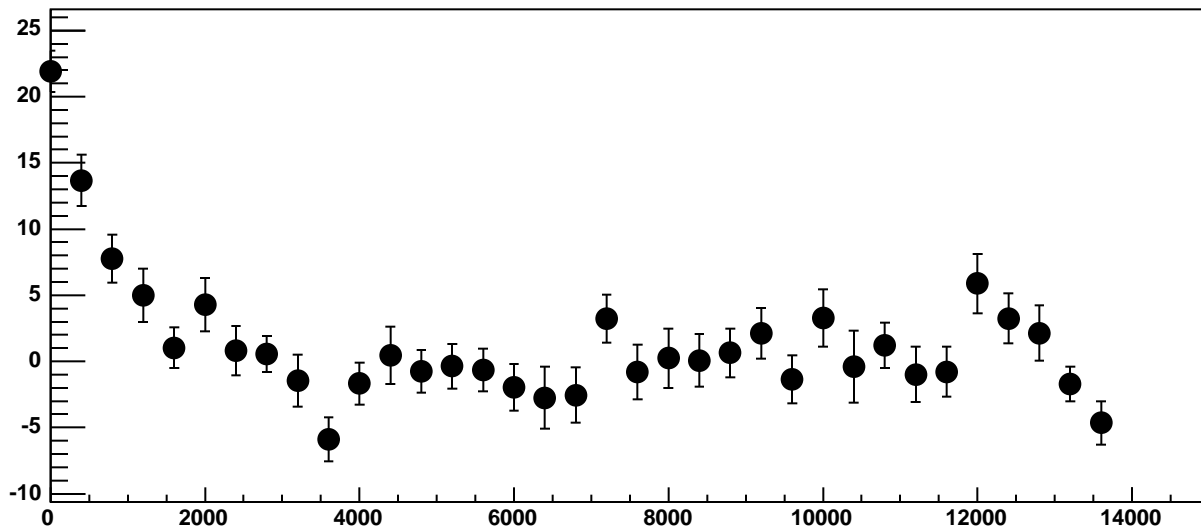
Chip 6, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



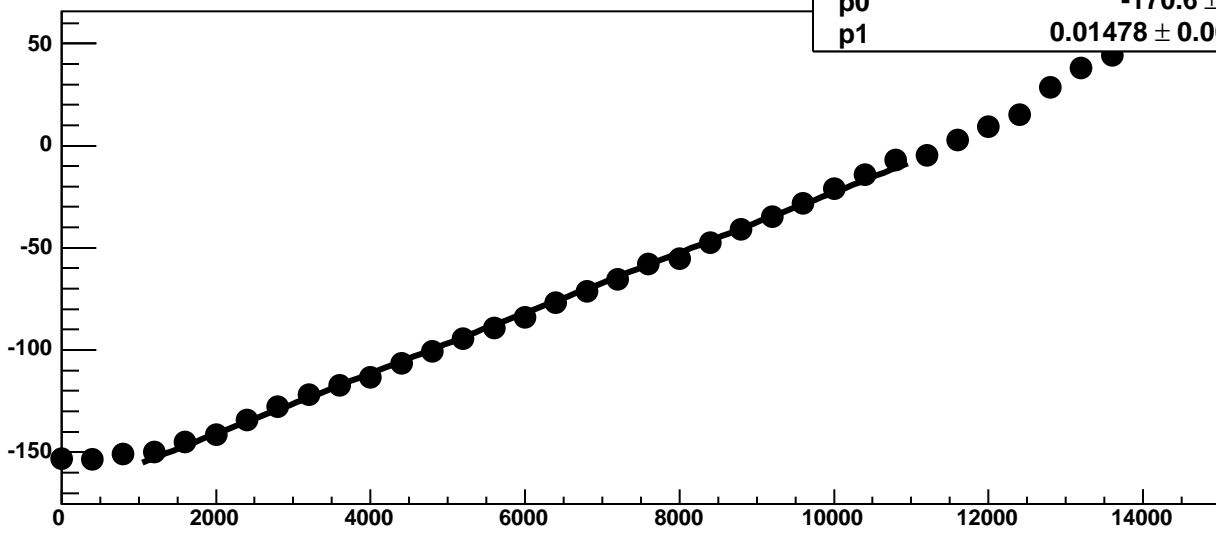
Chip 6, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

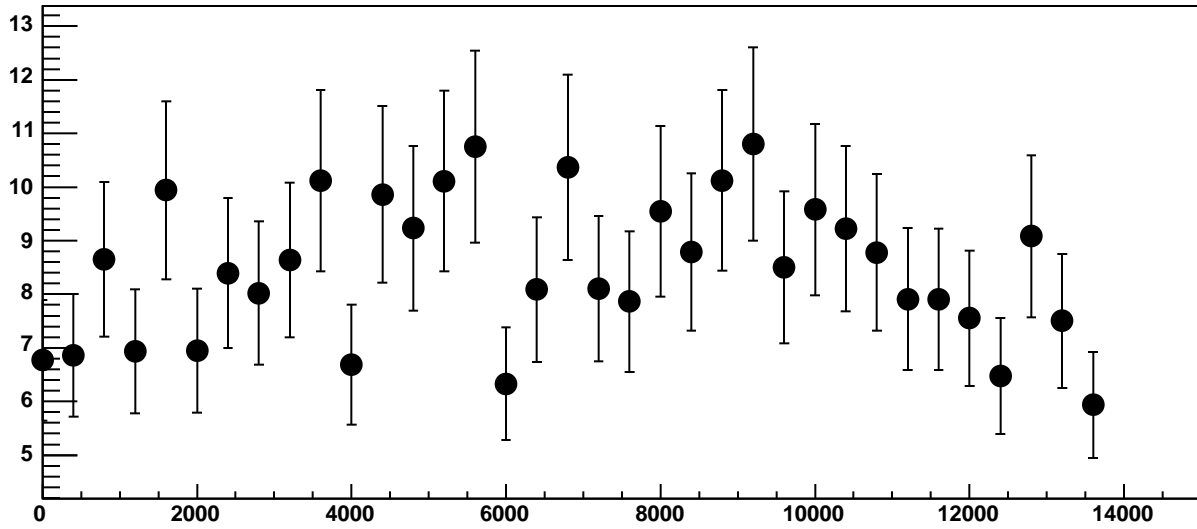


Chip 6, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

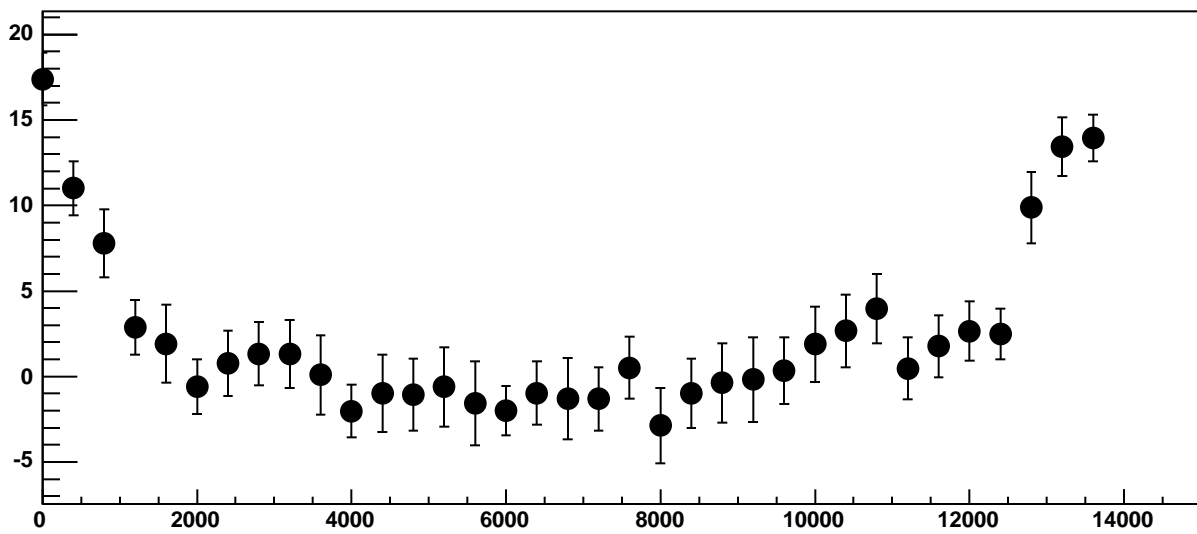


χ^2 / ndf 19.15 / 23
p0 -170.6 ± 0.8681
p1 0.01478 ± 0.0001358

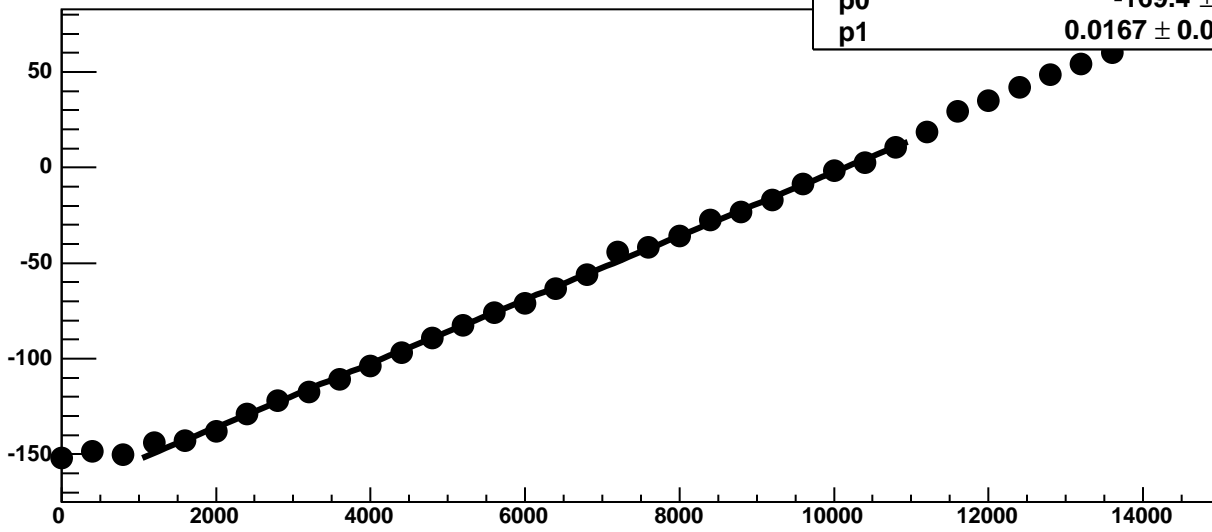
Chip 6, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



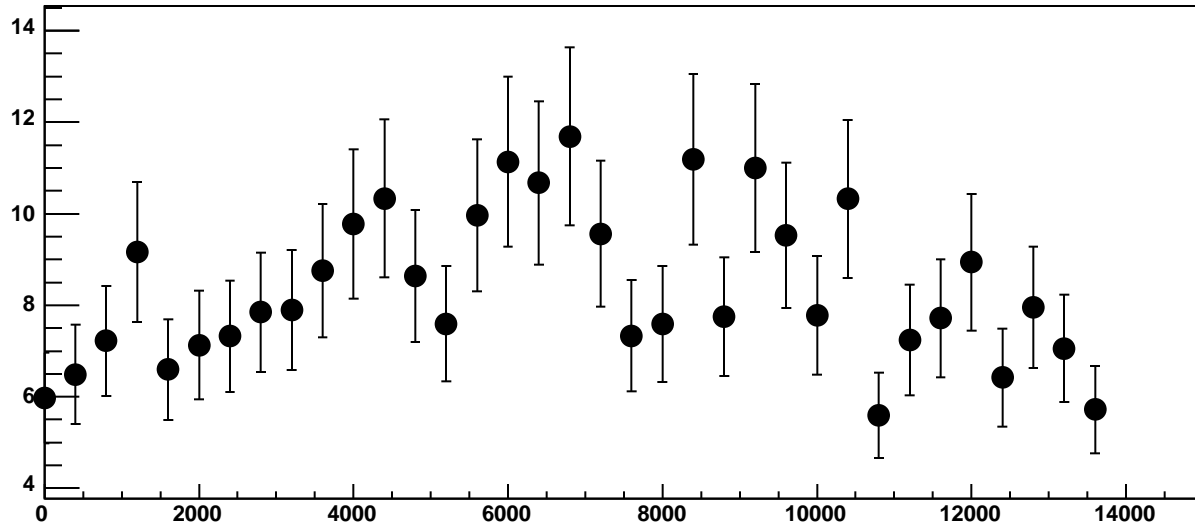
Chip 6, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



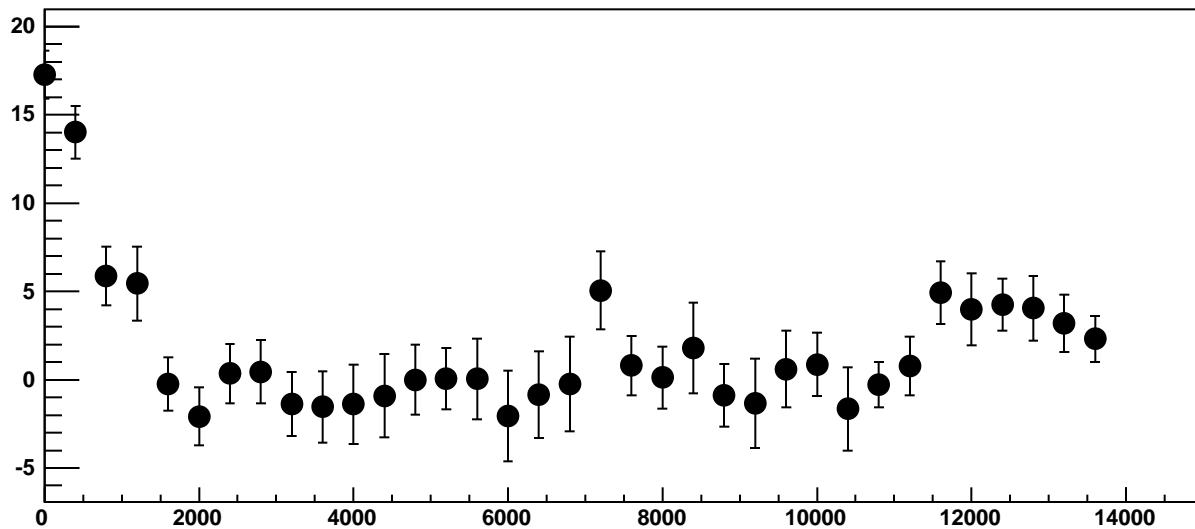
Chip 6, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC



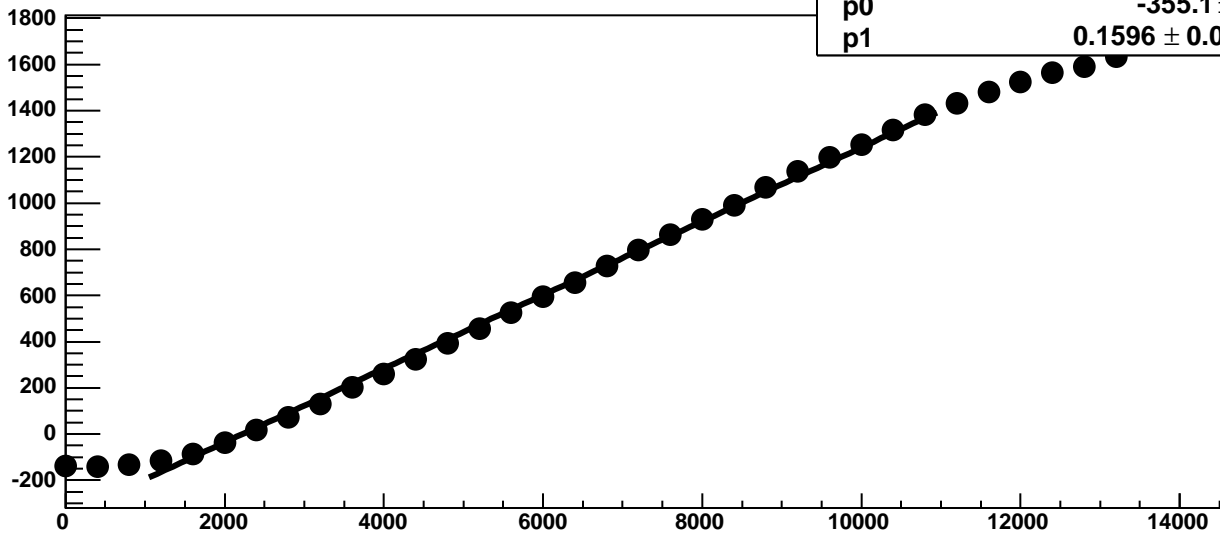
Chip 6, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



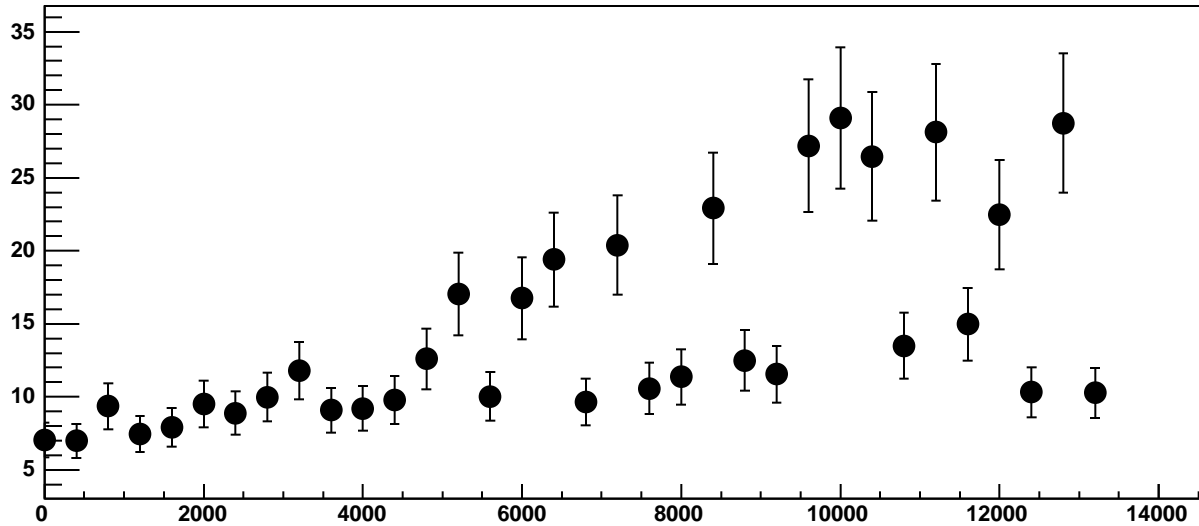
Chip 6, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC



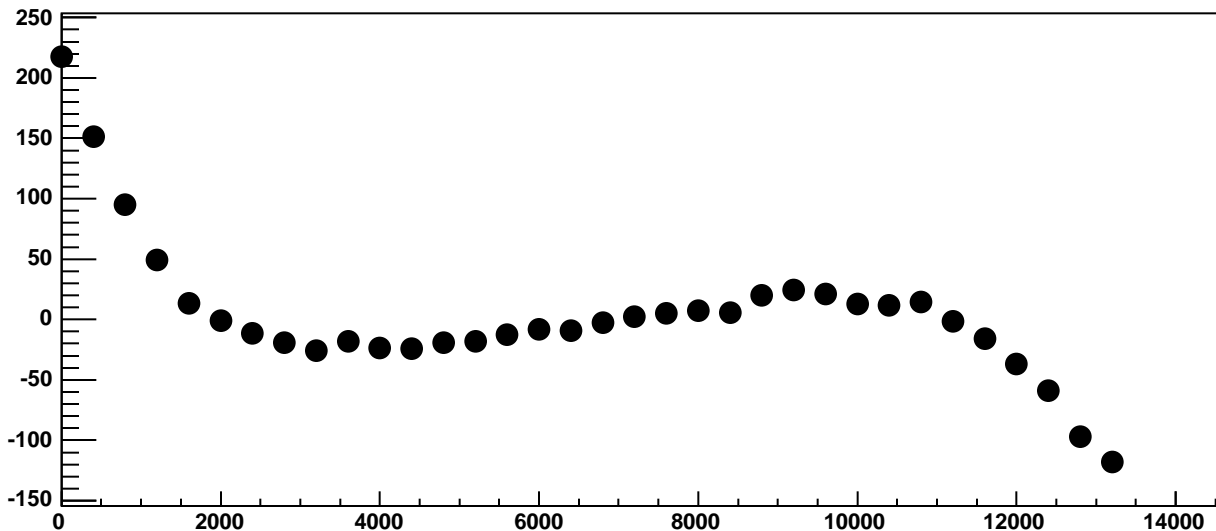
Chip 6, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



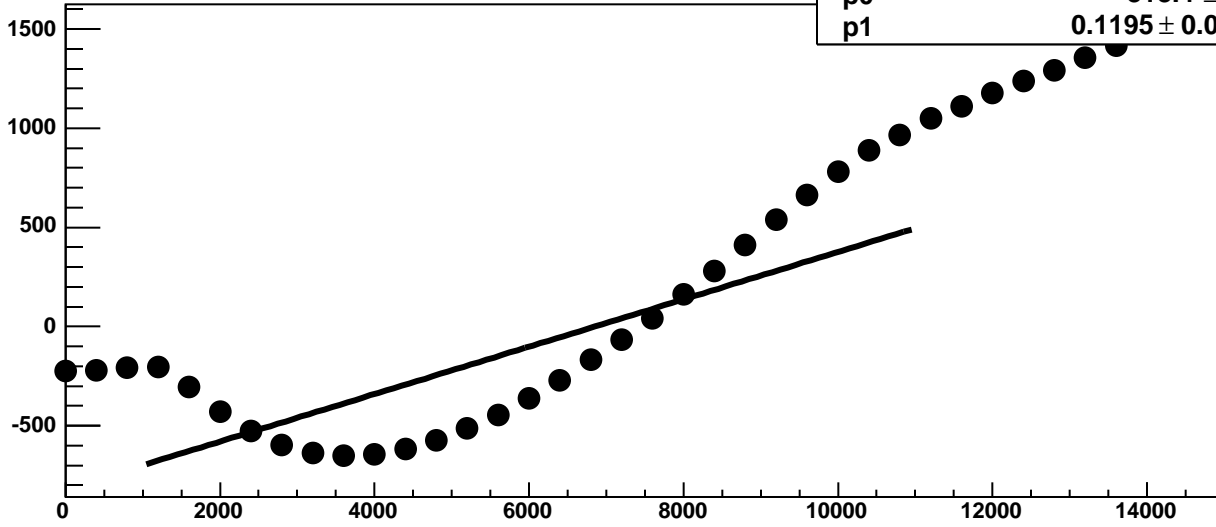
Chip 6, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



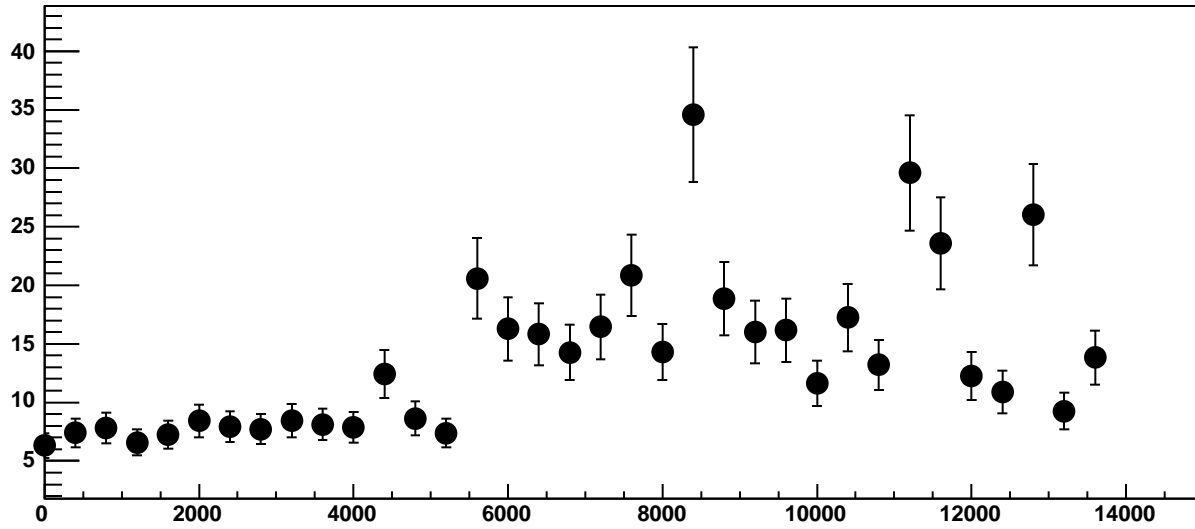
Chip 6, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



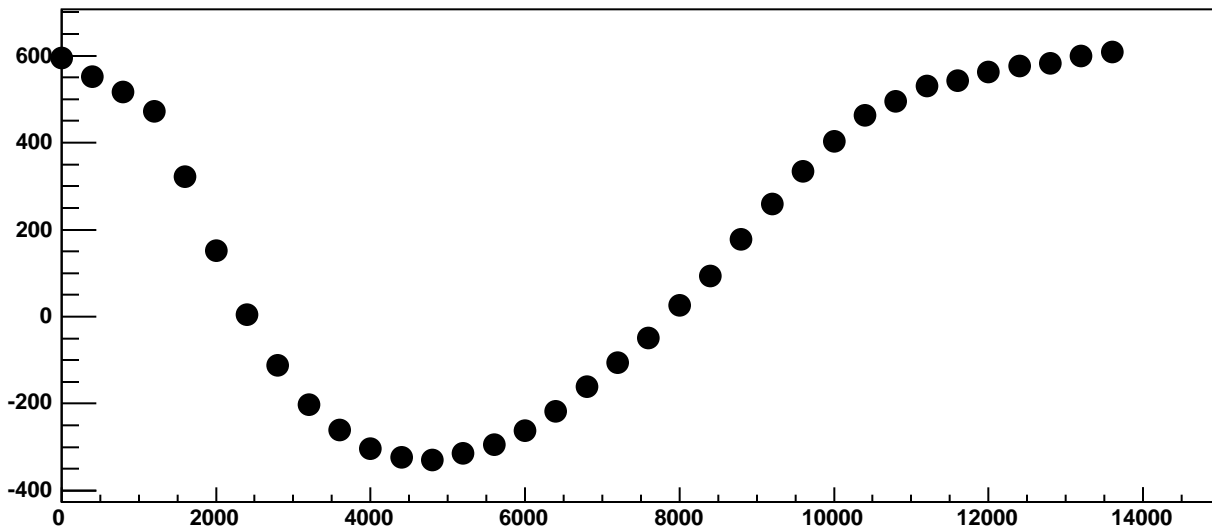
Chip 6, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



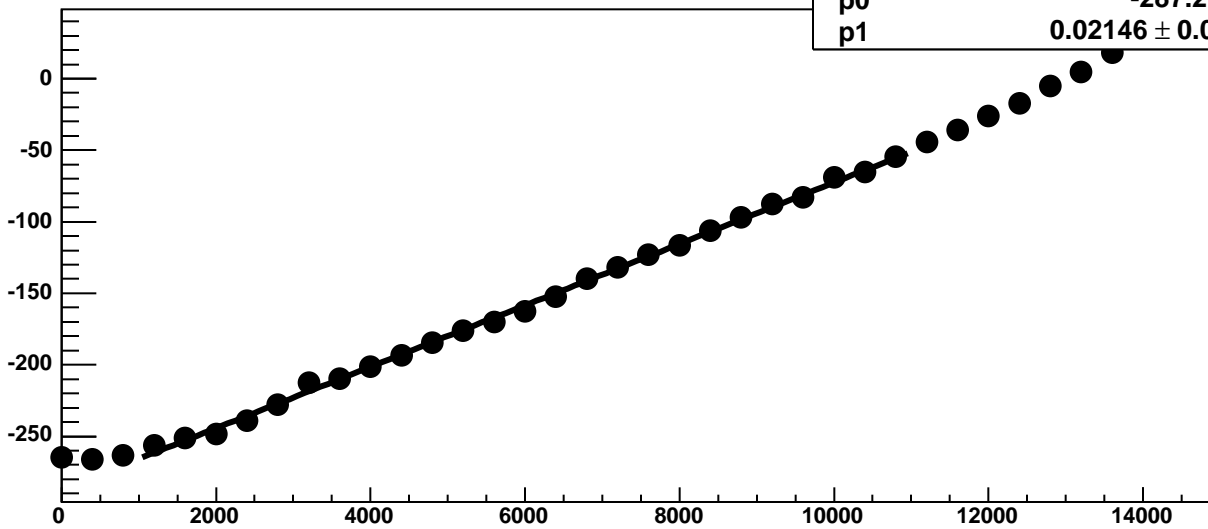
Chip 6, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

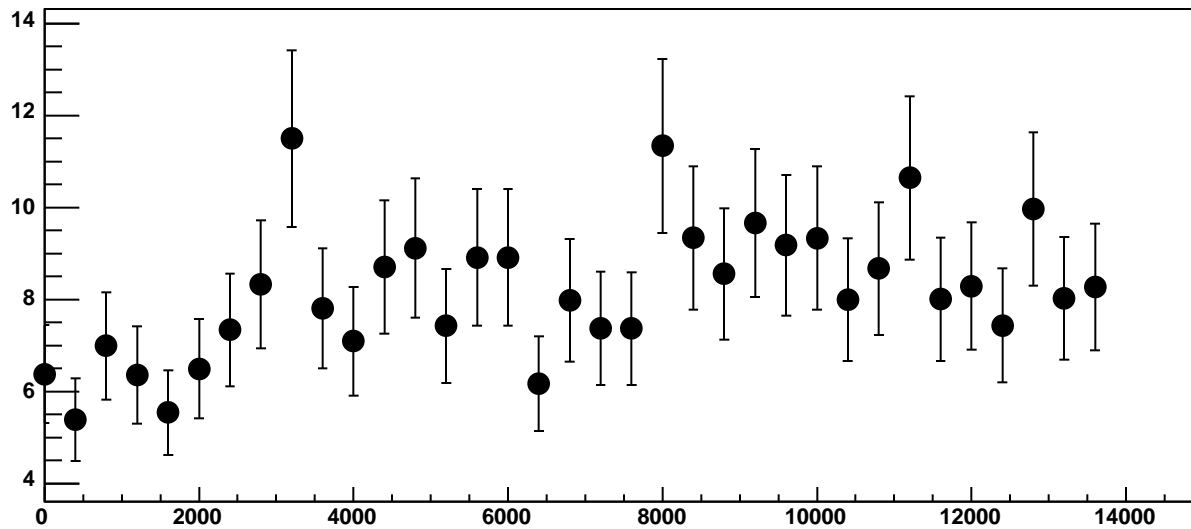


Chip 6, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

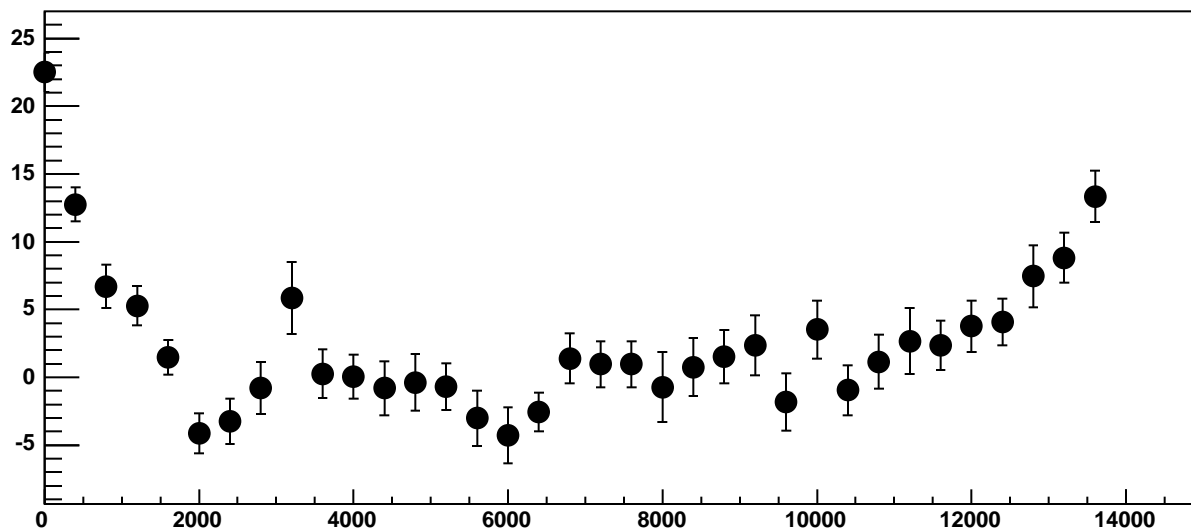


χ^2 / ndf 48.49 / 23
p0 -287.2 ± 0.762
p1 0.02146 ± 0.0001226

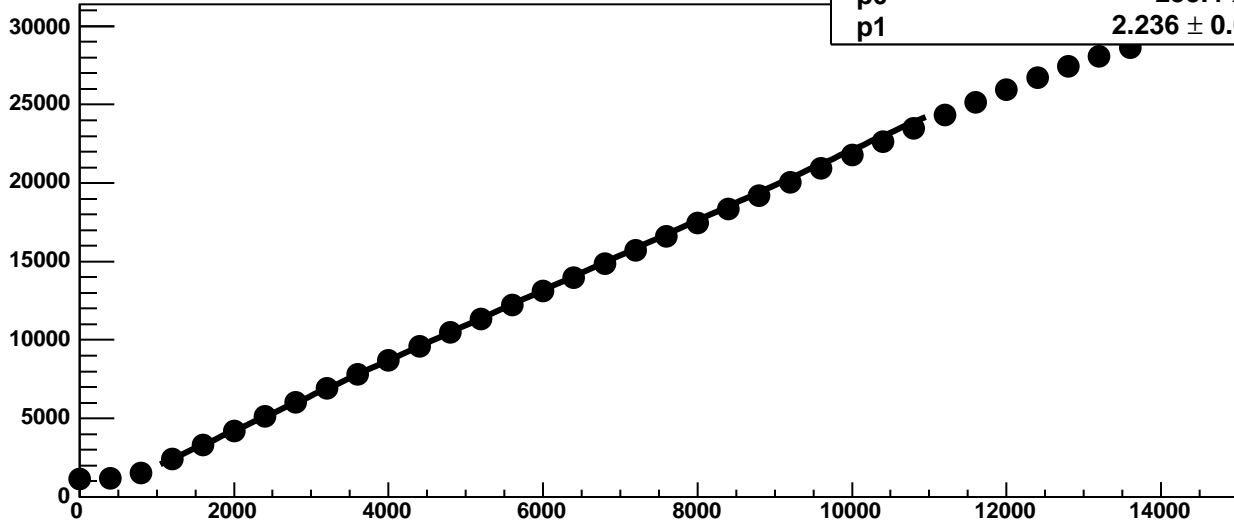
Chip 6, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



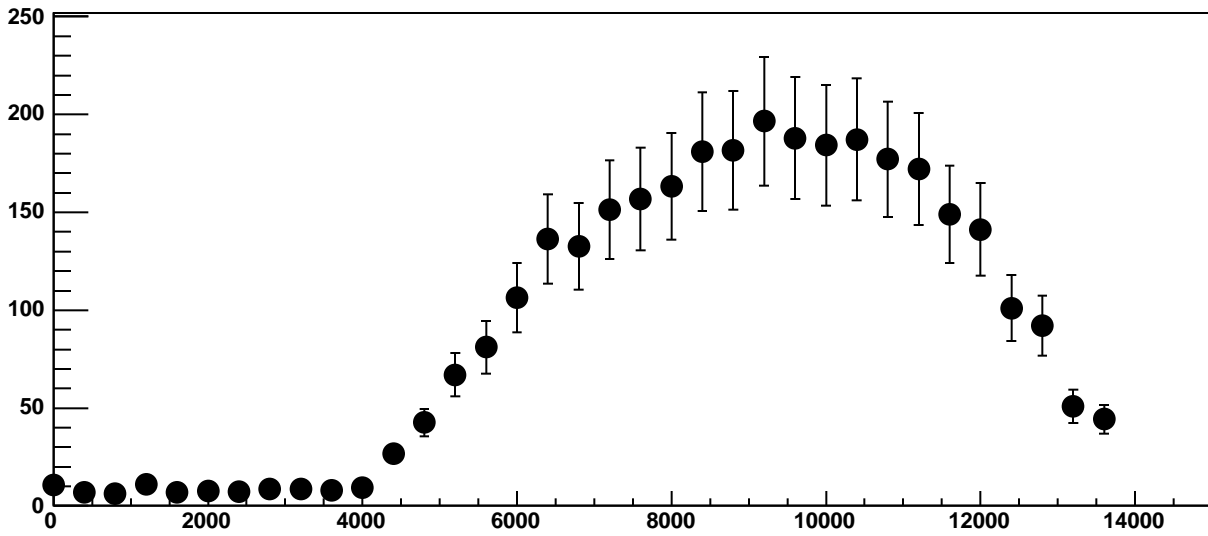
Chip 6, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



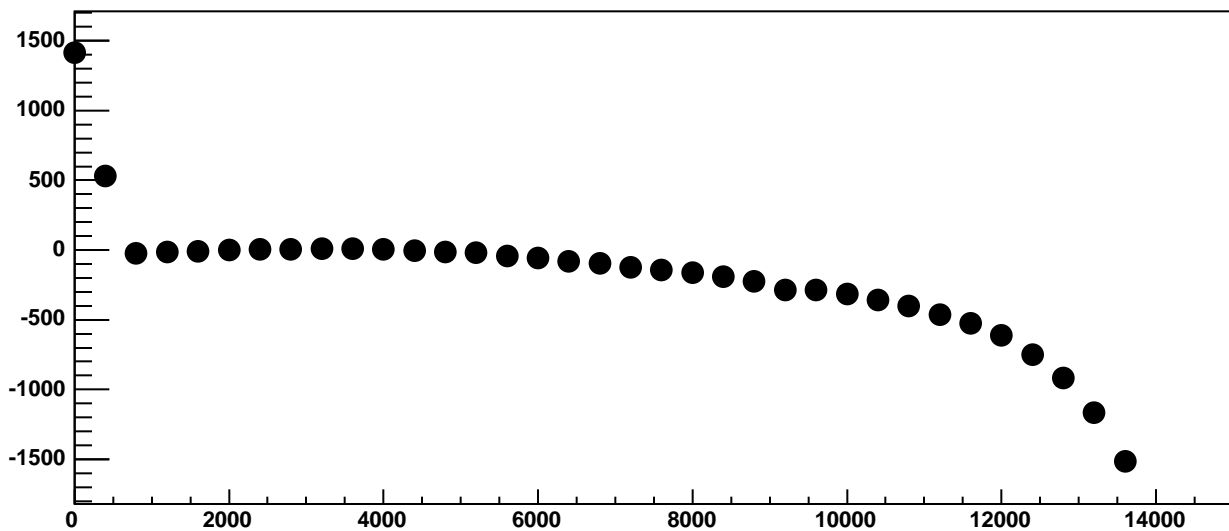
Chip 6, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC



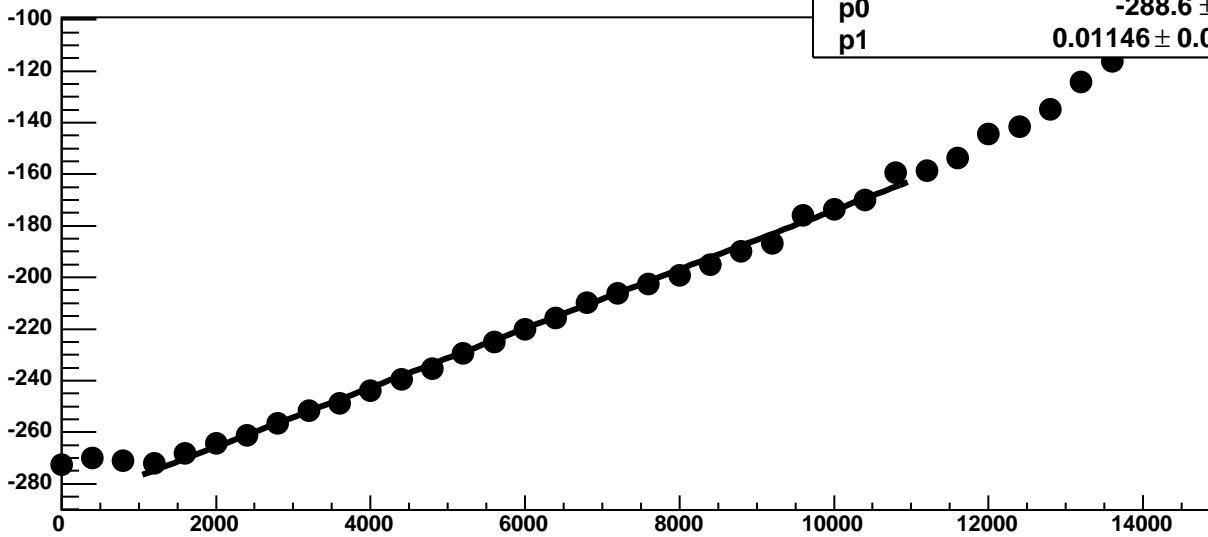
Chip 6, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



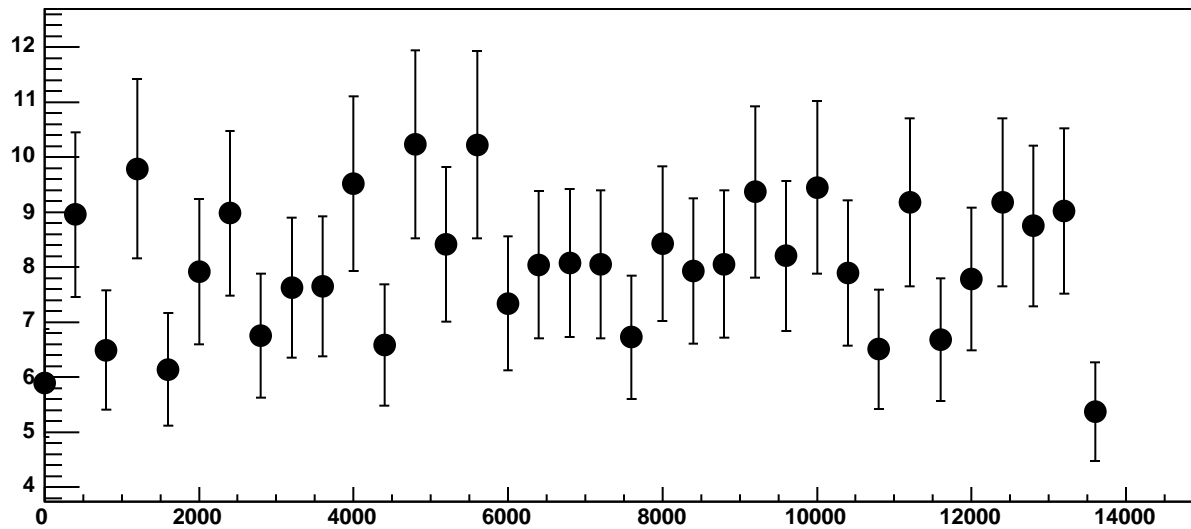
Chip 6, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC



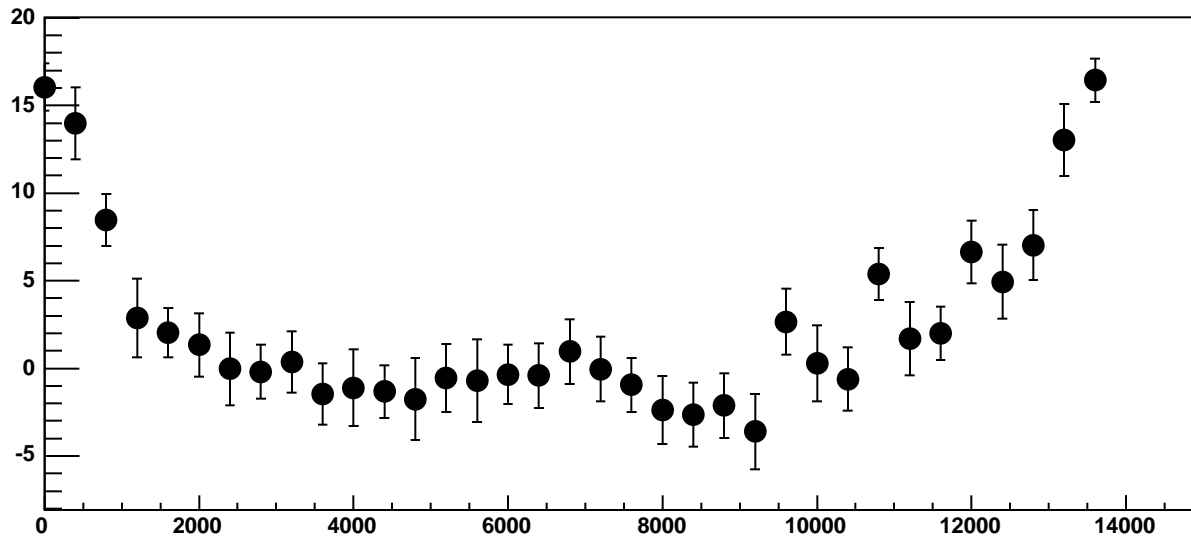
Chip 6, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



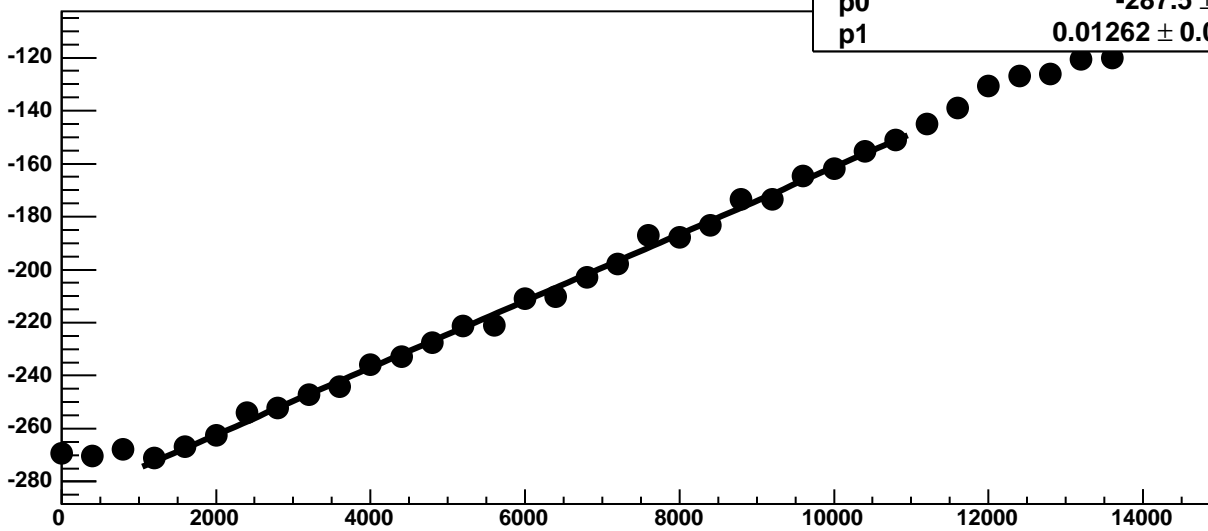
Chip 6, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



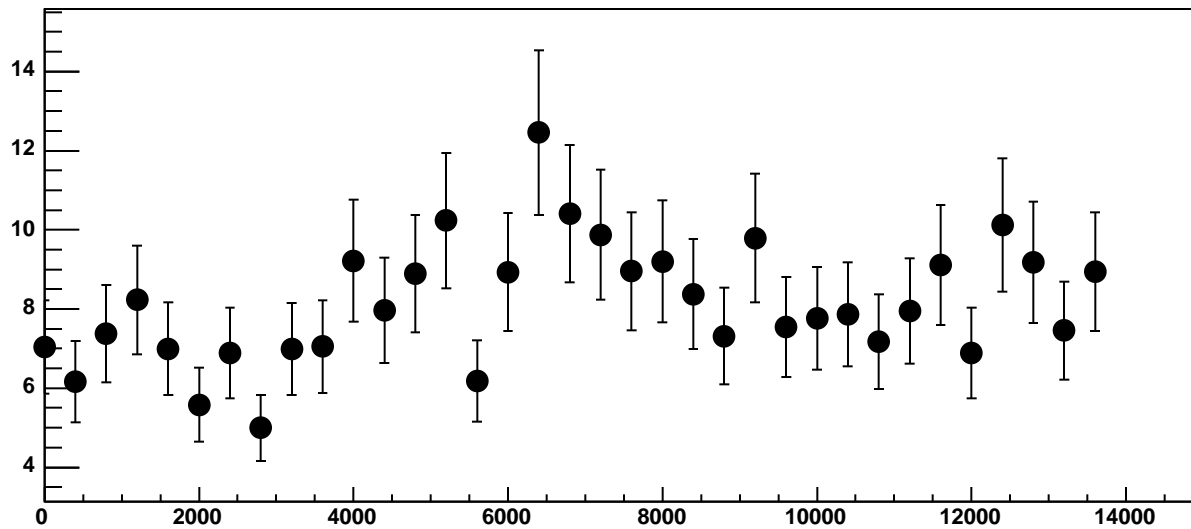
Chip 6, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



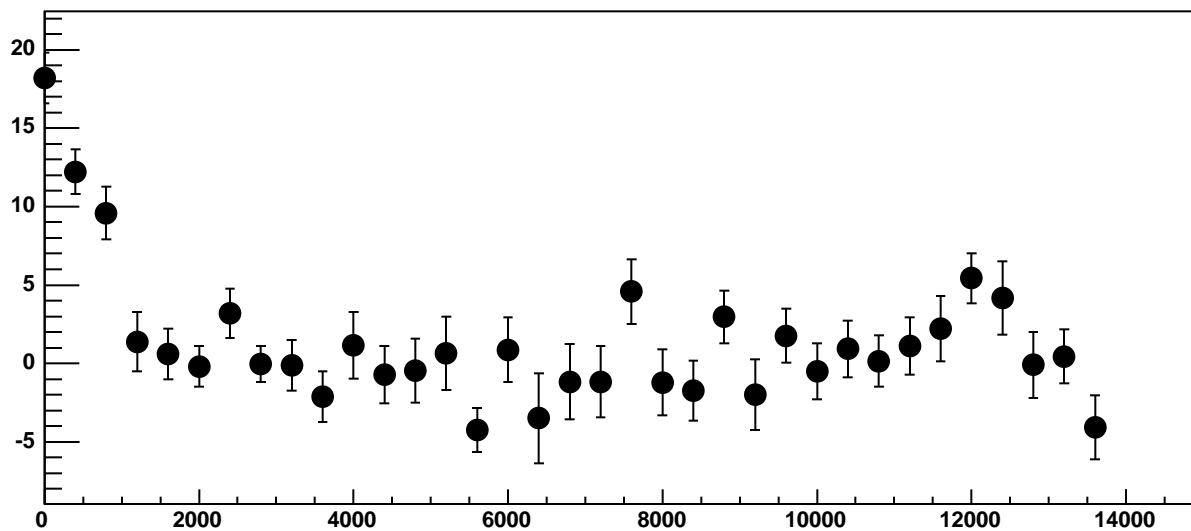
Chip 6, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



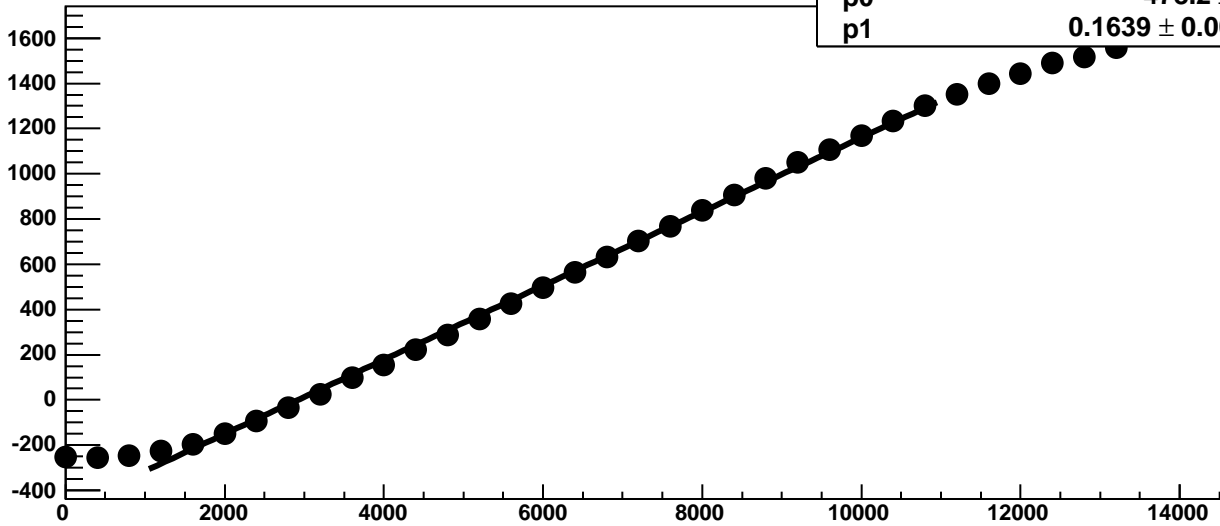
Chip 6, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



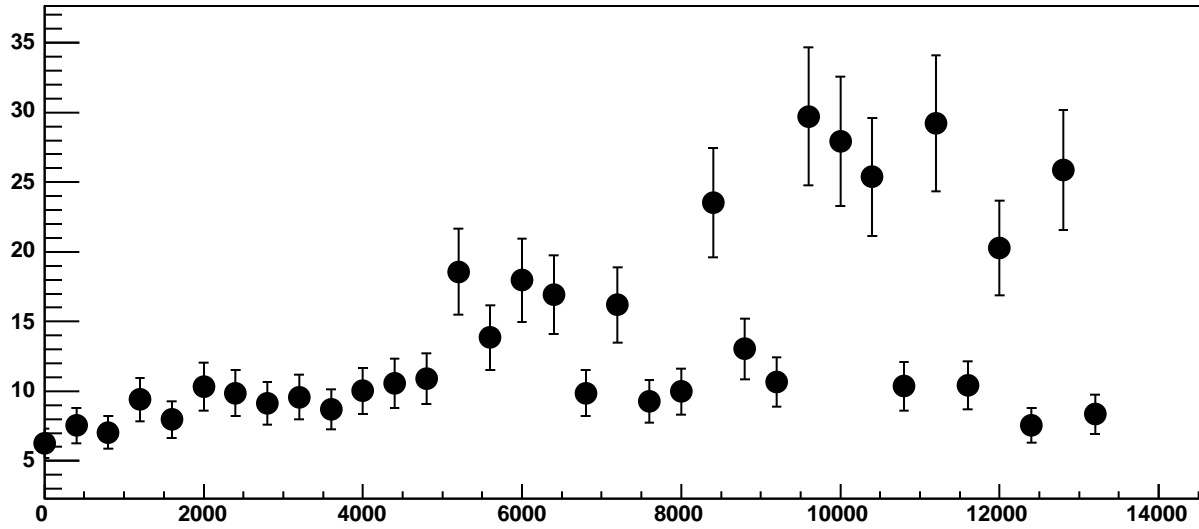
Chip 6, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



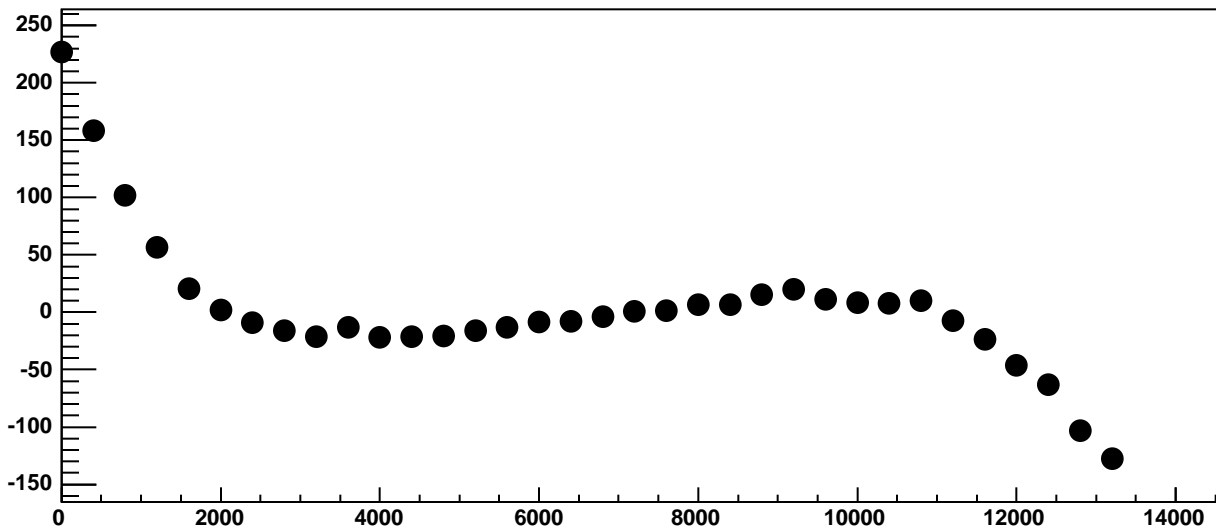
Chip 6, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC



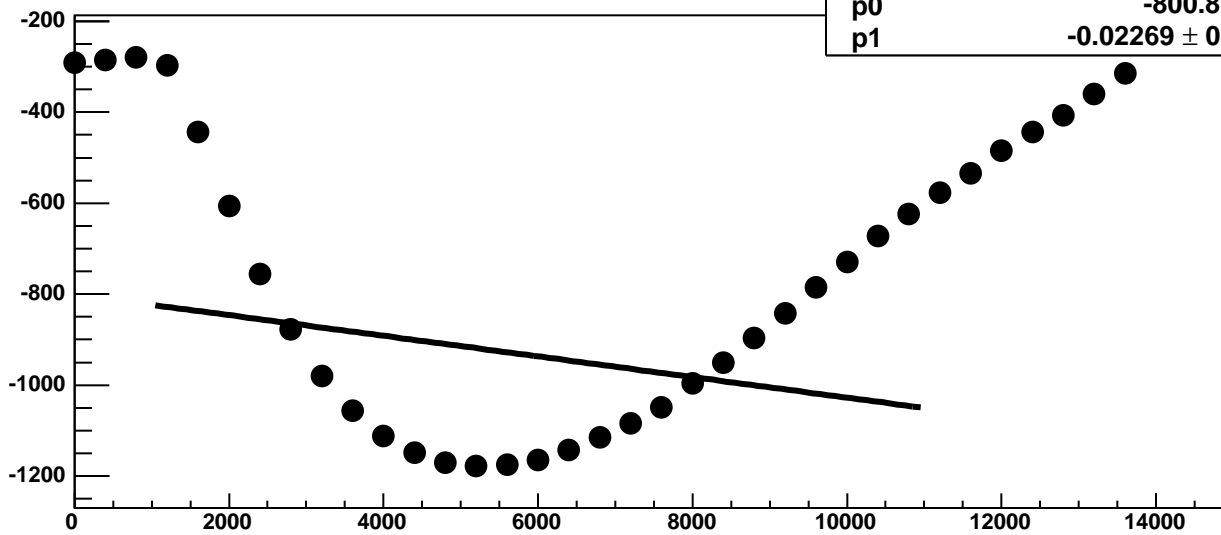
Chip 6, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC

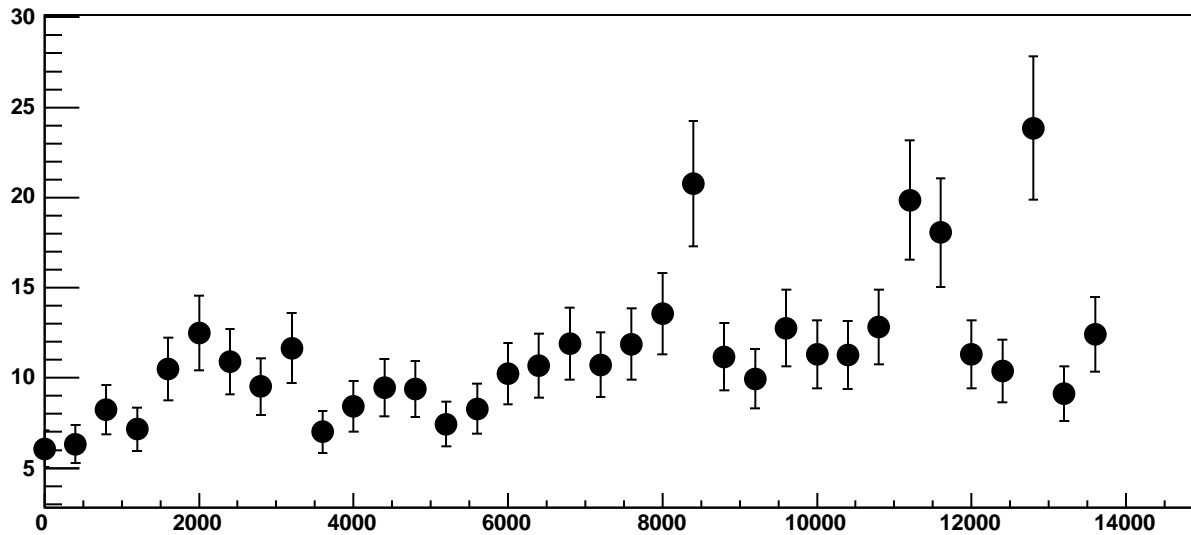


Chip 6, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

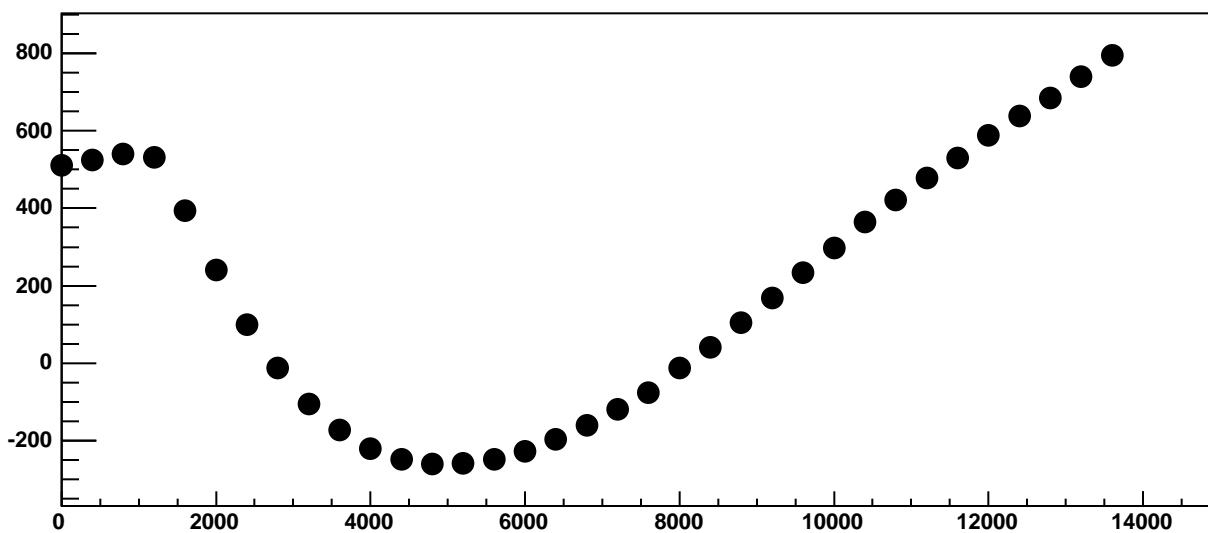


χ^2 / ndf 3.25e+05 / 23
p0 -800.8 ± 1.009
p1 -0.02269 ± 0.000167

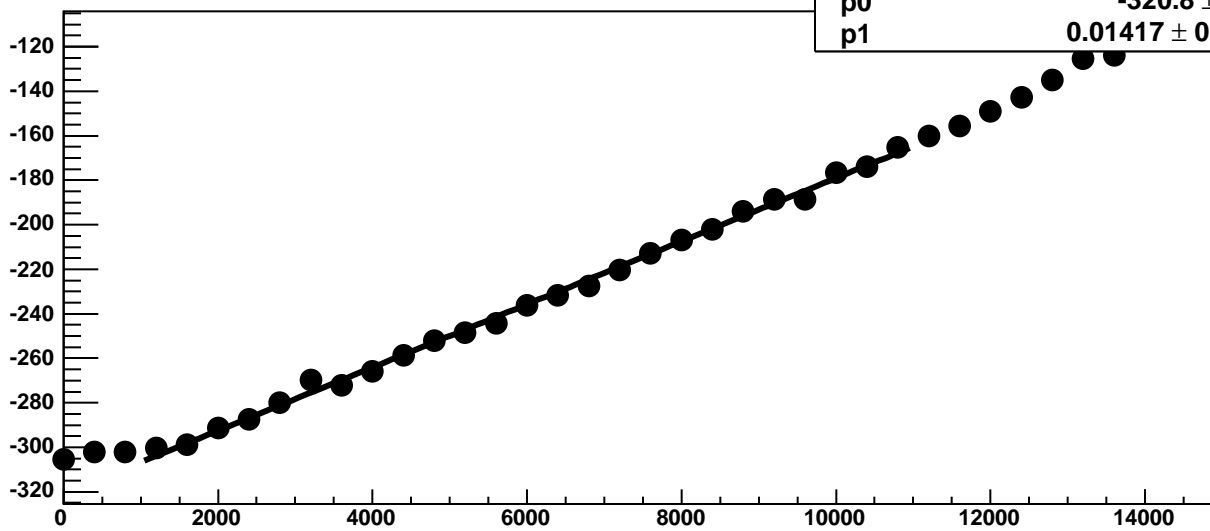
Chip 6, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

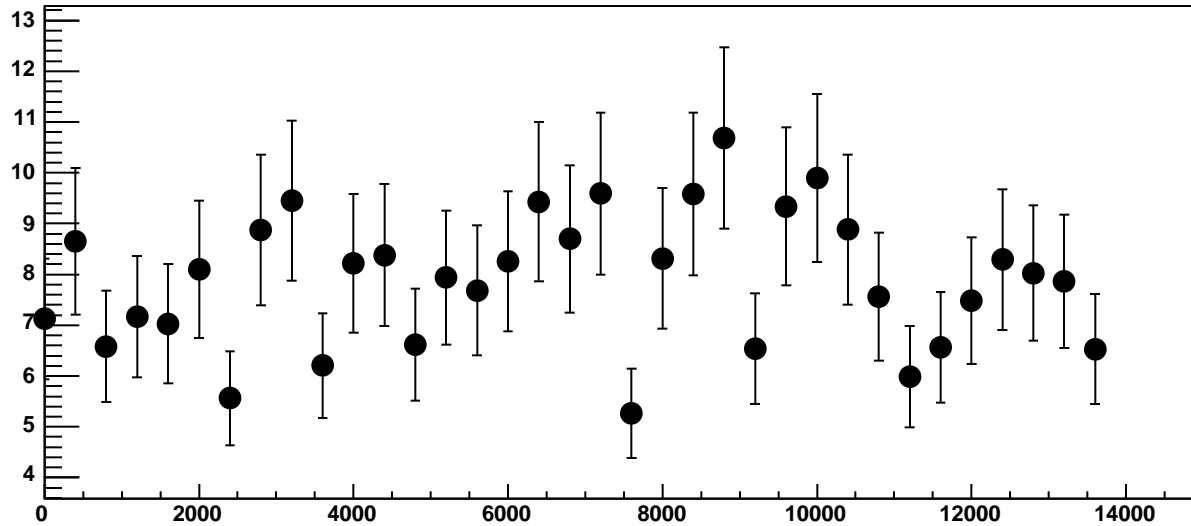


Chip 6, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

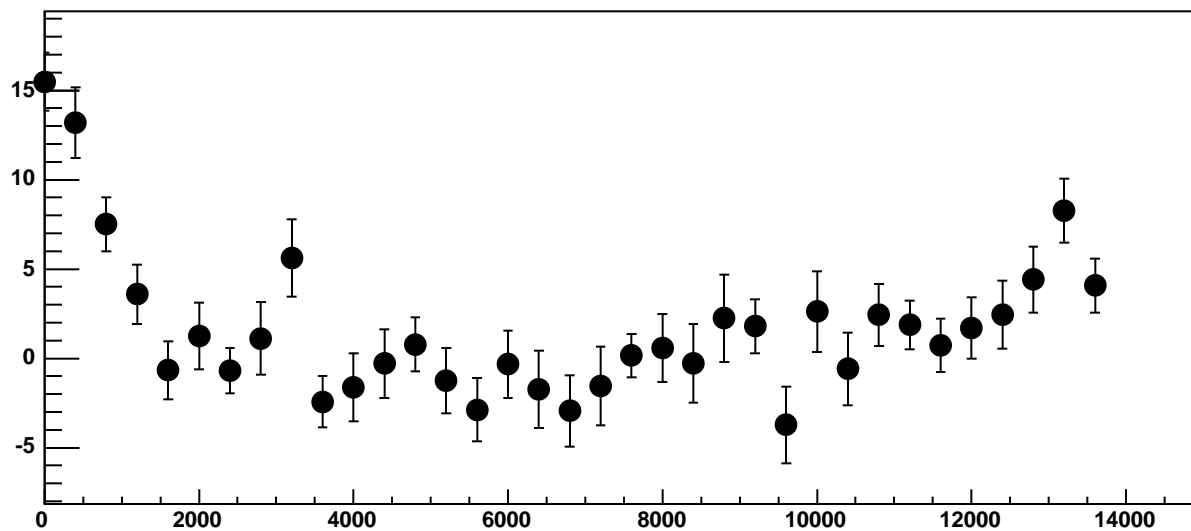


χ^2 / ndf 31.84 / 23
p0 -320.8 ± 0.7844
p1 0.01417 ± 0.000123

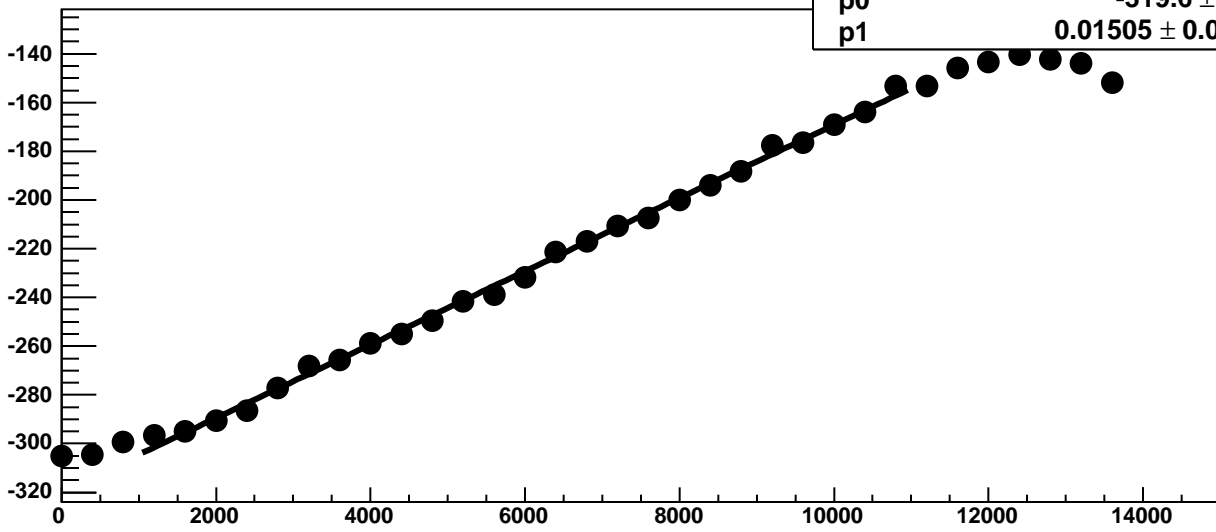
Chip 6, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



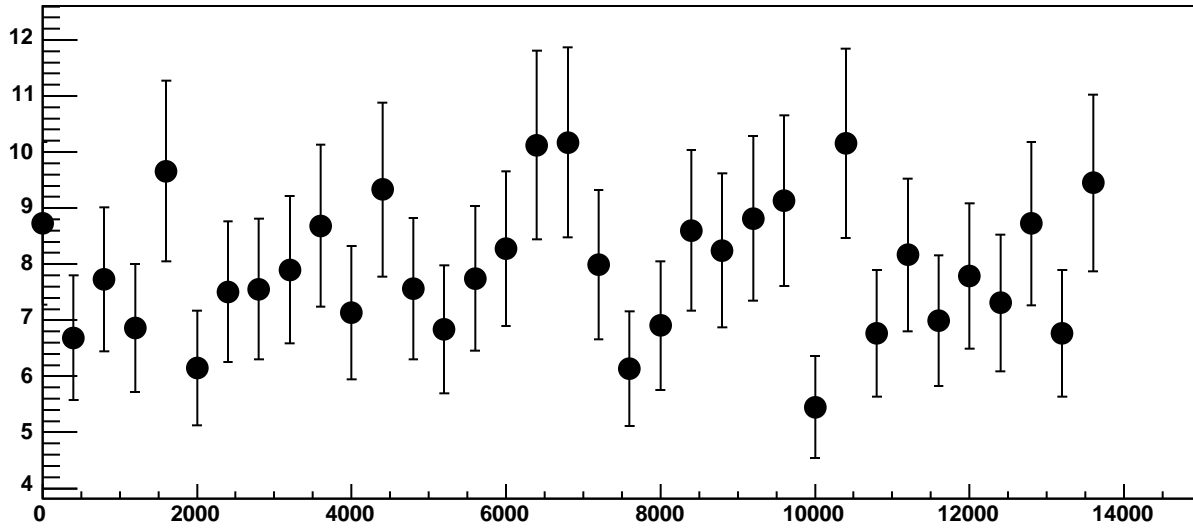
Chip 6, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



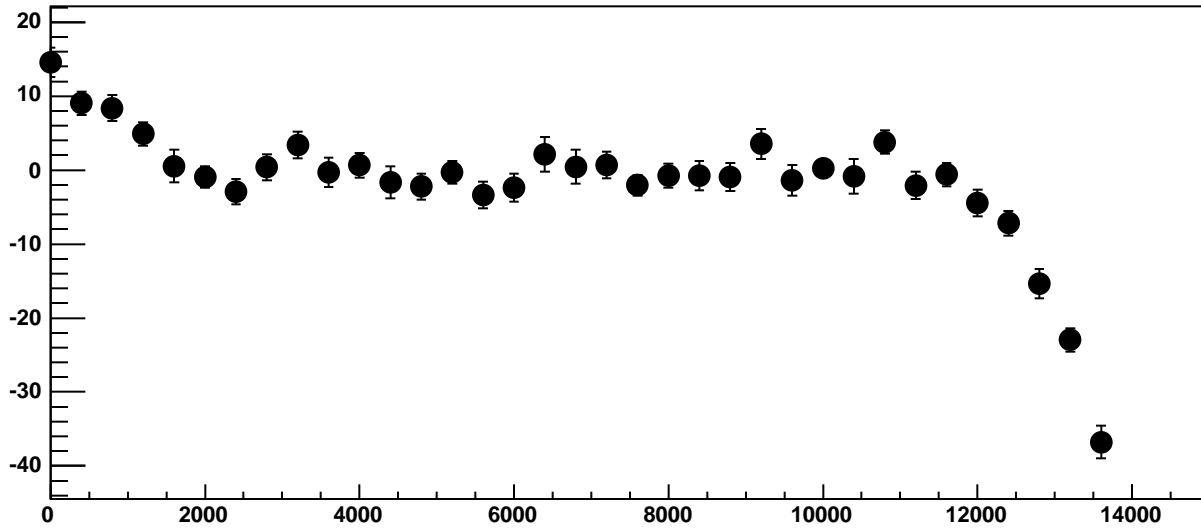
Chip 6, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC



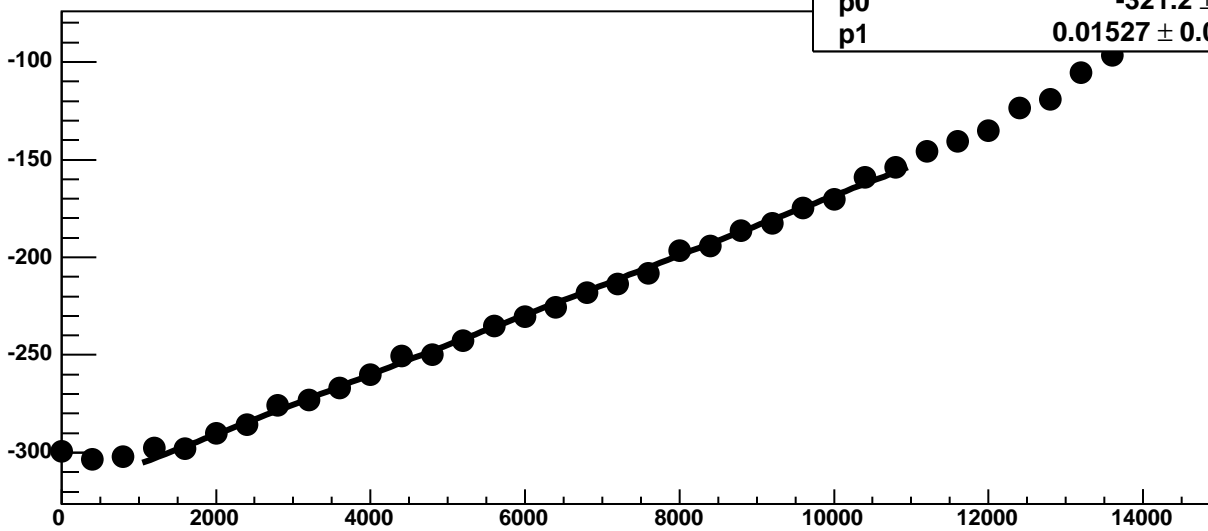
Chip 6, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



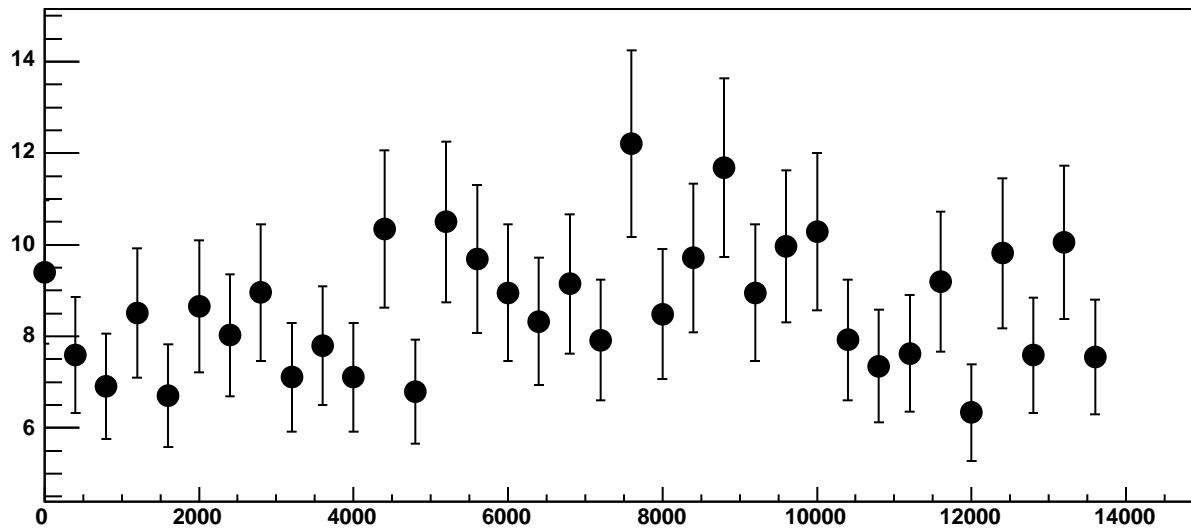
Chip 6, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC



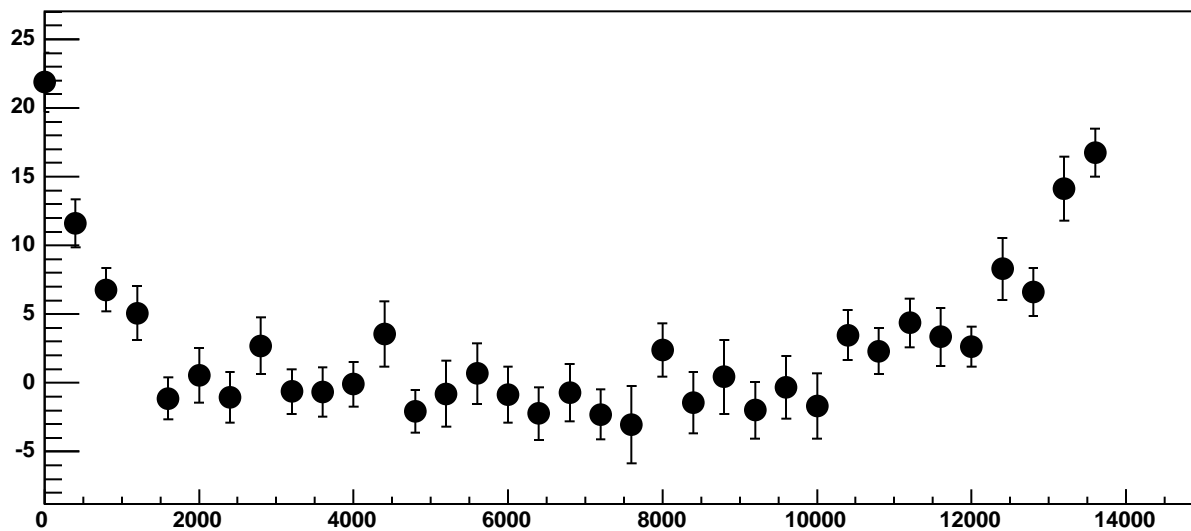
Chip 6, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



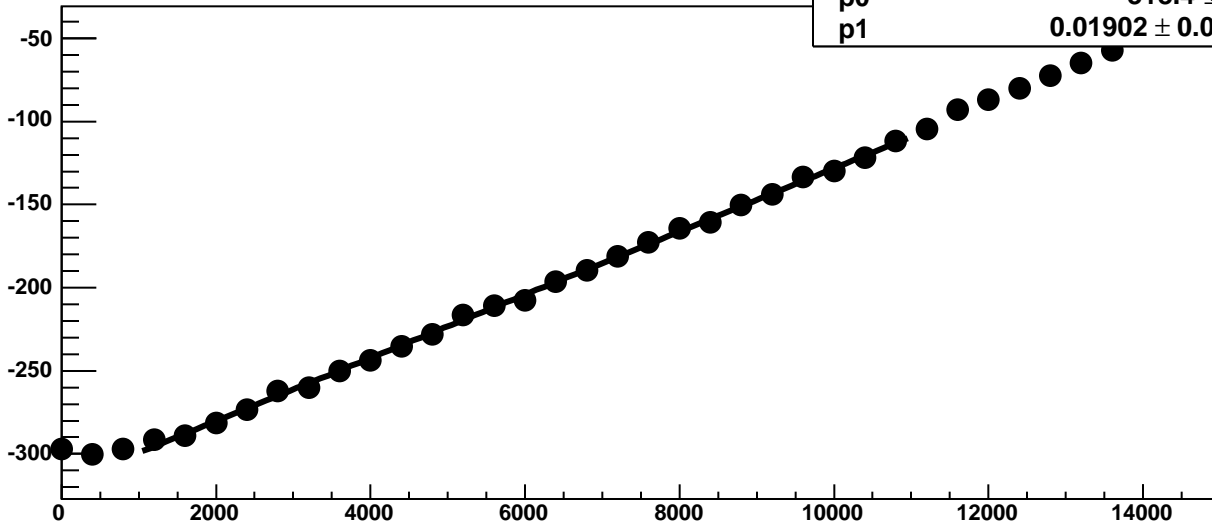
Chip 6, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

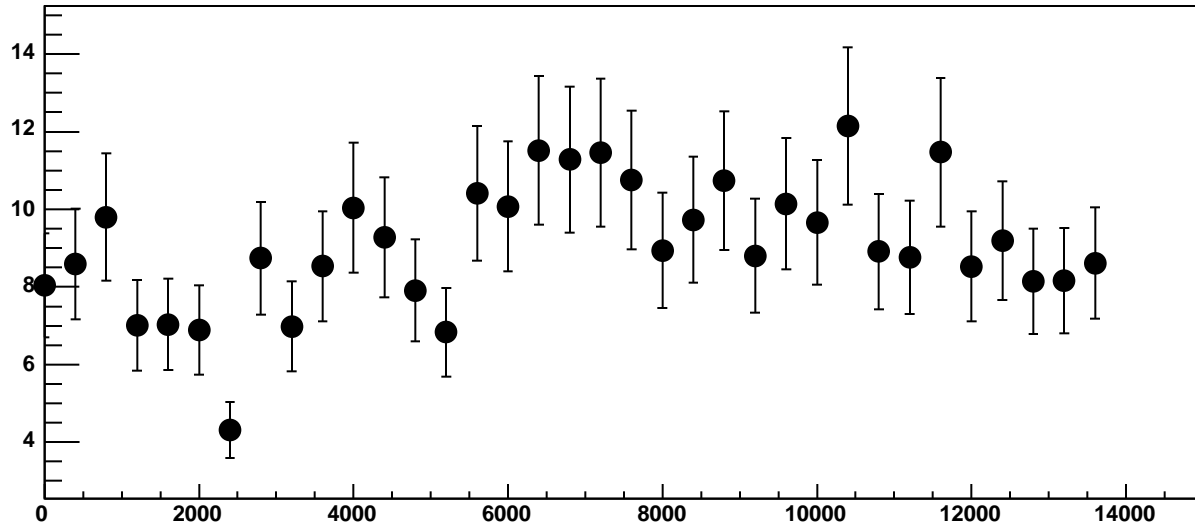


Chip 6, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

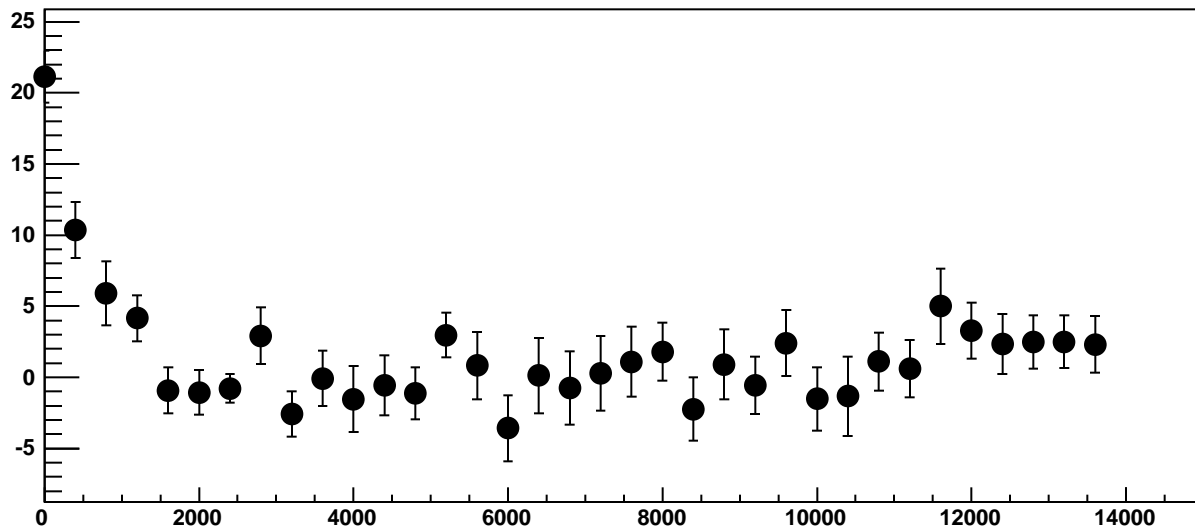


χ^2 / ndf 24.09 / 23
p0 -318.4 ± 0.7571
p1 0.01902 ± 0.0001306

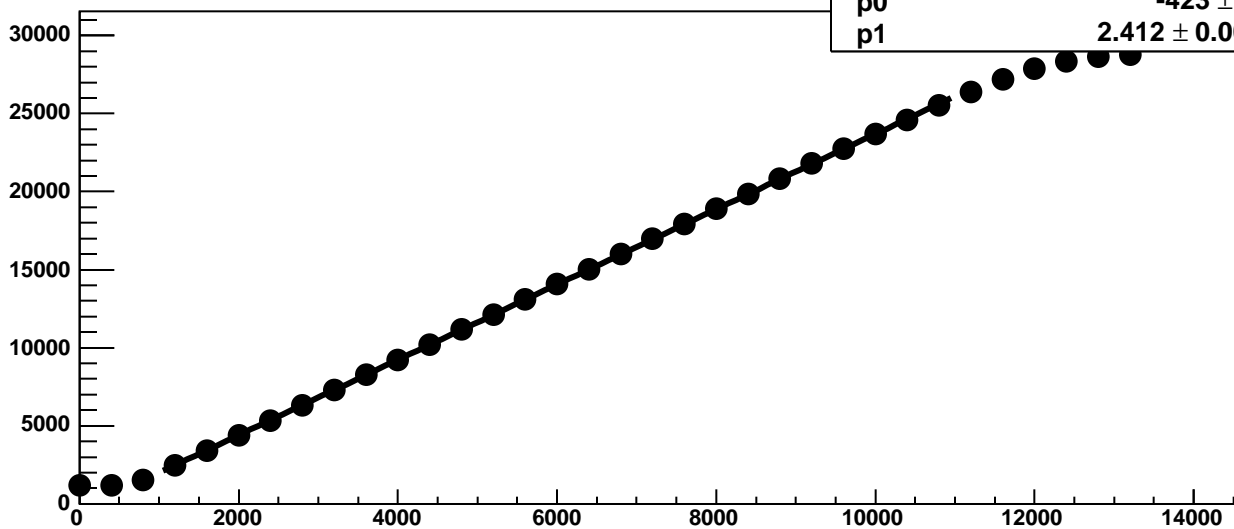
Chip 6, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



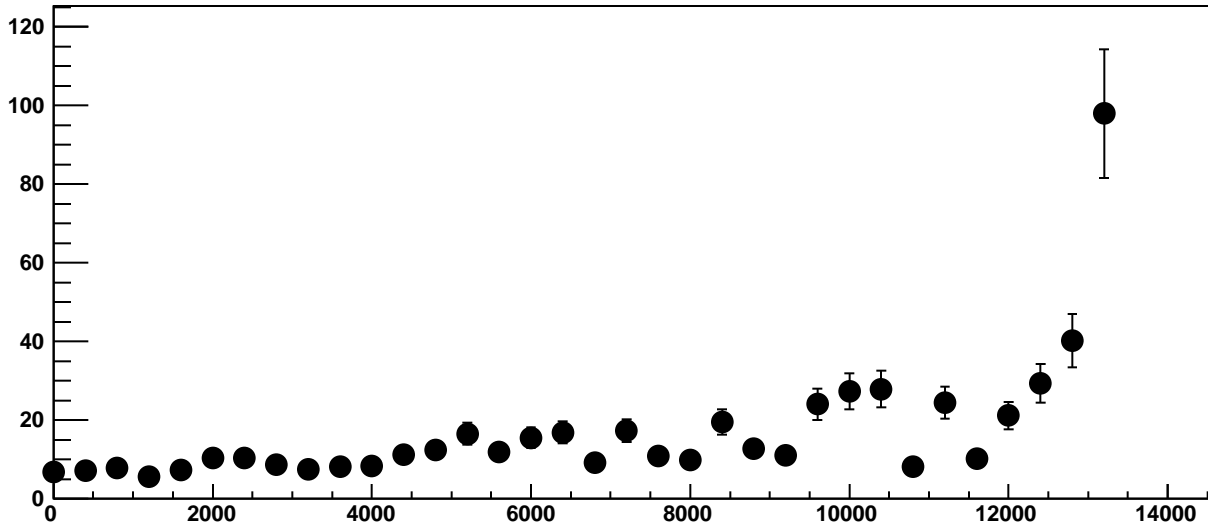
Chip 6, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



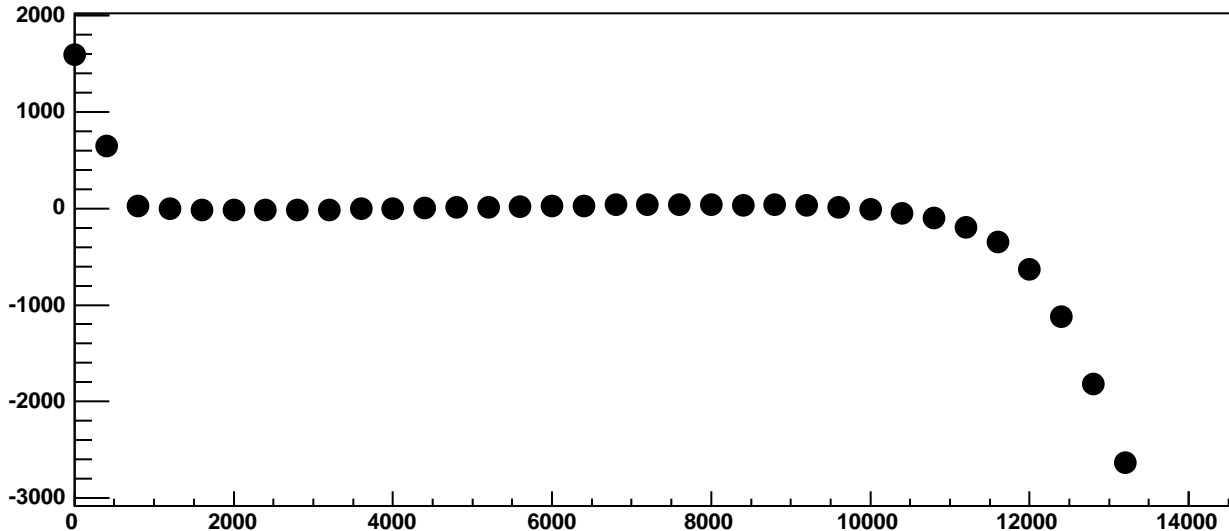
Chip 6, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC



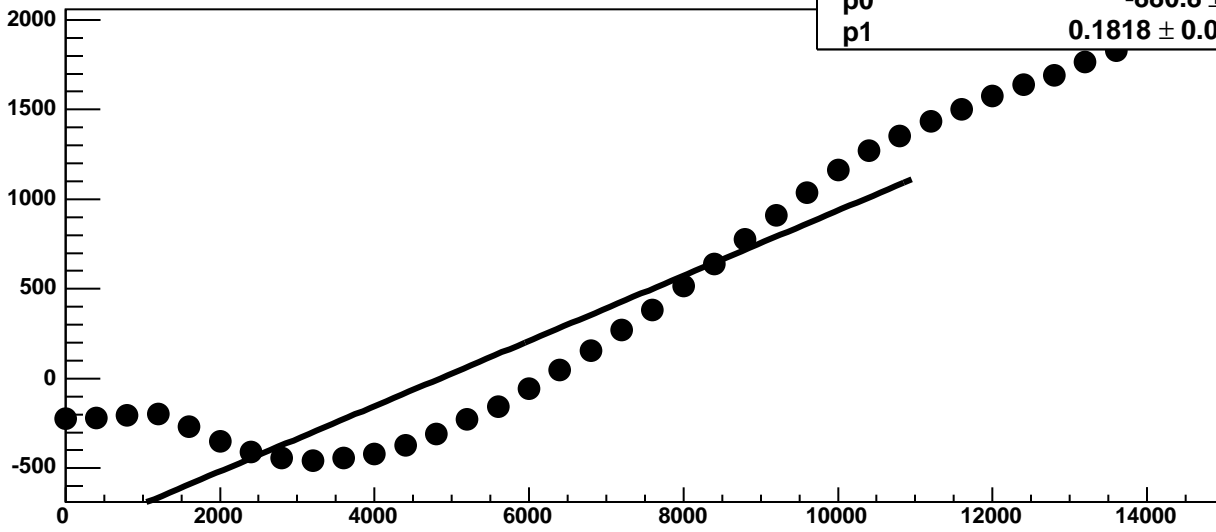
Chip 6, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC

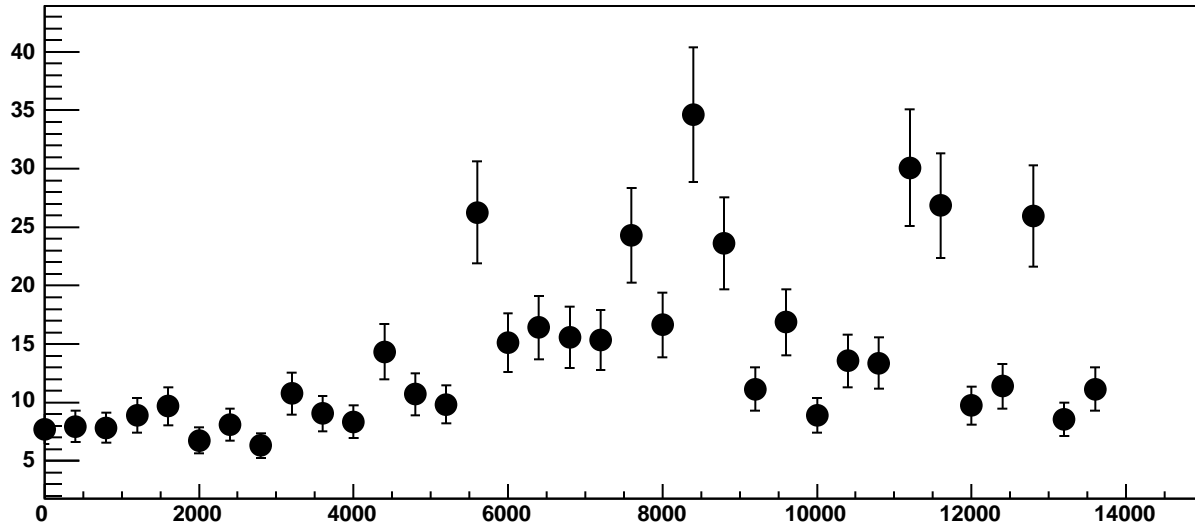


Chip 6, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

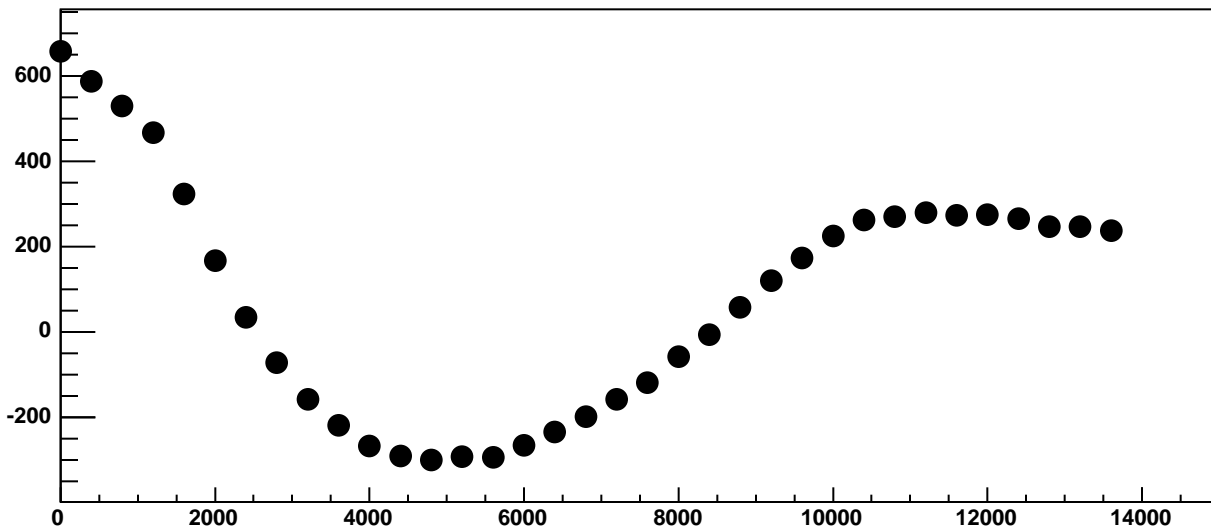


χ^2 / ndf 2.111e+05 / 23
p0 -880.8 ± 0.9491
p1 0.1818 ± 0.0001715

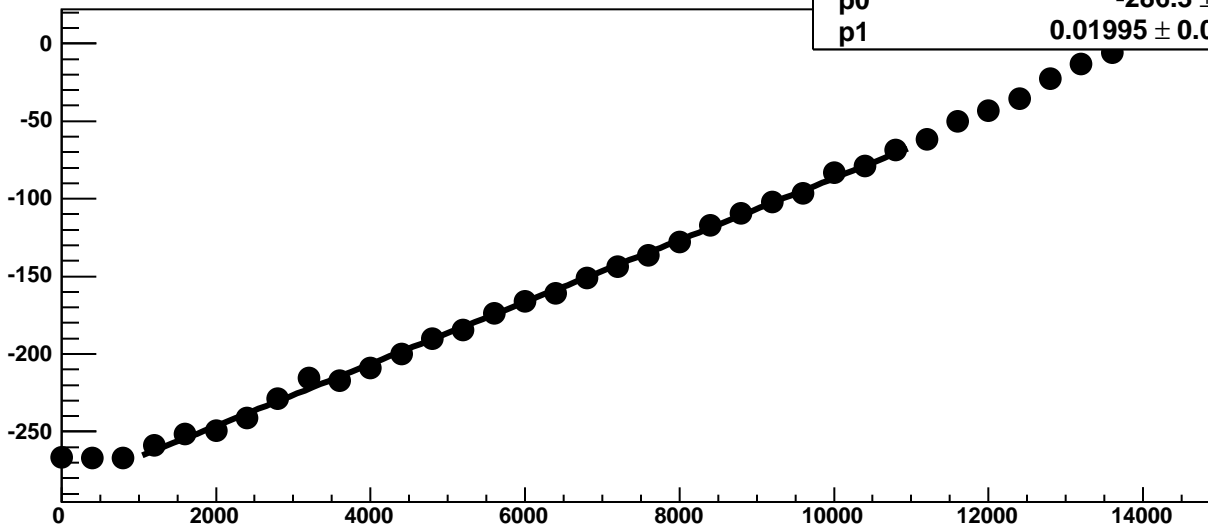
Chip 6, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

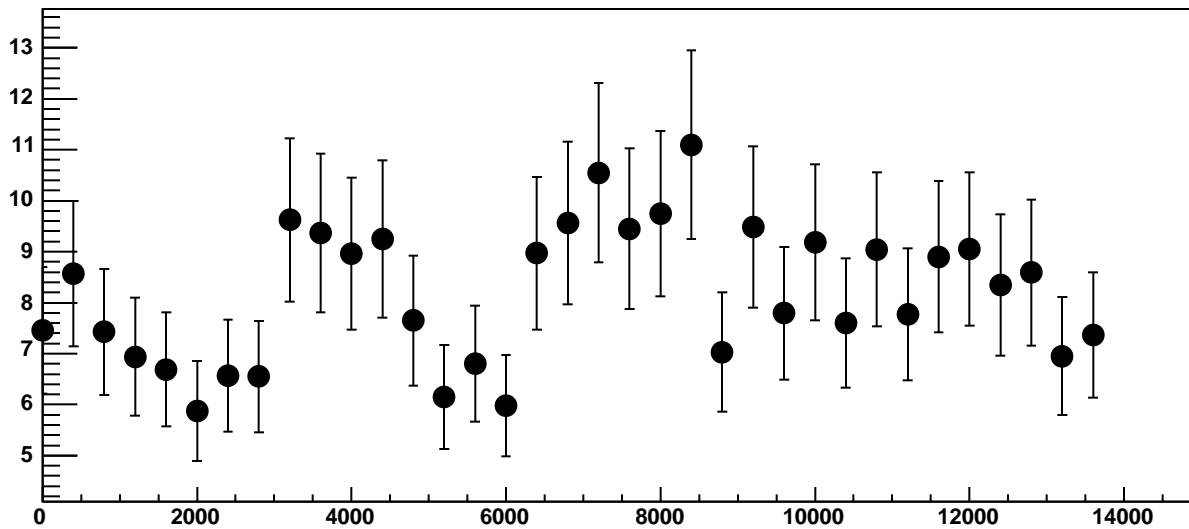


Chip 6, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

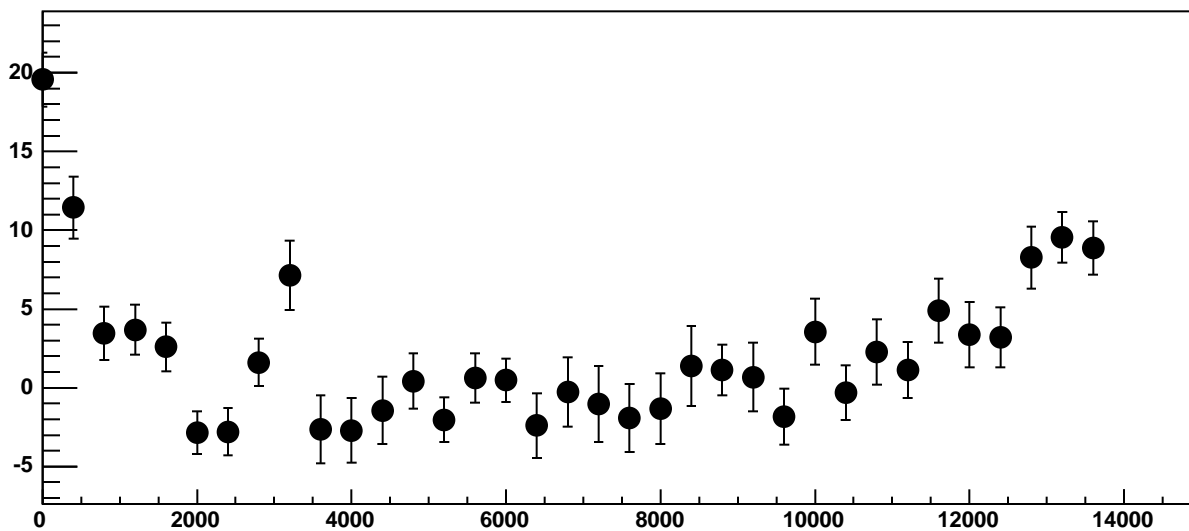


χ^2 / ndf 42.55 / 23
p0 -286.3 ± 0.7619
p1 0.01995 ± 0.0001223

Chip 6, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

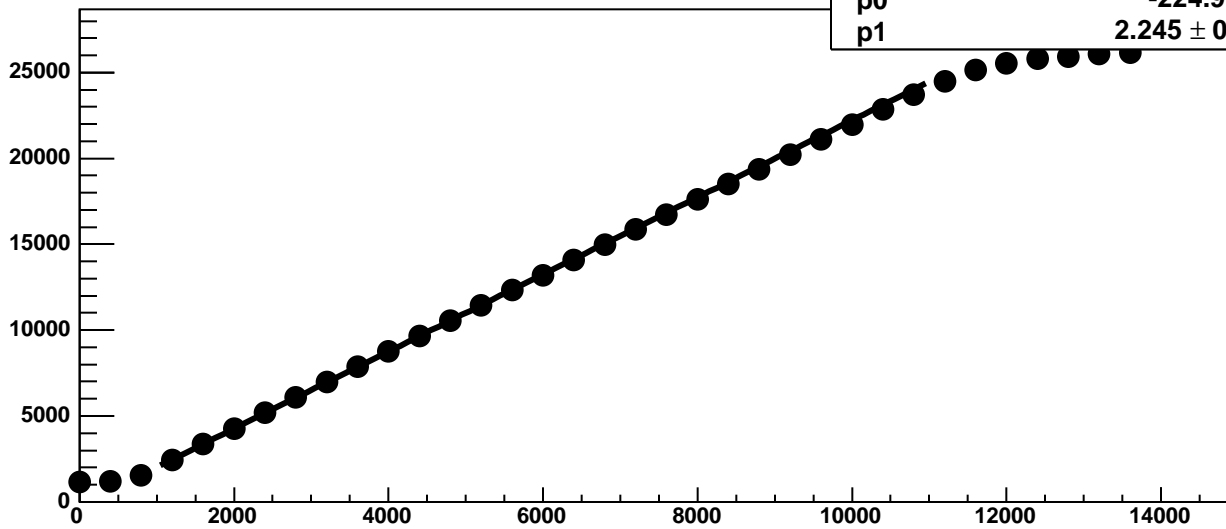


Chip 6, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

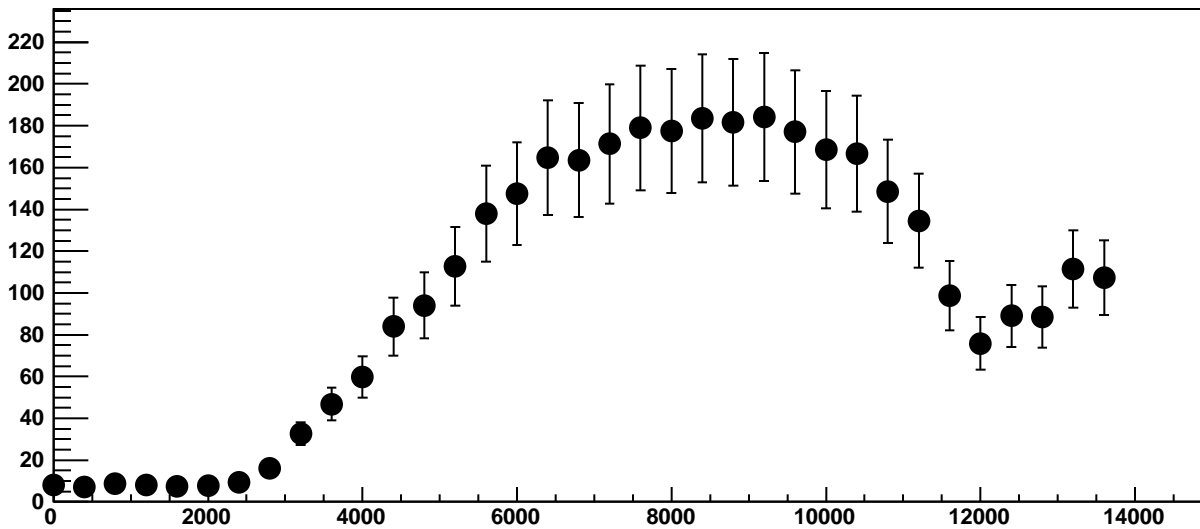


Chip 6, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC

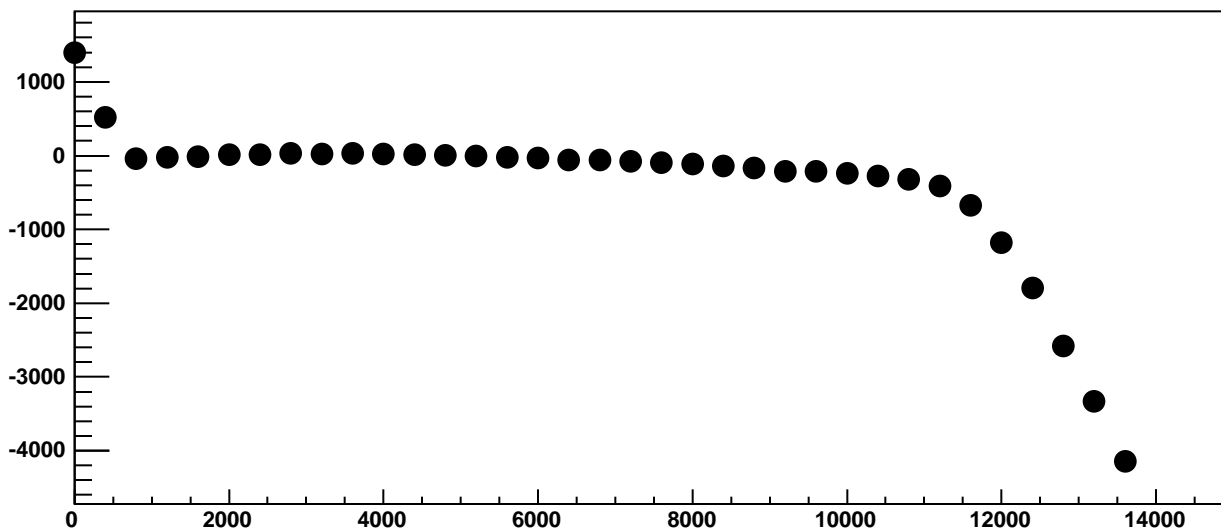
χ^2 / ndf 581.3 / 23
p0 -224.9 ± 2.308
p1 2.245 ± 0.001108



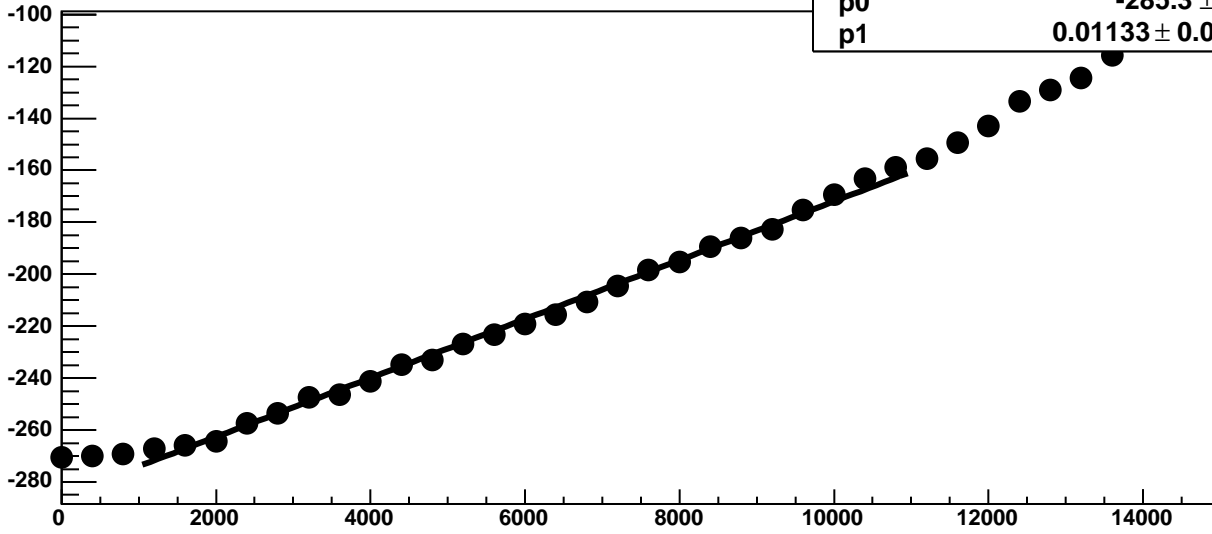
Chip 6, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC

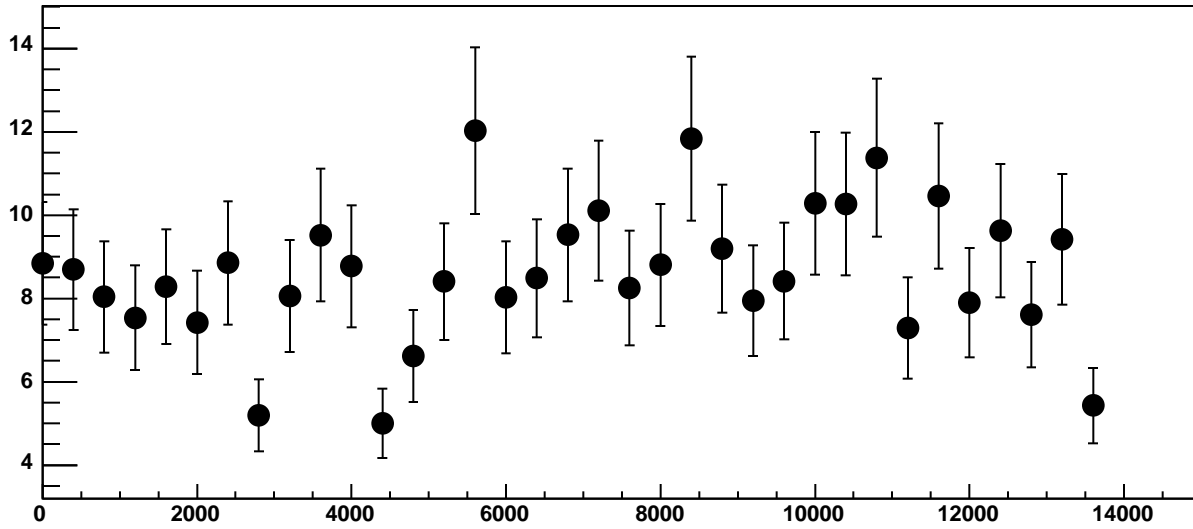


Chip 6, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

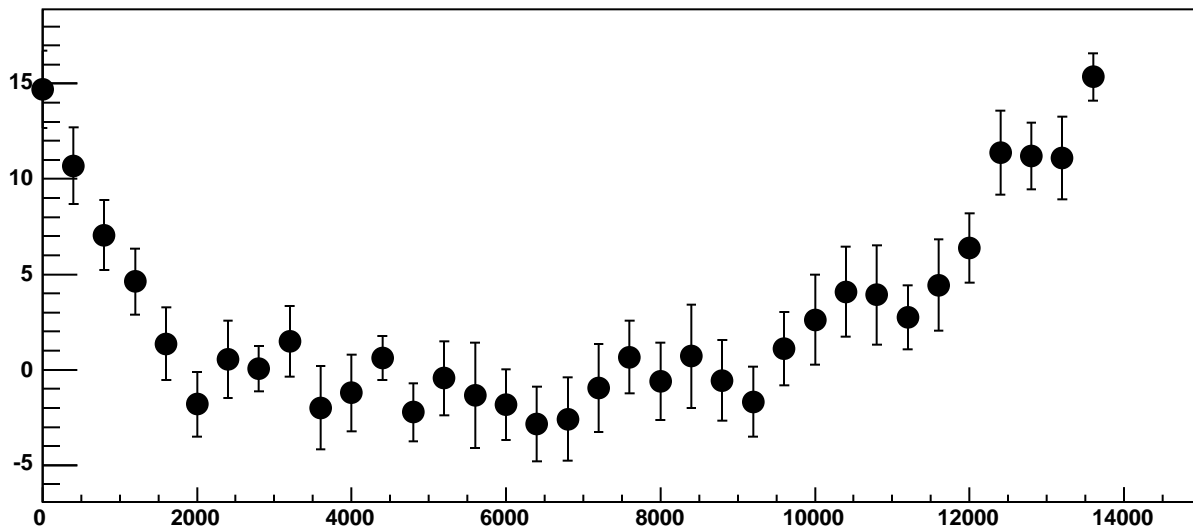


χ^2 / ndf 26.17 / 23
p0 -285.3 ± 0.8179
p1 0.01133 ± 0.0001358

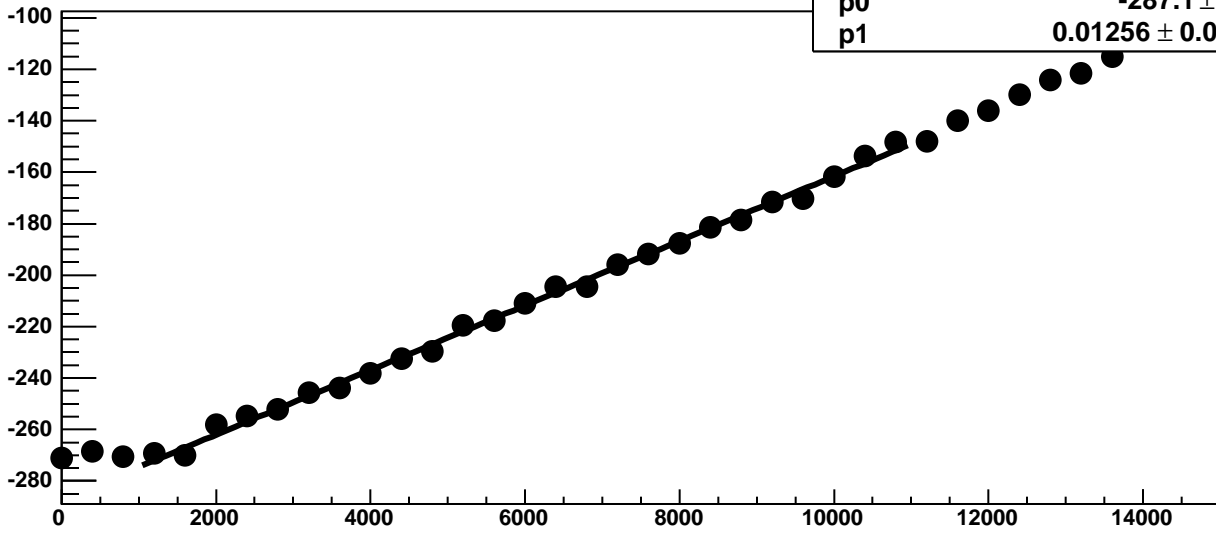
Chip 6, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



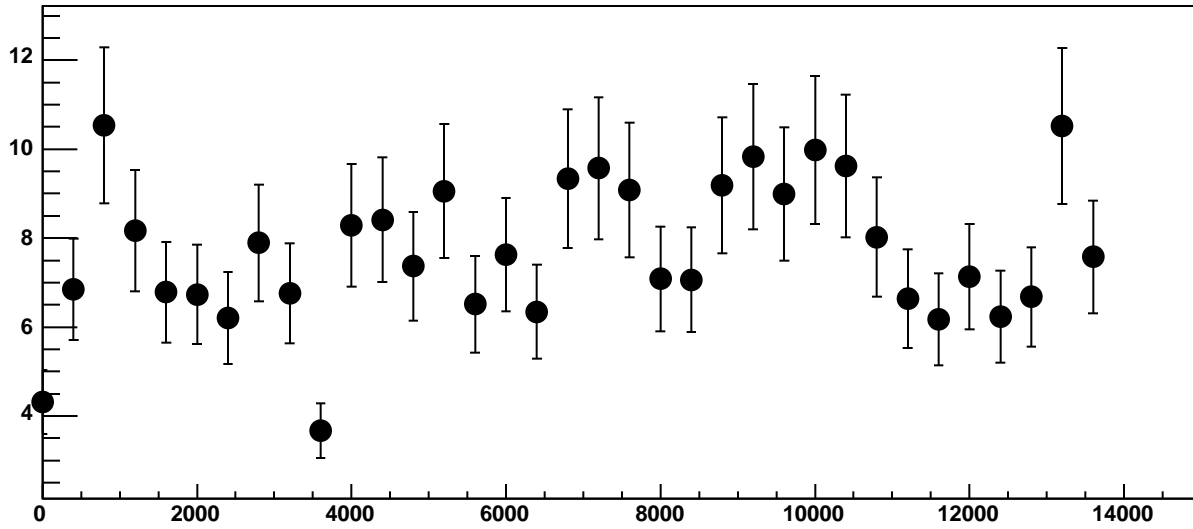
Chip 6, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



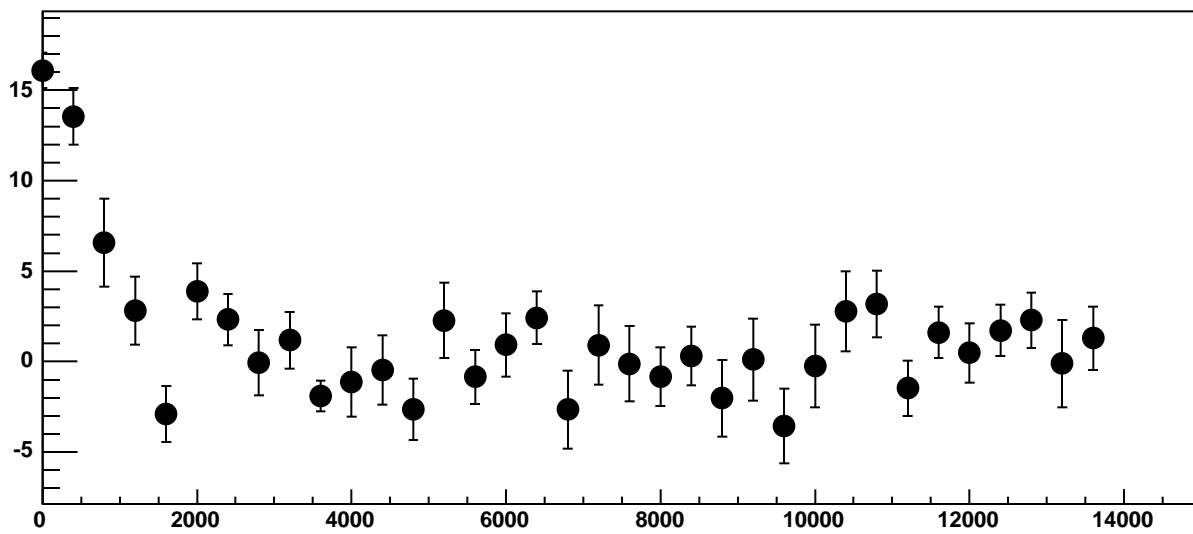
Chip 6, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



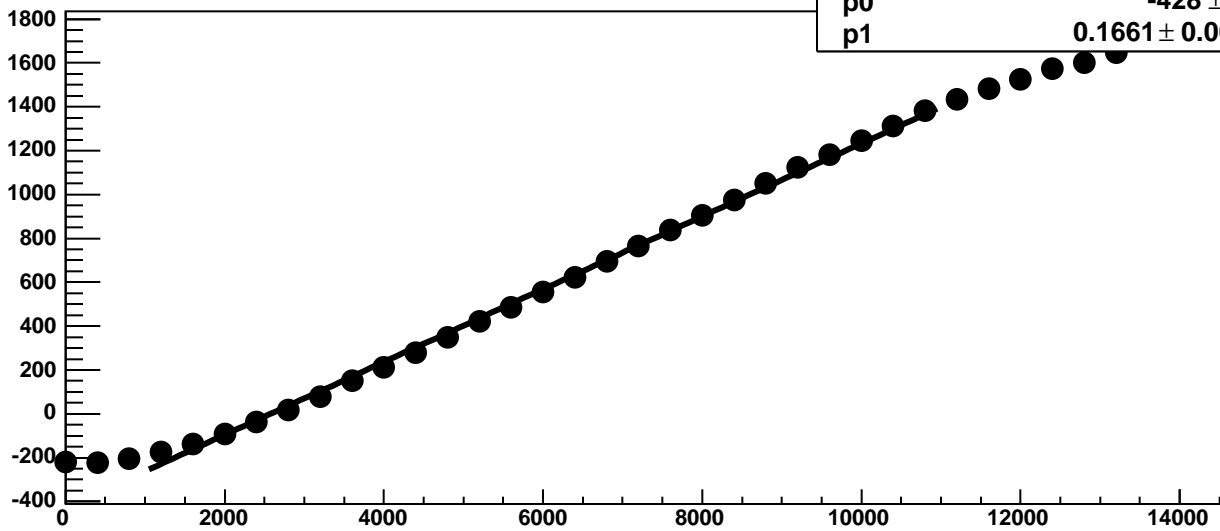
Chip 6, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



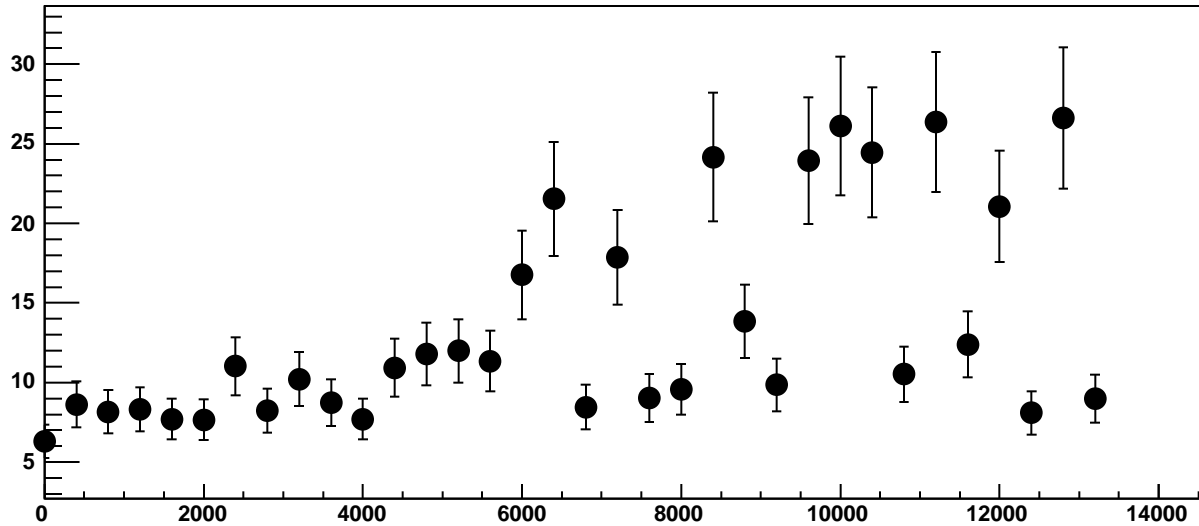
Chip 6, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



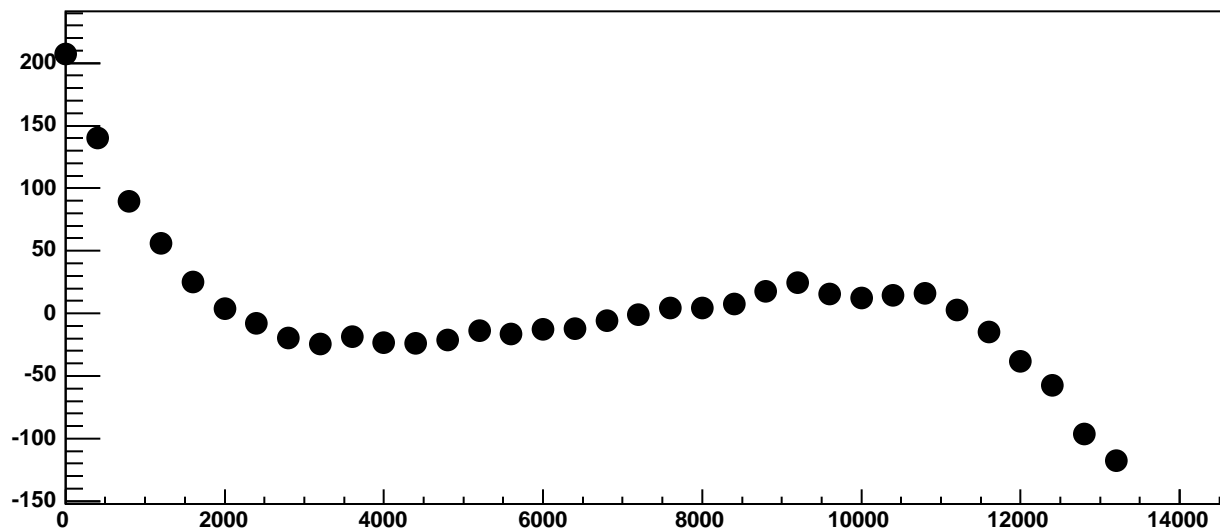
Chip 6, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC



Chip 6, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC

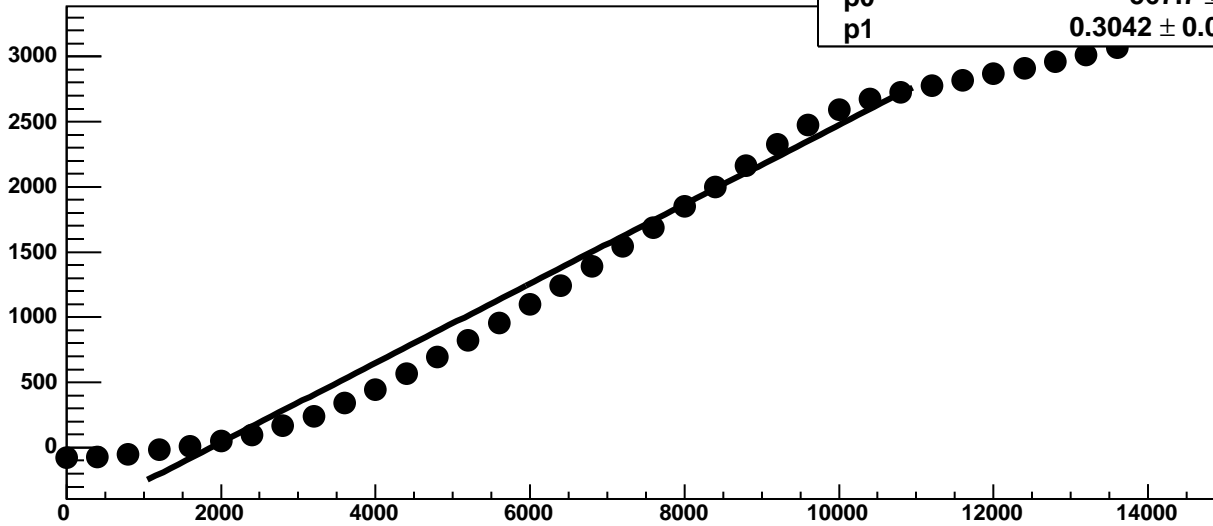


Chip 6, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC

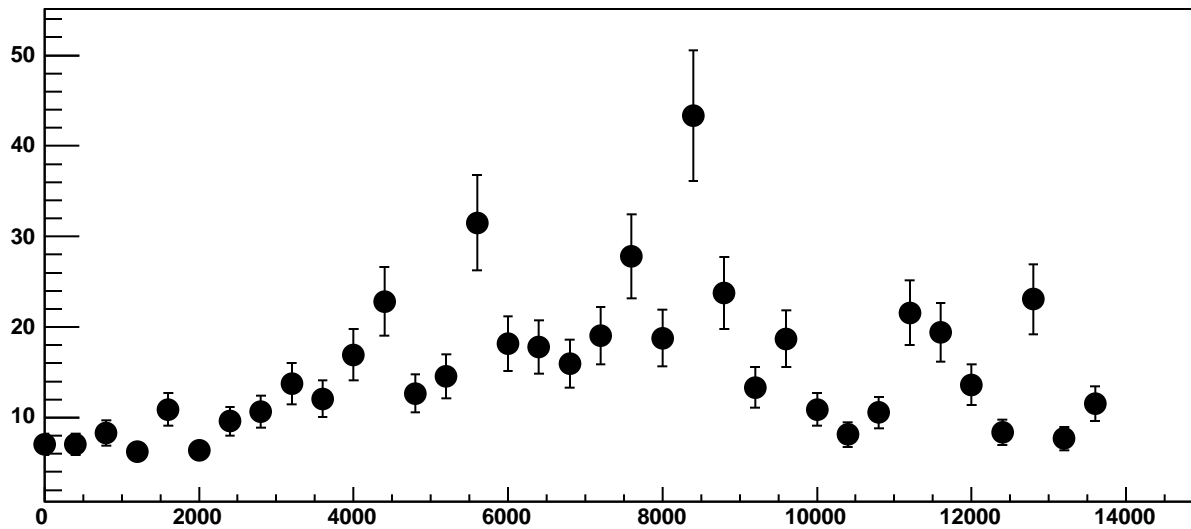


Chip 7, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

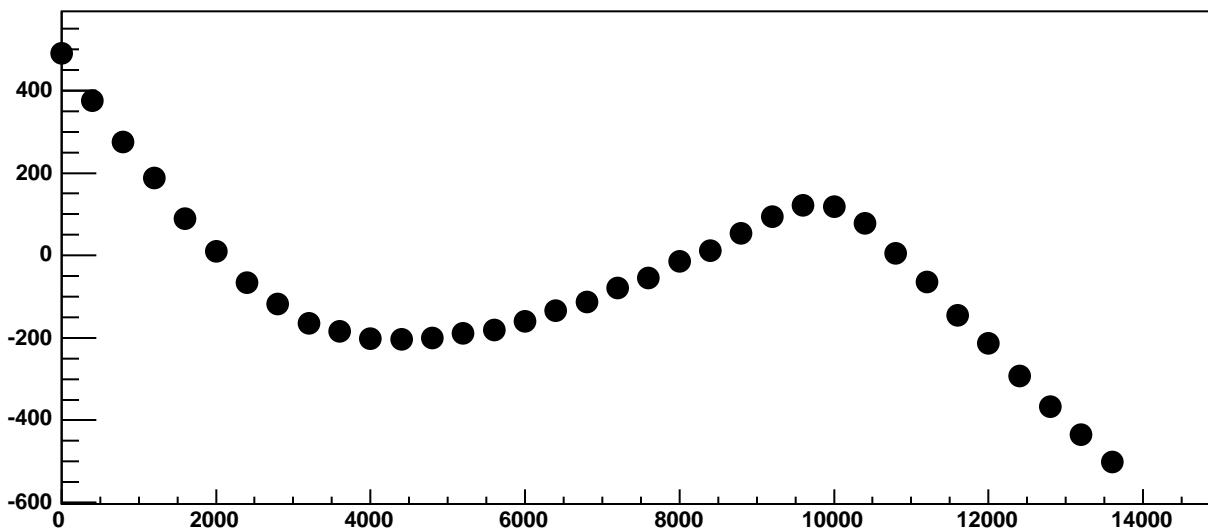
χ^2 / ndf 5.15e+04 / 23
p0 -567.7 ± 0.9642
p1 0.3042 ± 0.0001597



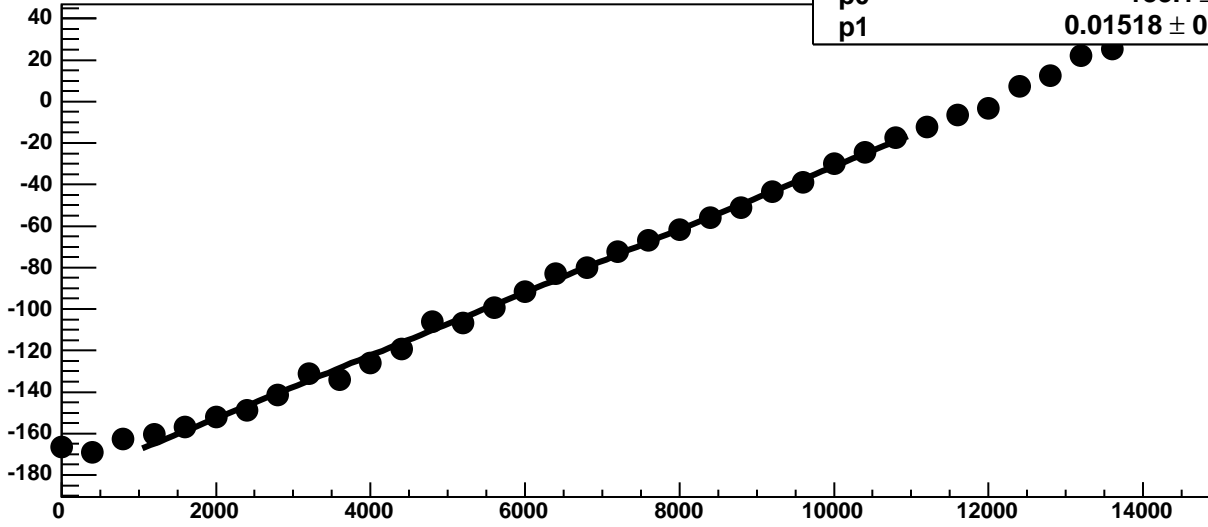
Chip 7, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



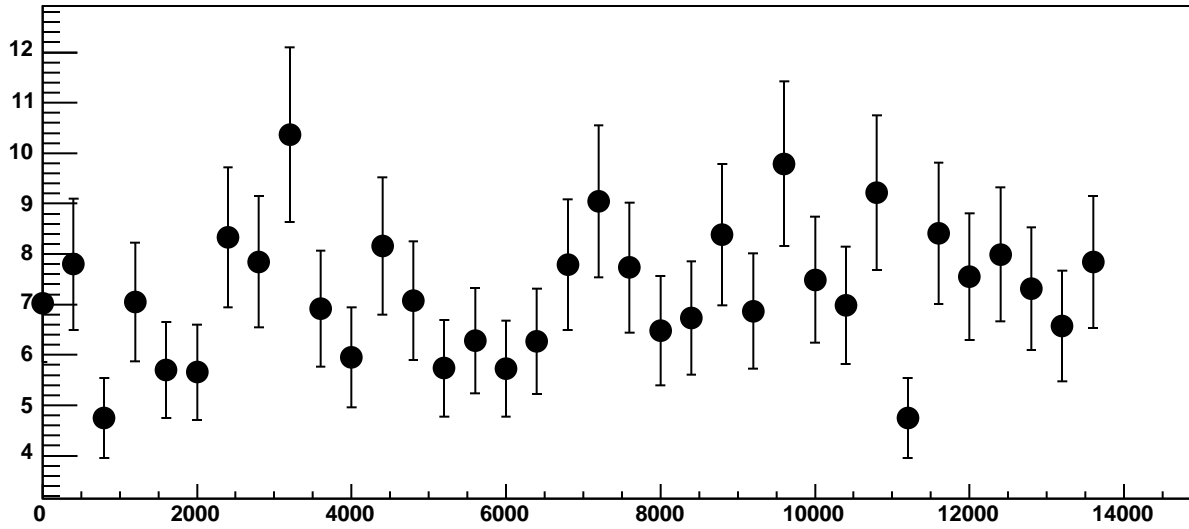
Chip 7, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC



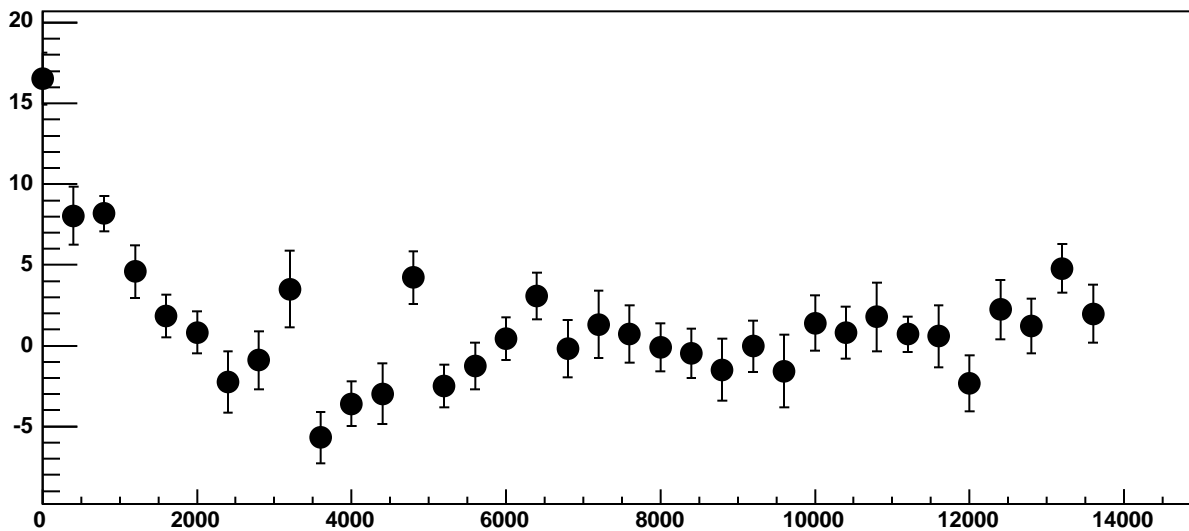
Chip 7, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC



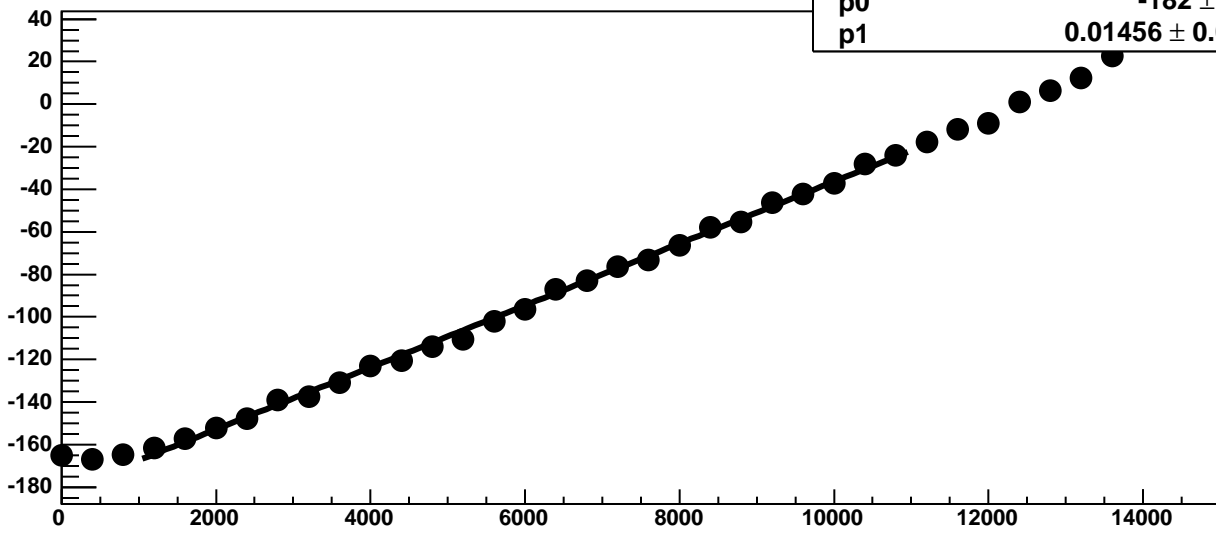
Chip 7, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

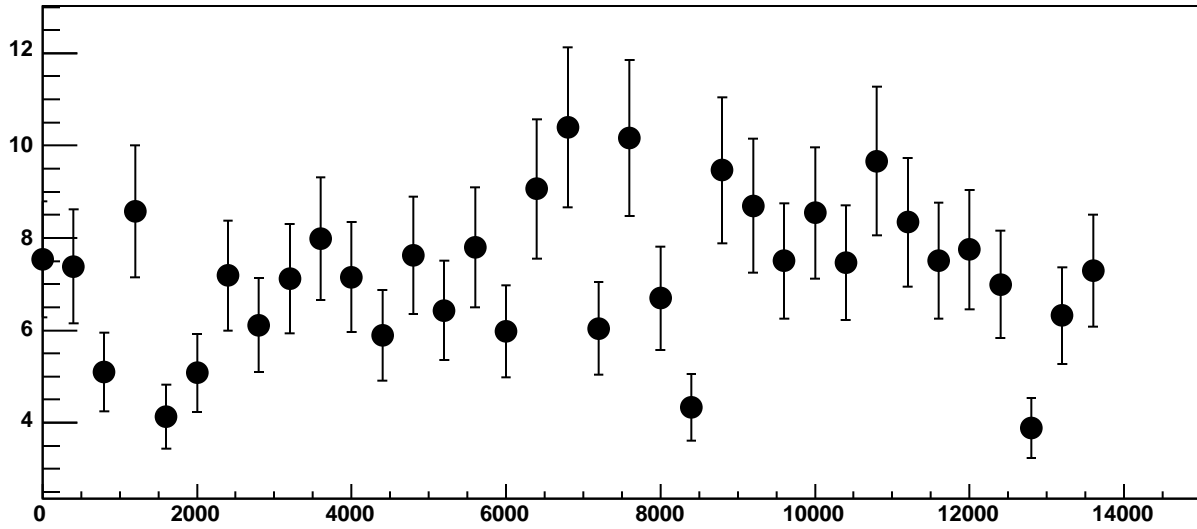


Chip 7, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

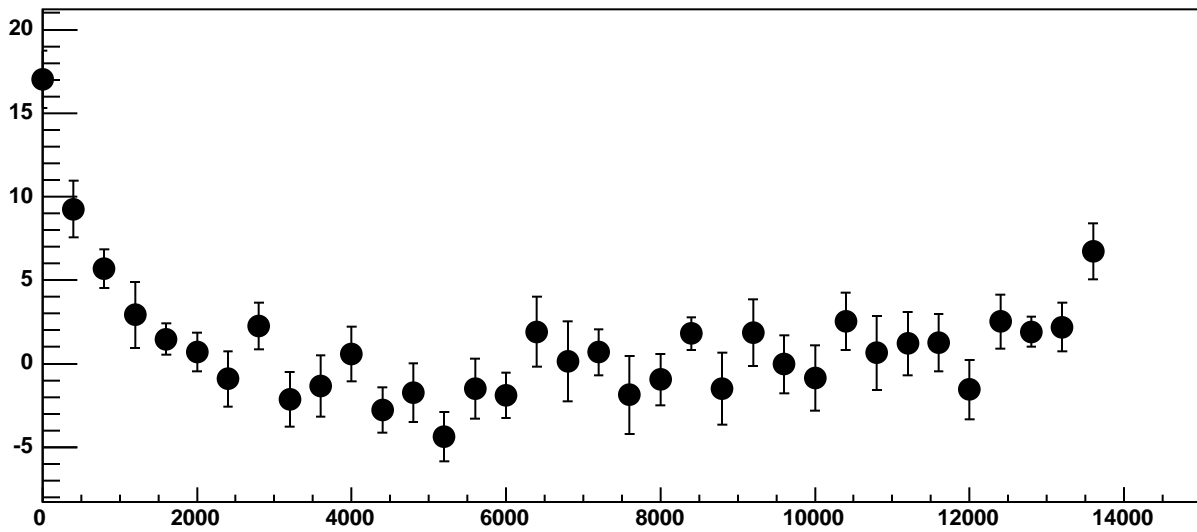


χ^2 / ndf 35.95 / 23
p0 -182 ± 0.6602
p1 0.01456 ± 0.000107

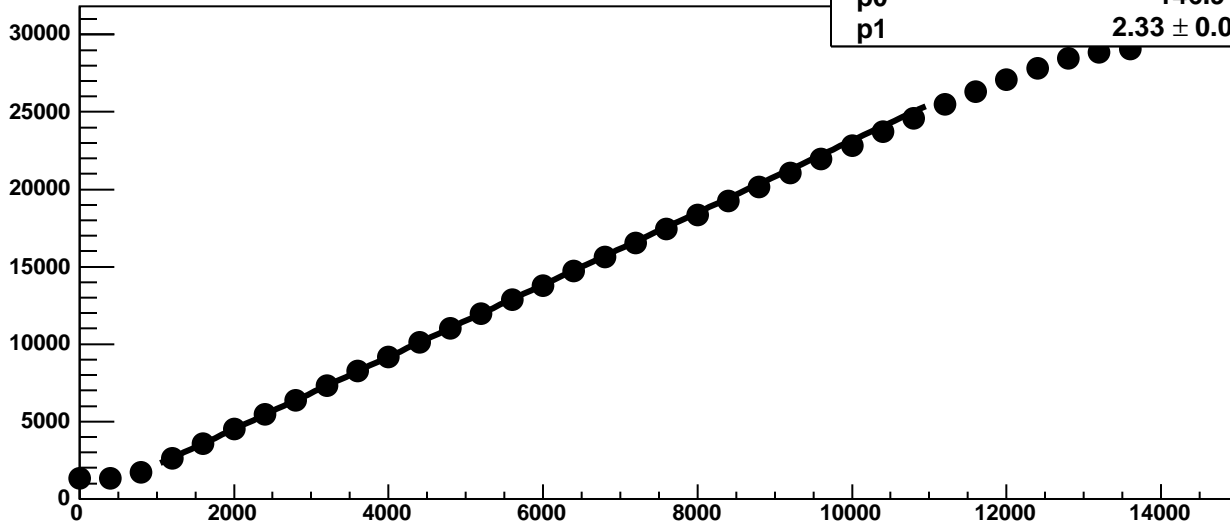
Chip 7, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

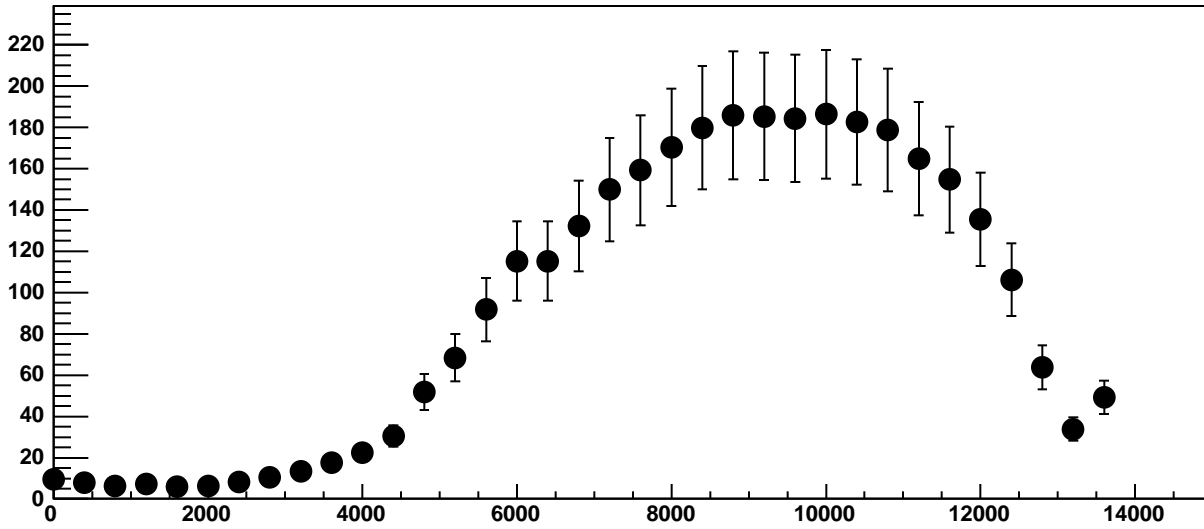


Chip 7, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC

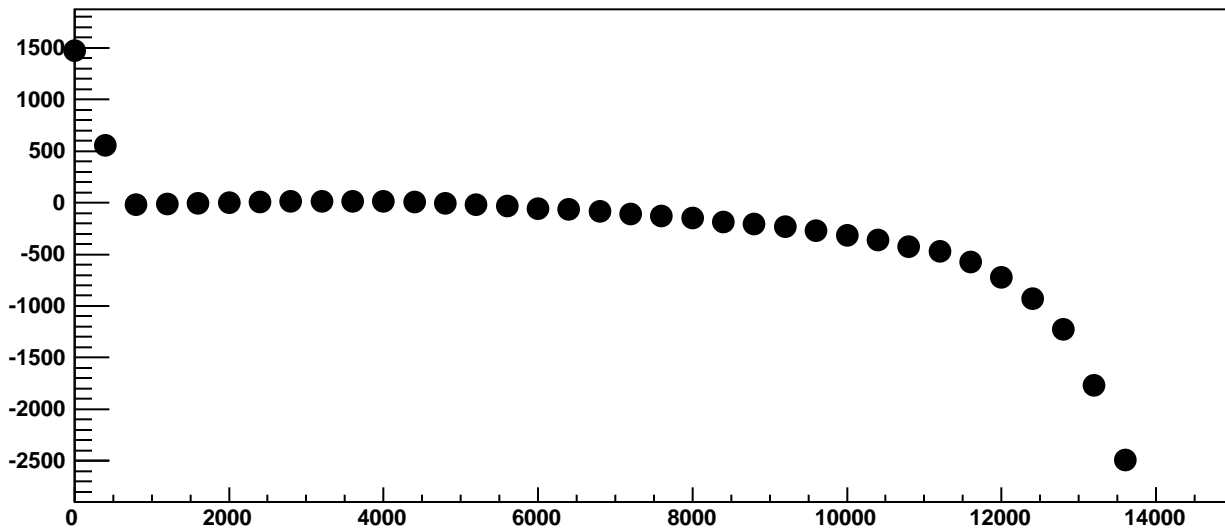


χ^2 / ndf 575.7 / 23
p0 -146.9 ± 1.814
p1 2.33 ± 0.0008042

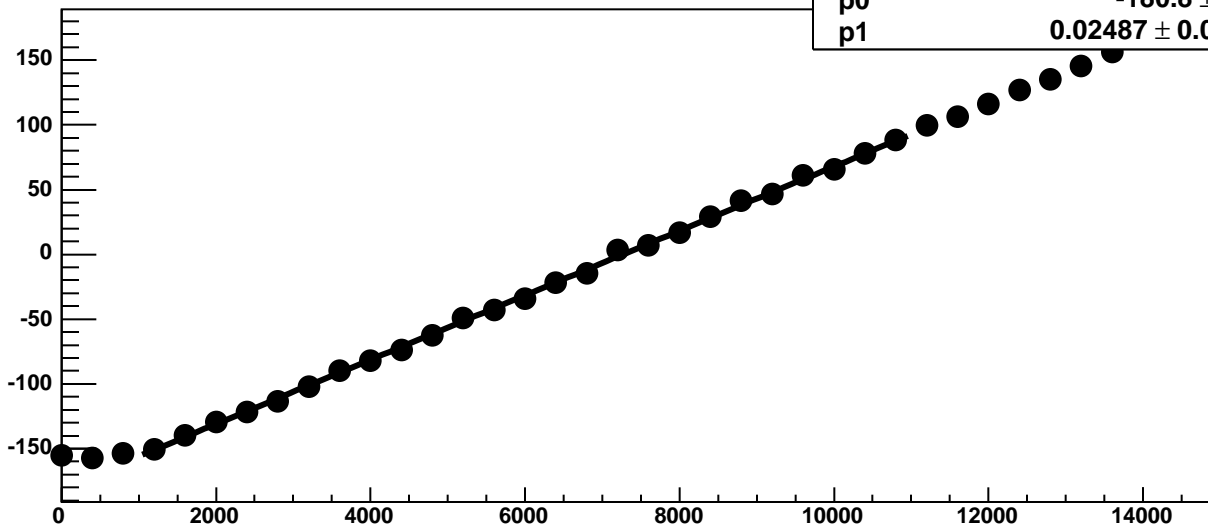
Chip 7, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC

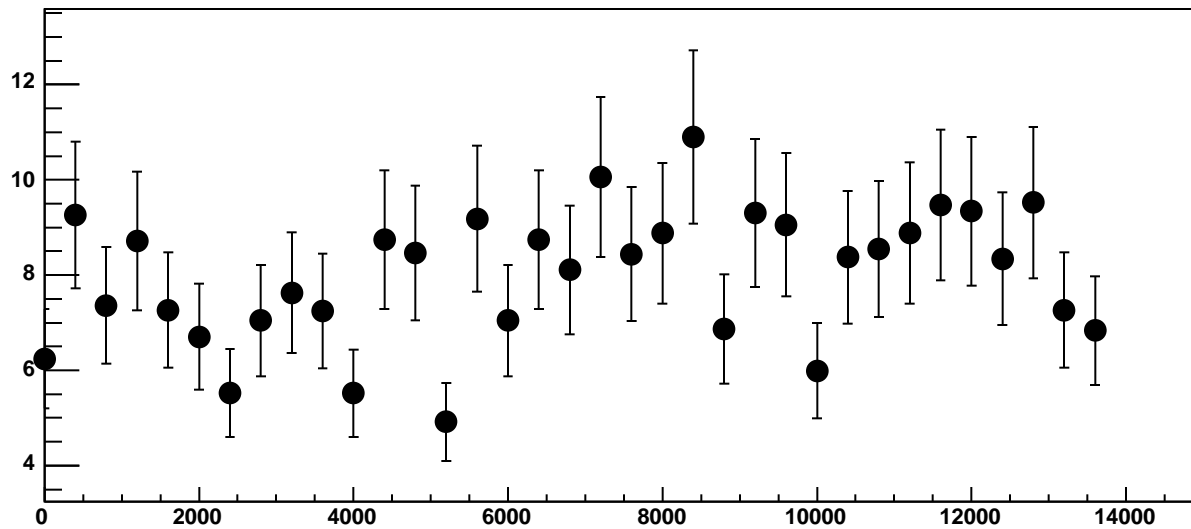


Chip 7, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

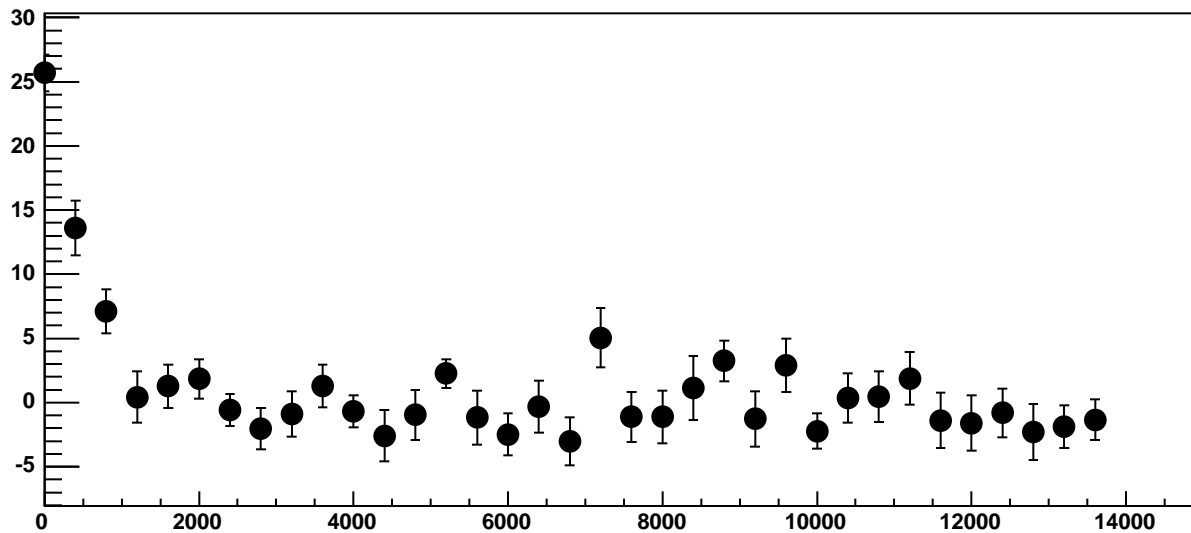


χ^2 / ndf 31.12 / 23
p0 -180.8 ± 0.7524
p1 0.02487 ± 0.0001195

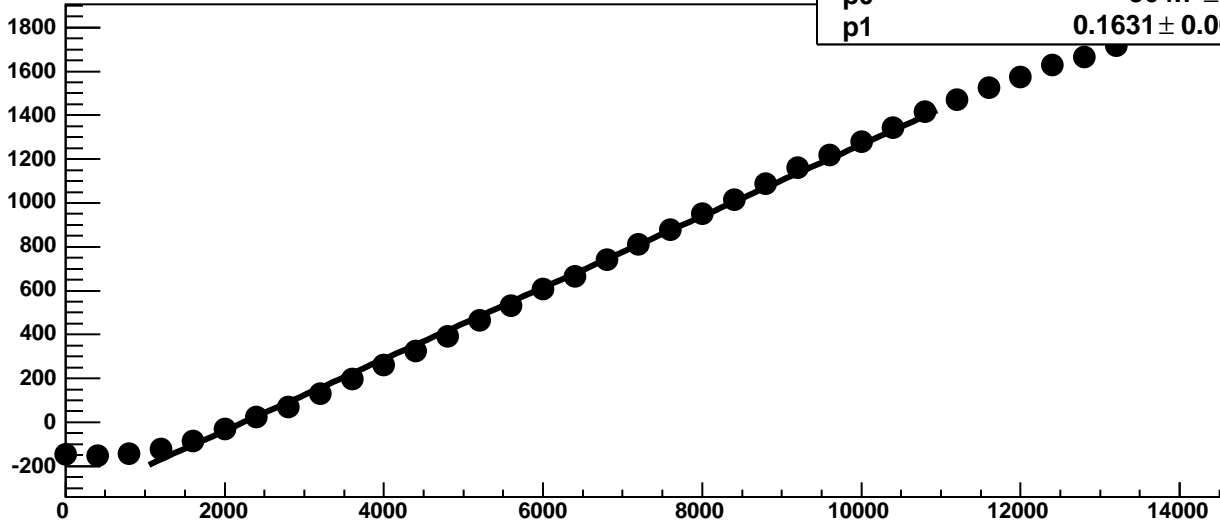
Chip 7, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 7, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

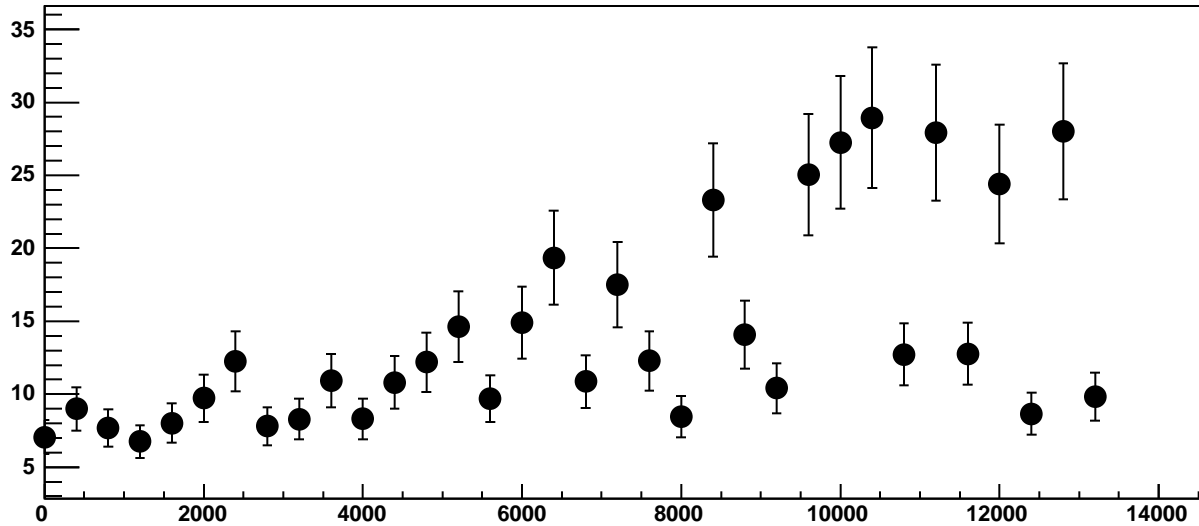


Chip 7, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

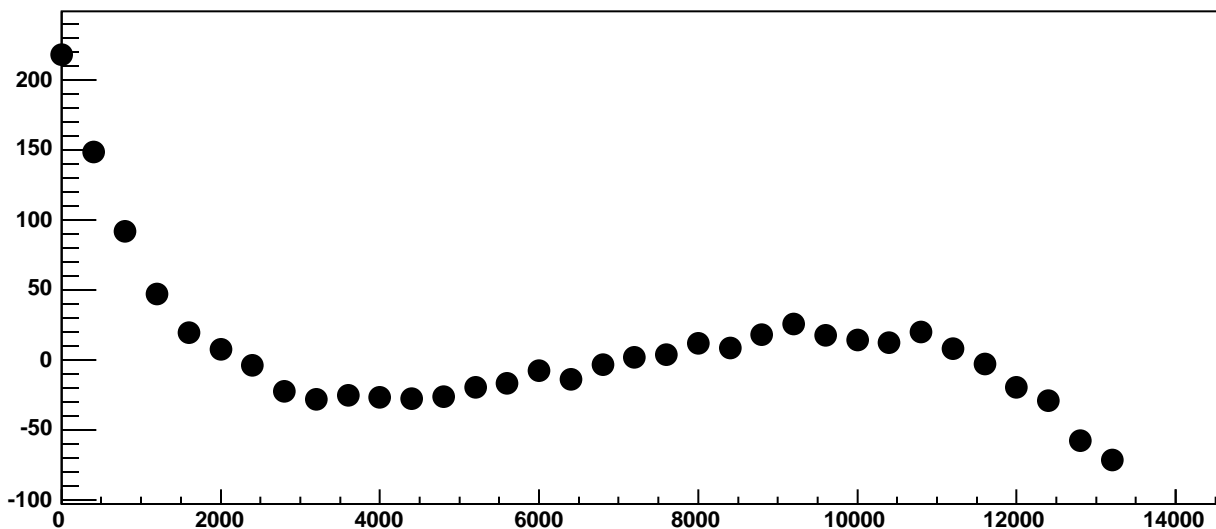


χ^2 / ndf 2293 / 23
p0 -364.7 ± 0.9972
p1 0.1631 ± 0.0001829

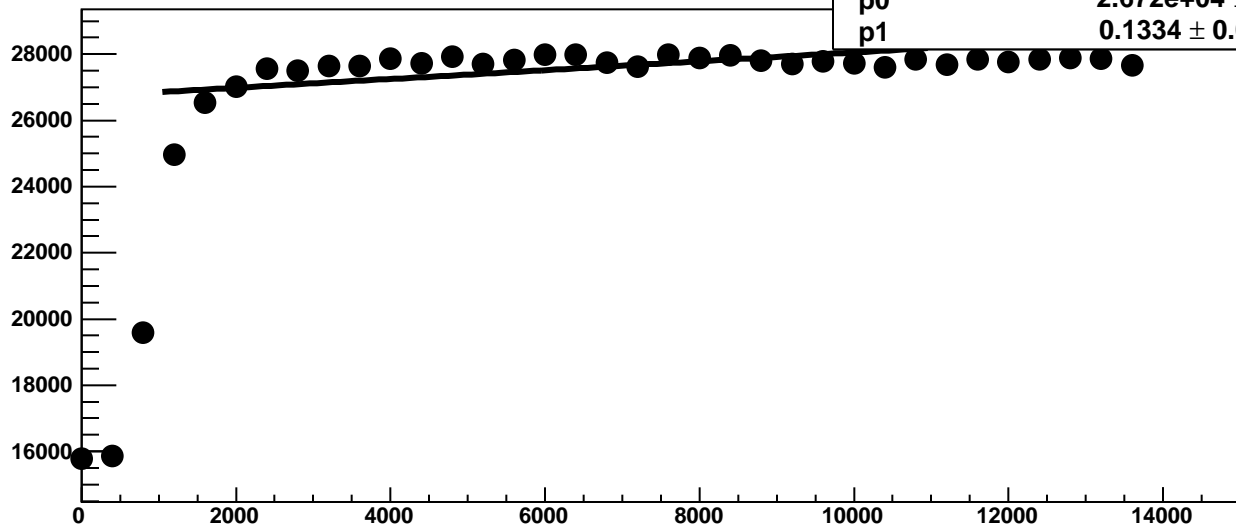
Chip 7, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



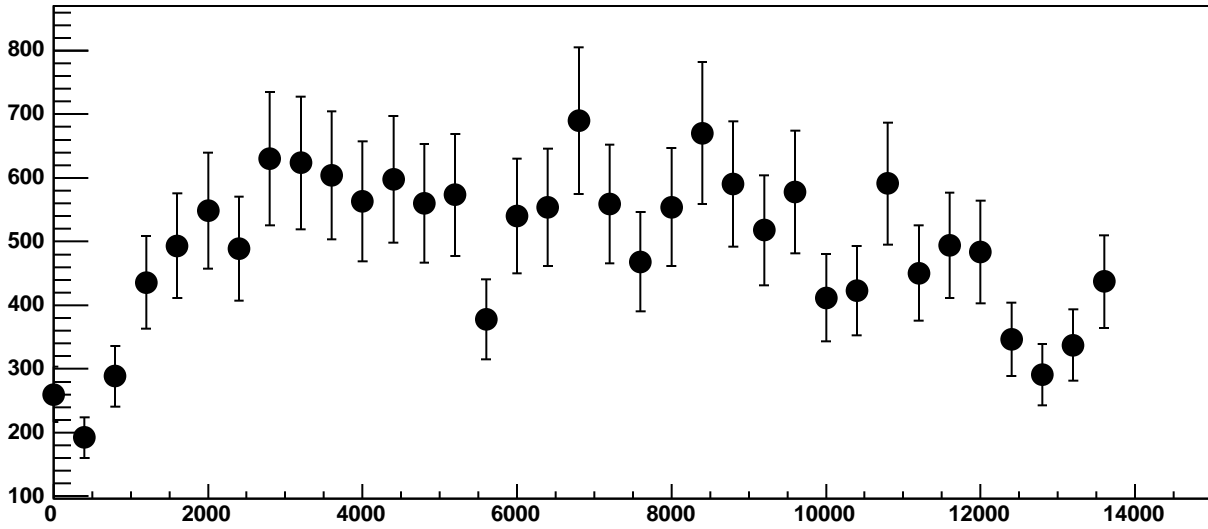
Chip 7, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



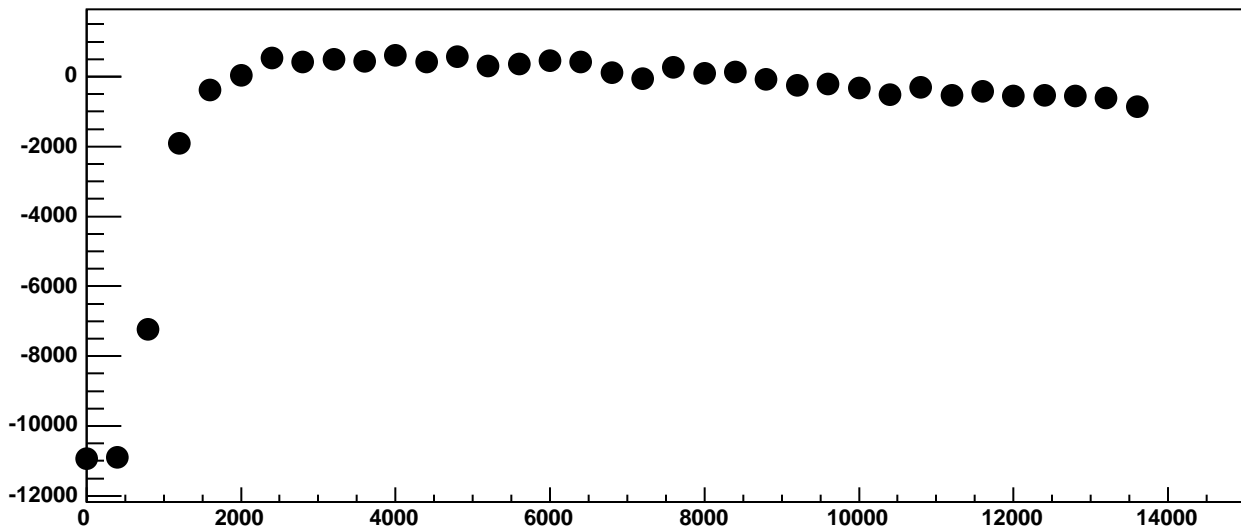
Chip 7, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



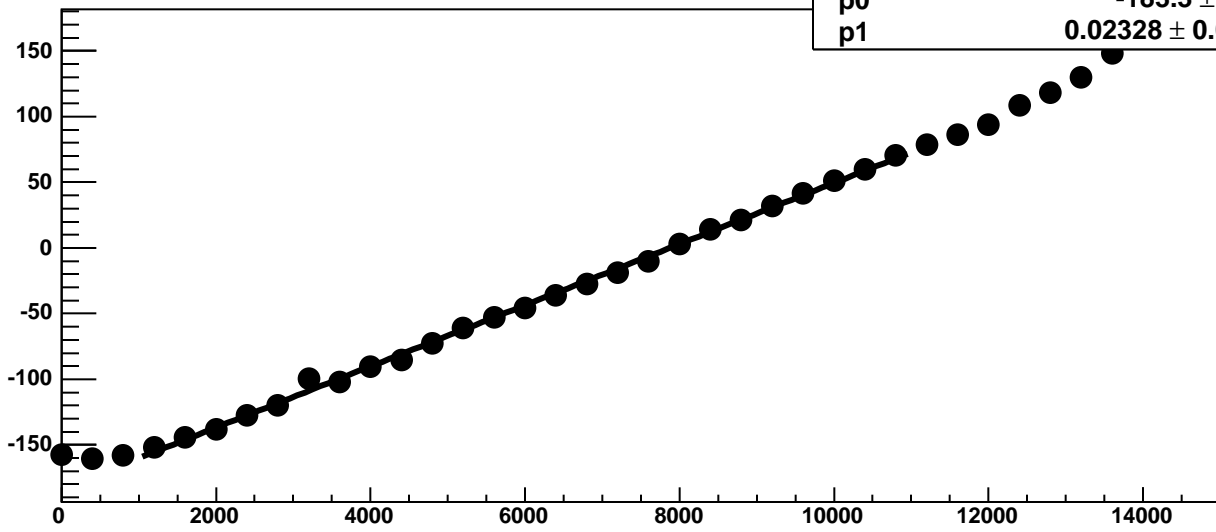
Chip 7, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC

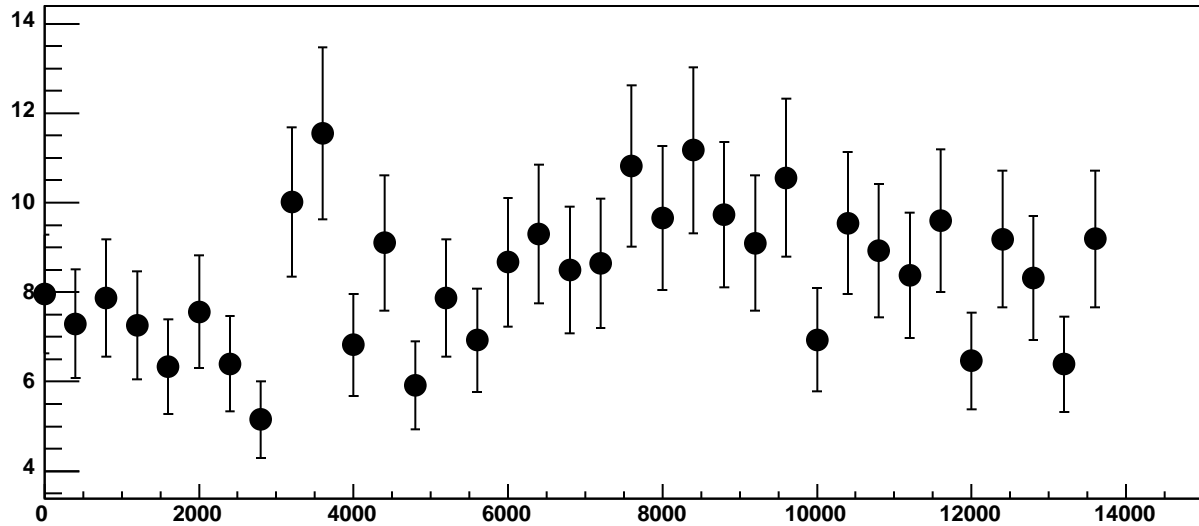


Chip 7, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

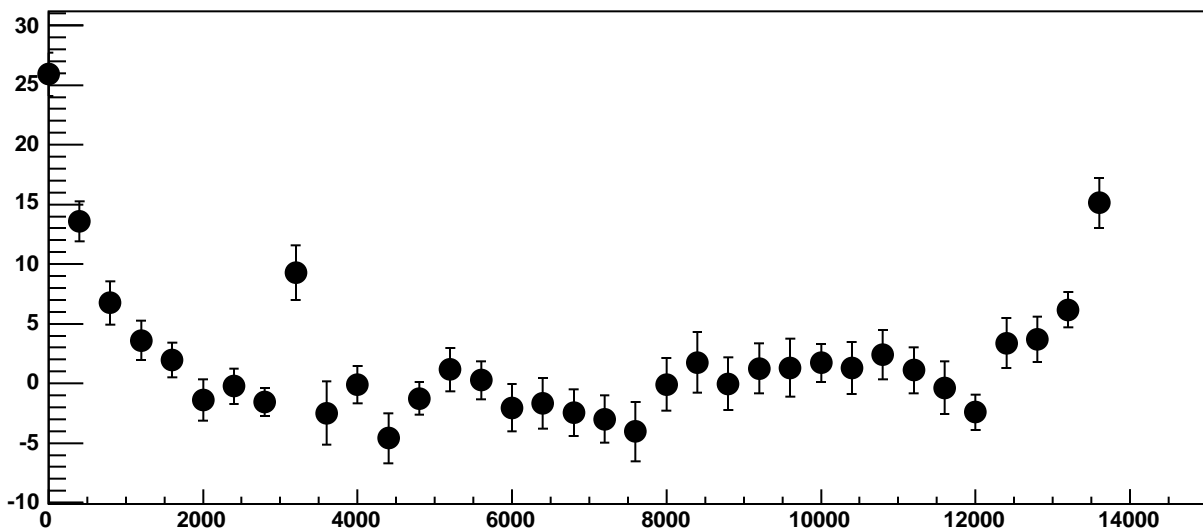


χ^2 / ndf 44.49 / 23
p0 -183.3 ± 0.7642
p1 0.02328 ± 0.000126

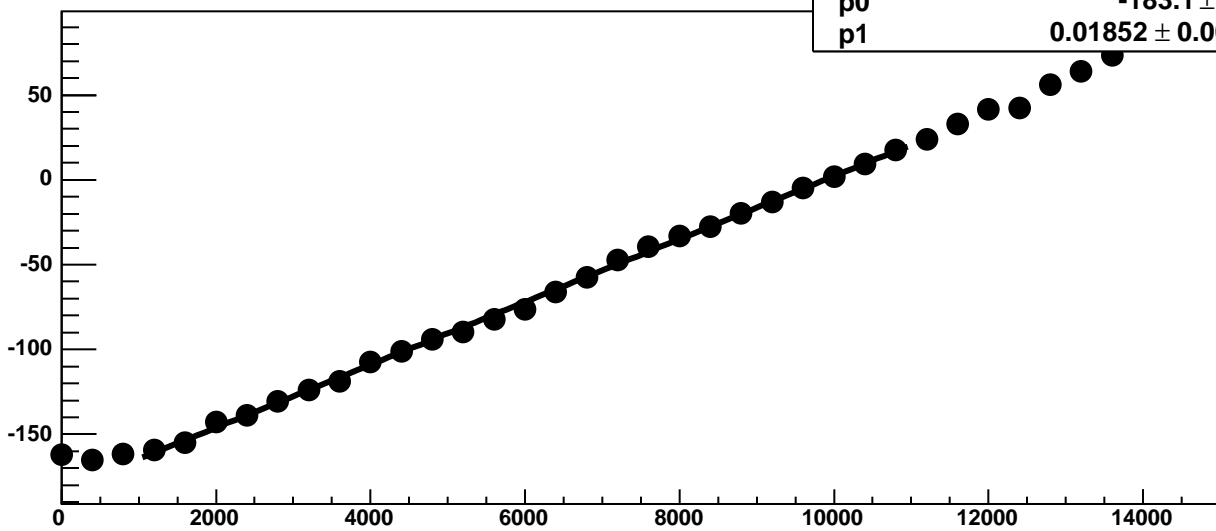
Chip 7, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



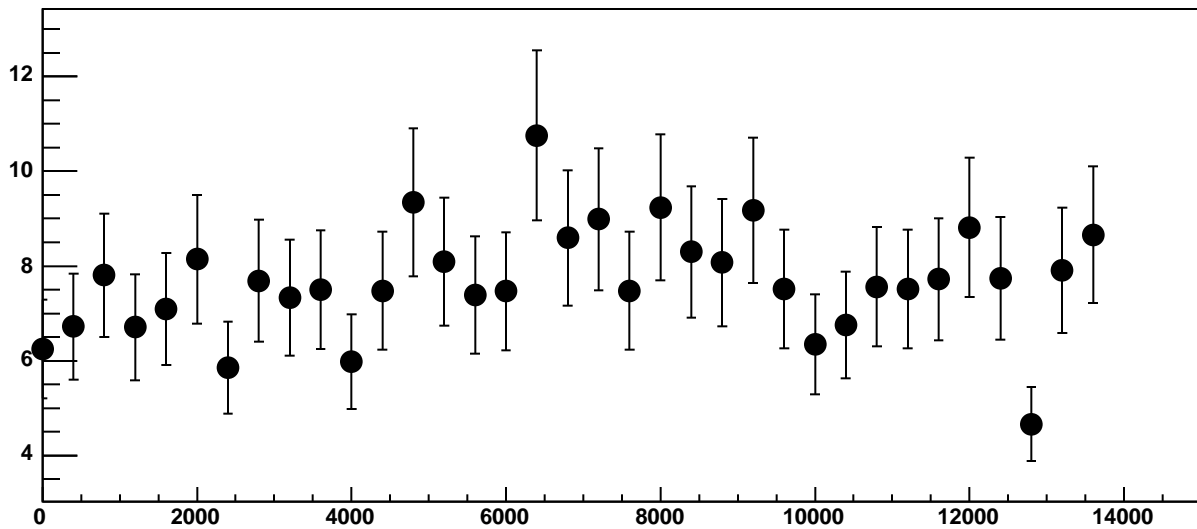
Chip 7, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



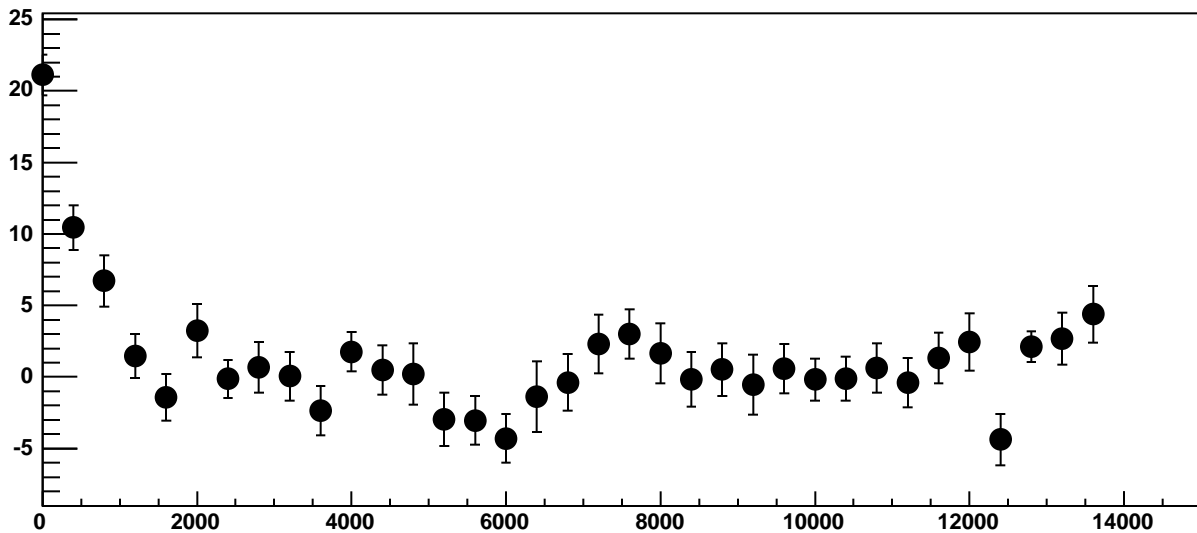
Chip 7, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



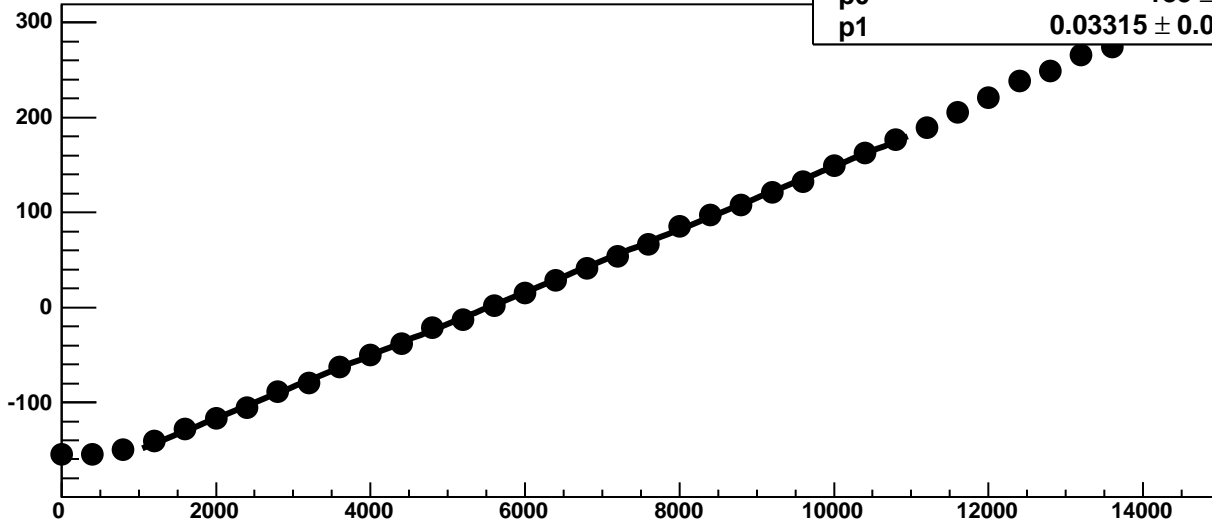
Chip 7, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

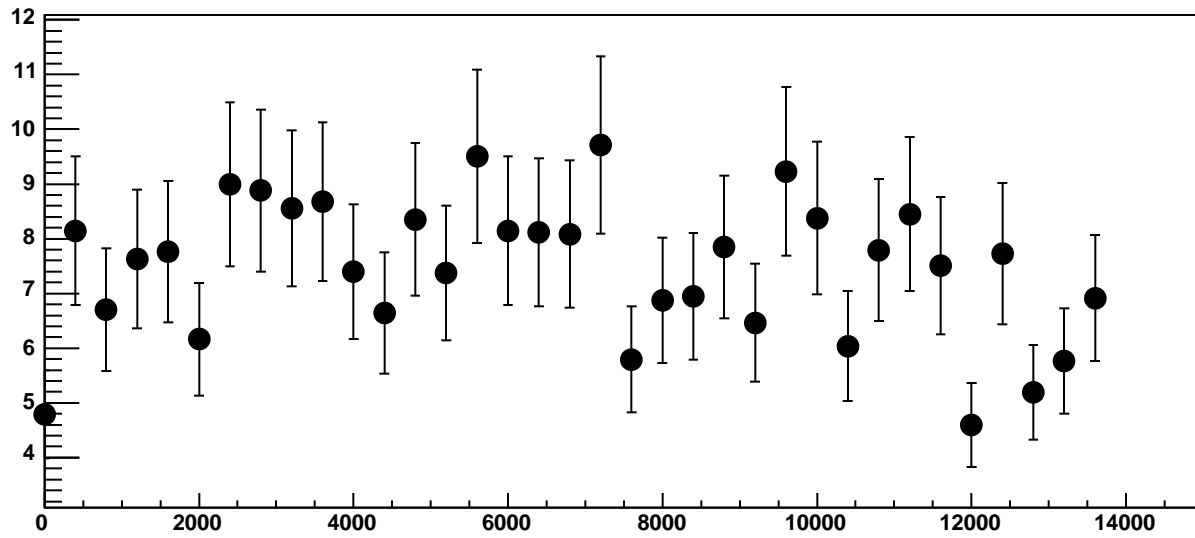


Chip 7, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC

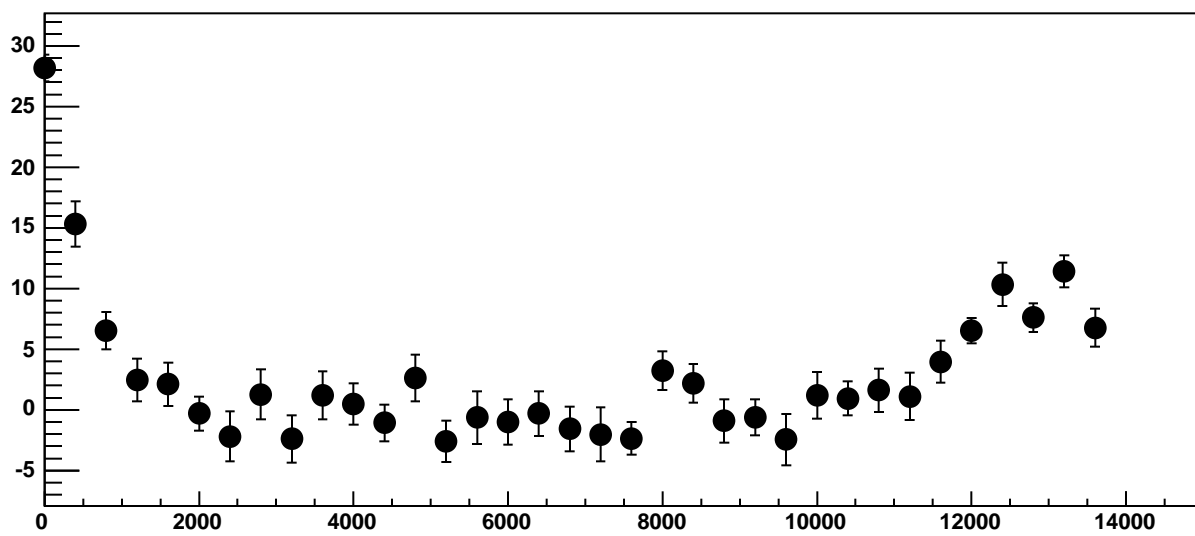


χ^2 / ndf 26.21 / 23
p0 -183 ± 0.8088
p1 0.03315 ± 0.0001187

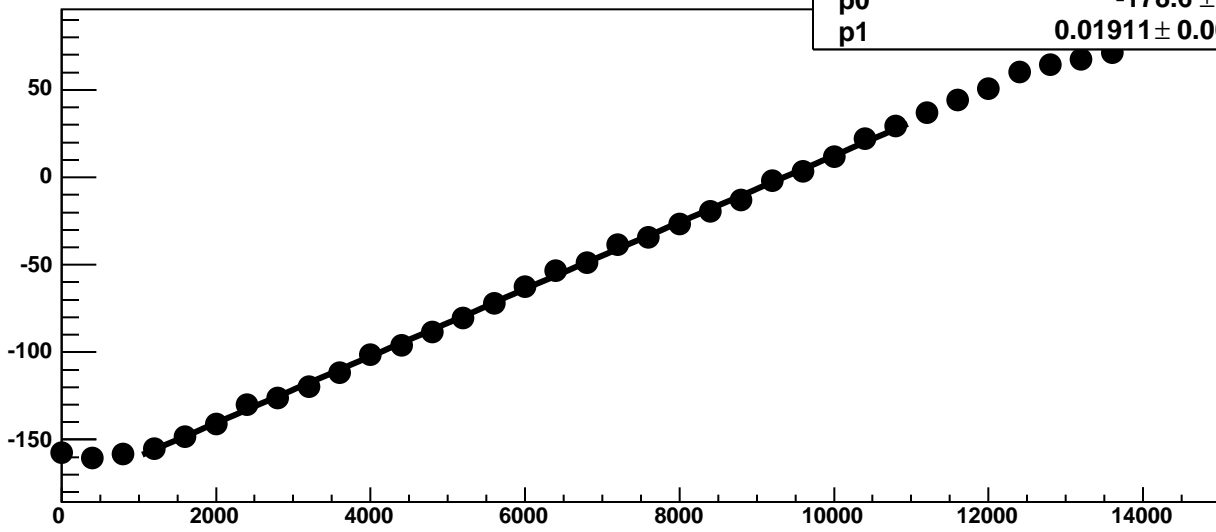
Chip 7, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC

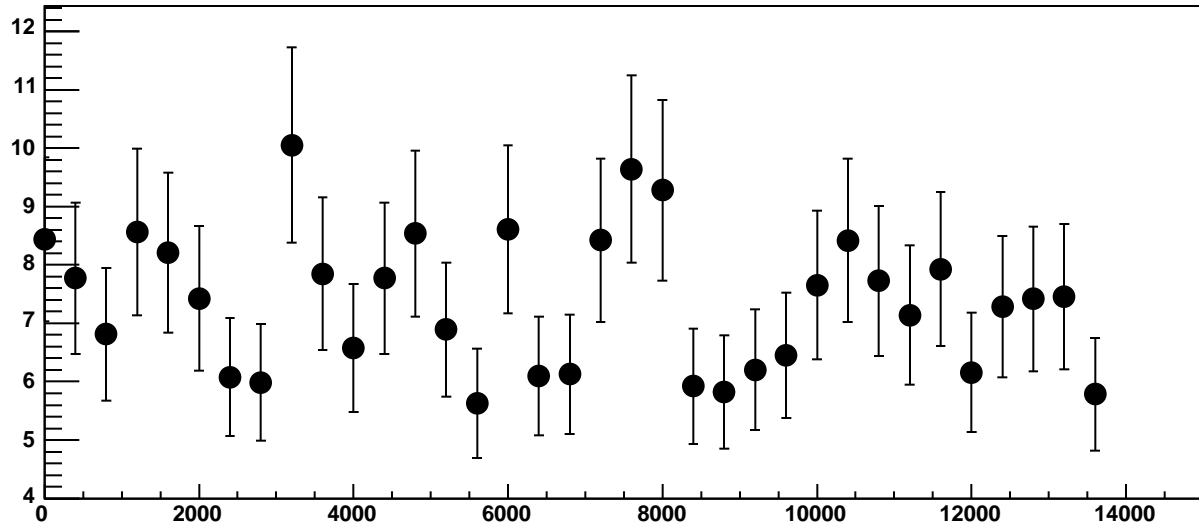


Chip 7, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

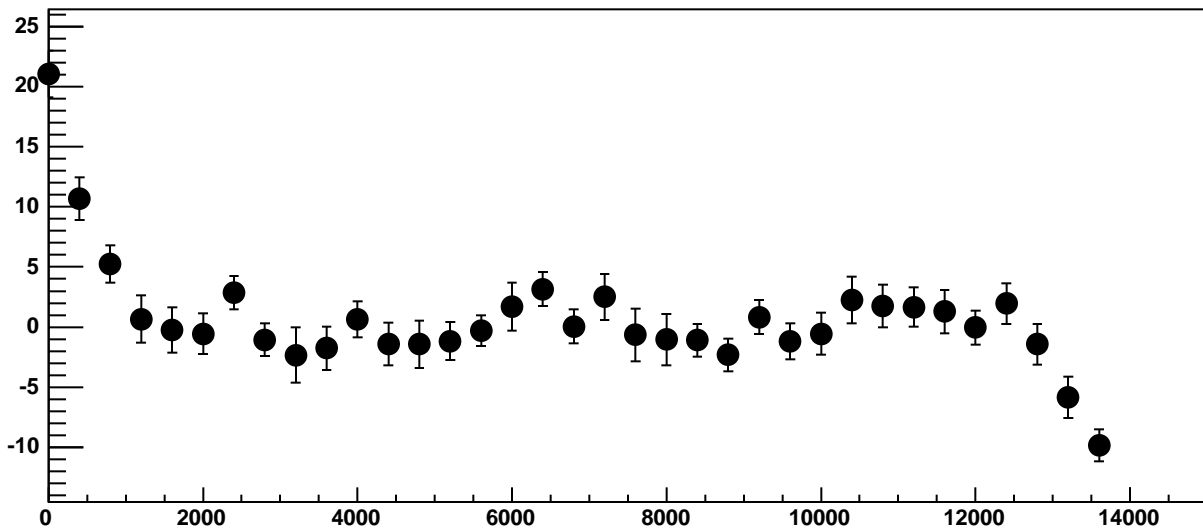


χ^2 / ndf 23.89 / 23
p0 -178.6 ± 0.7805
p1 0.01911 ± 0.0001163

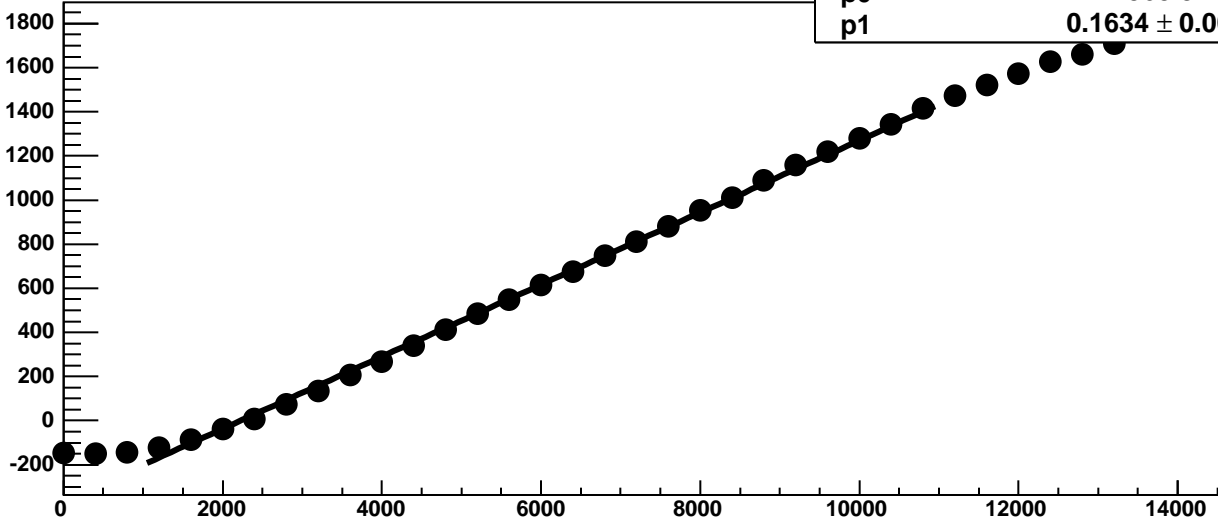
Chip 7, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



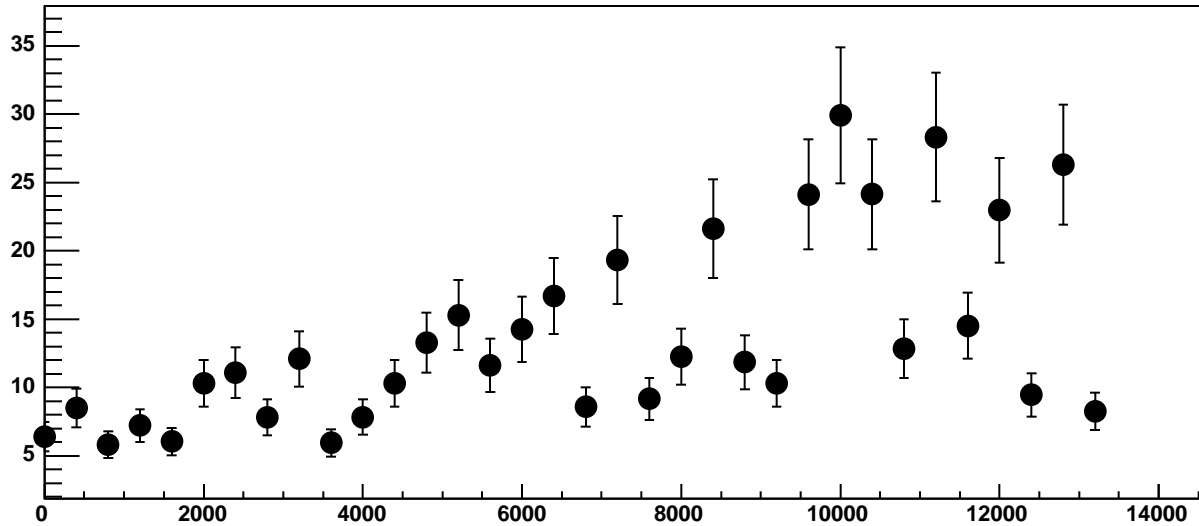
Chip 7, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



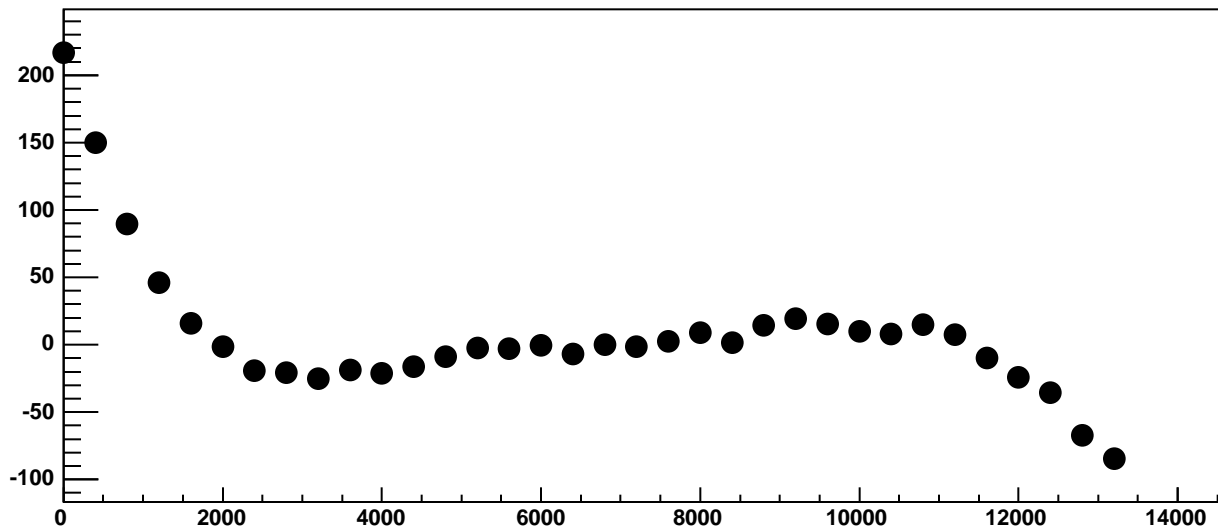
Chip 7, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



Chip 7, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

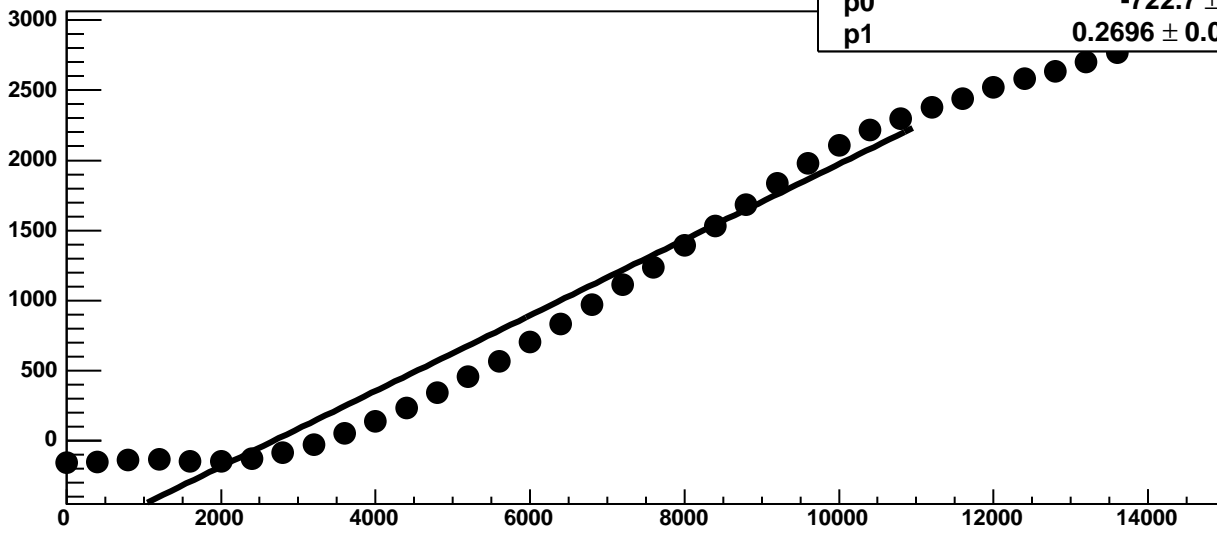


Chip 7, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

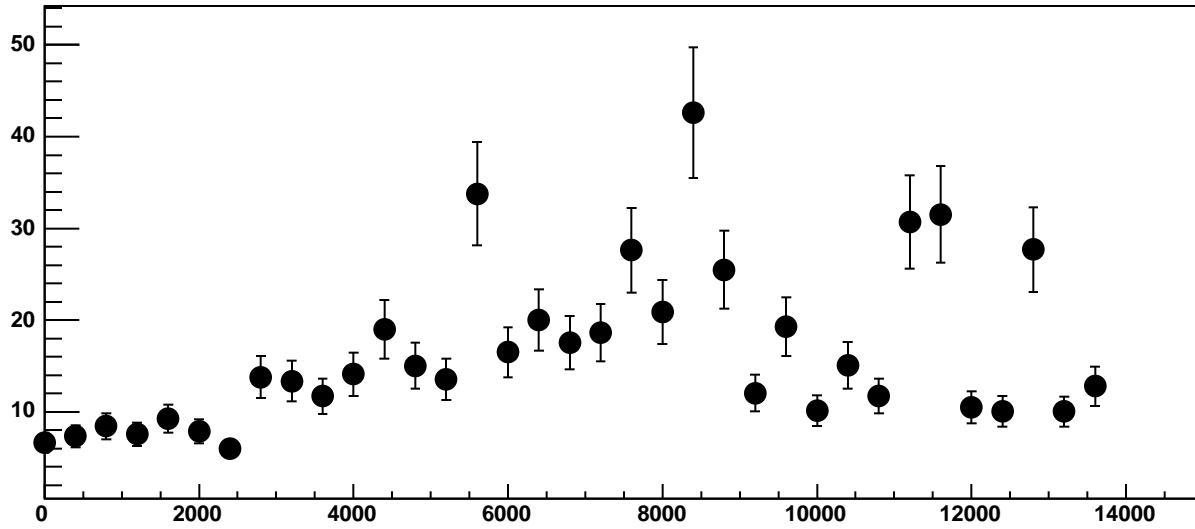


Chip 7, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC

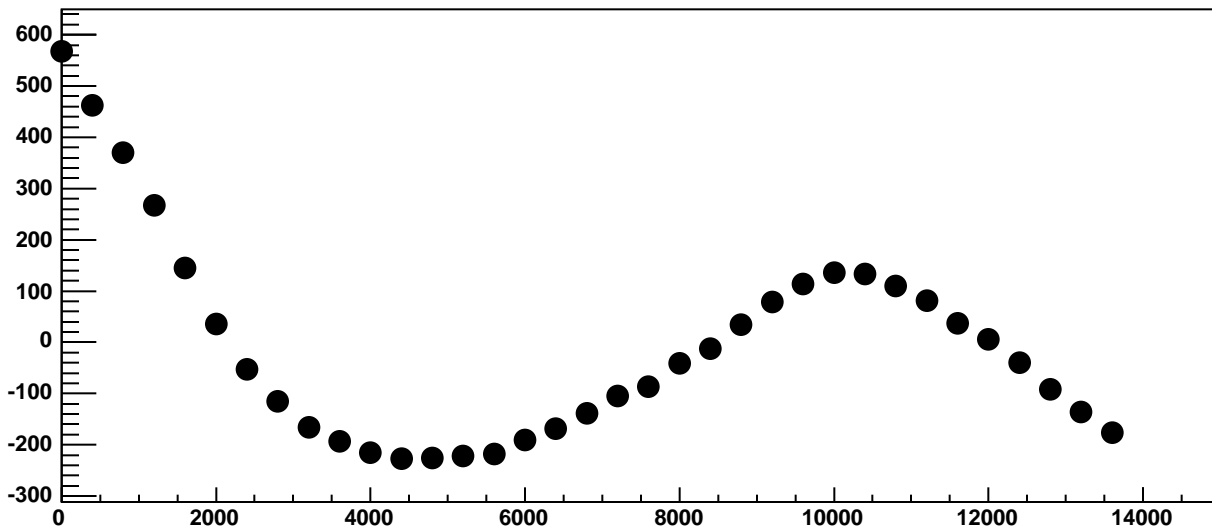
χ^2 / ndf 7.13e+04 / 23
p0 -722.7 ± 0.9946
p1 0.2696 ± 0.0001781



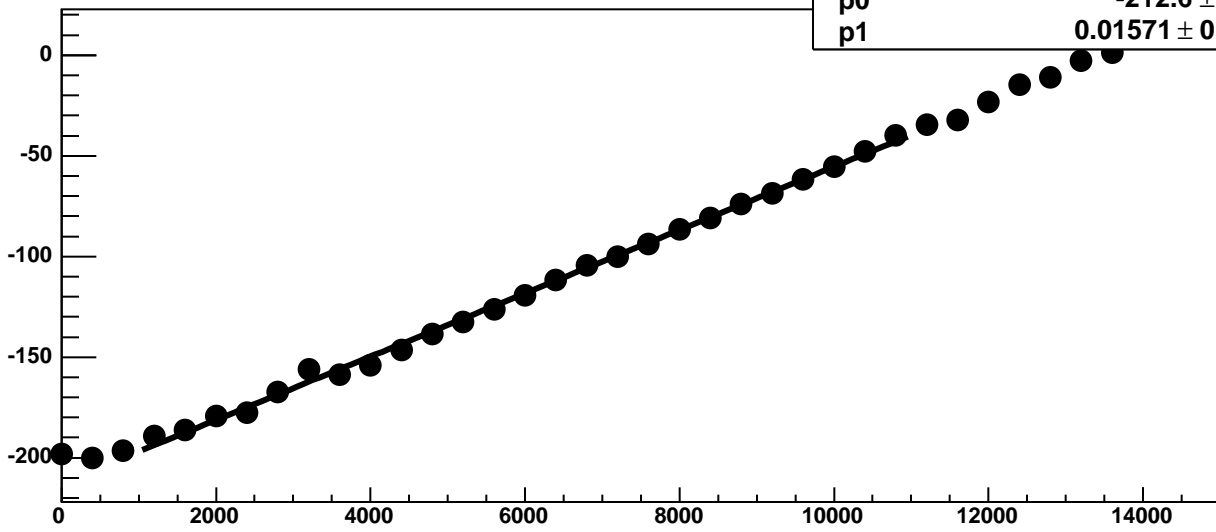
Chip 7, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

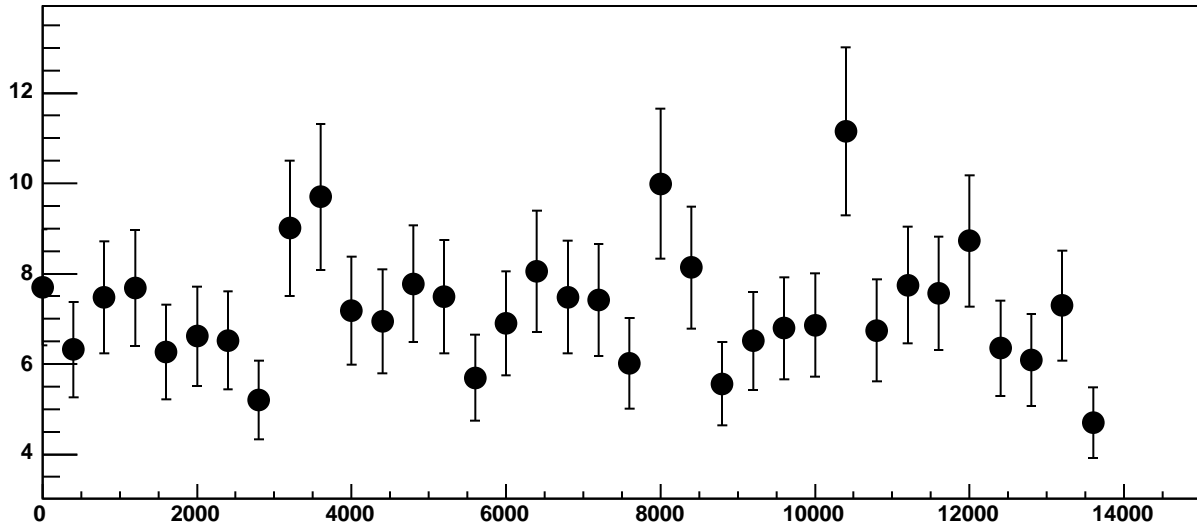


Chip 7, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

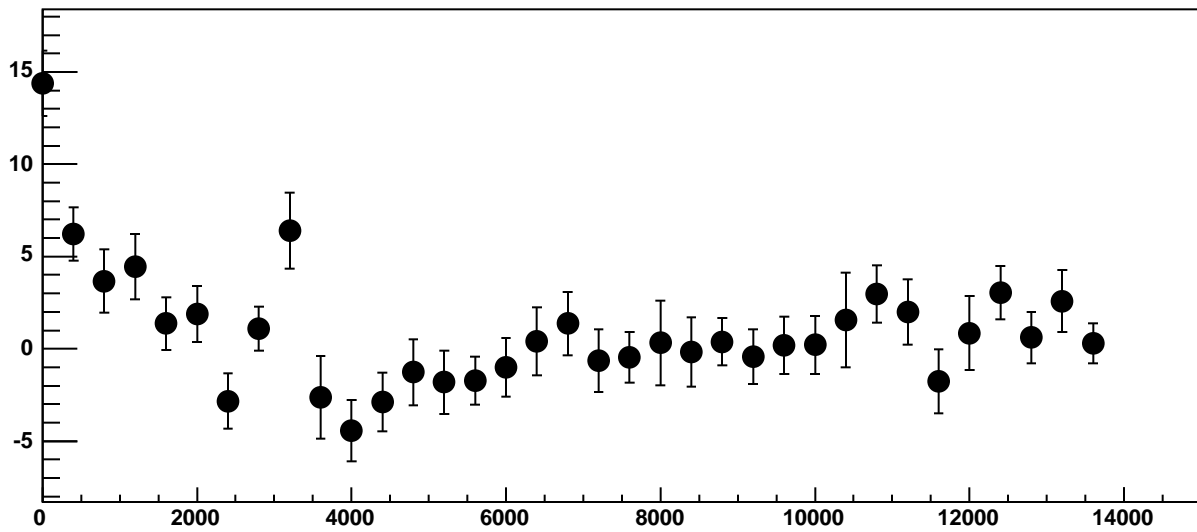


χ^2 / ndf 43.67 / 23
p0 -212.6 ± 0.7292
p1 0.01571 ± 0.000111

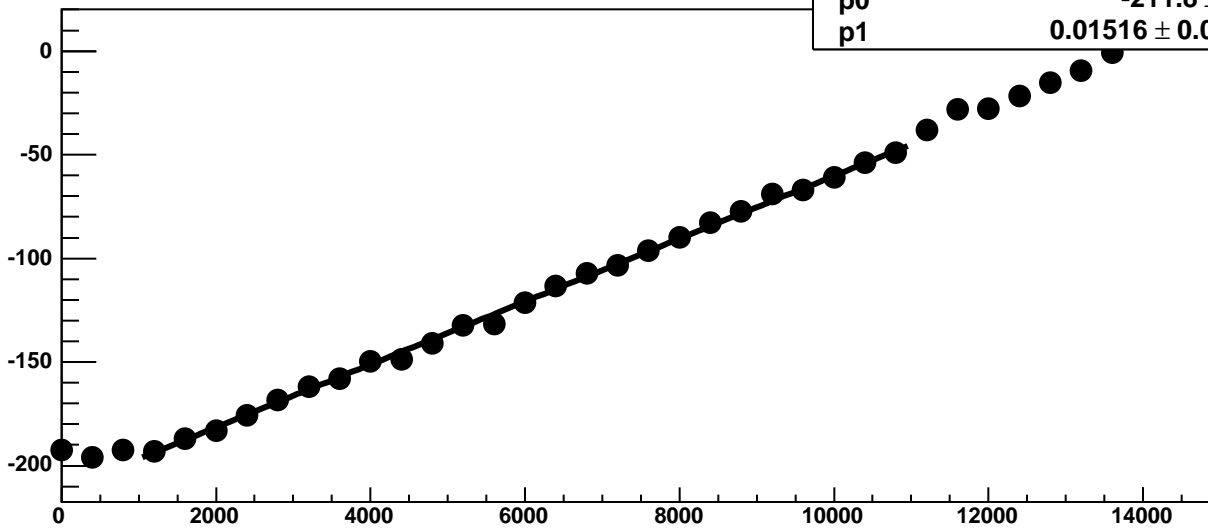
Chip 7, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

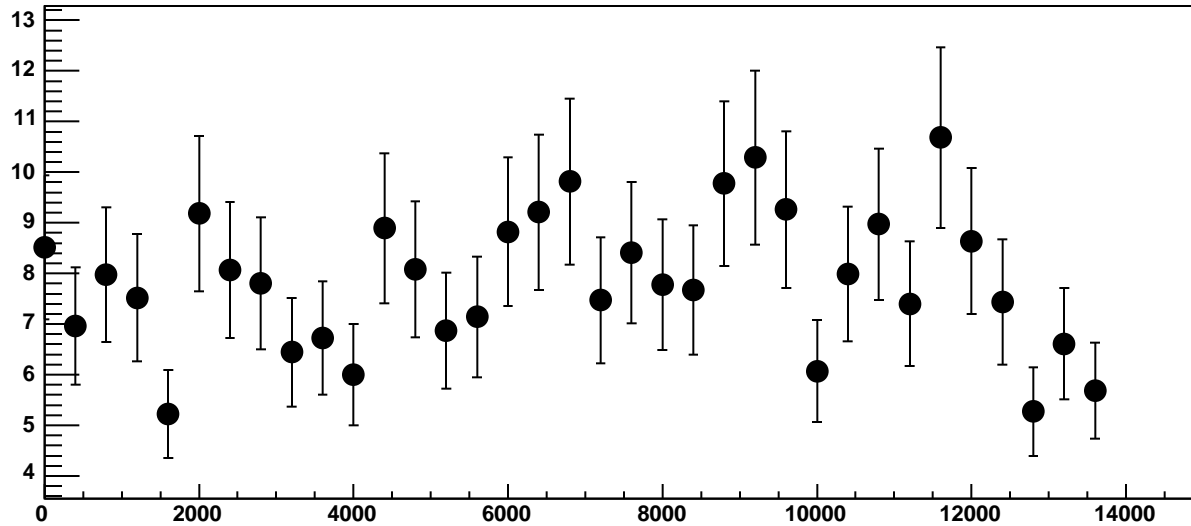


Chip 7, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

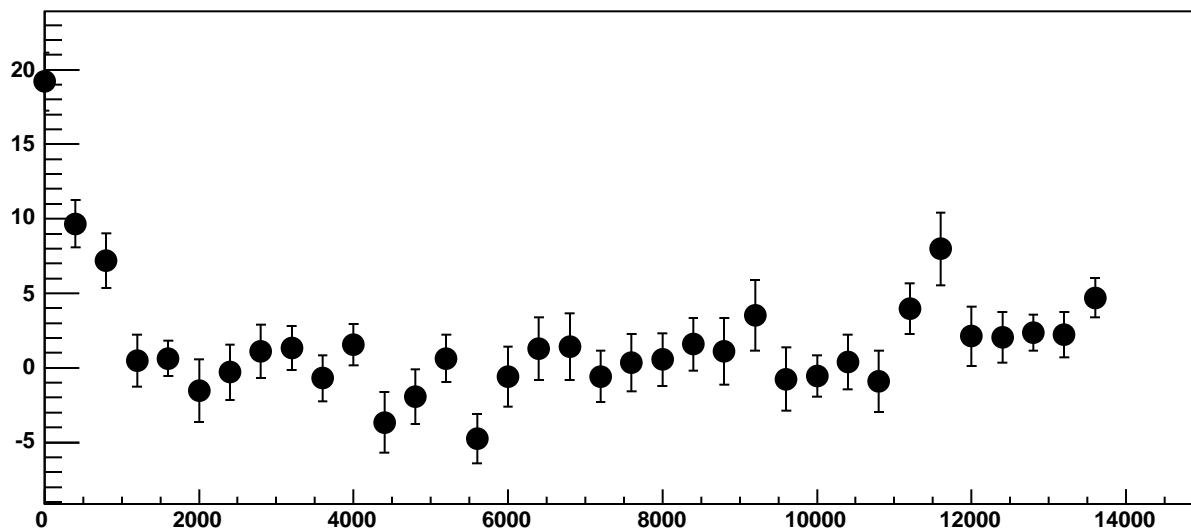


χ^2 / ndf 21.36 / 23
p0 -211.8 ± 0.7551
p1 0.01516 ± 0.0001194

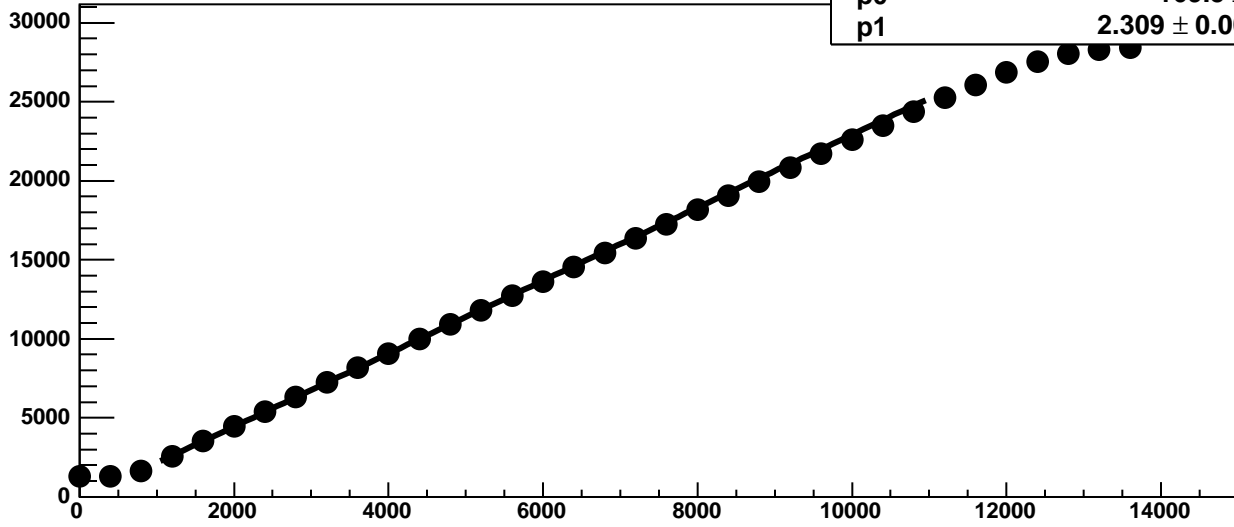
Chip 7, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



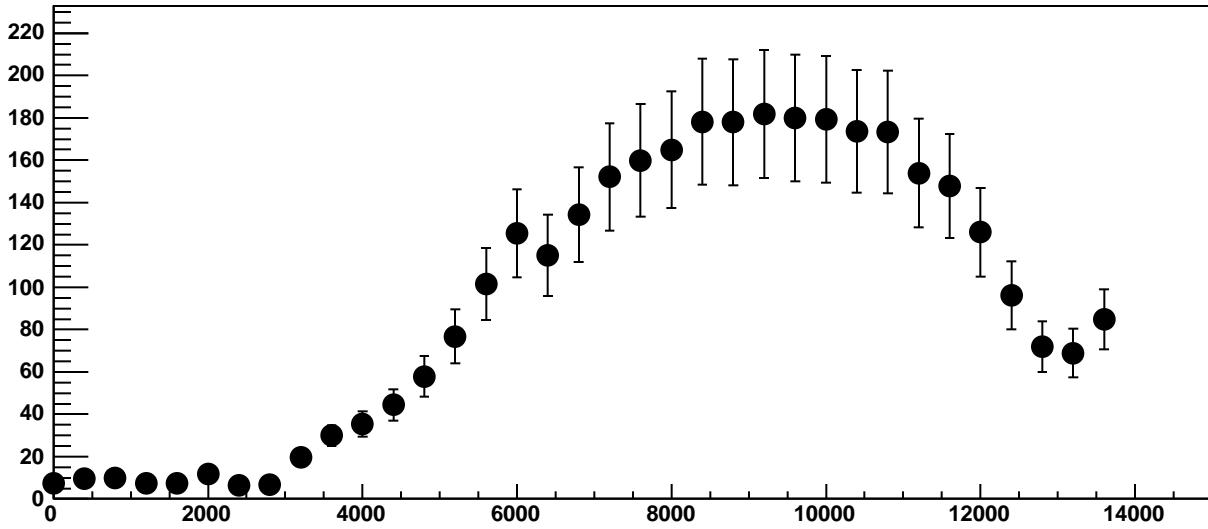
Chip 7, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



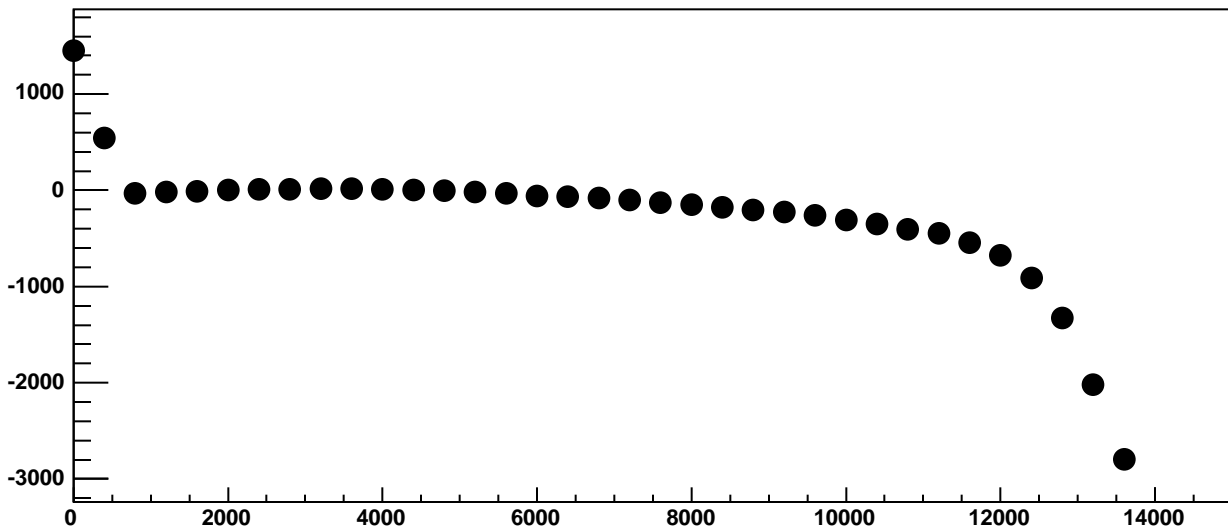
Chip 7, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC



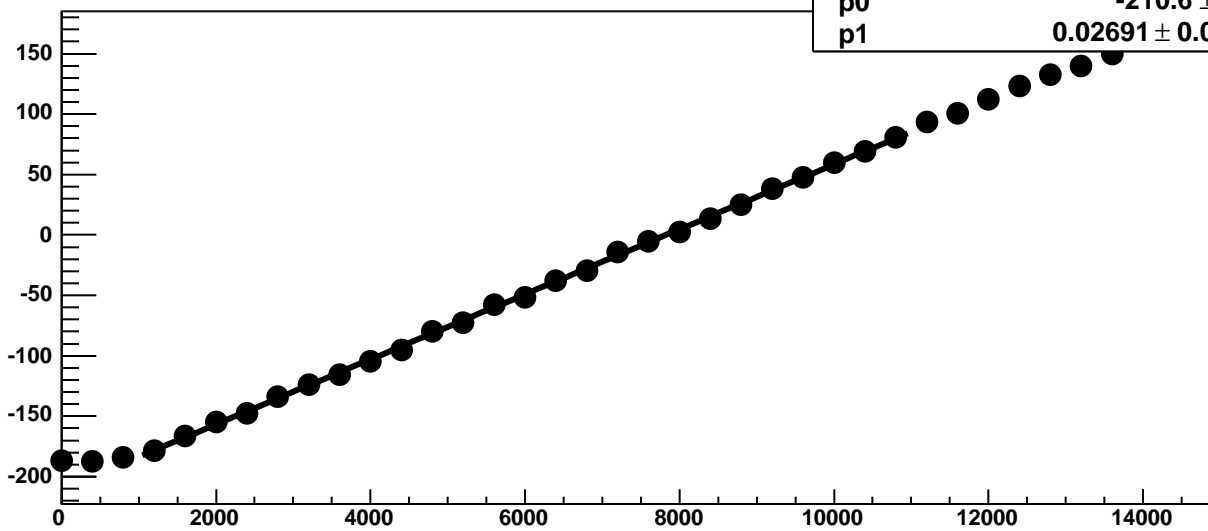
Chip 7, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC

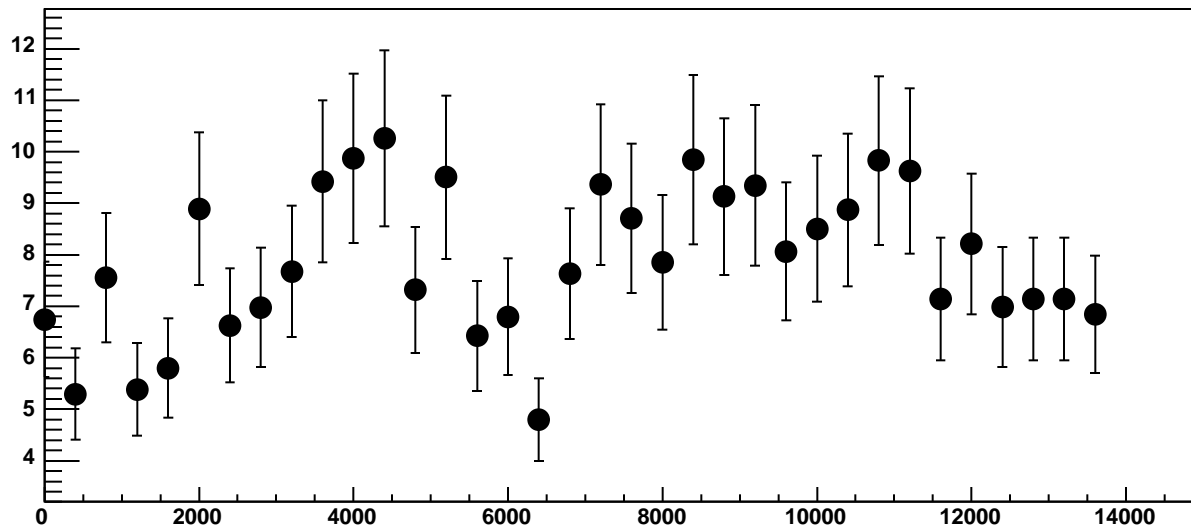


Chip 7, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

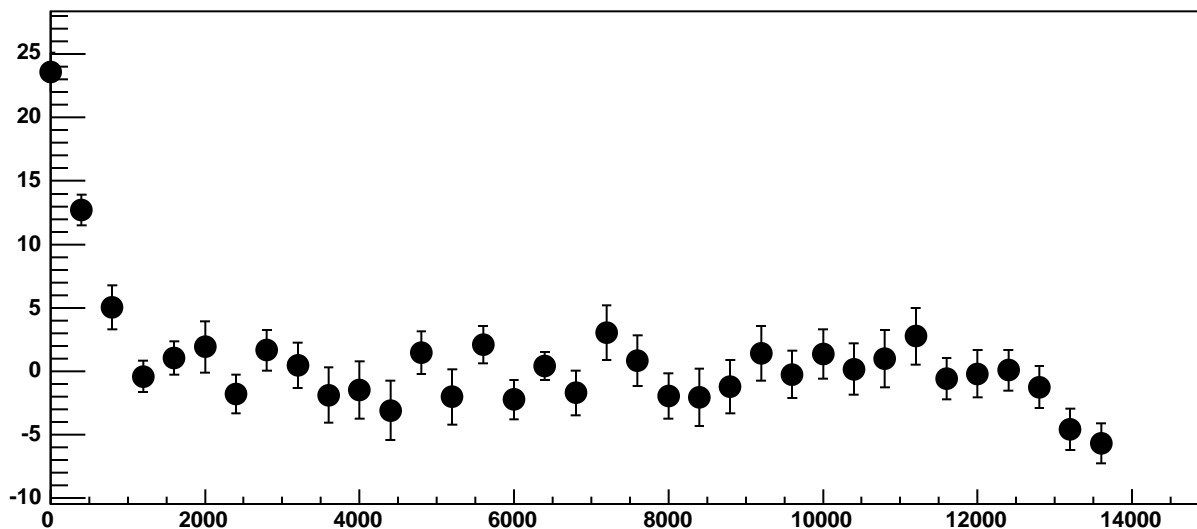


χ^2 / ndf 19.51 / 23
p0 -210.6 ± 0.7494
p1 0.02691 ± 0.0001216

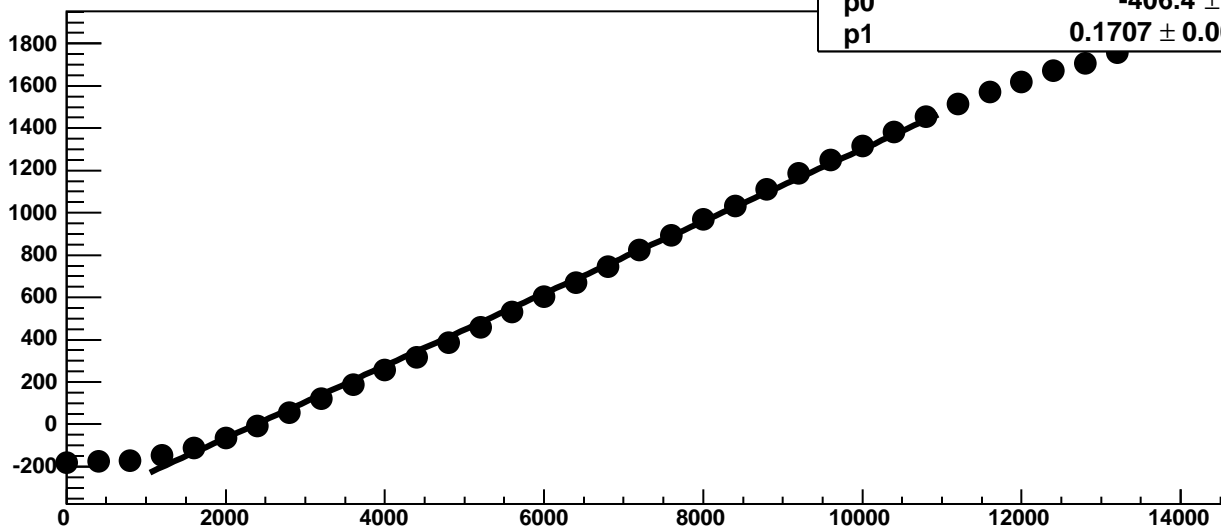
Chip 7, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



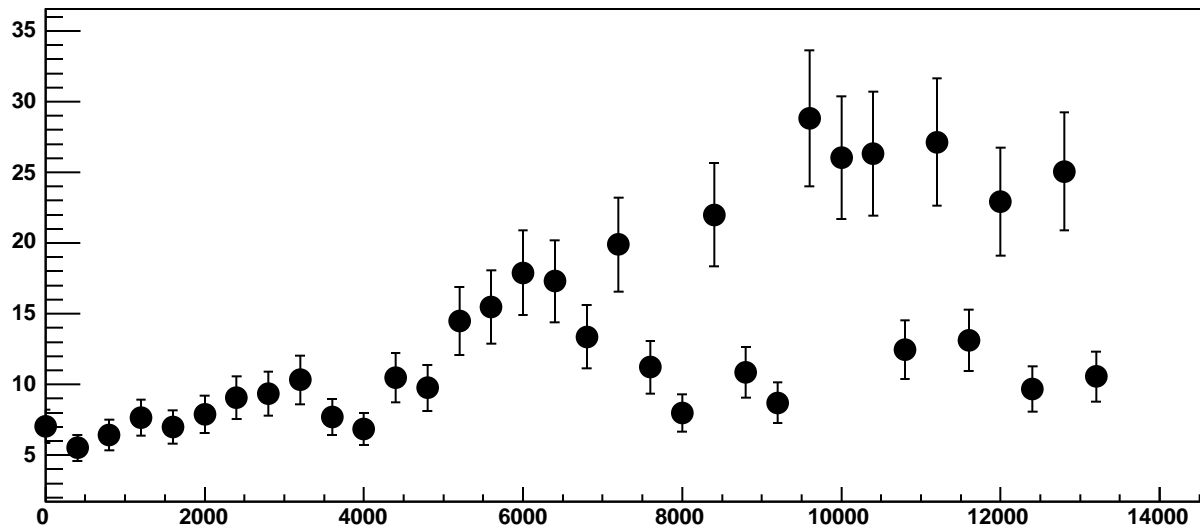
Chip 7, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



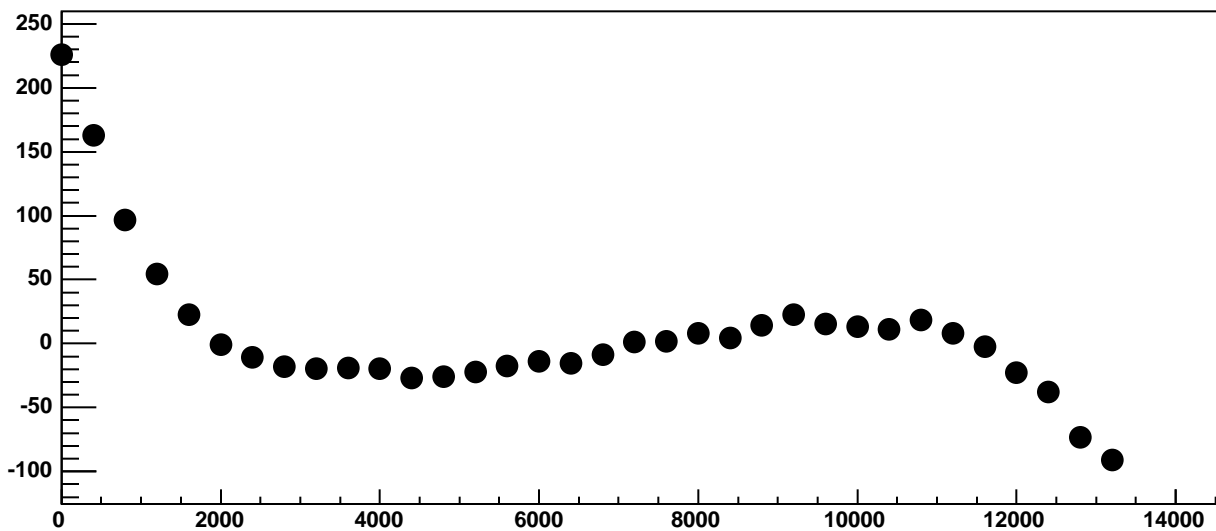
Chip 7, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



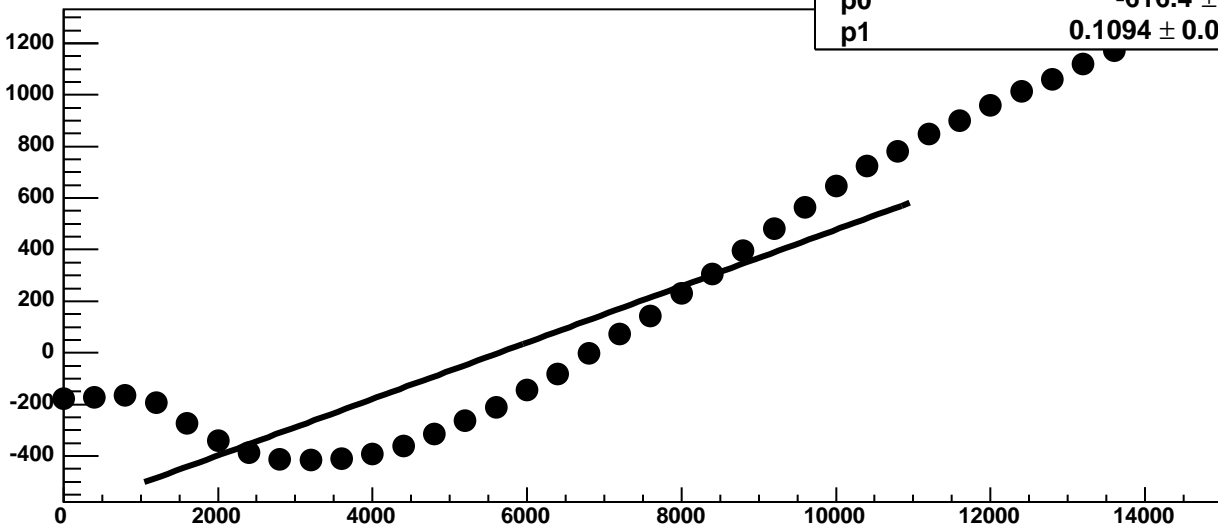
Chip 7, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 7, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

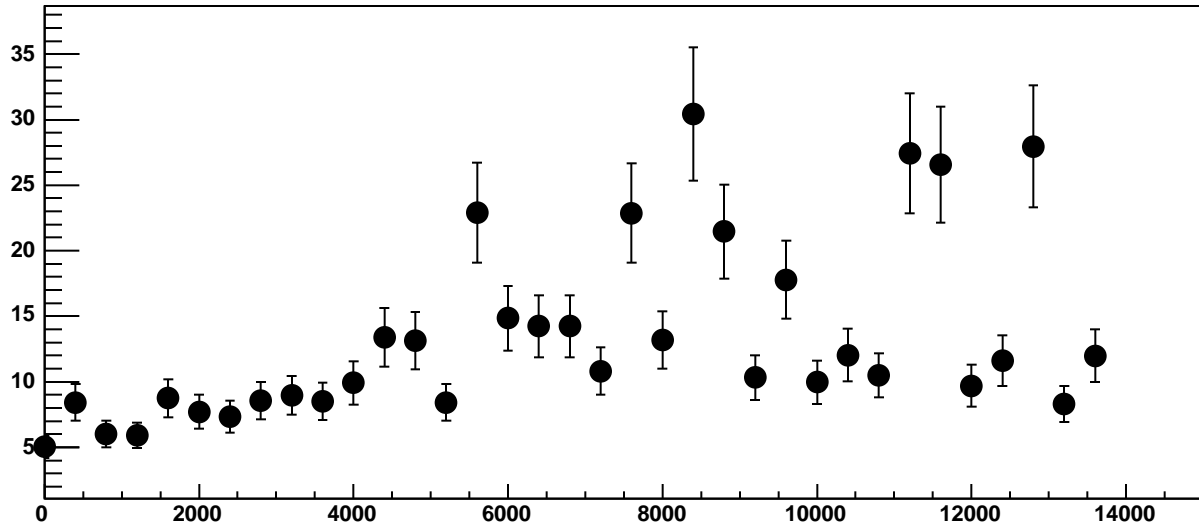


Chip 7, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

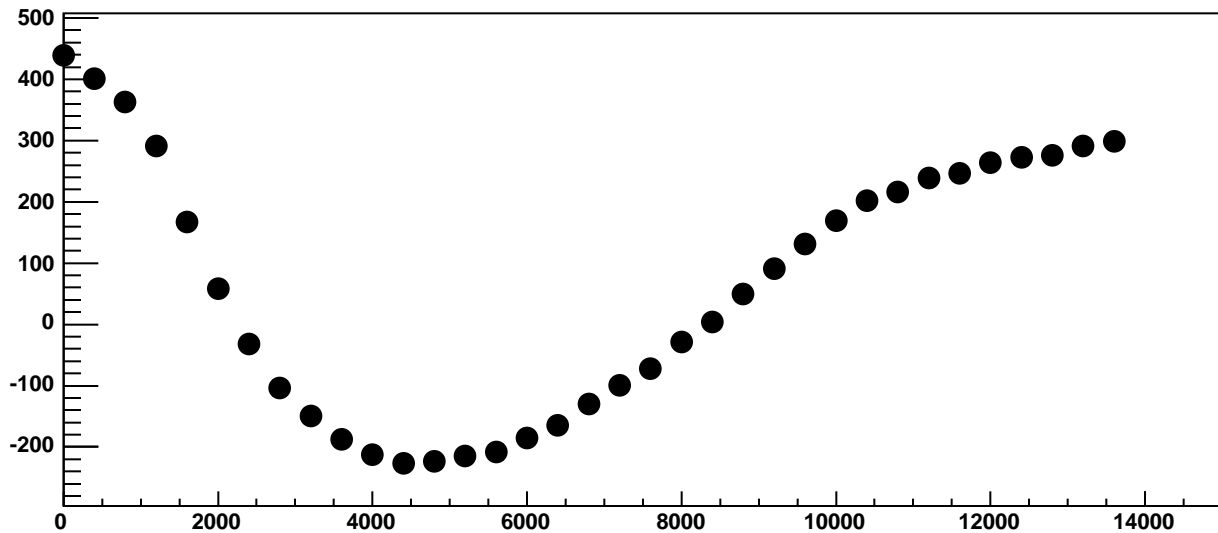


χ^2 / ndf 1.365e+05 / 23
p0 -616.4 ± 0.8795
p1 0.1094 ± 0.0001561

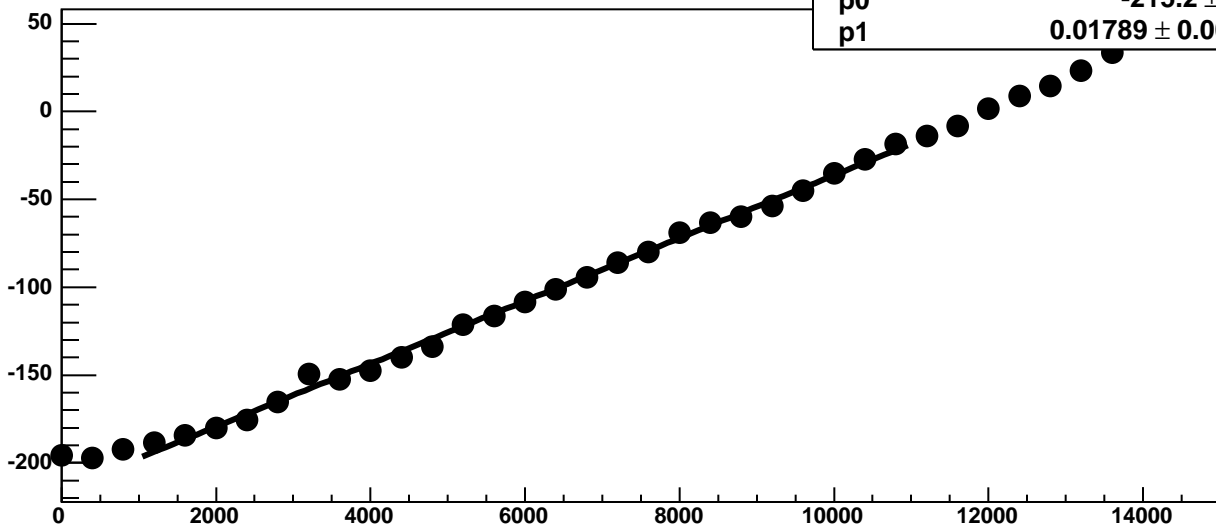
Chip 7, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



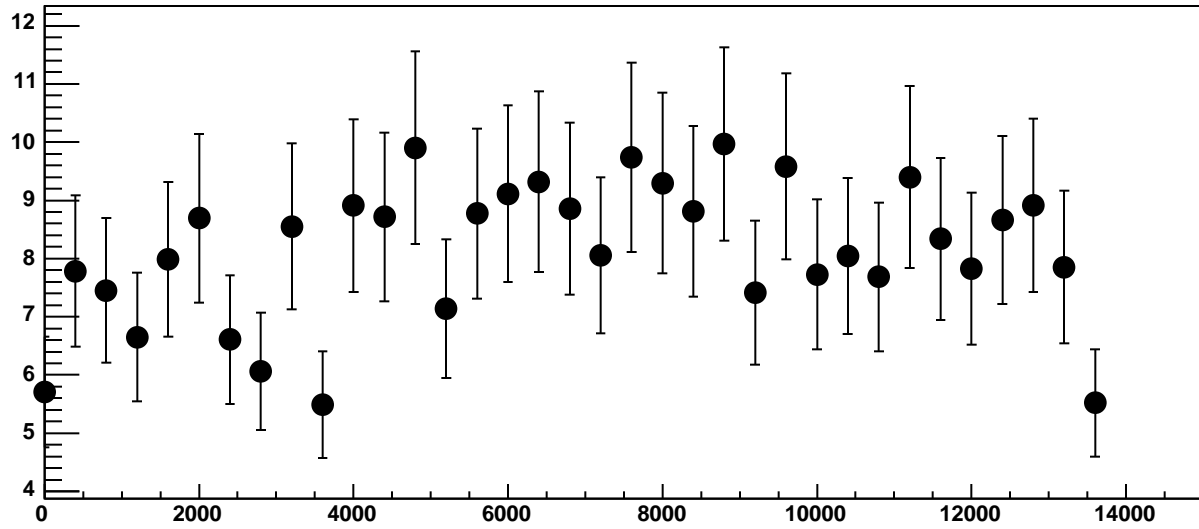
Chip 7, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



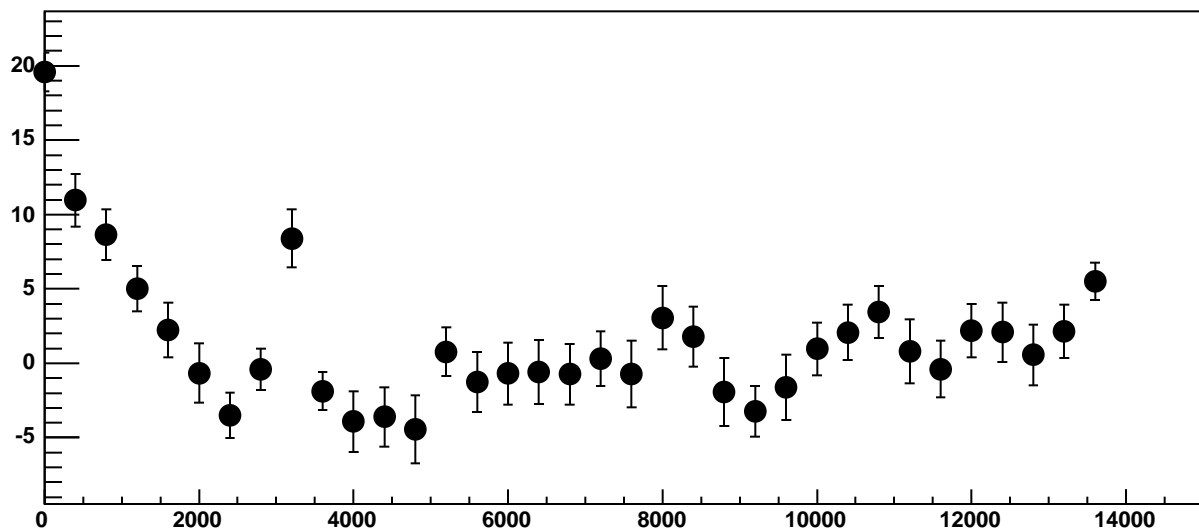
Chip 7, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC



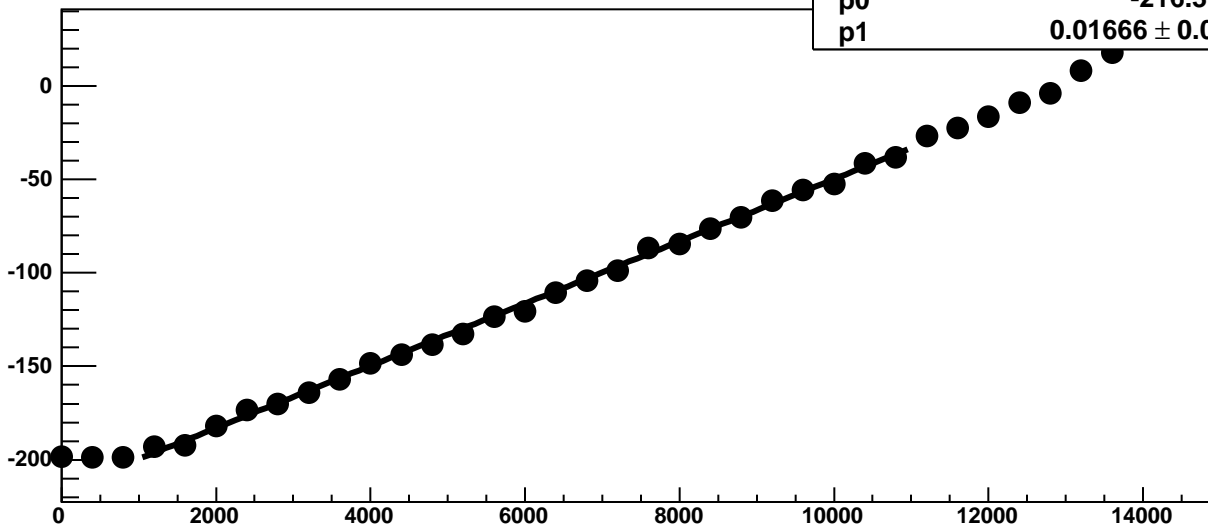
Chip 7, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC

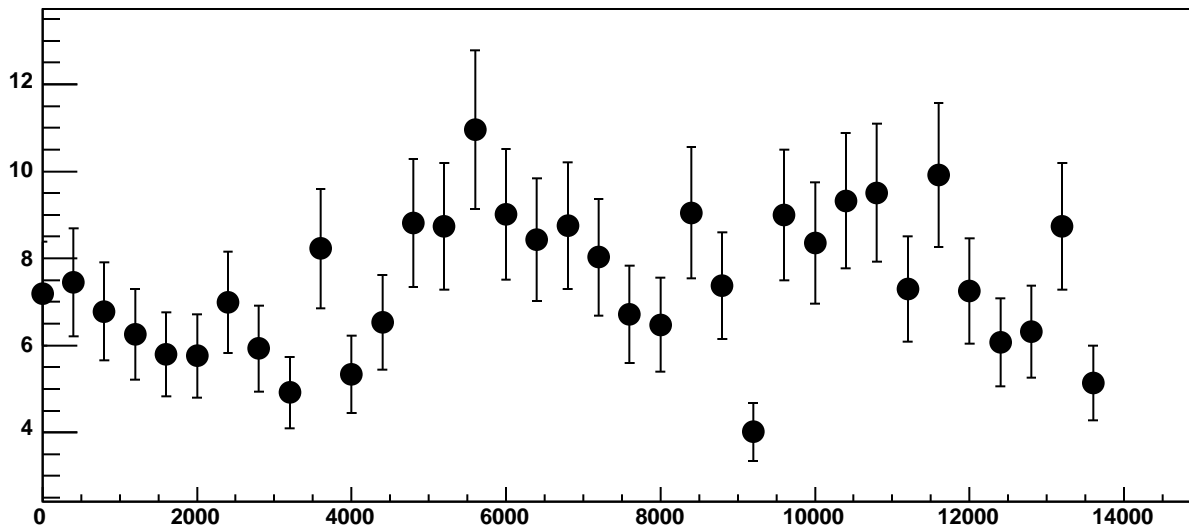


Chip 7, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

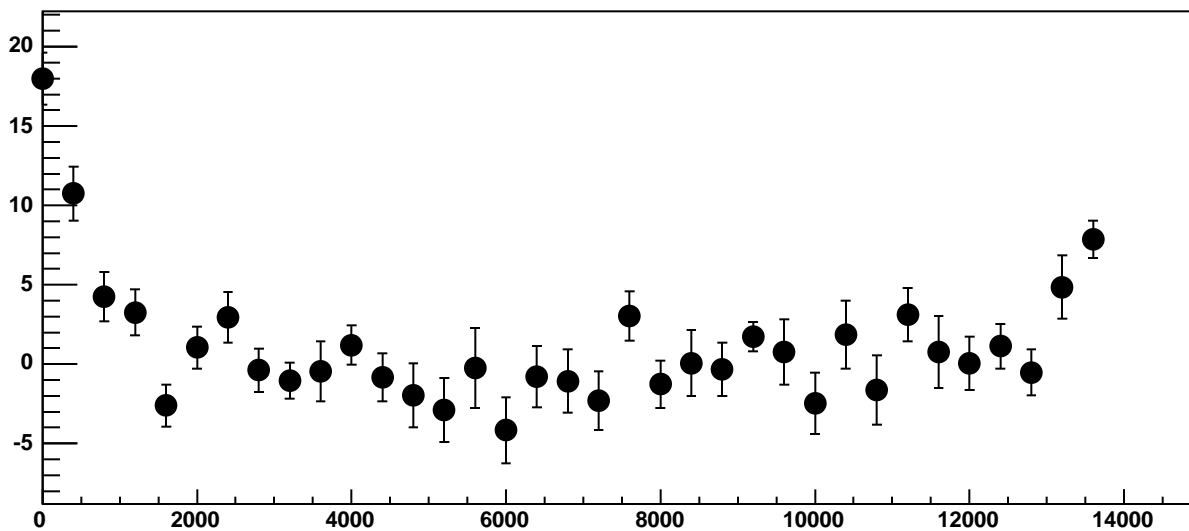


χ^2 / ndf 35.78 / 23
p0 -216.3 ± 0.672
p1 0.01666 ± 0.0001064

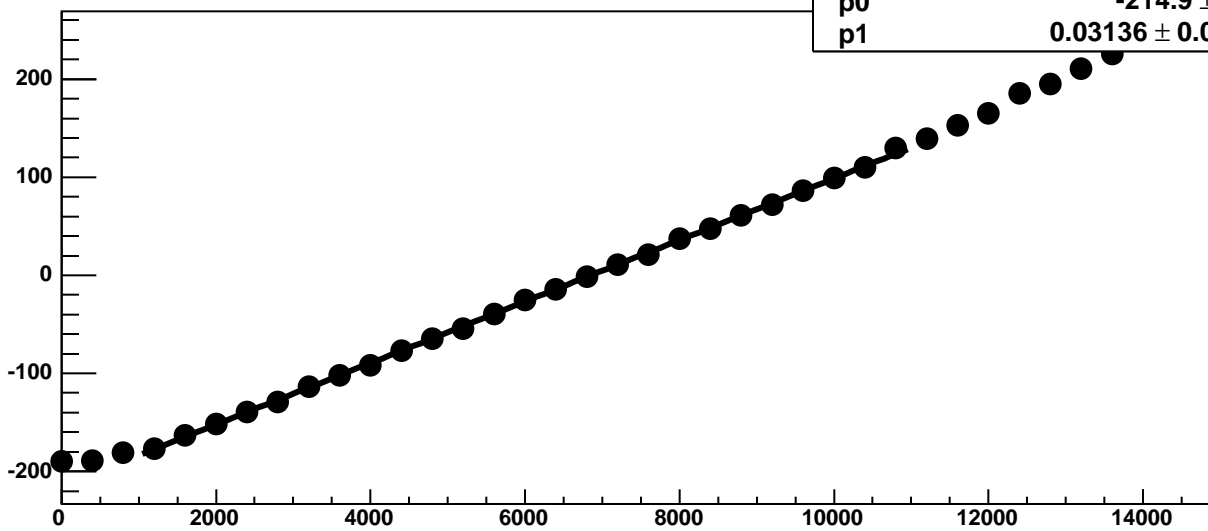
Chip 7, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



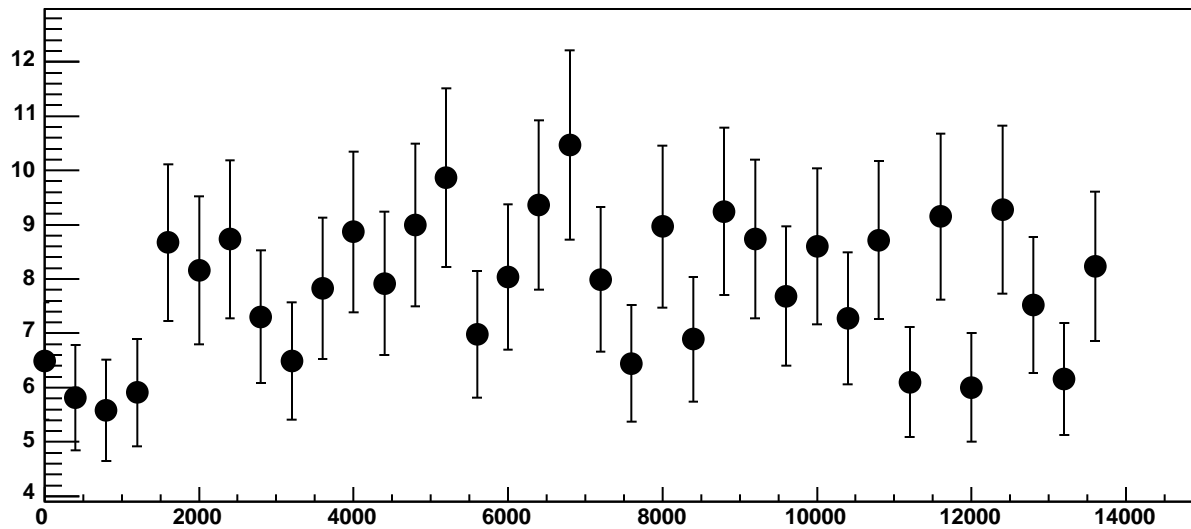
Chip 7, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC



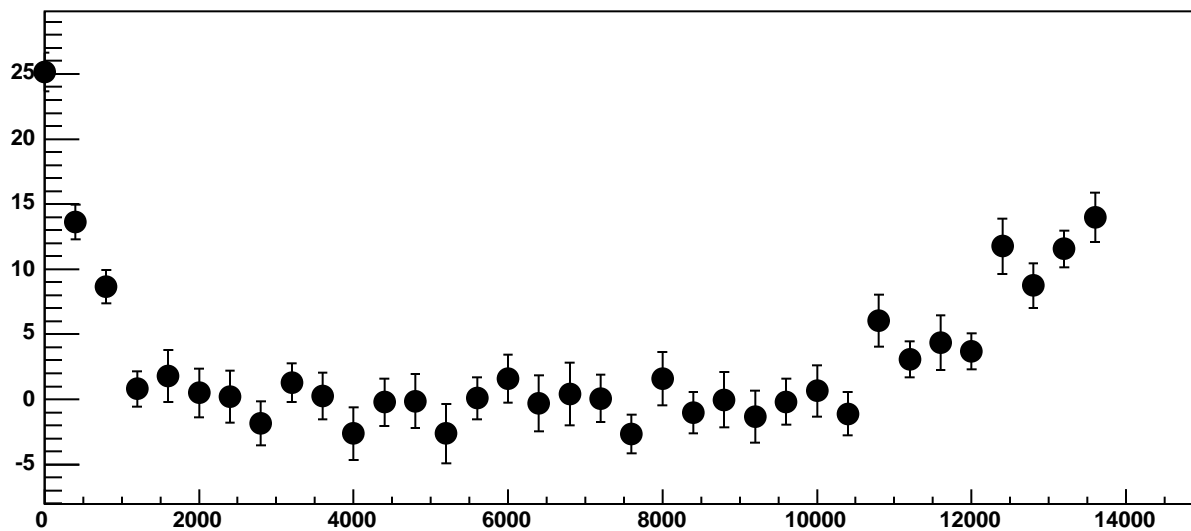
Chip 7, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC



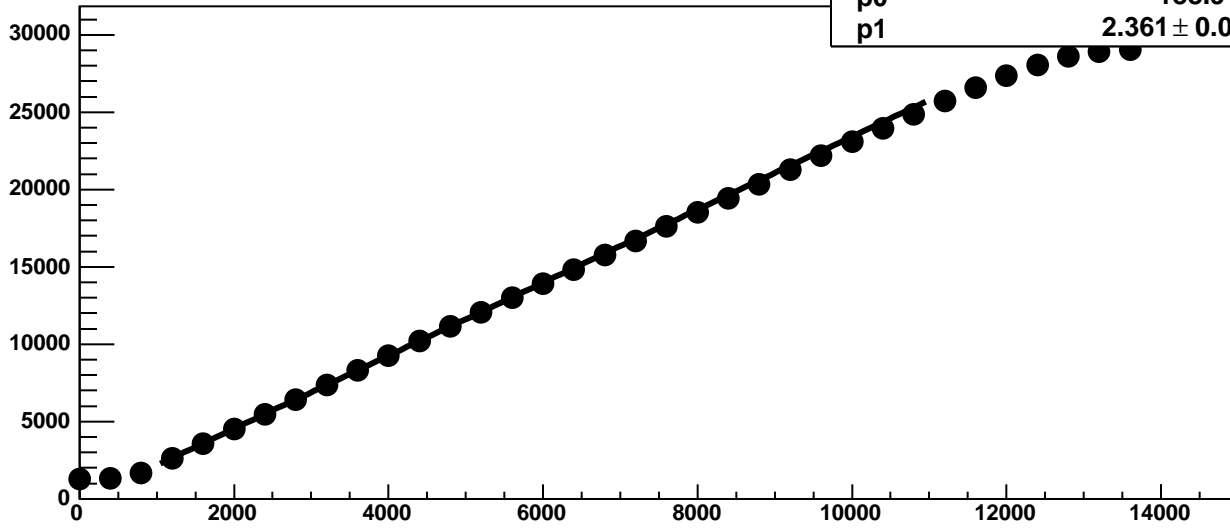
Chip 7, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



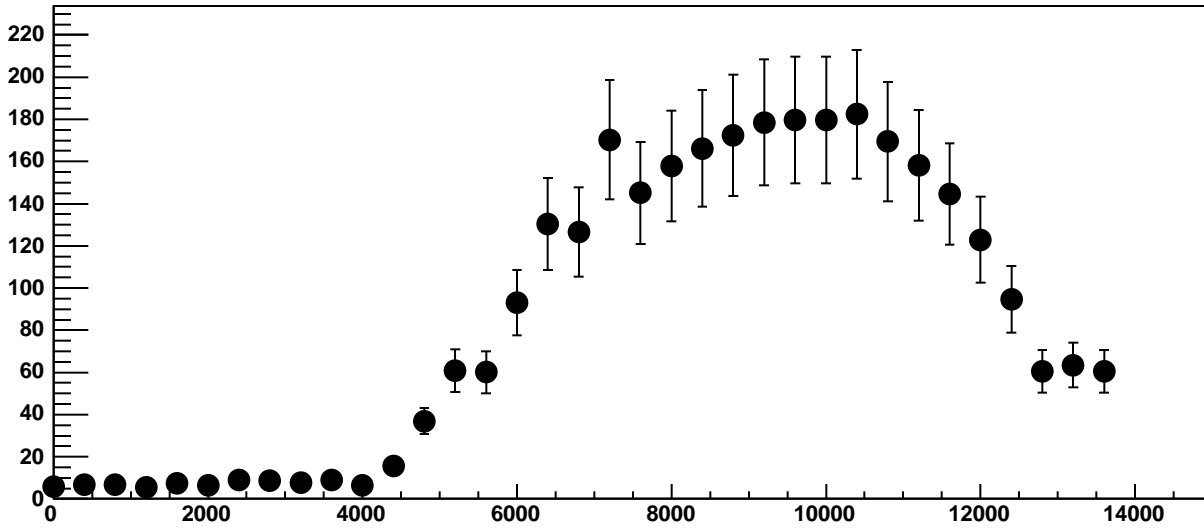
Chip 7, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



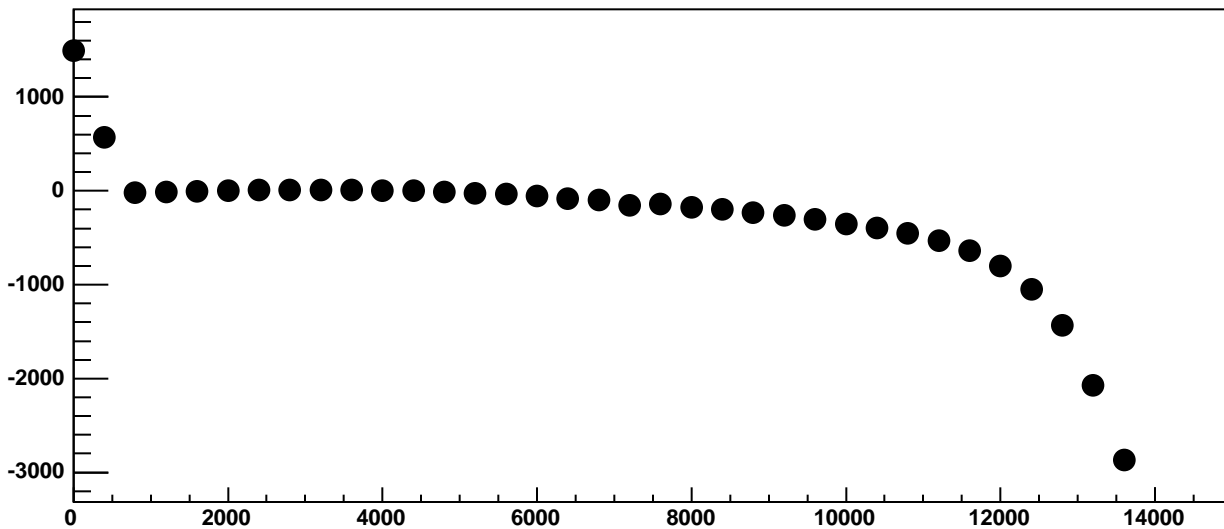
Chip 7, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC



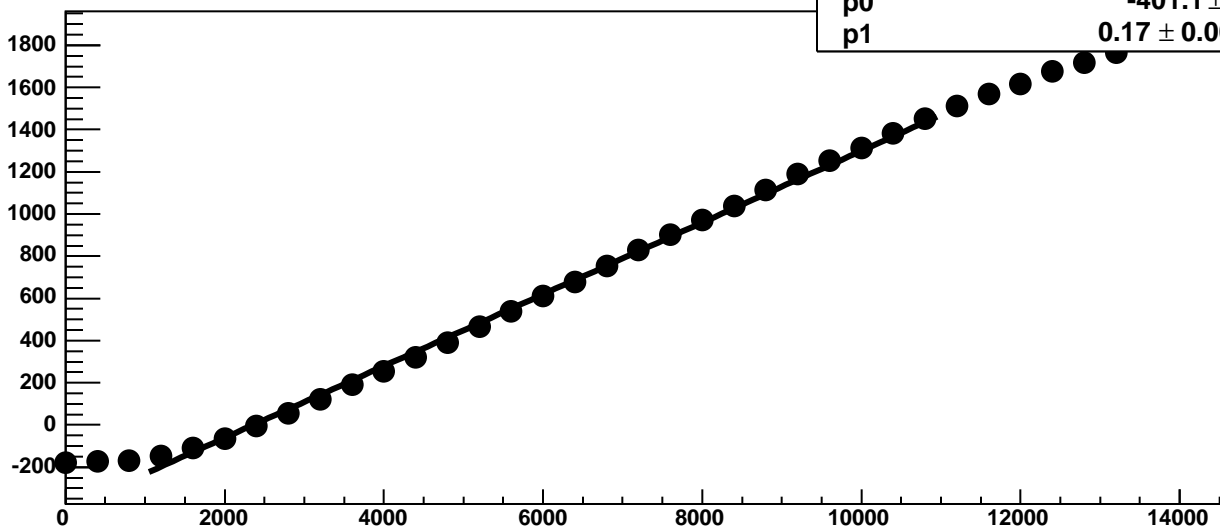
Chip 7, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



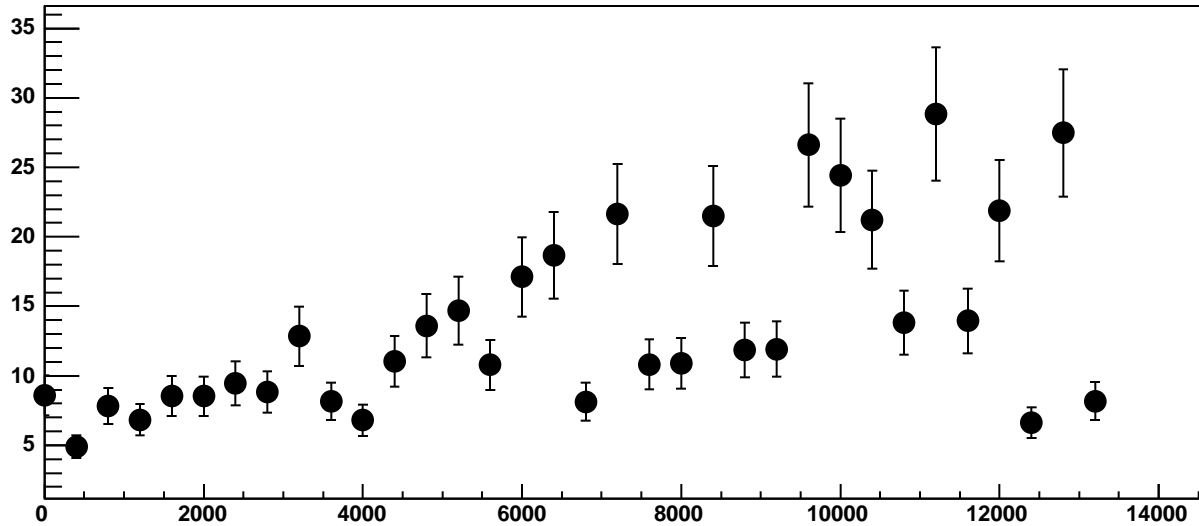
Chip 7, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC



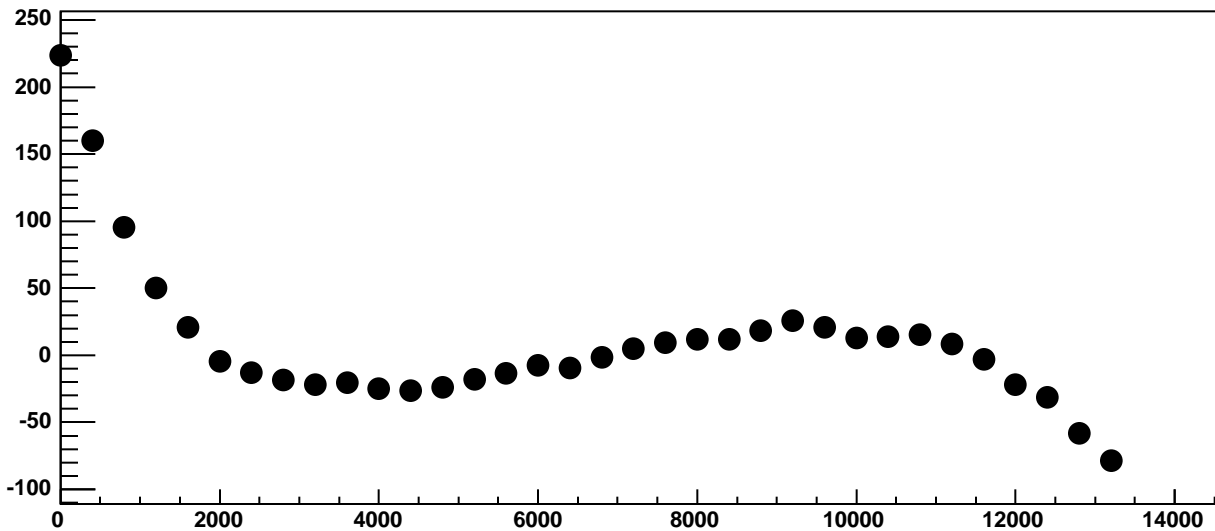
Chip 7, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



Chip 7, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC

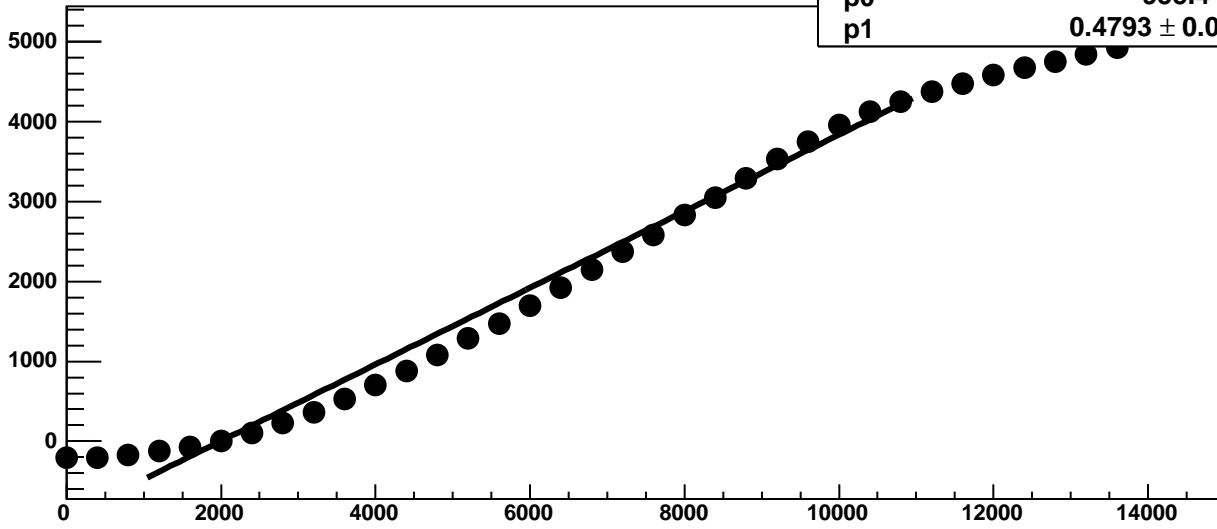


Chip 7, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

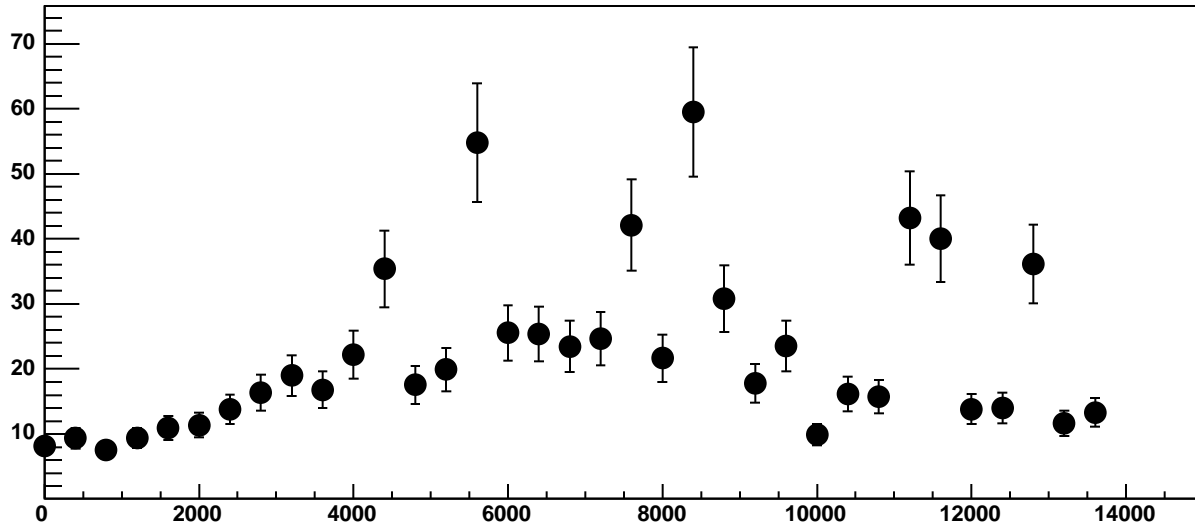


Chip 7, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

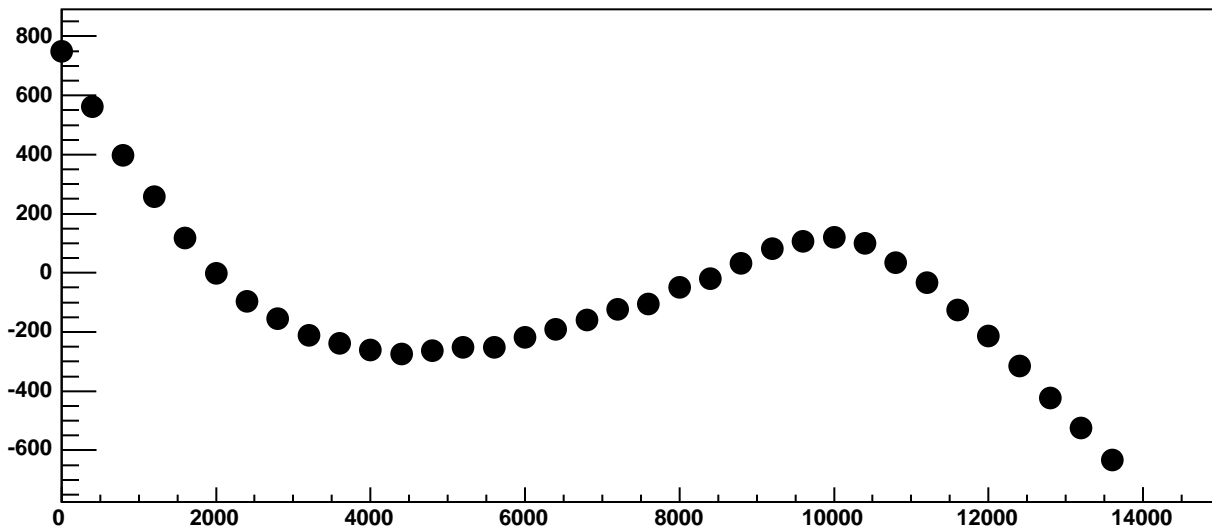
χ^2 / ndf 4.527e+04 / 23
p0 -955.4 ± 1.388
p1 0.4793 ± 0.0002224



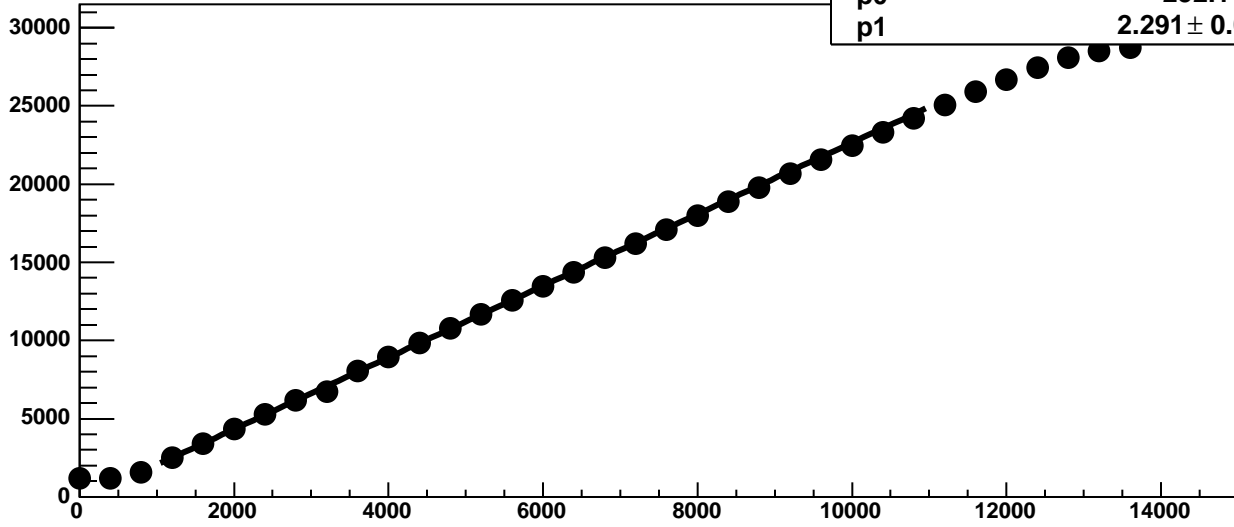
Chip 7, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



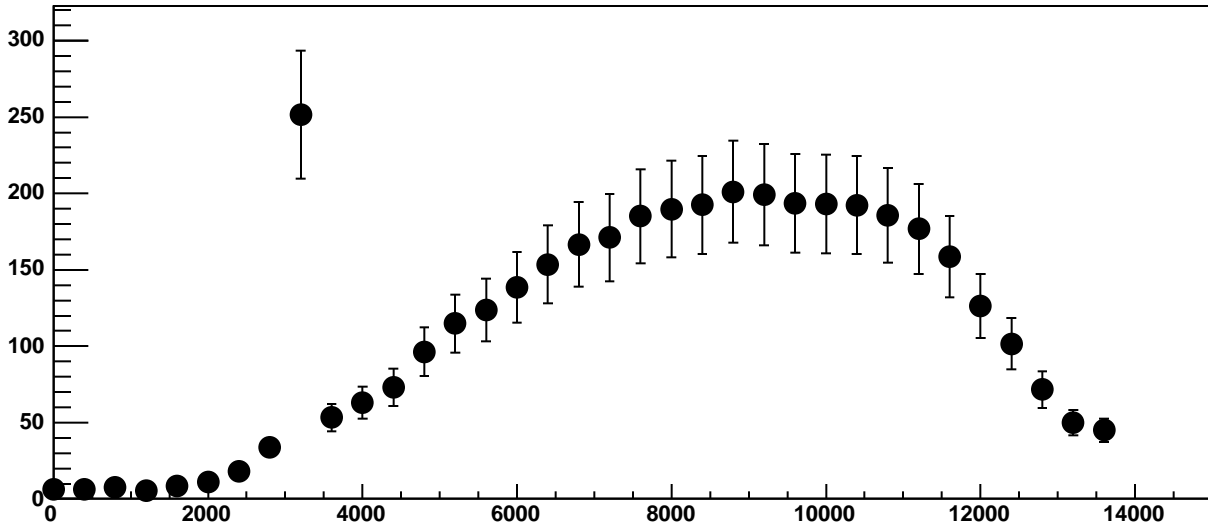
Chip 7, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



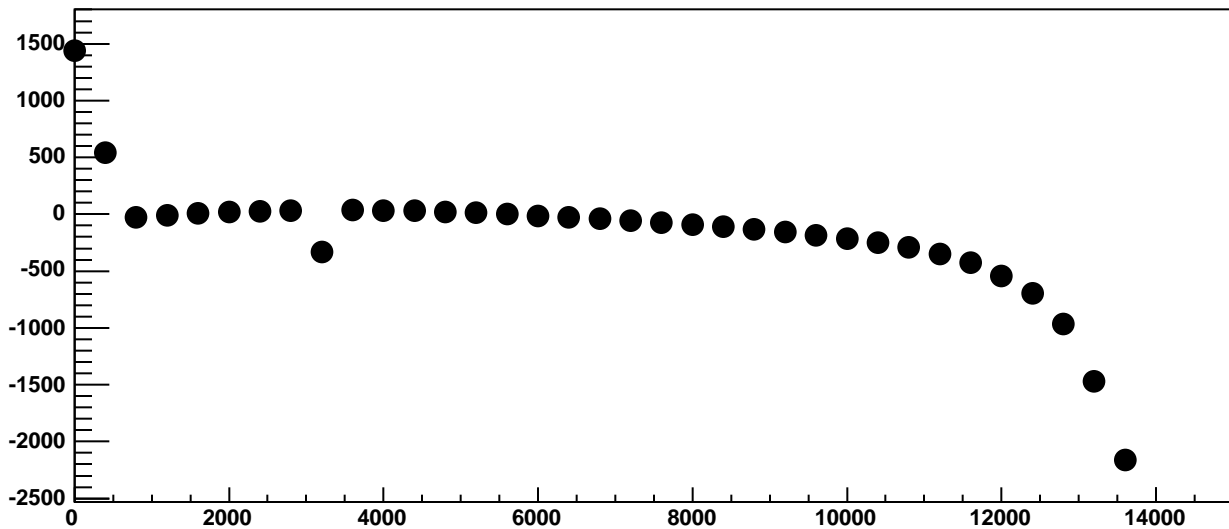
Chip 7, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC



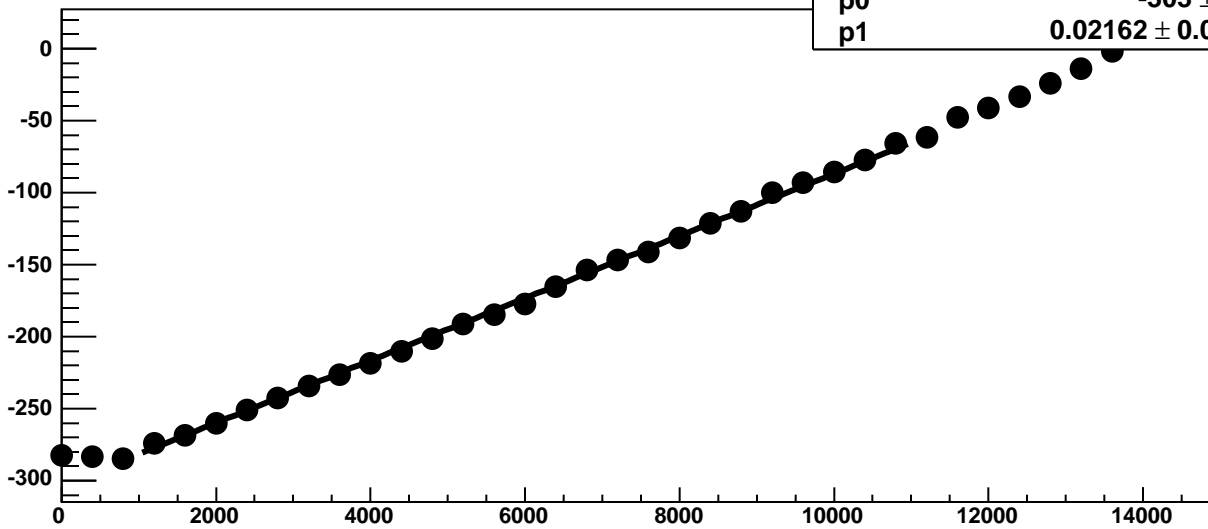
Chip 7, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC

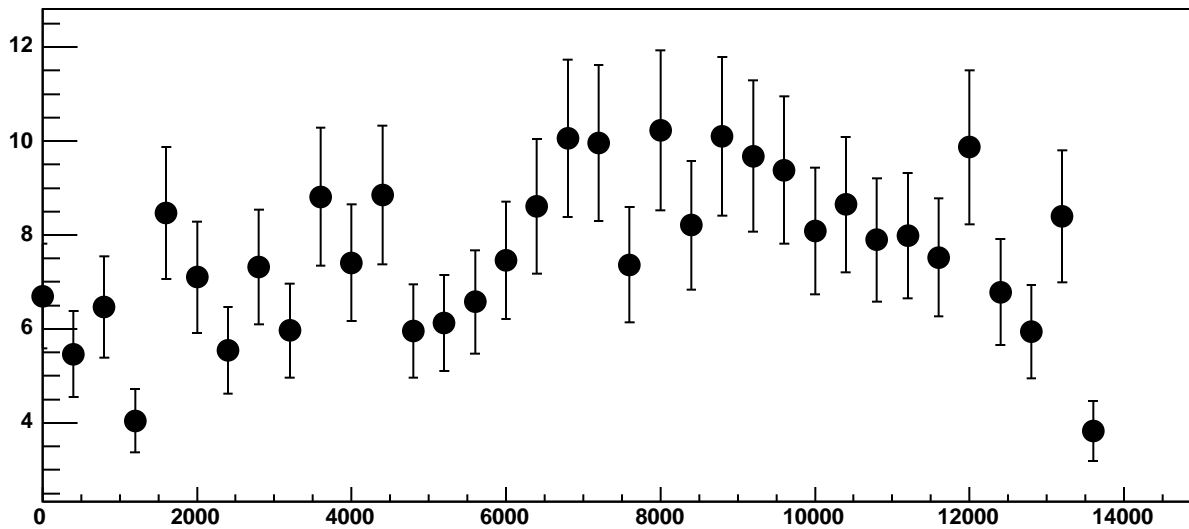


Chip 7, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

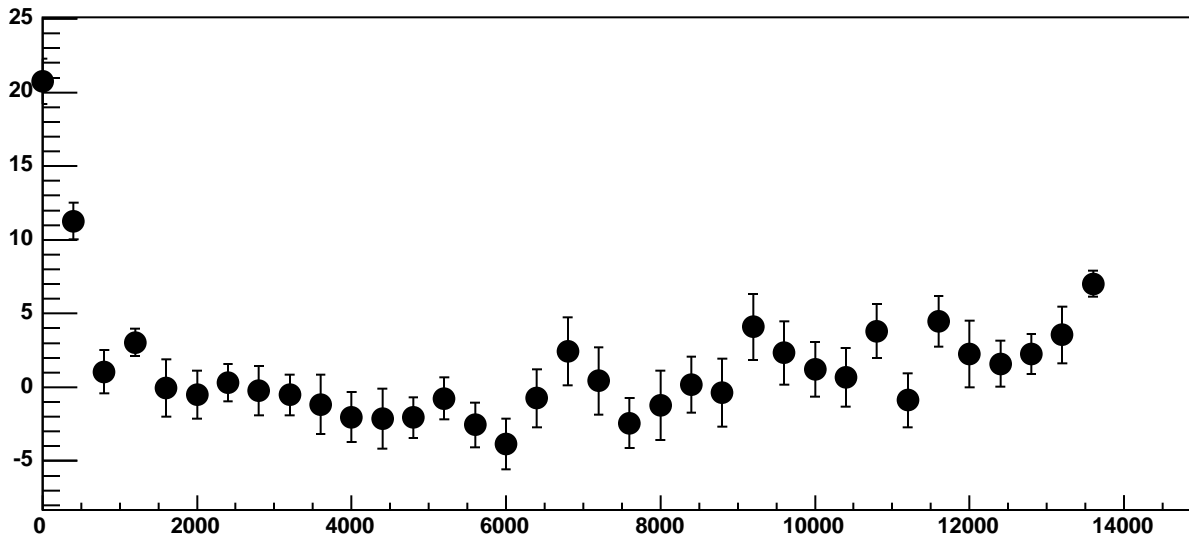


χ^2 / ndf 37.54 / 23
p0 -303 ± 0.6678
p1 0.02162 ± 0.0001138

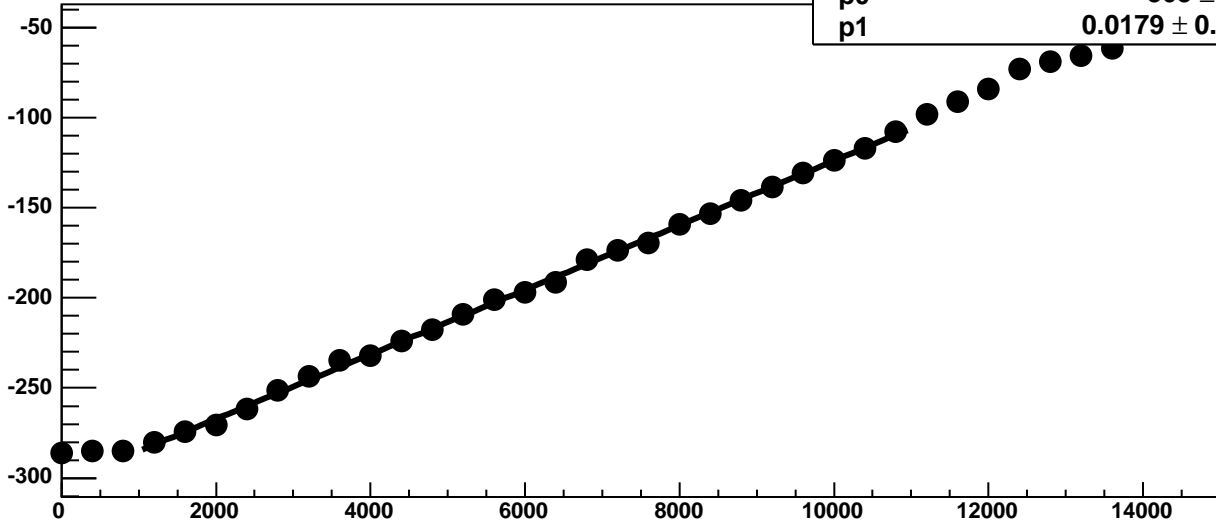
Chip 7, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

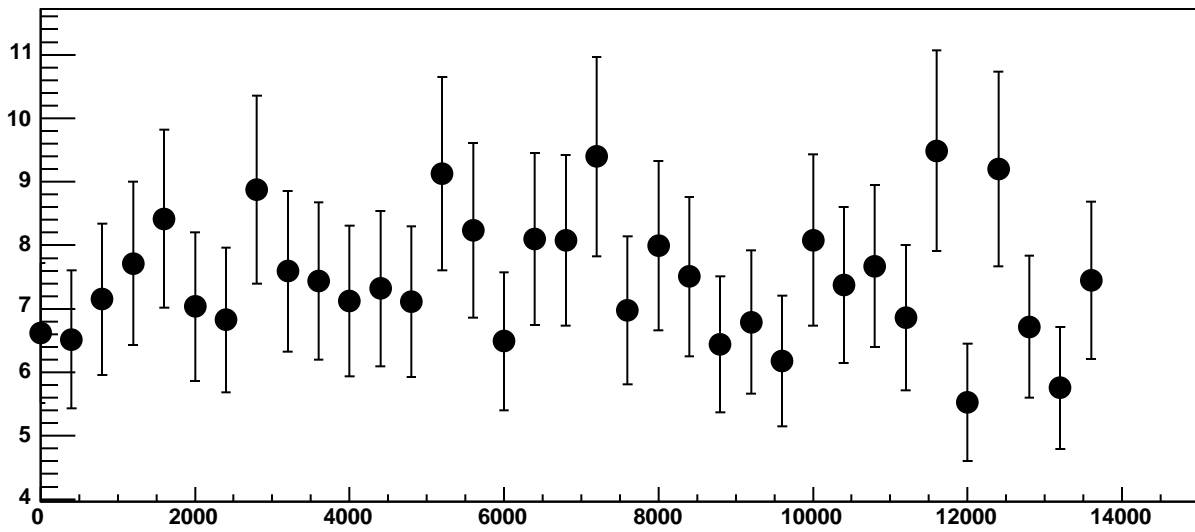


Chip 7, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

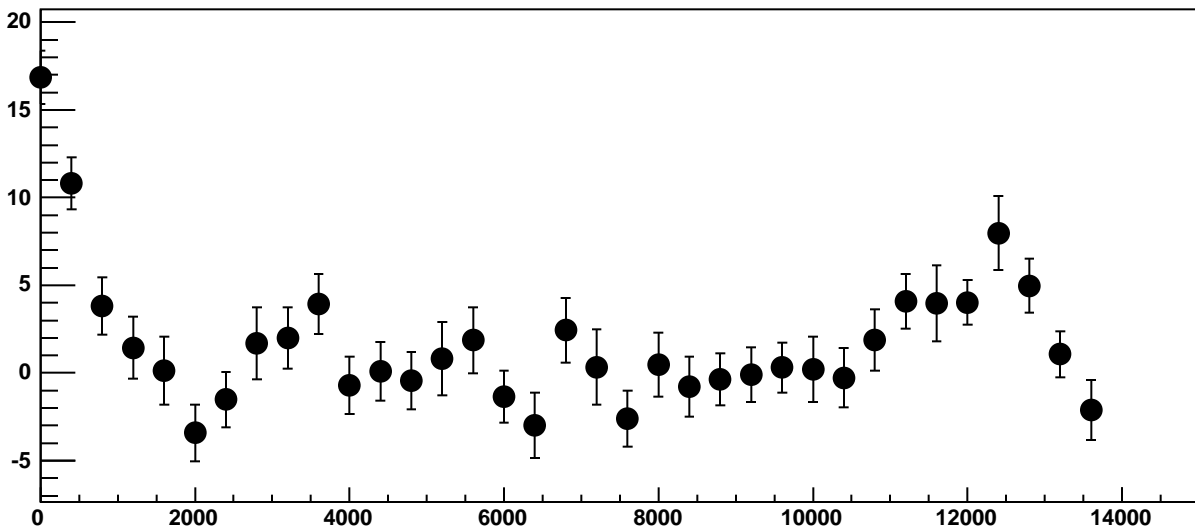


χ^2 / ndf 24.18 / 23
p0 -303 ± 0.7989
p1 0.0179 ± 0.000118

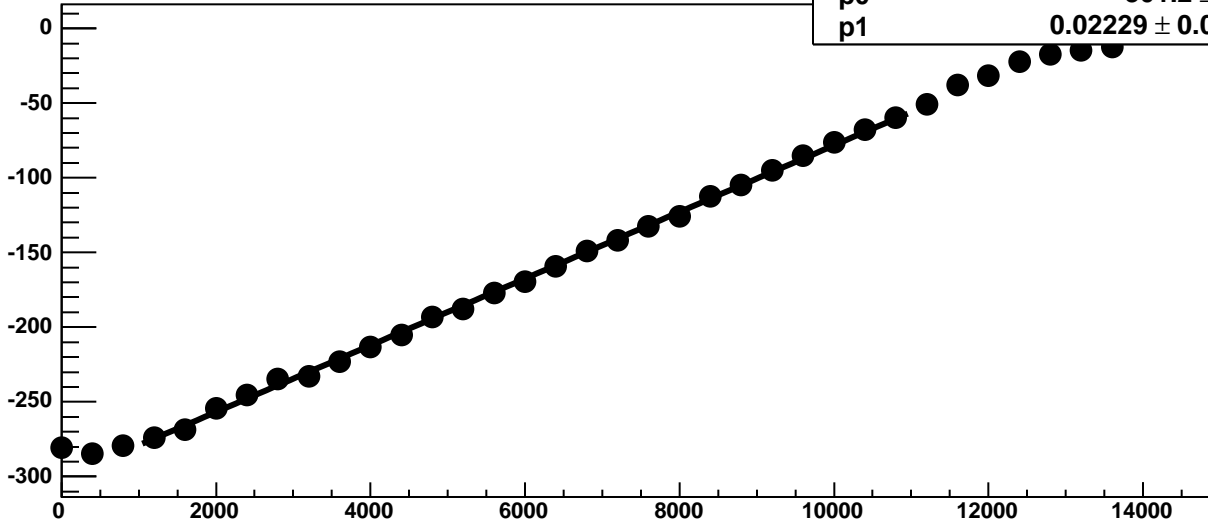
Chip 7, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



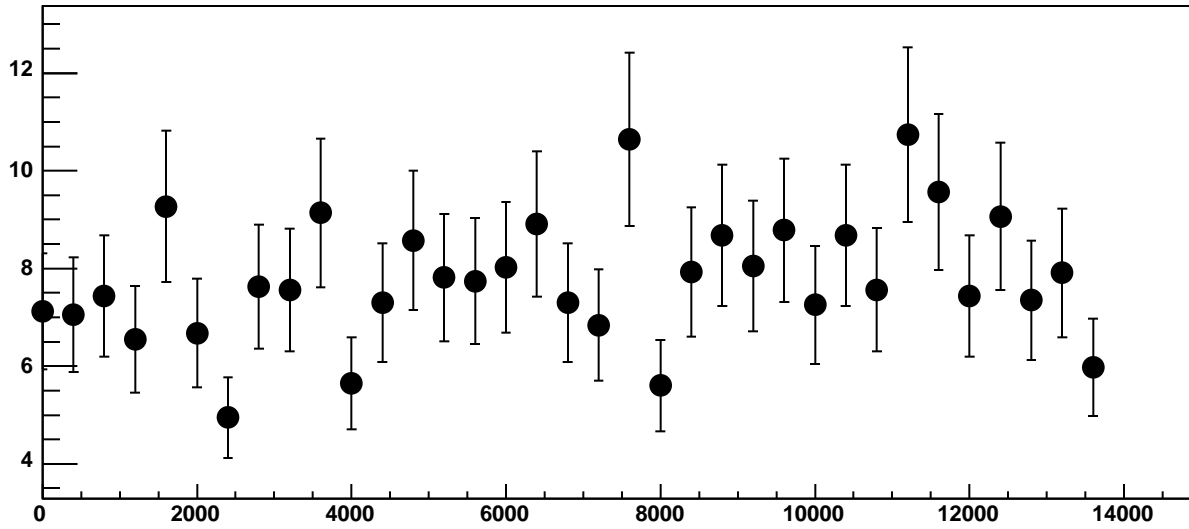
Chip 7, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



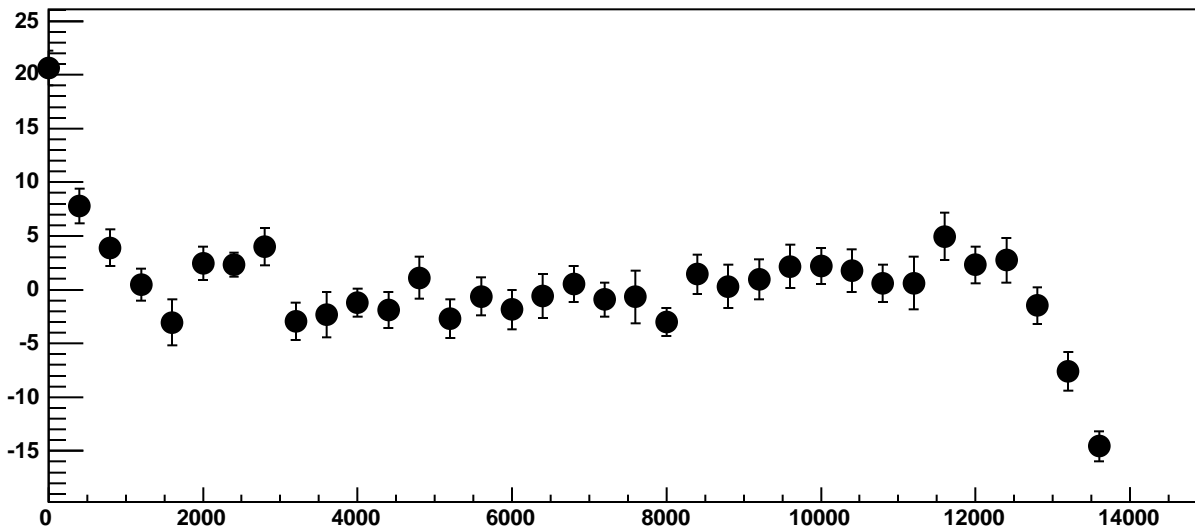
Chip 7, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC



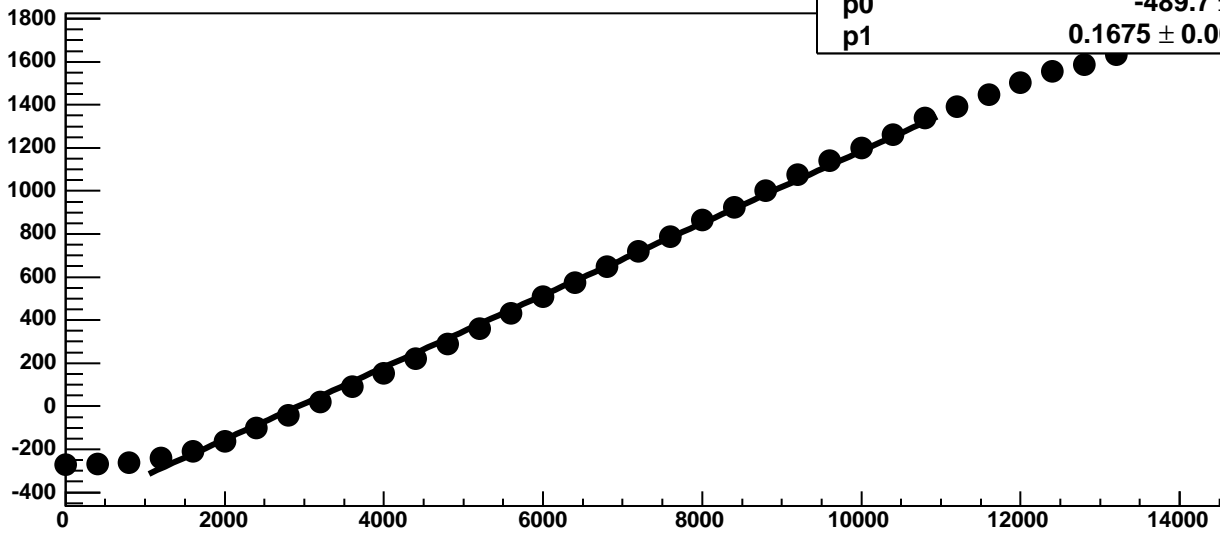
Chip 7, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



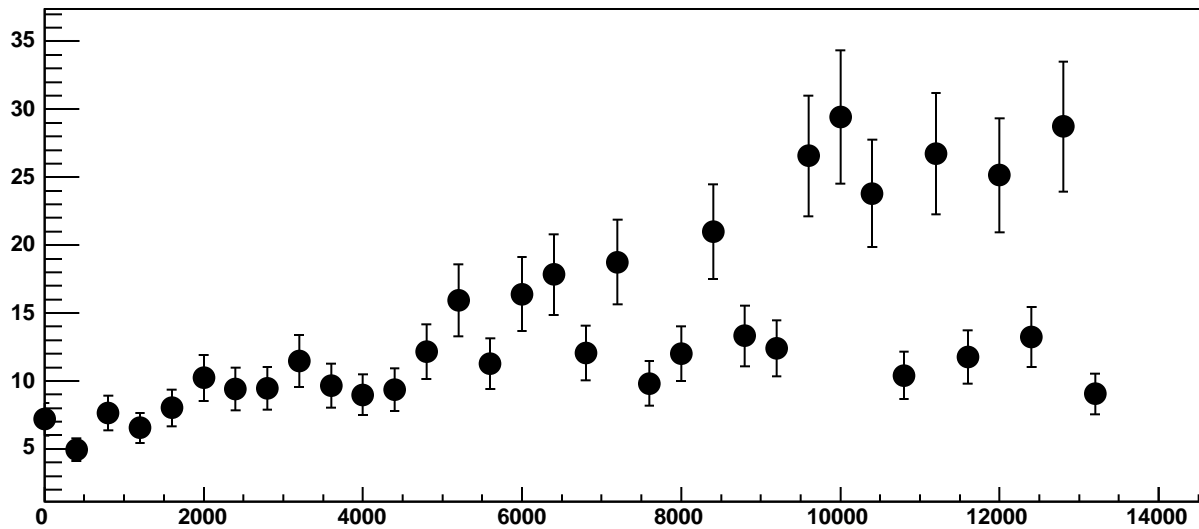
Chip 7, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC



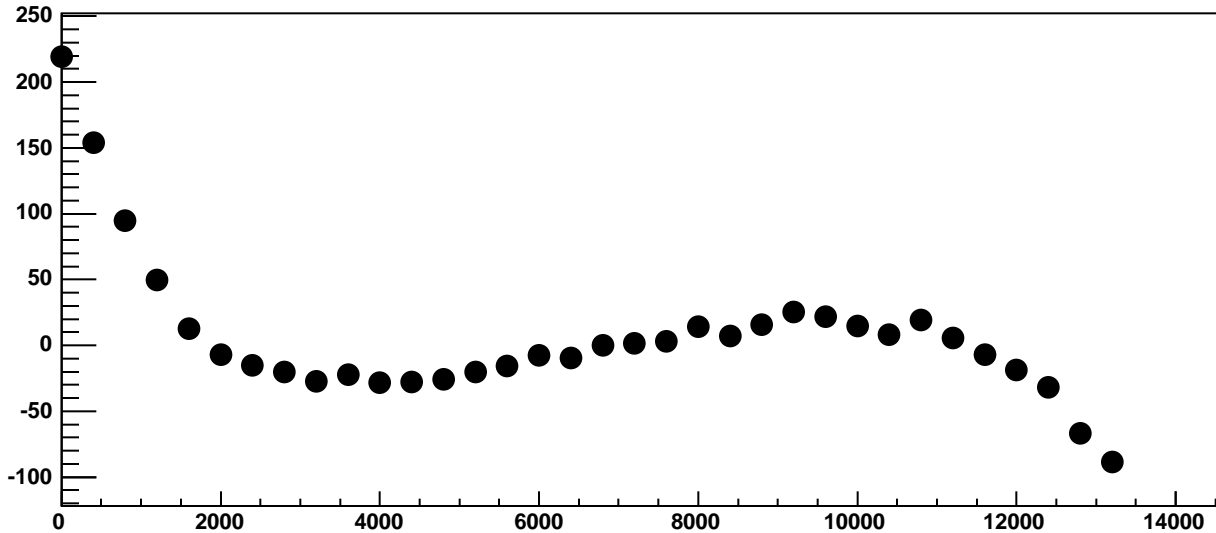
Chip 7, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



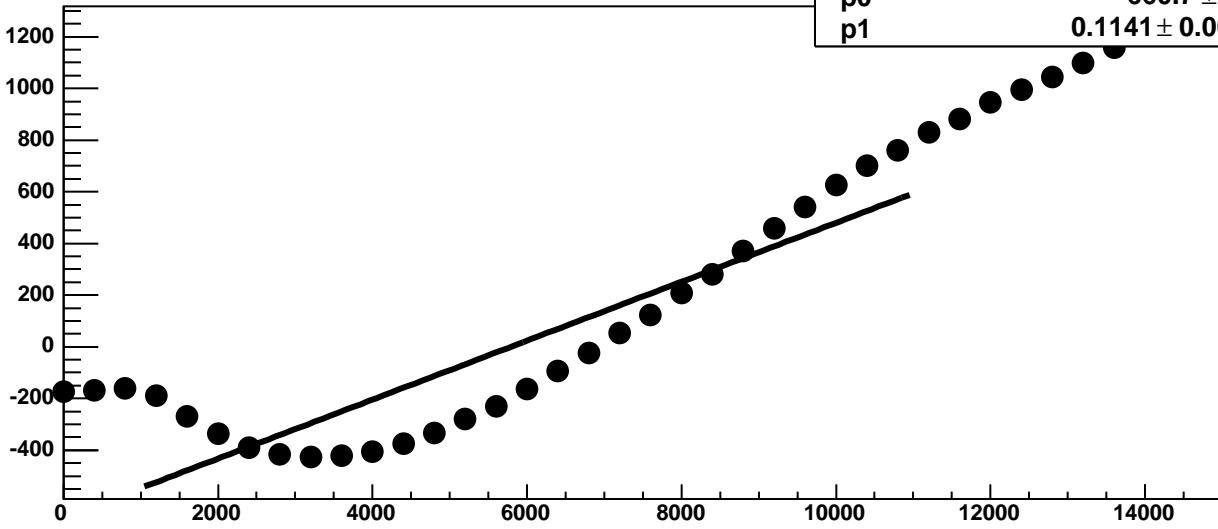
Chip 7, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 7, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

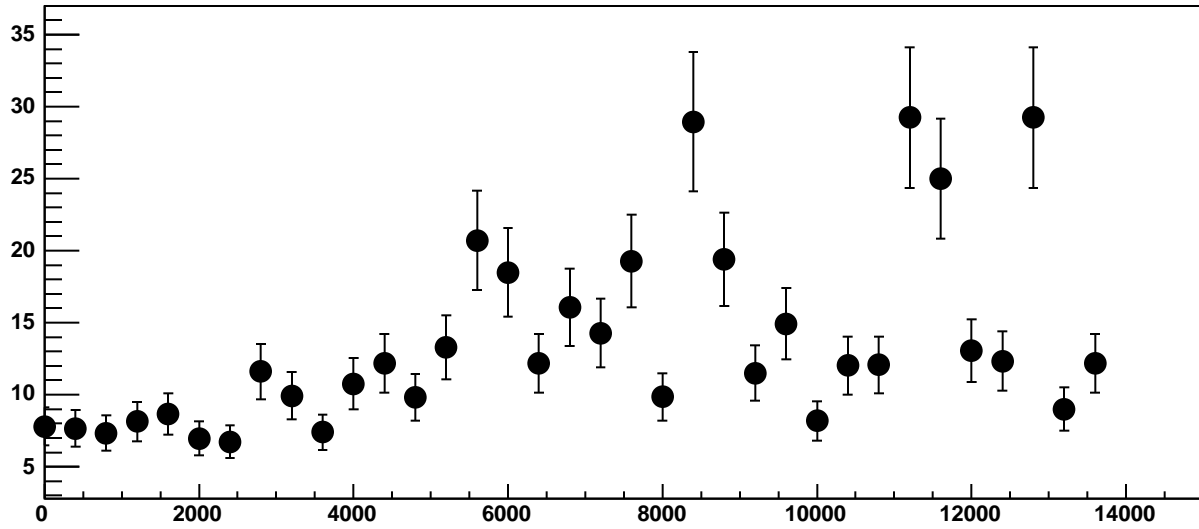


Chip 7, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

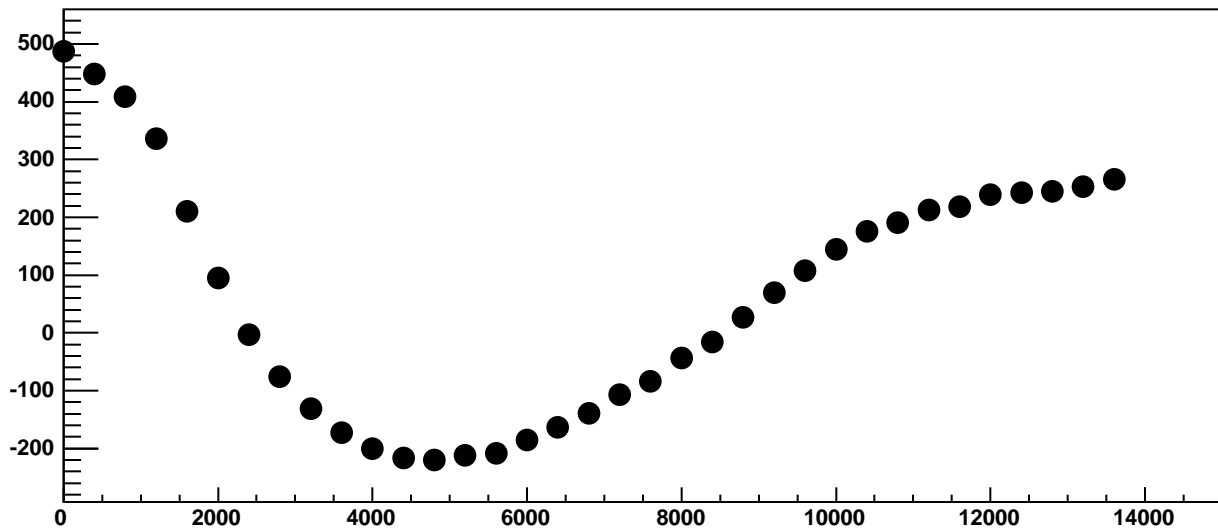


χ^2 / ndf 1.157e+05 / 23
p0 -660.7 ± 0.9312
p1 0.1141 ± 0.0001589

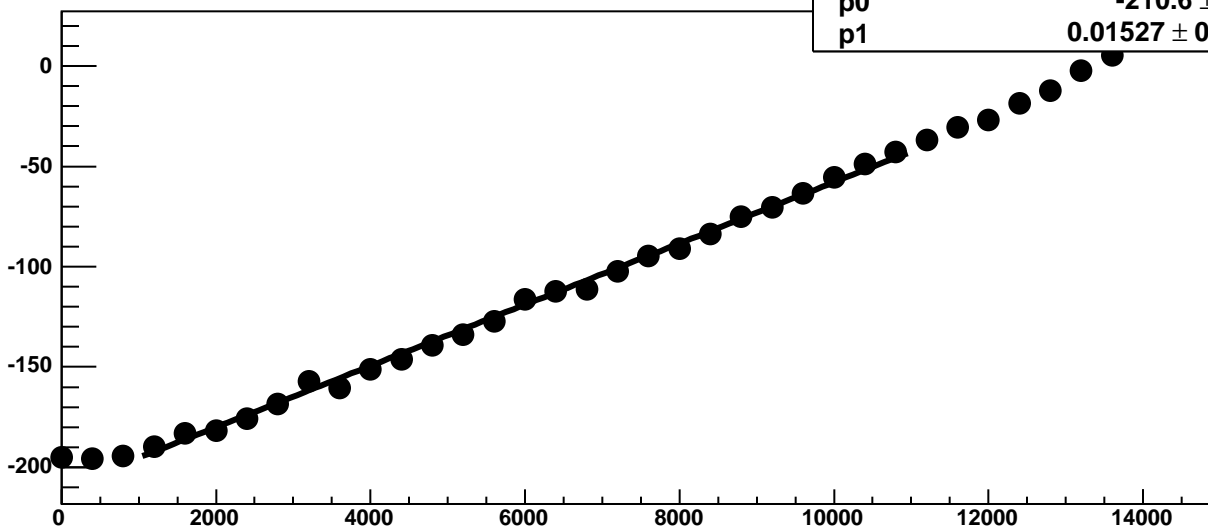
Chip 7, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



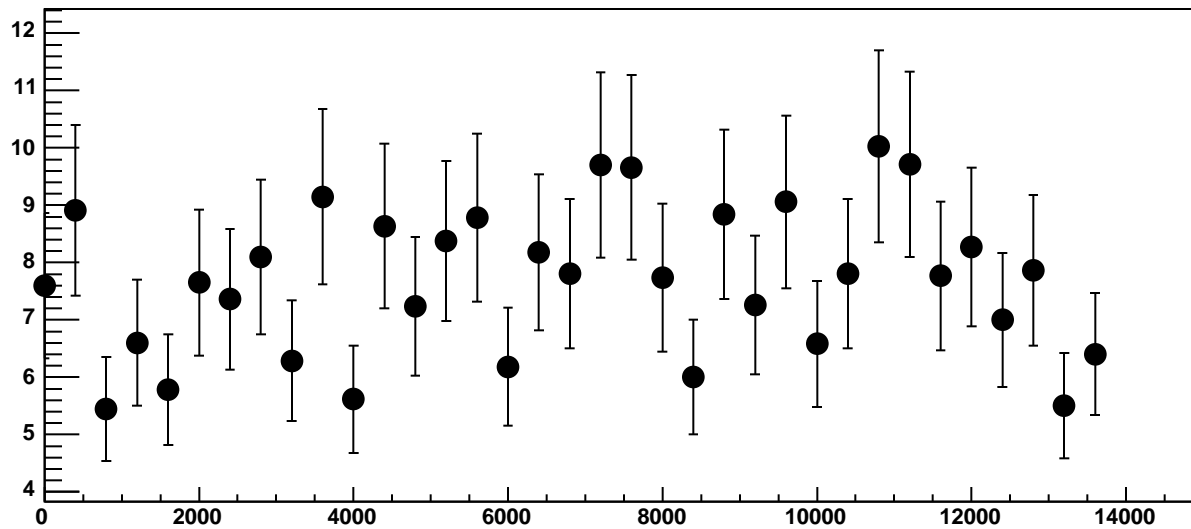
Chip 7, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



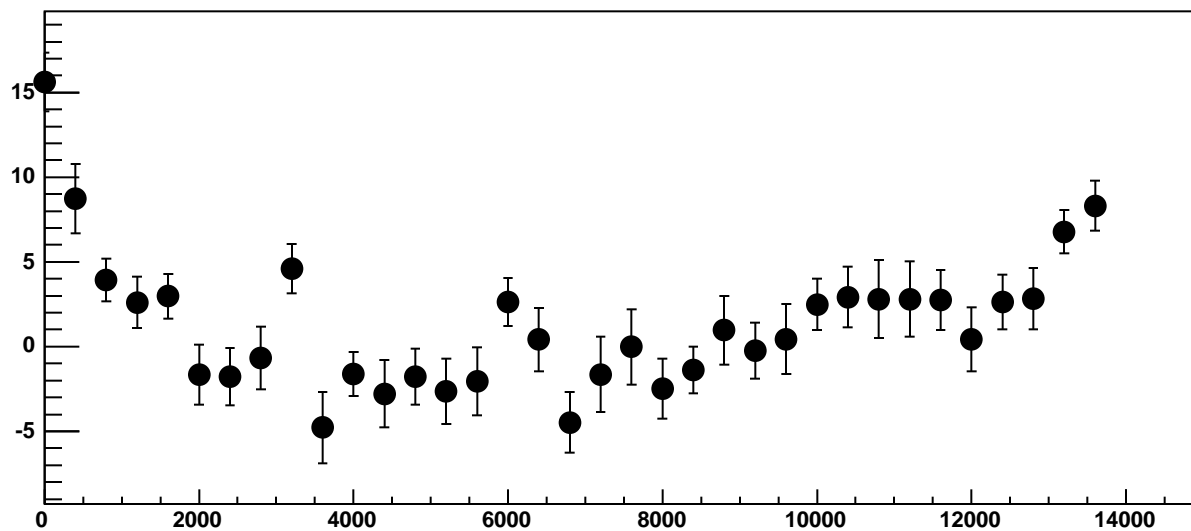
Chip 7, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC



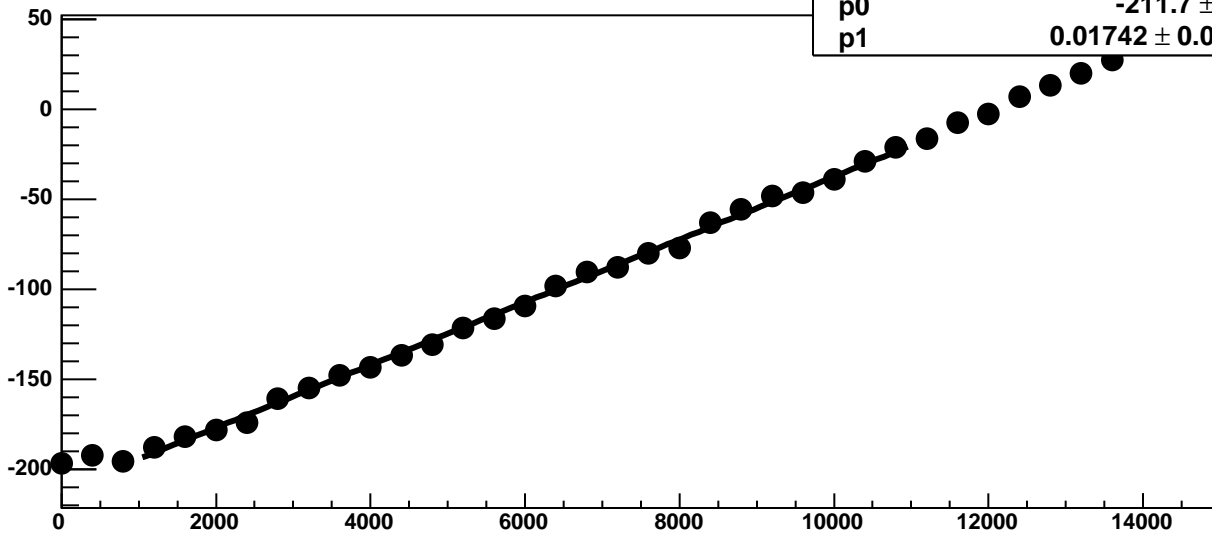
Chip 7, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

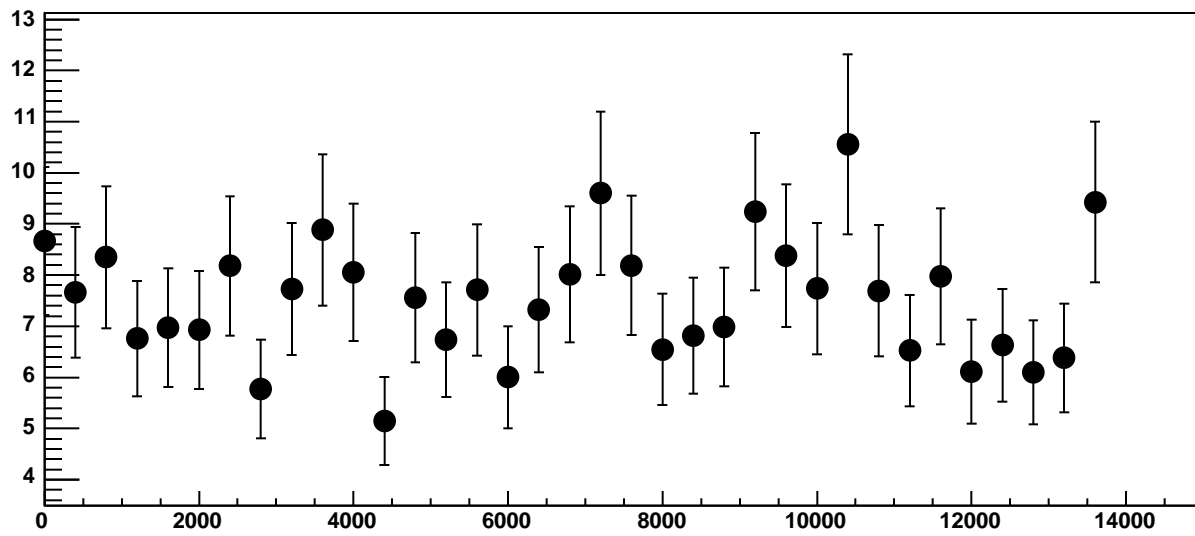


Chip 7, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

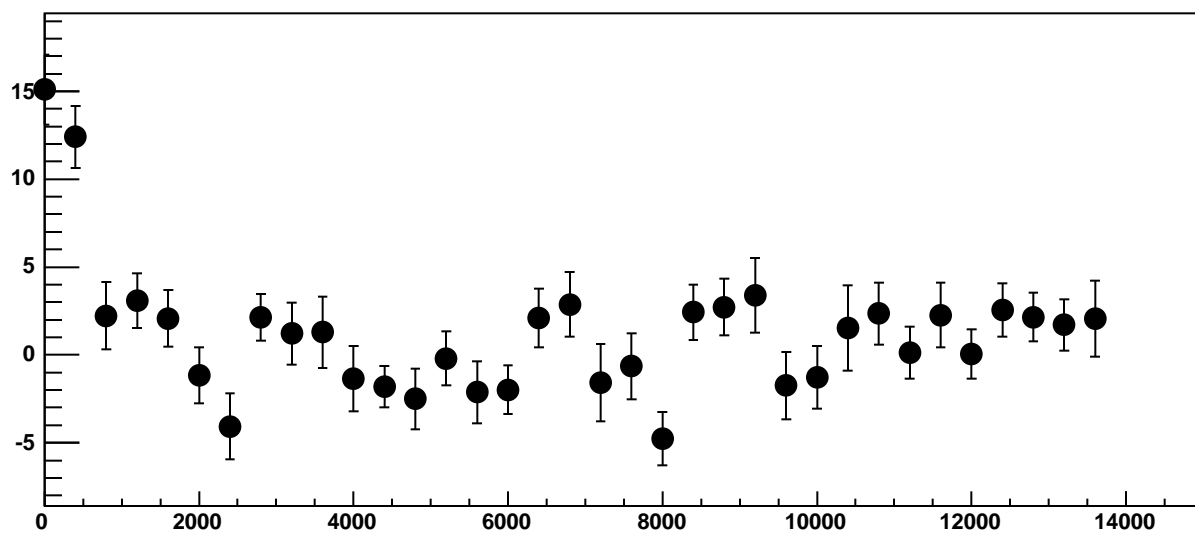


χ^2 / ndf 48.87 / 23
p0 -211.7 ± 0.7573
p1 0.01742 ± 0.0001197

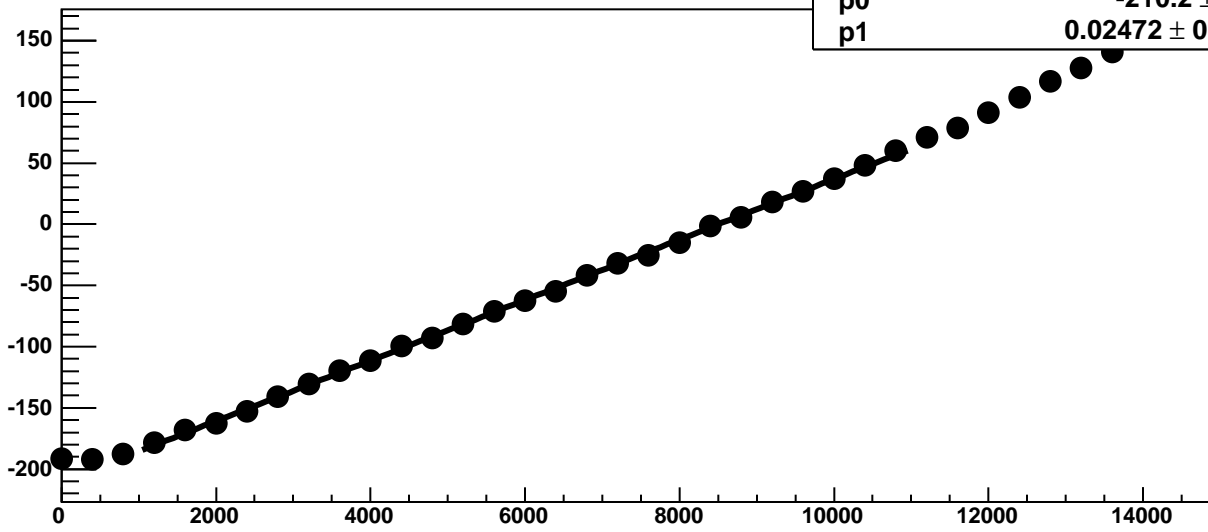
Chip 7, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

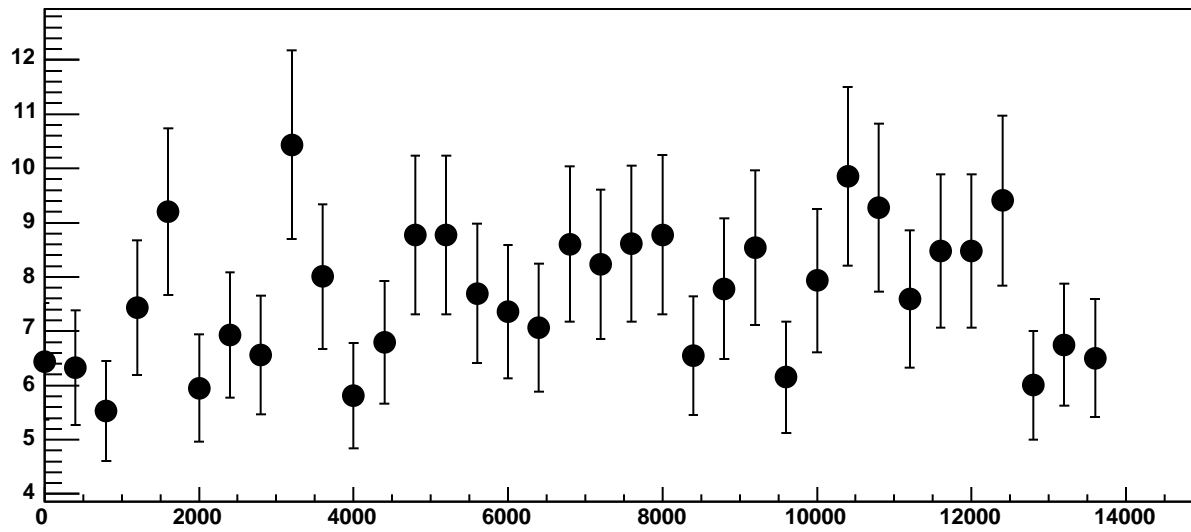


Chip 7, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

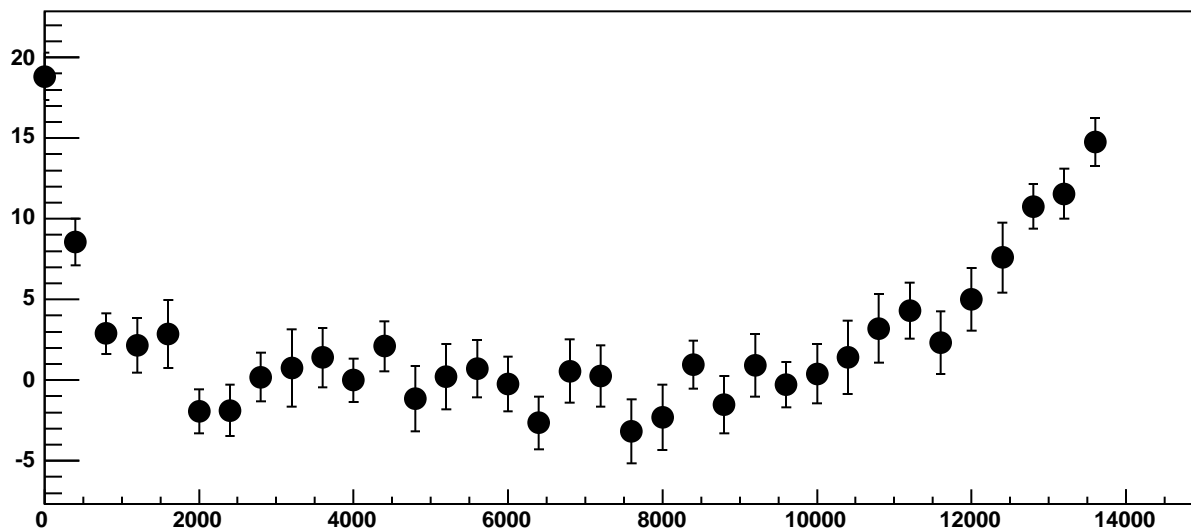


χ^2 / ndf 20.62 / 23
p0 -210.2 ± 0.7854
p1 0.02472 ± 0.000122

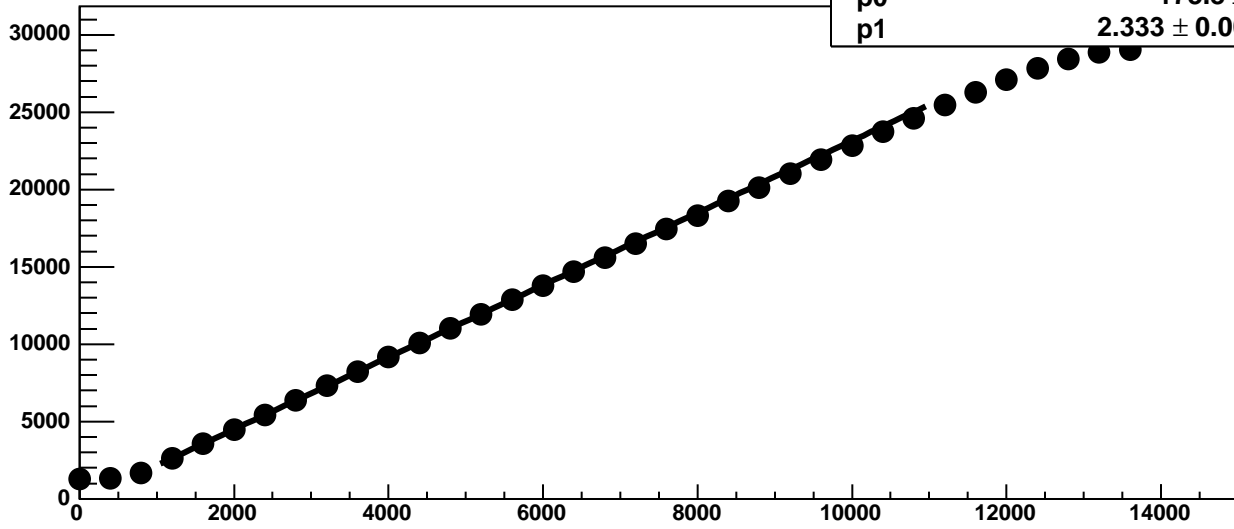
Chip 7, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



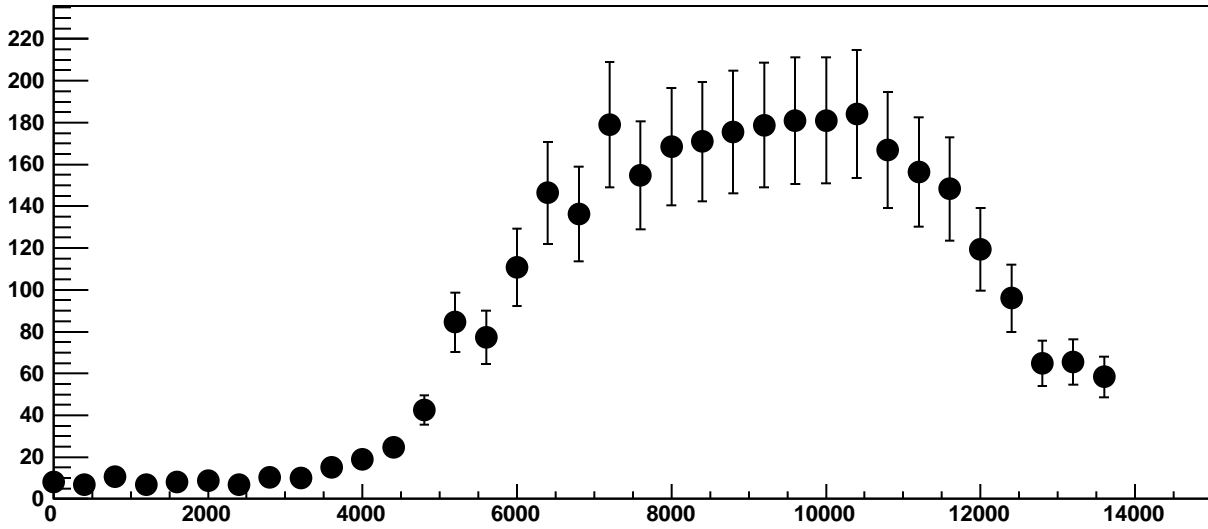
Chip 7, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



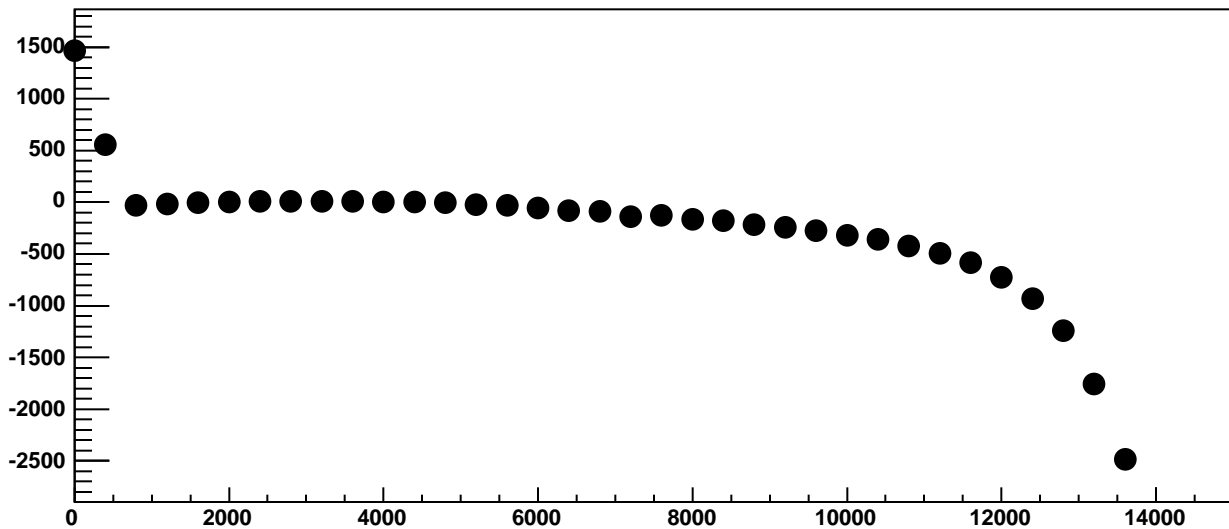
Chip 7, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC



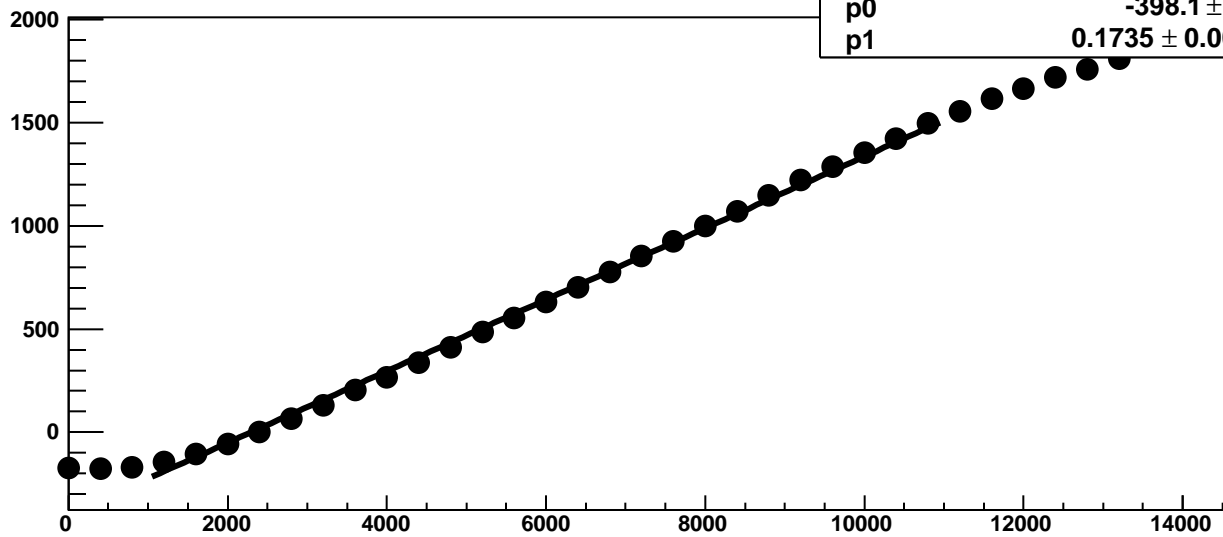
Chip 7, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC



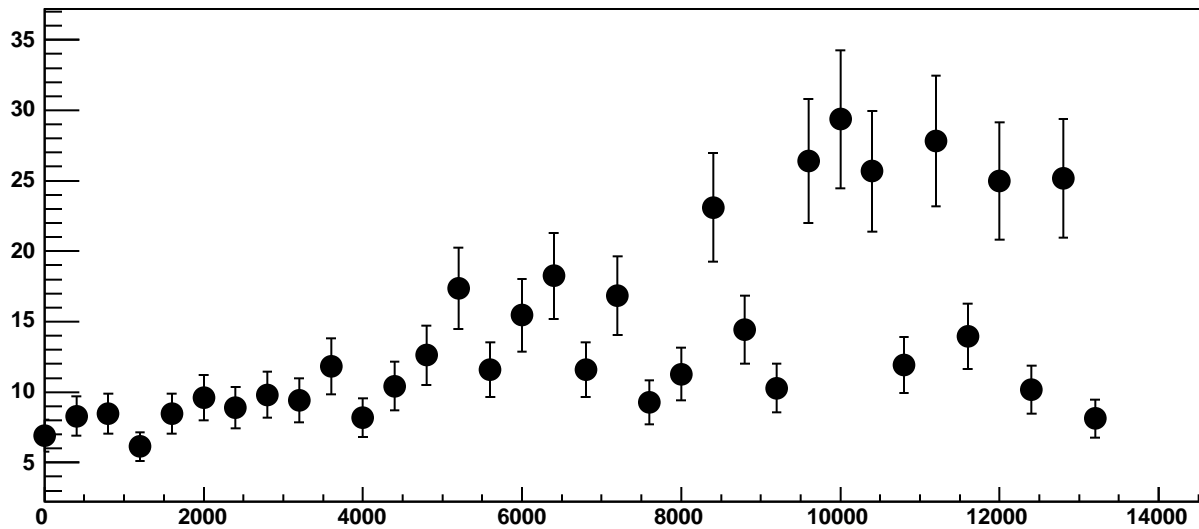
Chip 7, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC



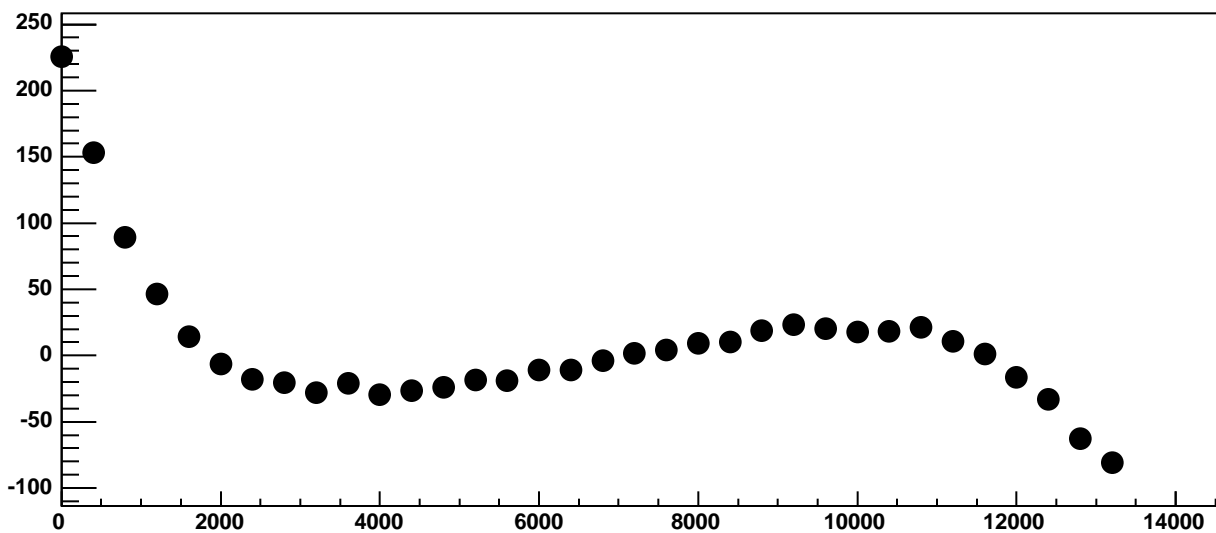
Chip 7, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



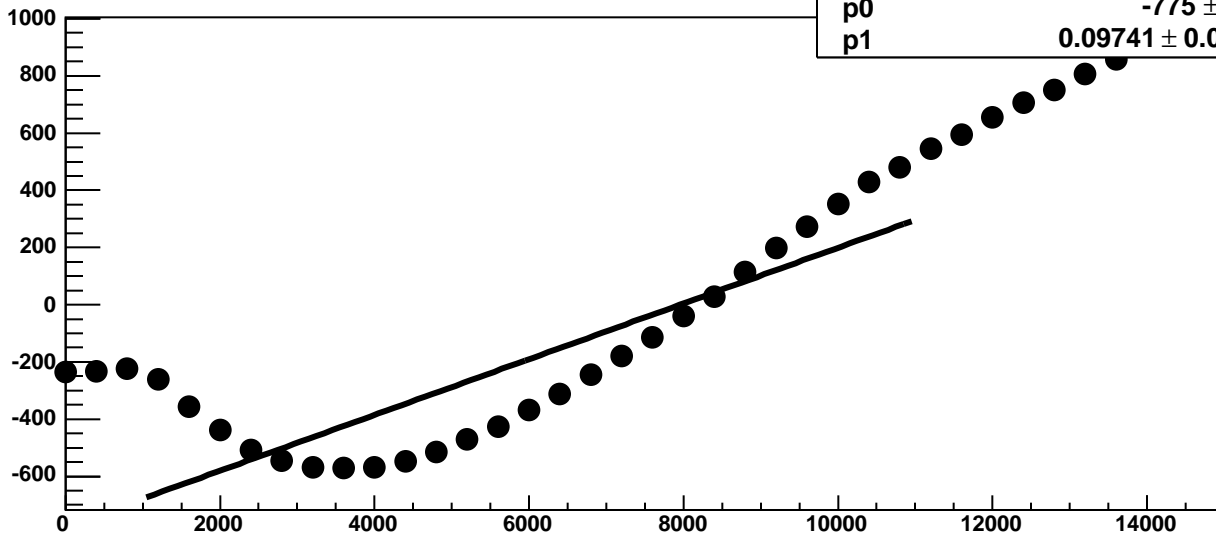
Chip 7, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



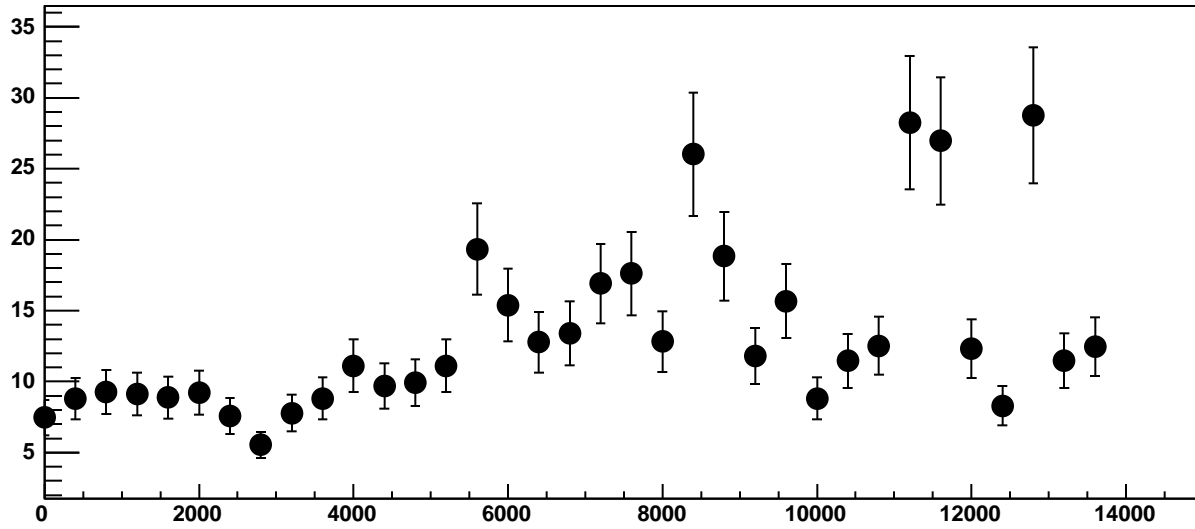
Chip 7, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



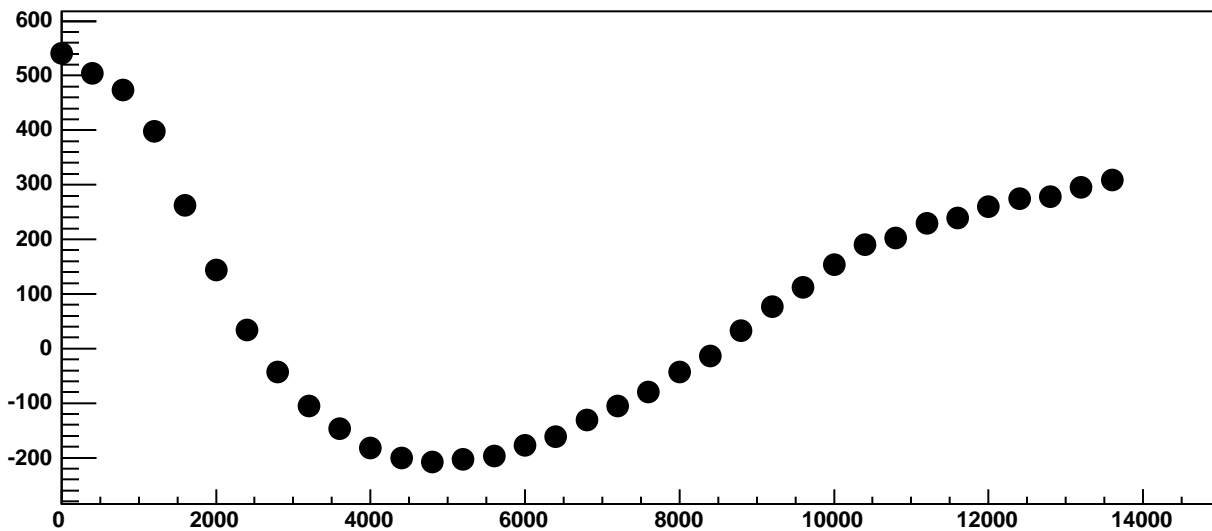
Chip 7, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



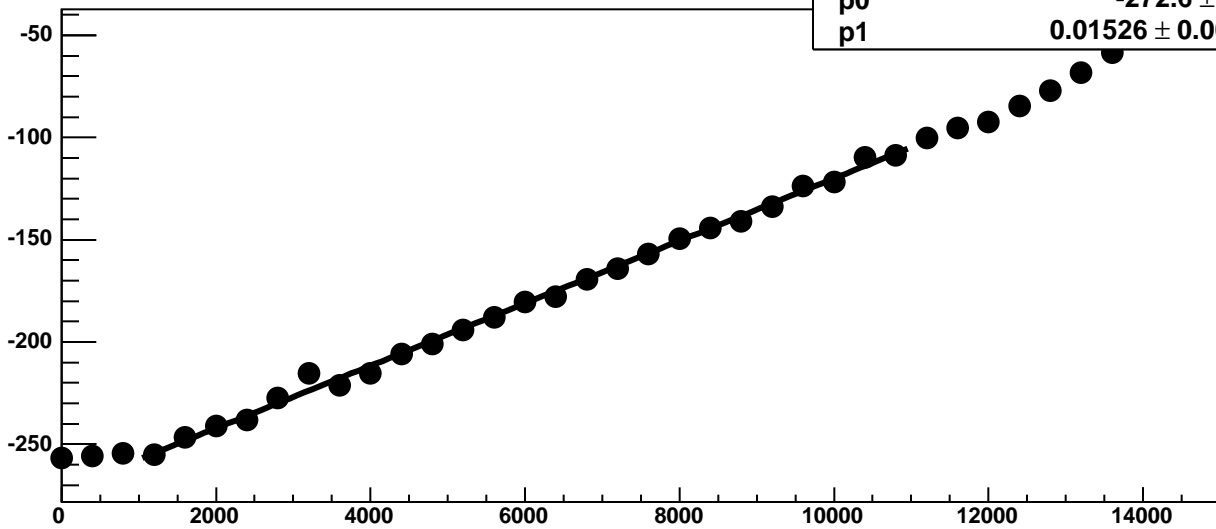
Chip 7, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



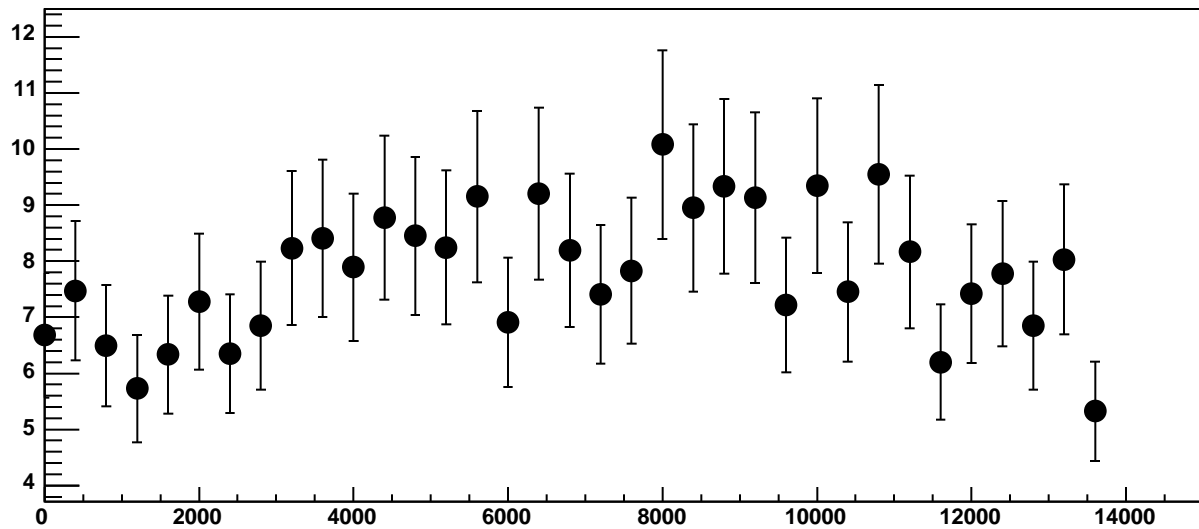
Chip 7, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



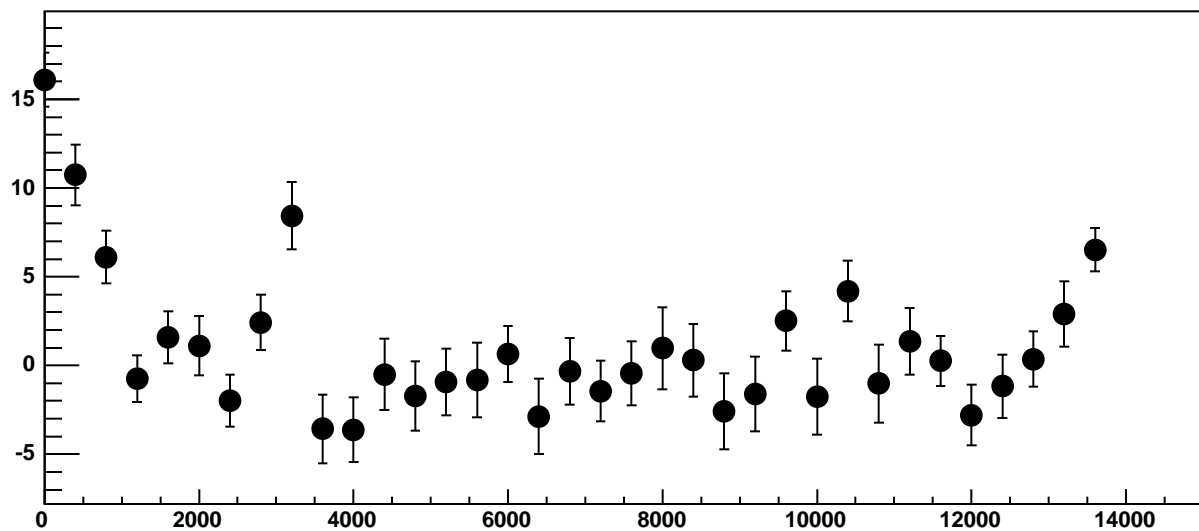
Chip 7, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



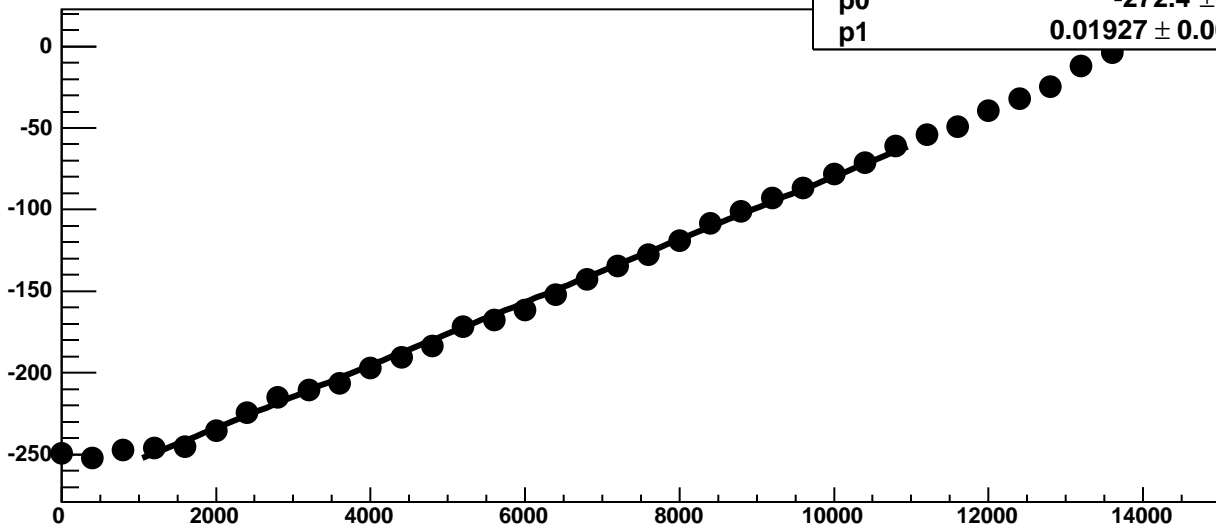
Chip 7, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

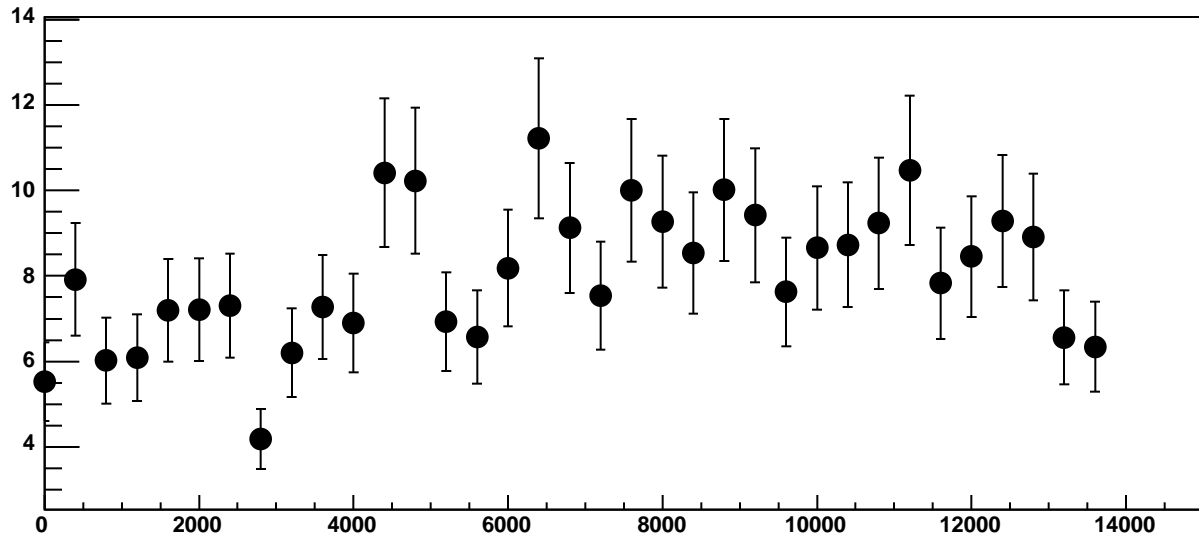


Chip 7, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC

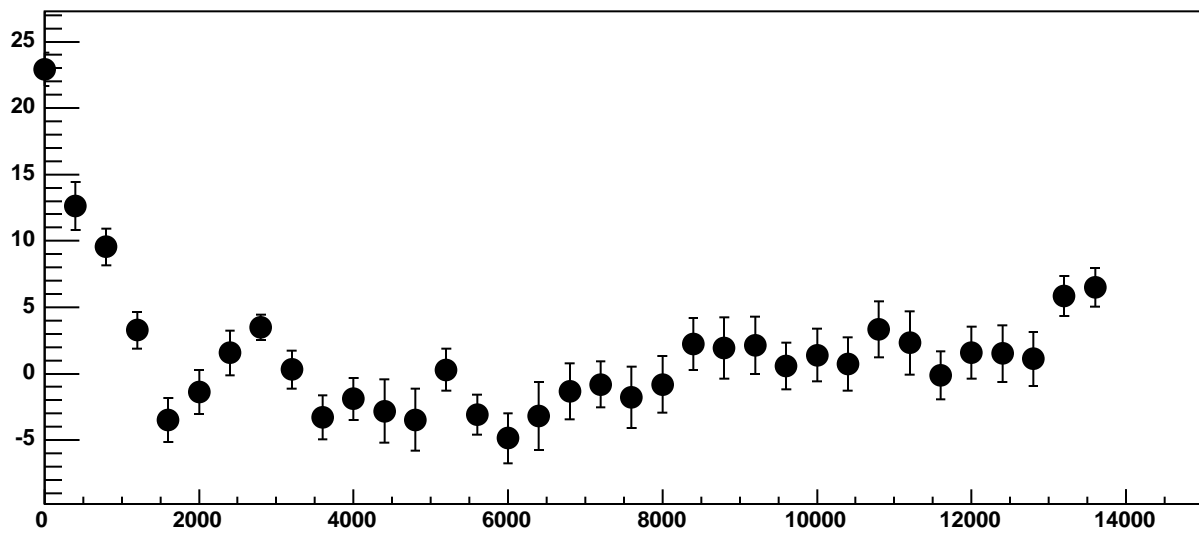


χ^2 / ndf 53.7 / 23
p0 -272.4 ± 0.7143
p1 0.01927 ± 0.0001203

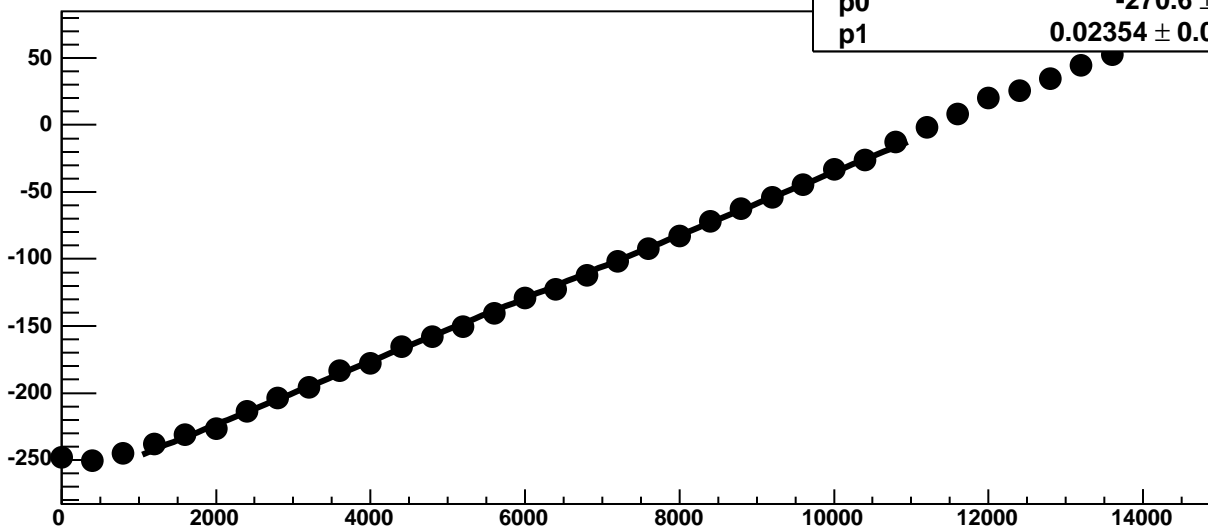
Chip 7, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



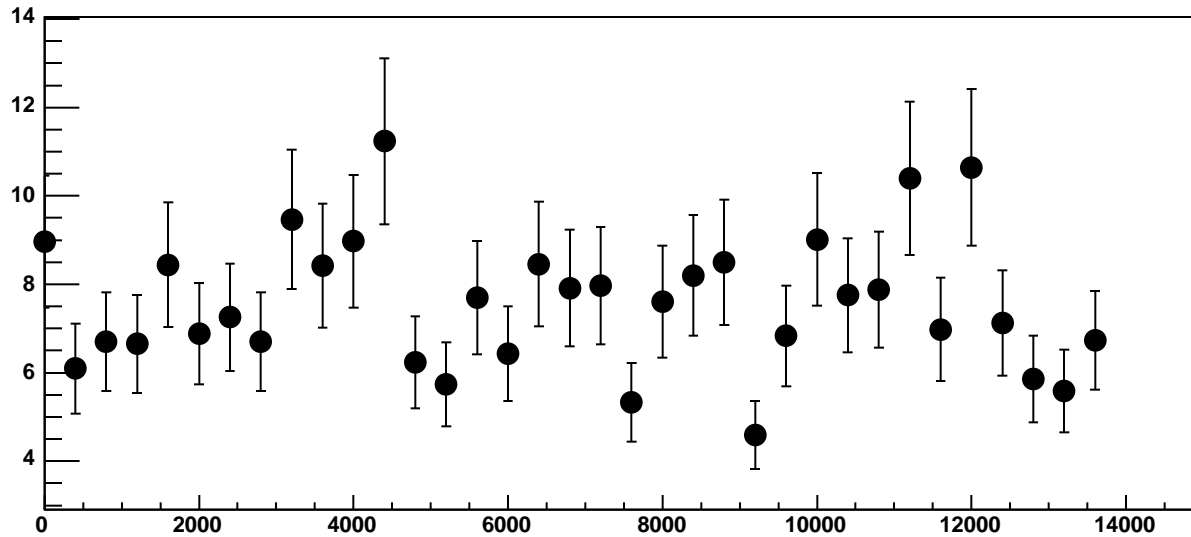
Chip 7, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC



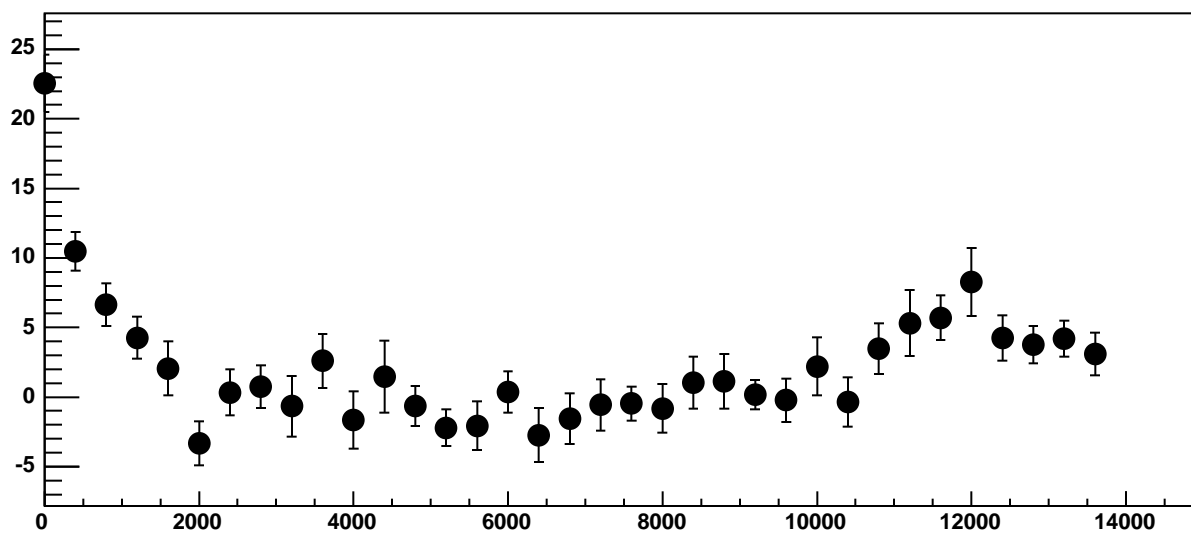
Chip 7, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



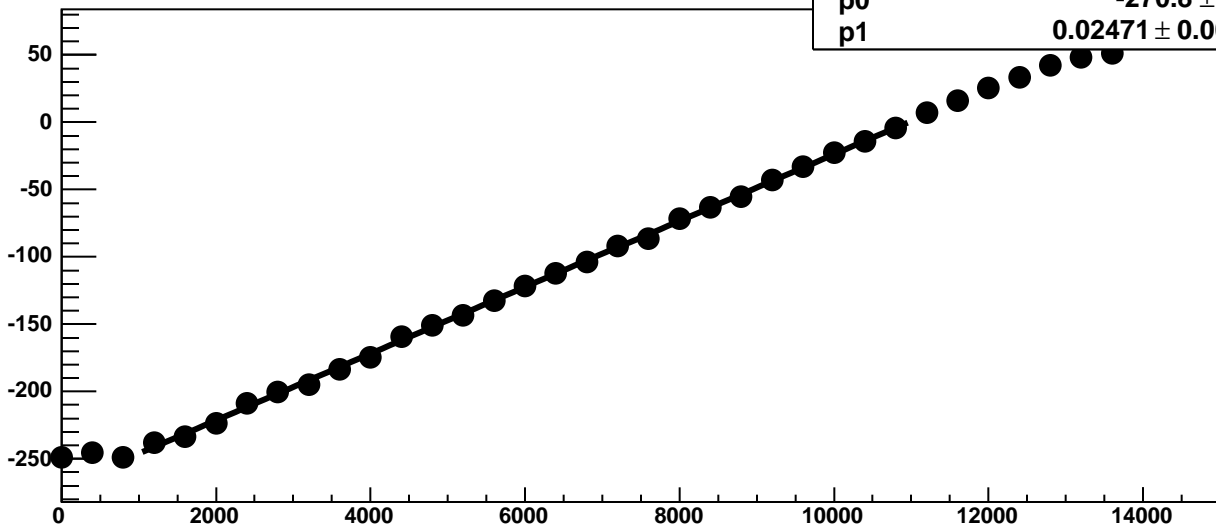
Chip 7, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

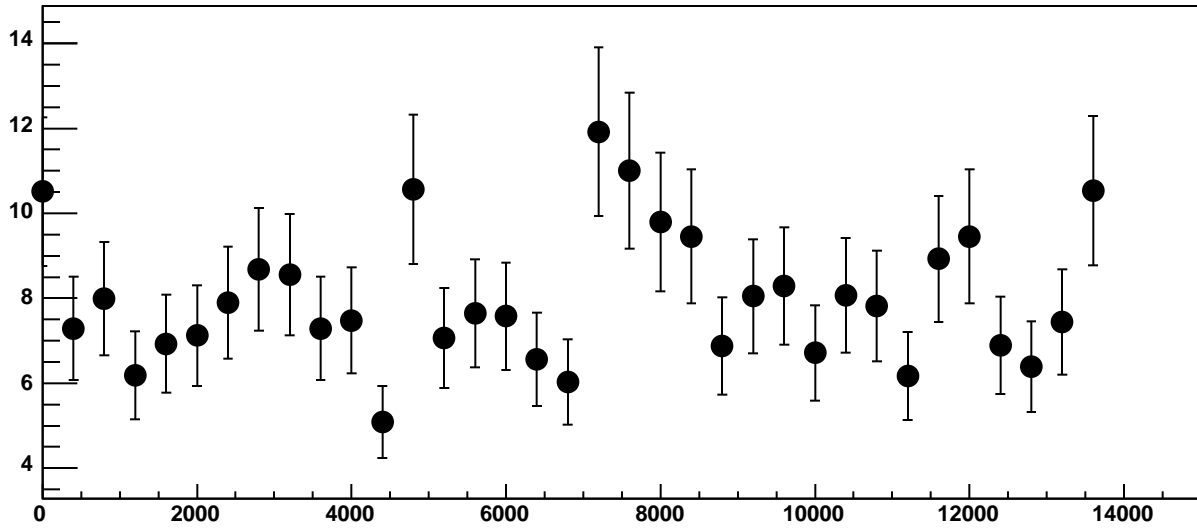


Chip 7, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

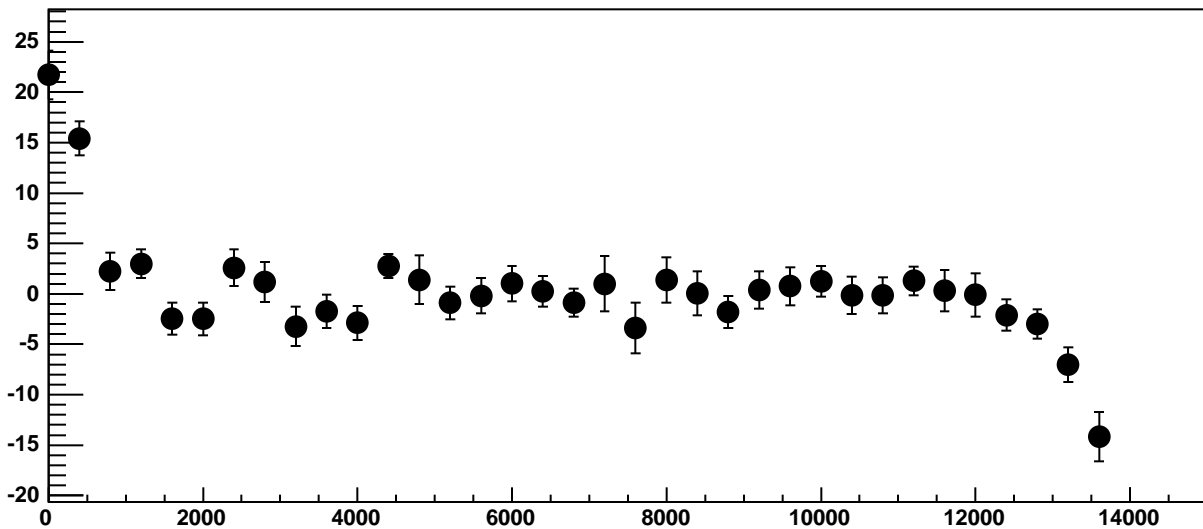


χ^2 / ndf 29.53 / 23
p0 -270.8 ± 0.7628
p1 0.02471 ± 0.0001185

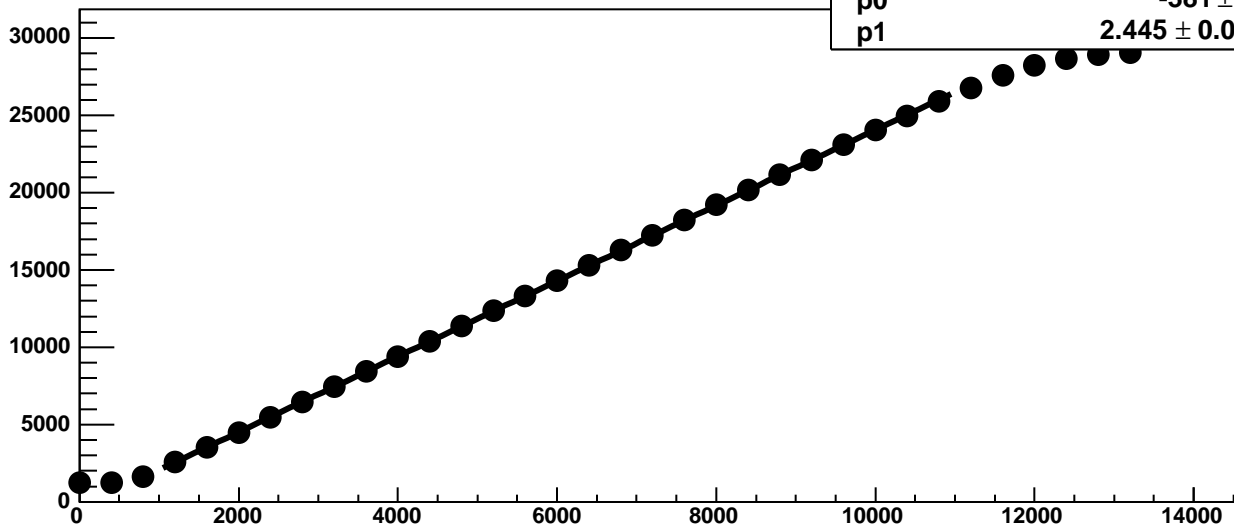
Chip 7, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



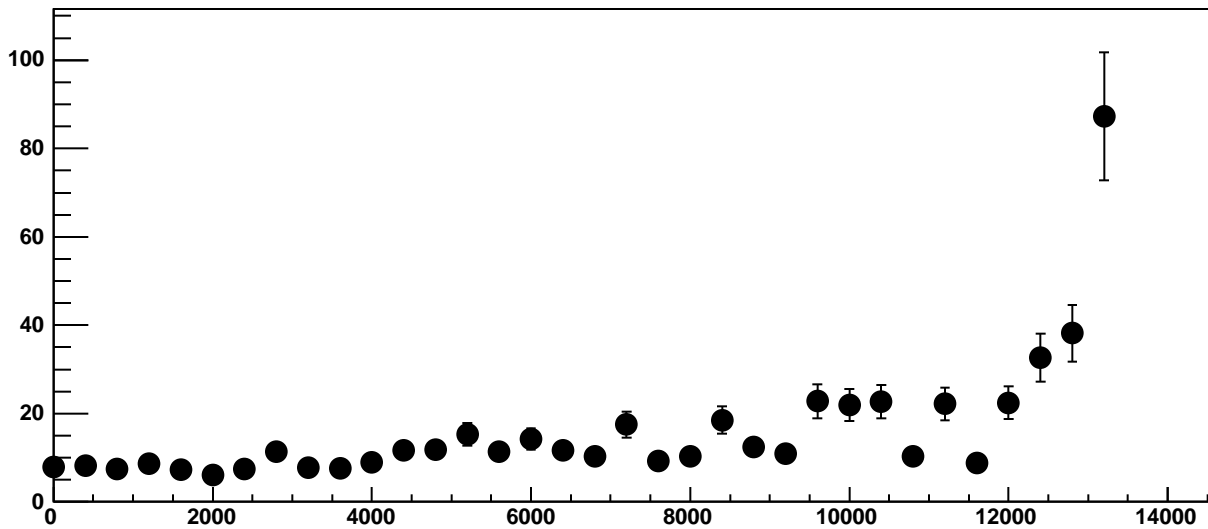
Chip 7, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



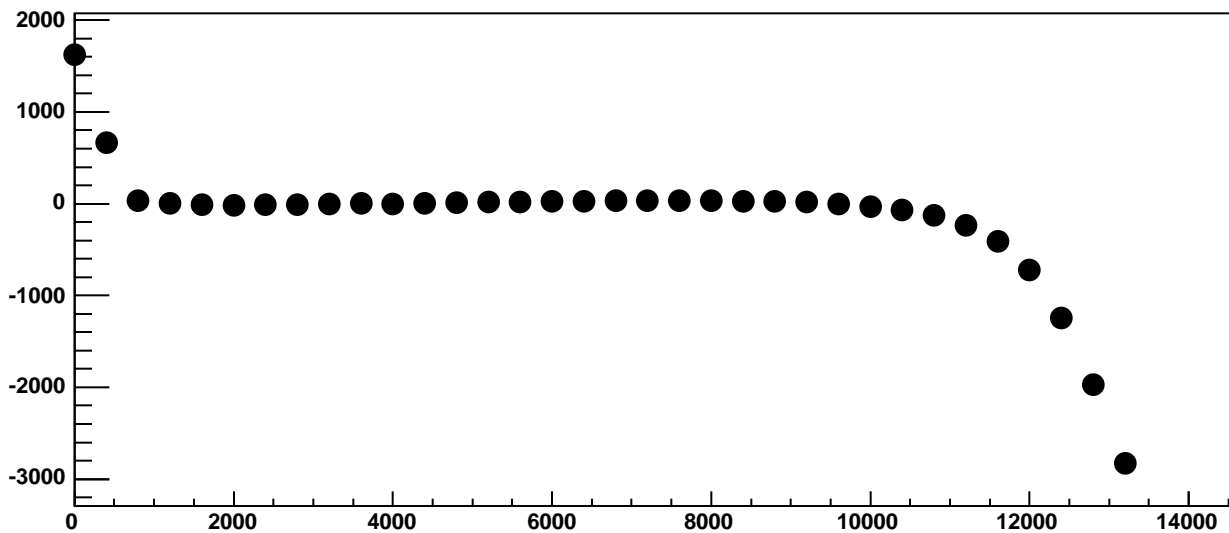
Chip 7, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC



Chip 7, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC

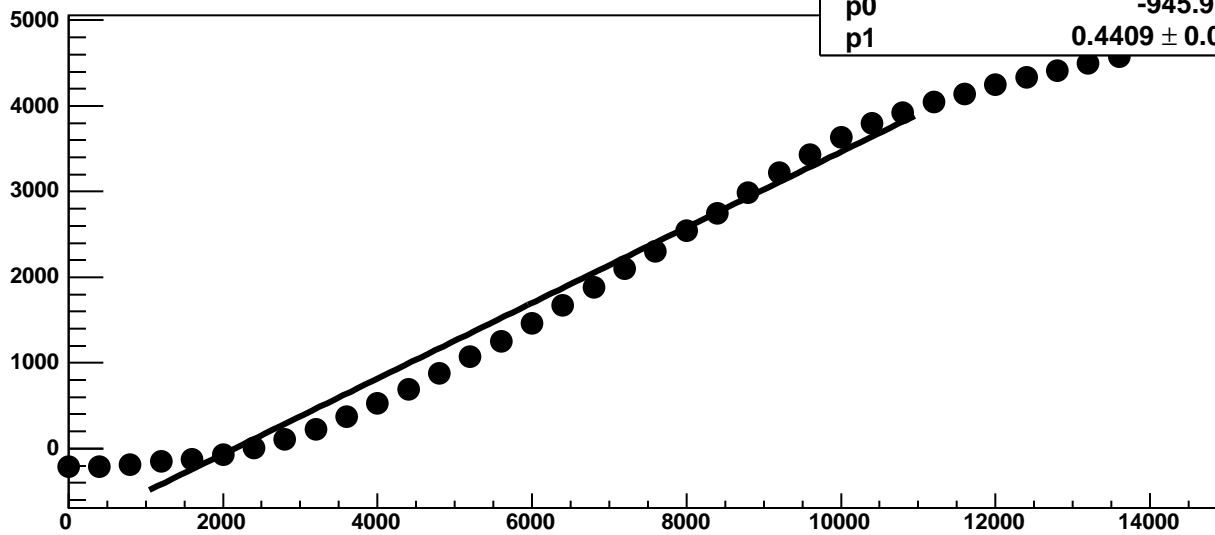


Chip 7, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC

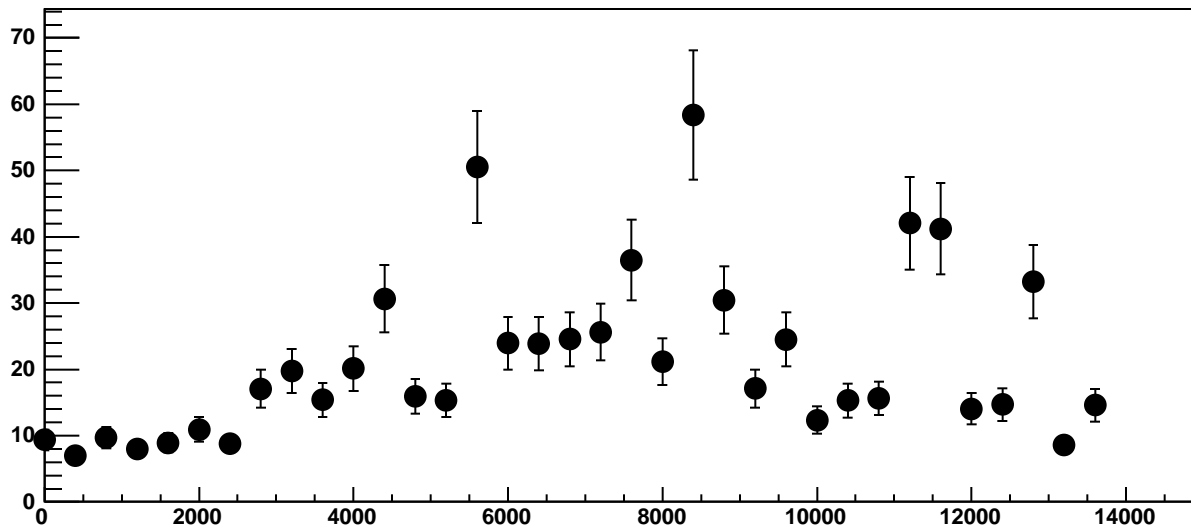


Chip 7, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

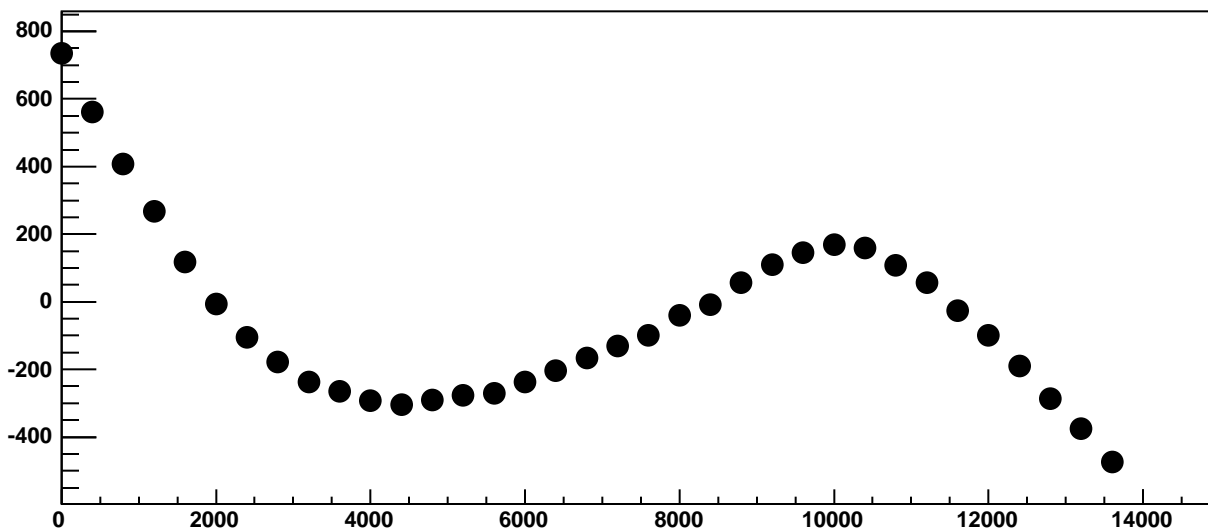
χ^2 / ndf 6.961e+04 / 23
p0 -945.9 ± 1.198
p1 0.4409 ± 0.0002154



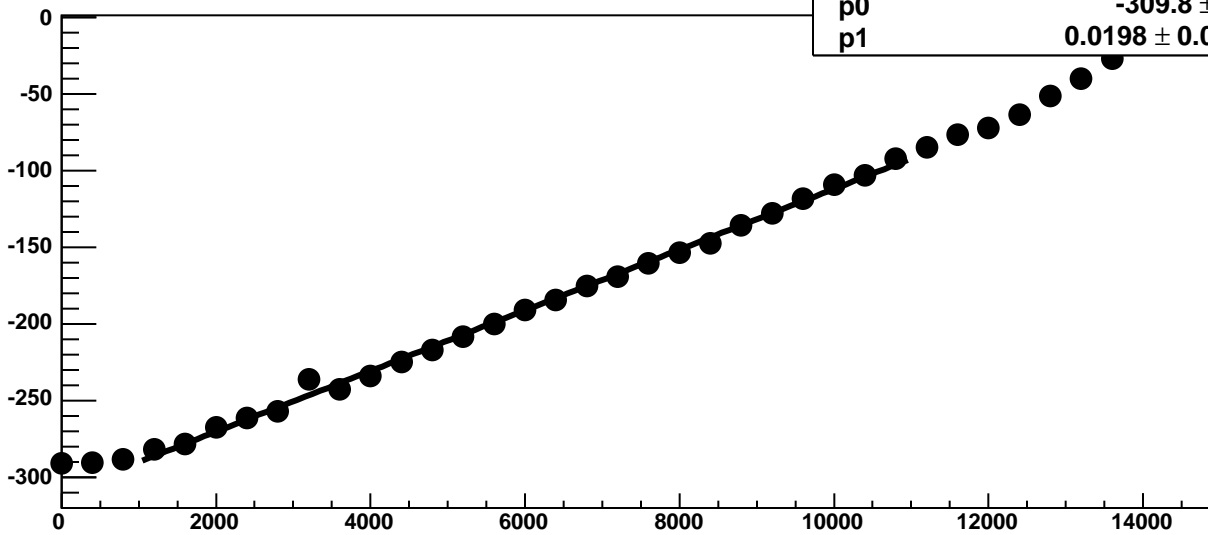
Chip 7, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

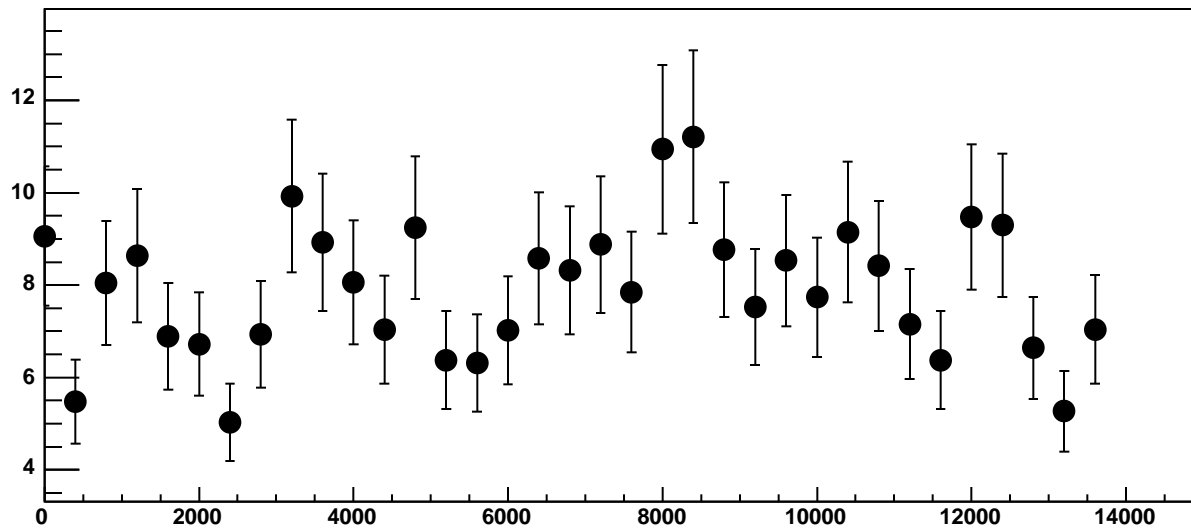


Chip 7, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

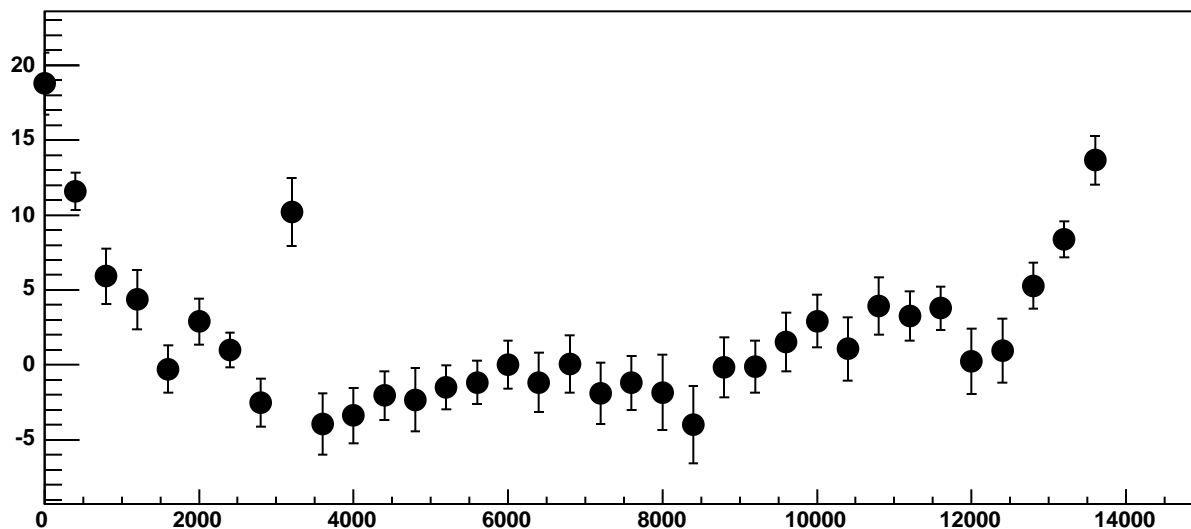


χ^2 / ndf 55.57 / 23
p0 -309.8 ± 0.7708
p1 0.0198 ± 0.0001237

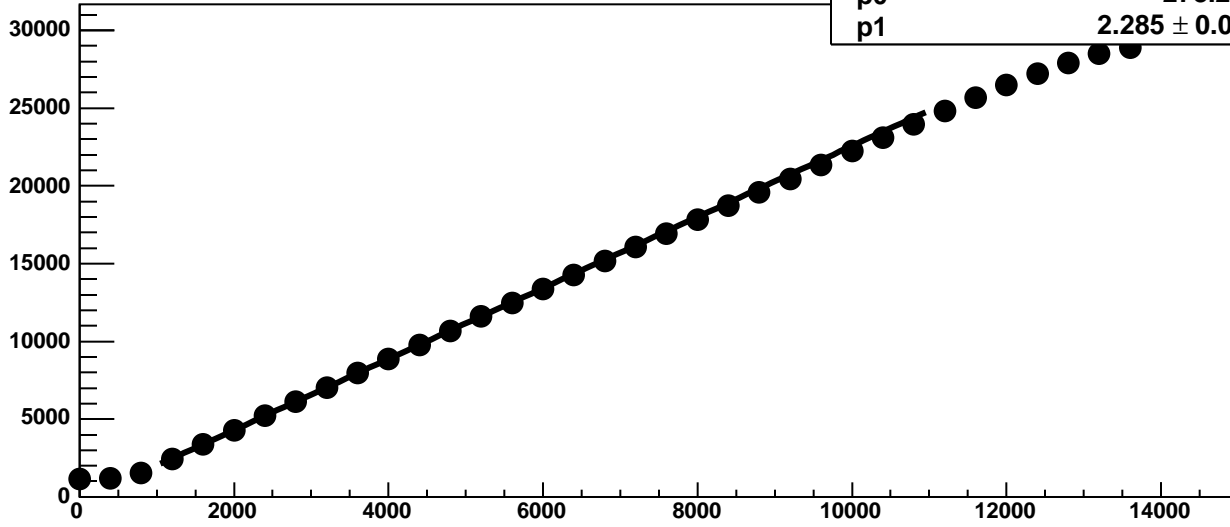
Chip 7, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

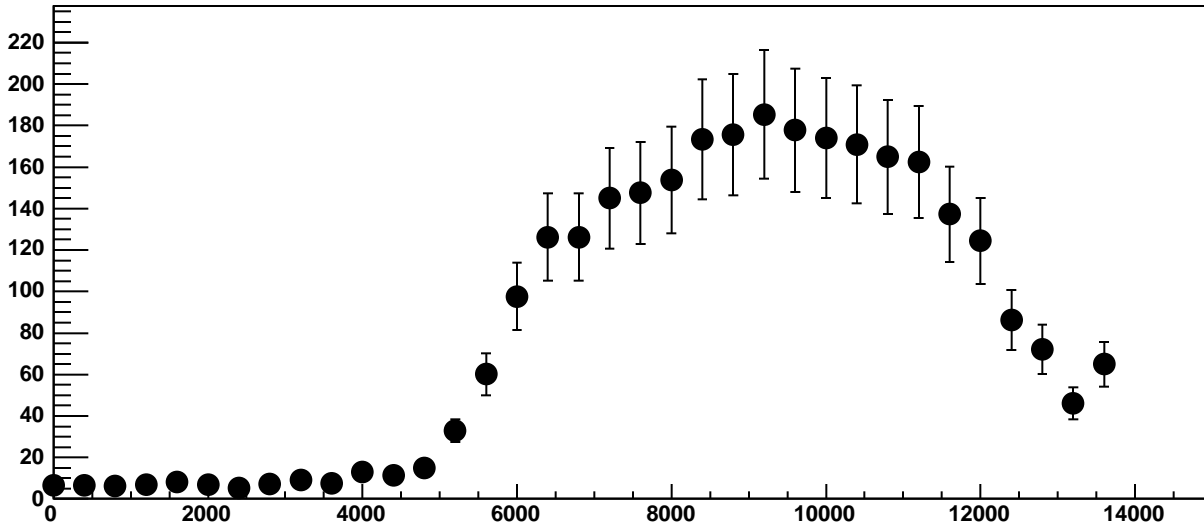


Chip 7, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC

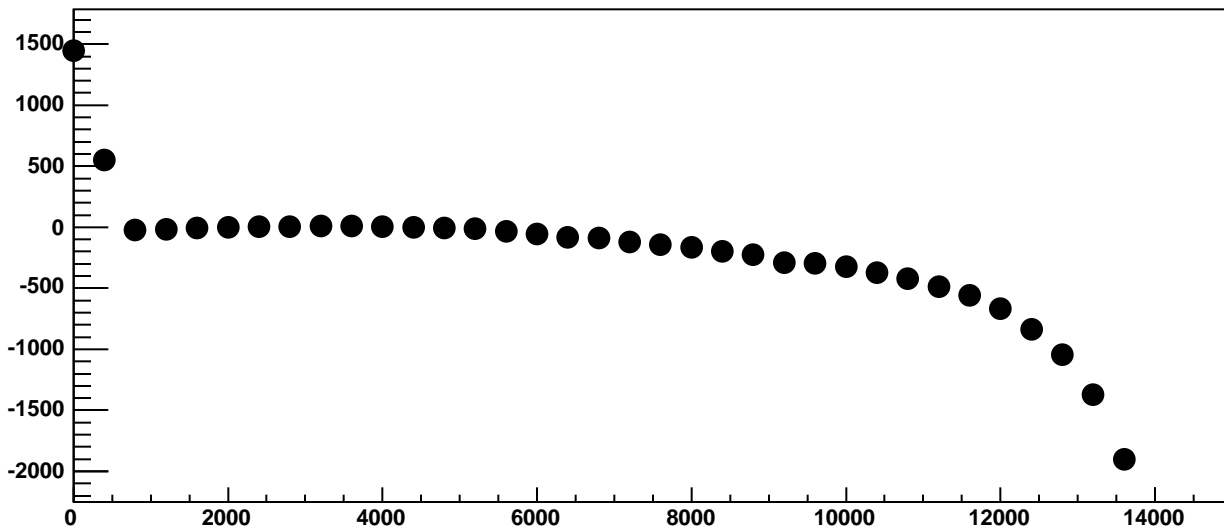


χ^2 / ndf 706.4 / 23
p0 -278.2 ± 1.541
p1 2.285 ± 0.0005497

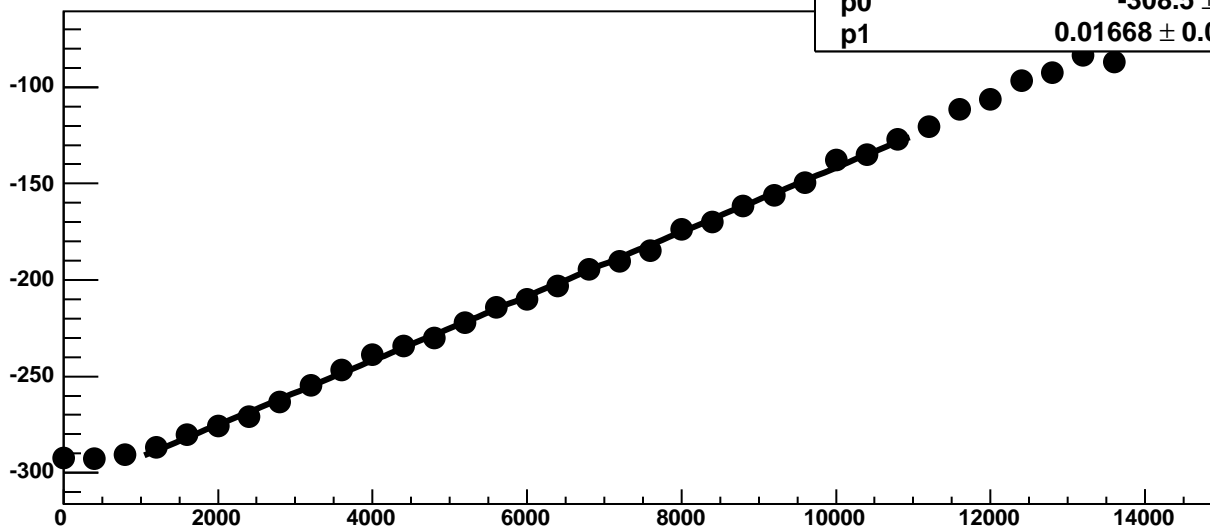
Chip 7, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC

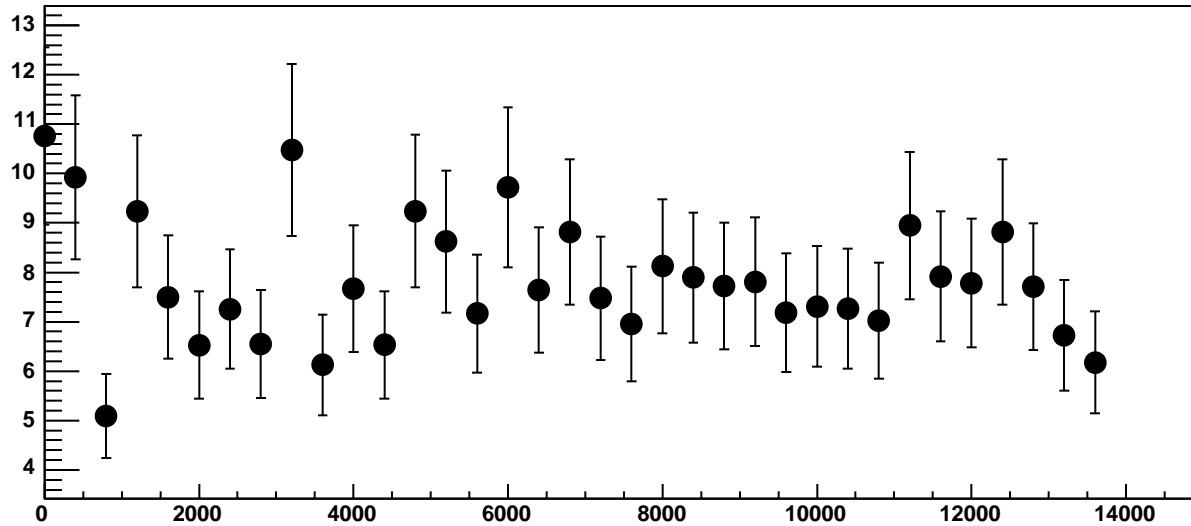


Chip 7, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

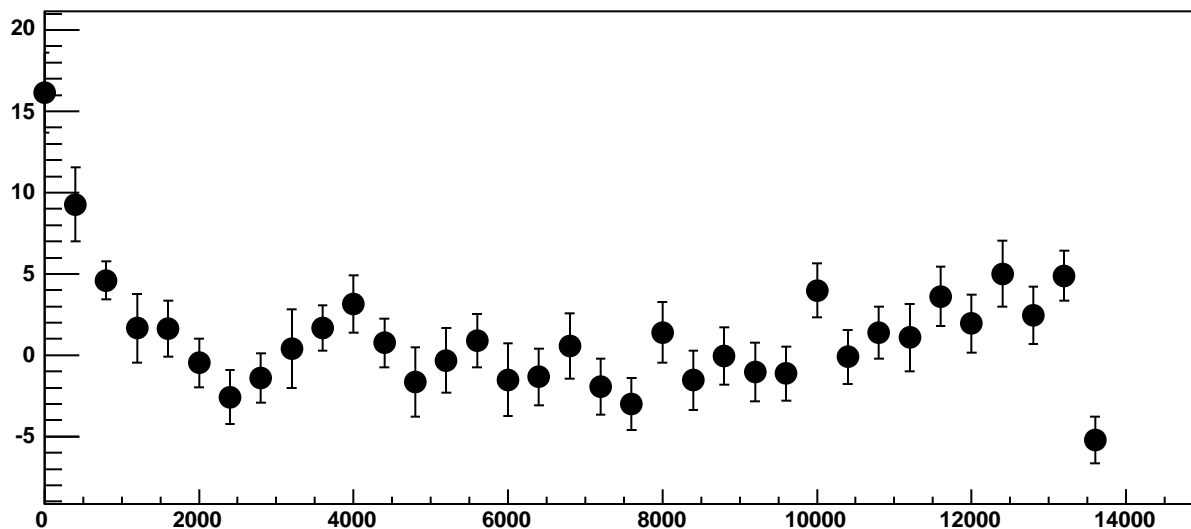


χ^2 / ndf 25.1 / 23
p0 -308.5 ± 0.7883
p1 0.01668 ± 0.0001179

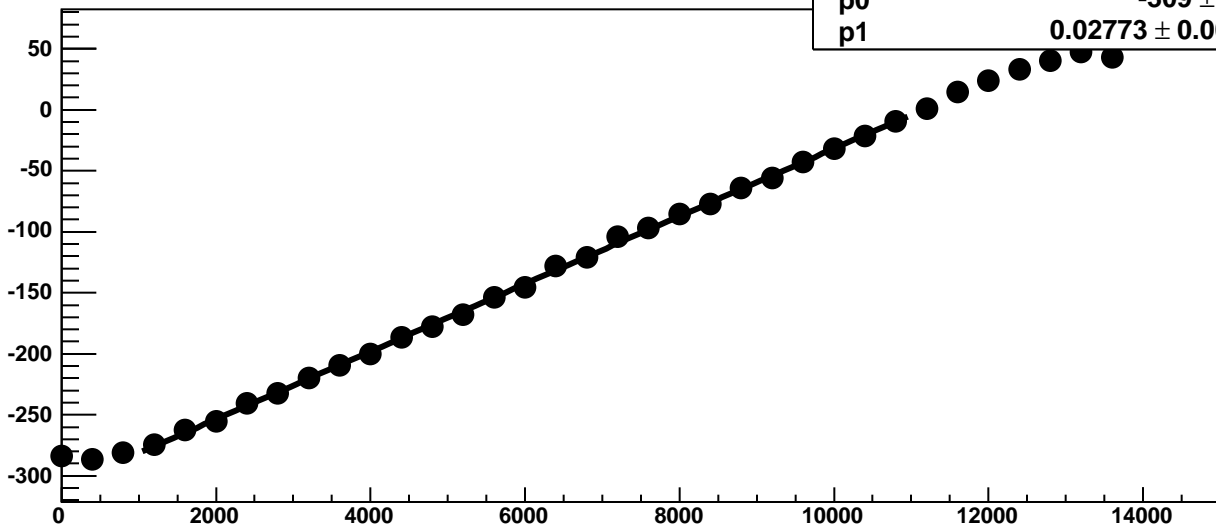
Chip 7, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



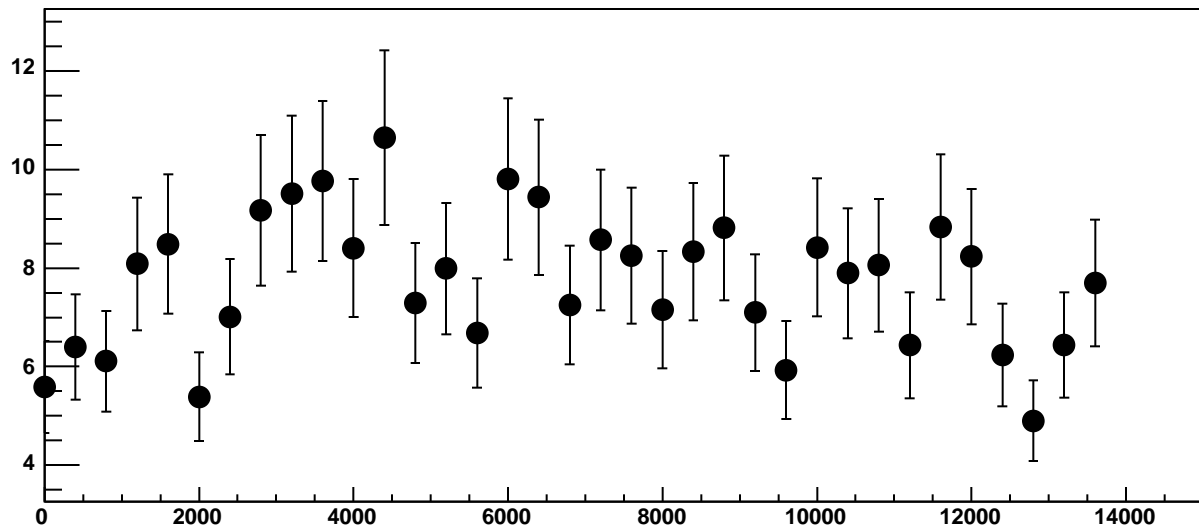
Chip 7, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



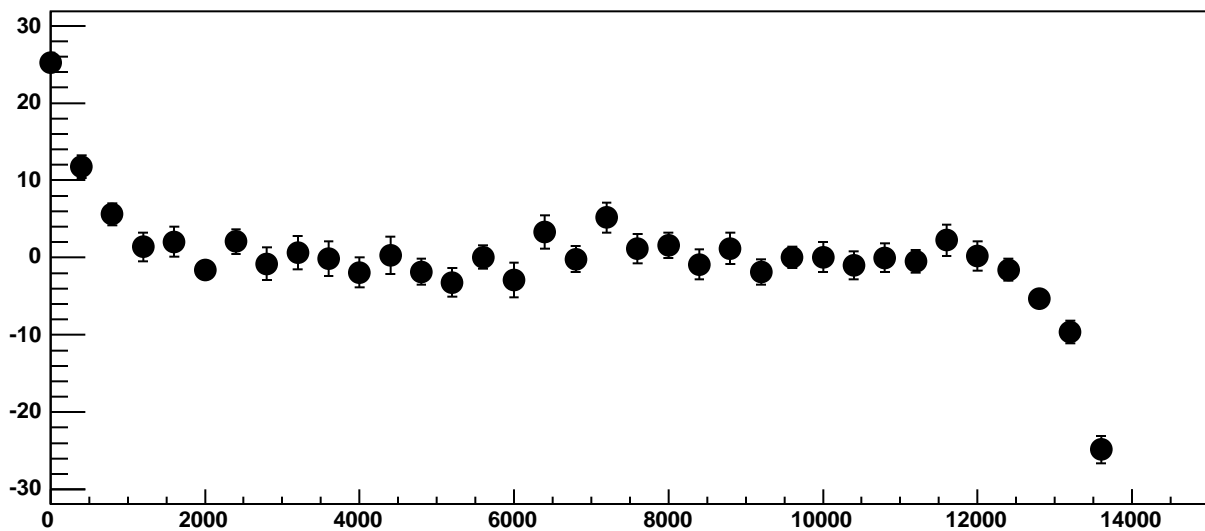
Chip 7, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



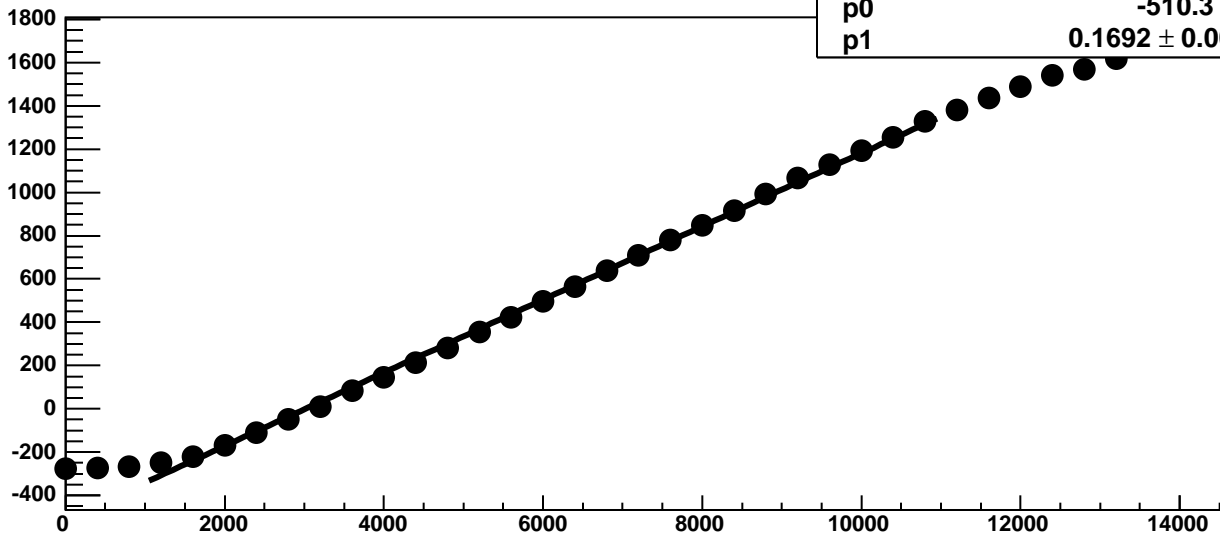
Chip 7, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



Chip 7, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1477 / 23

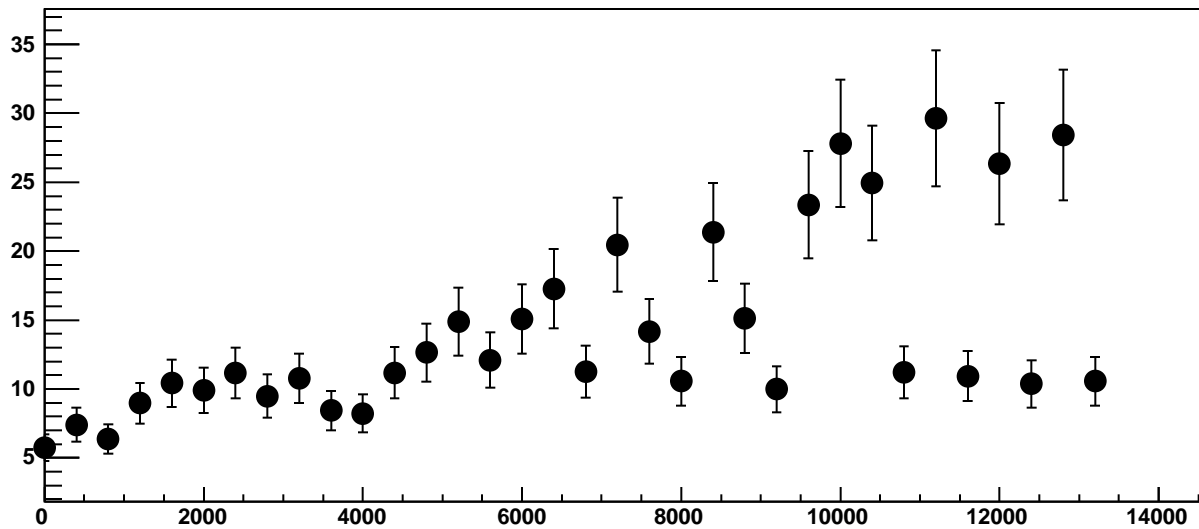
p0

-510.3 ± 1.119

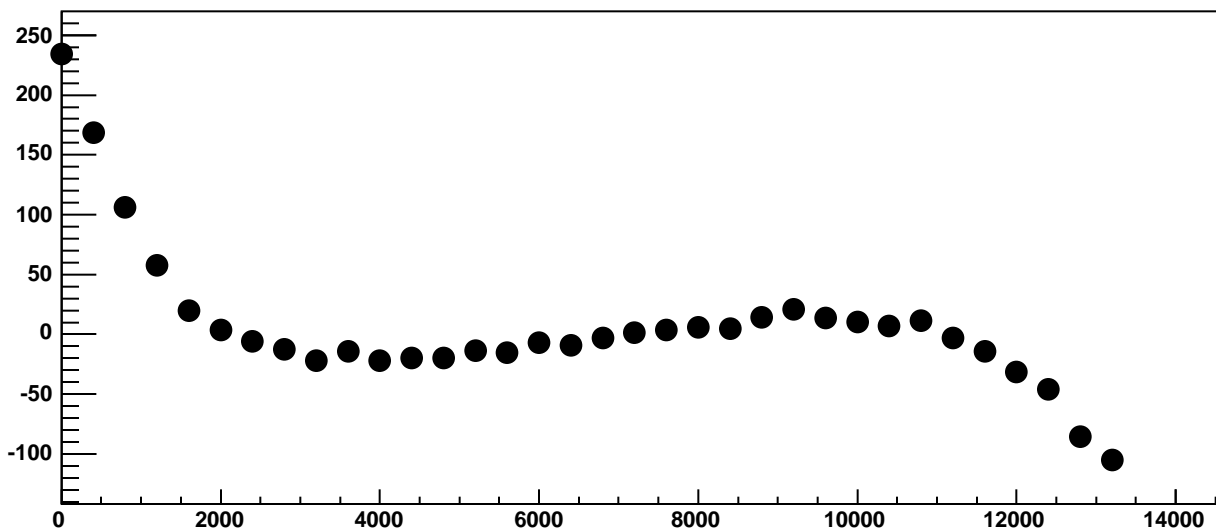
p1

0.1692 ± 0.0001956

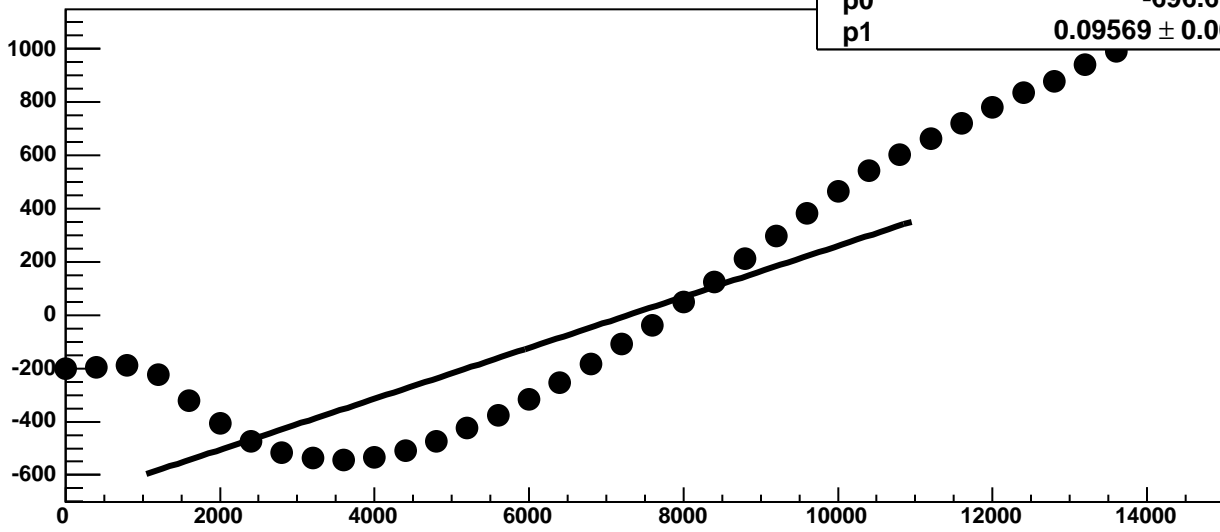
Chip 7, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



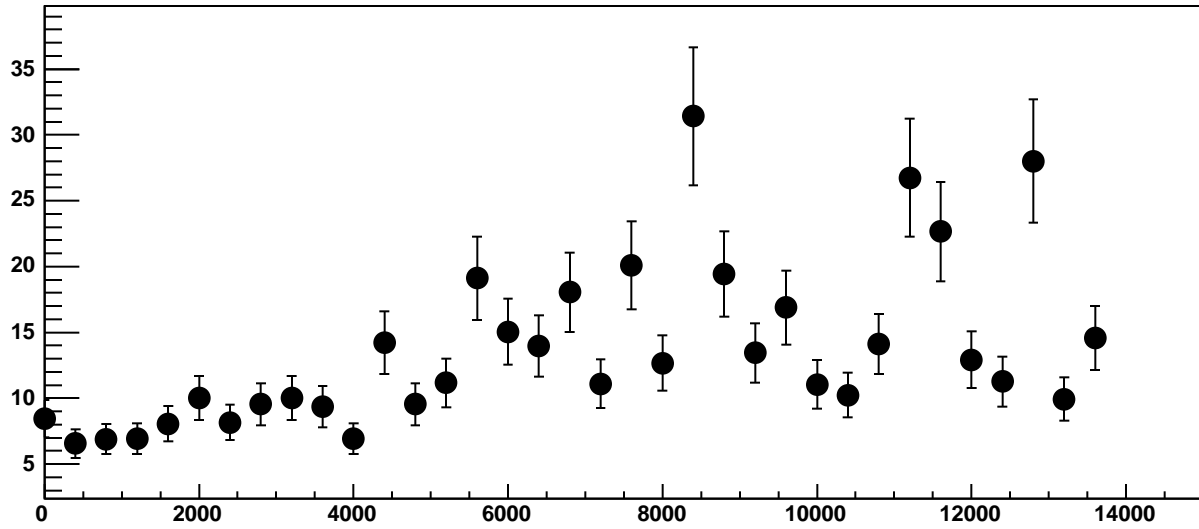
Chip 7, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC



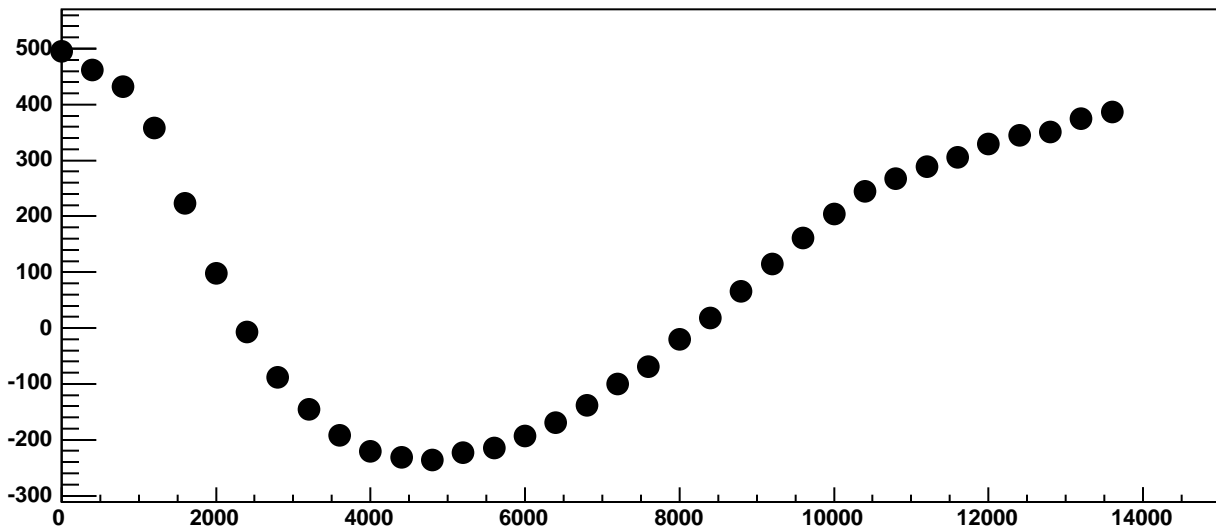
Chip 7, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



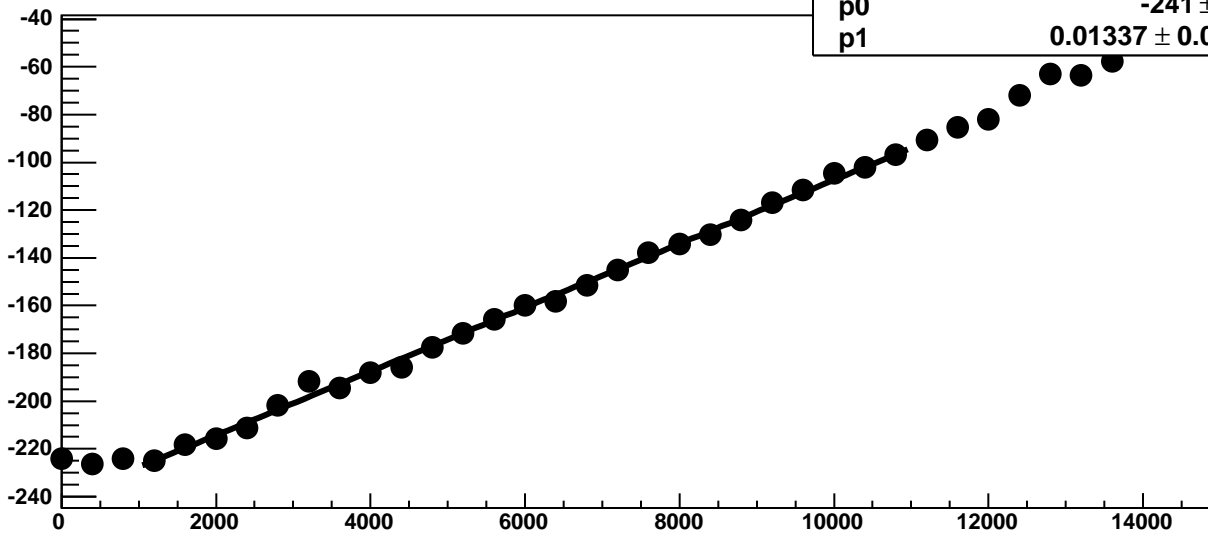
Chip 7, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

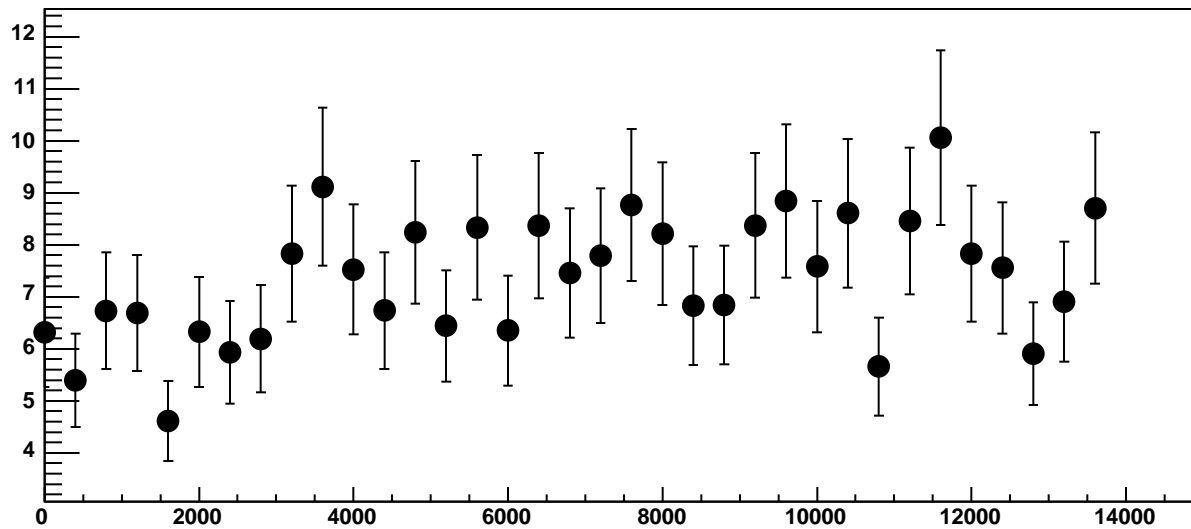


Chip 7, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

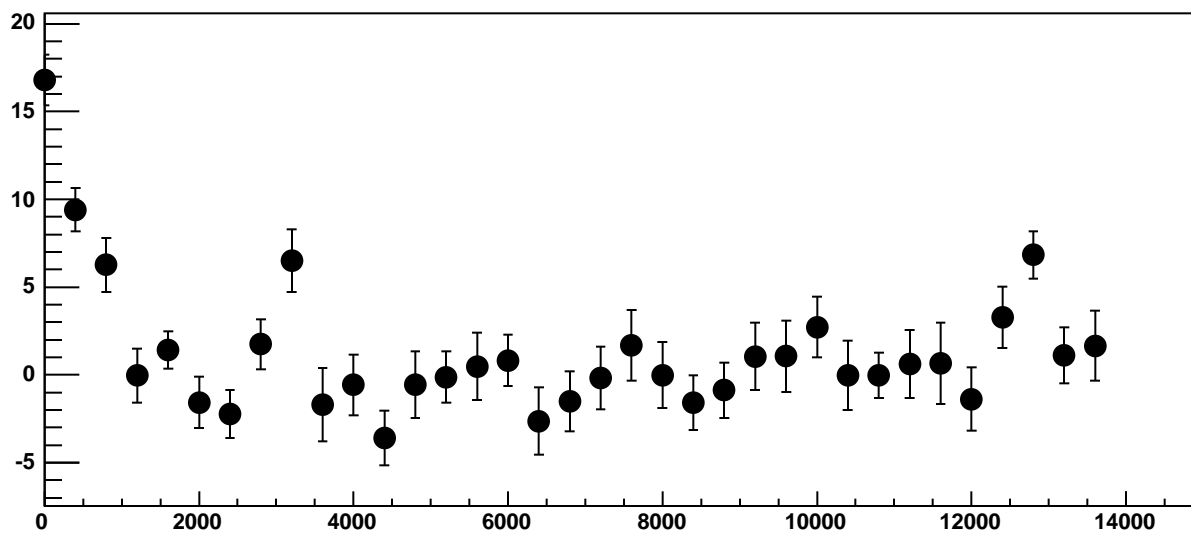


χ^2 / ndf 34.57 / 23
p0 -241 ± 0.6712
p1 0.01337 ± 0.0001056

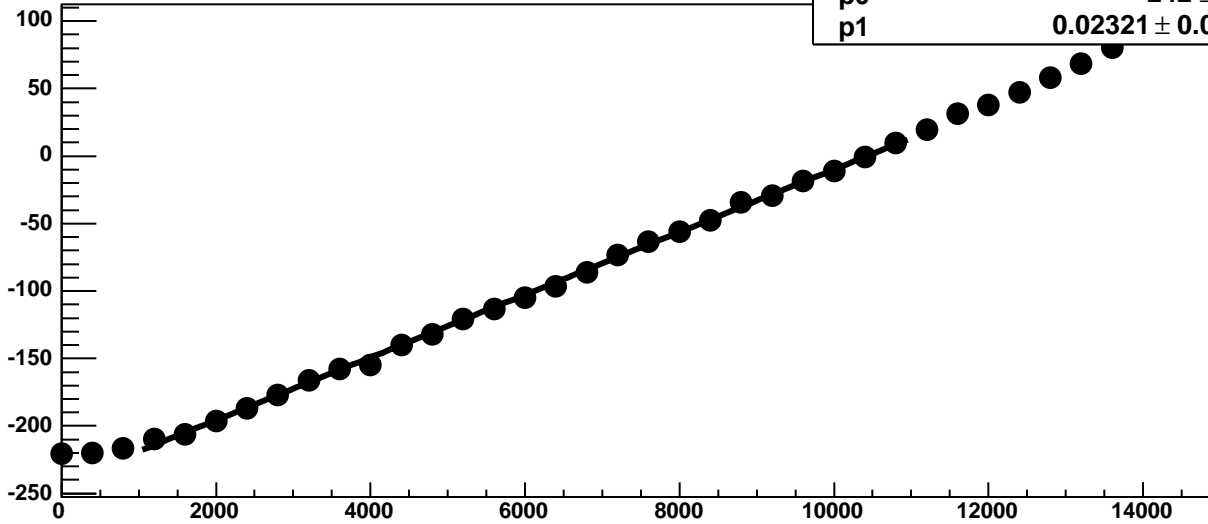
Chip 7, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



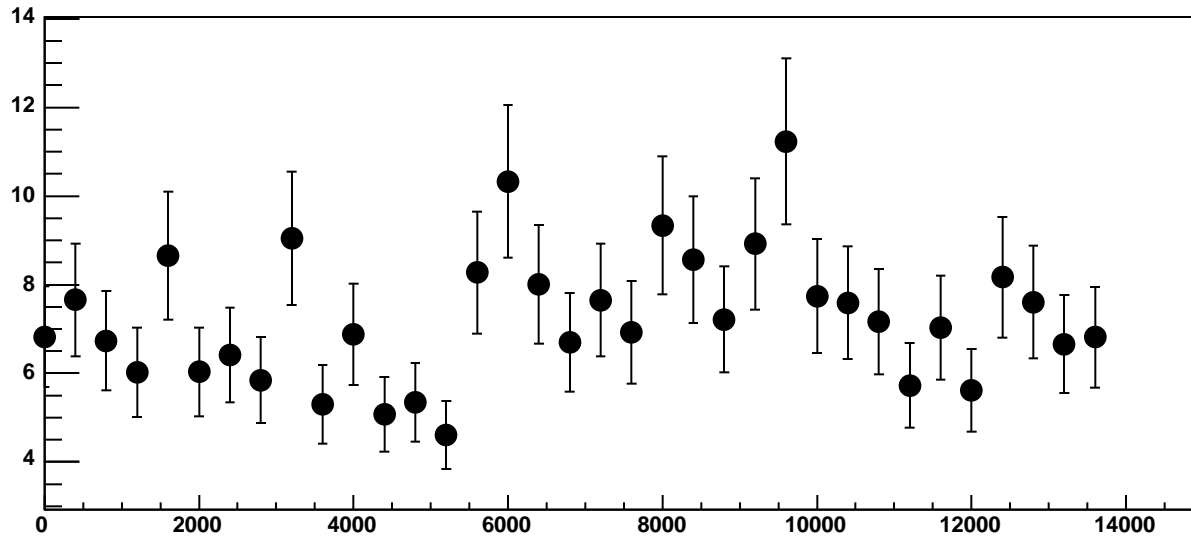
Chip 7, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



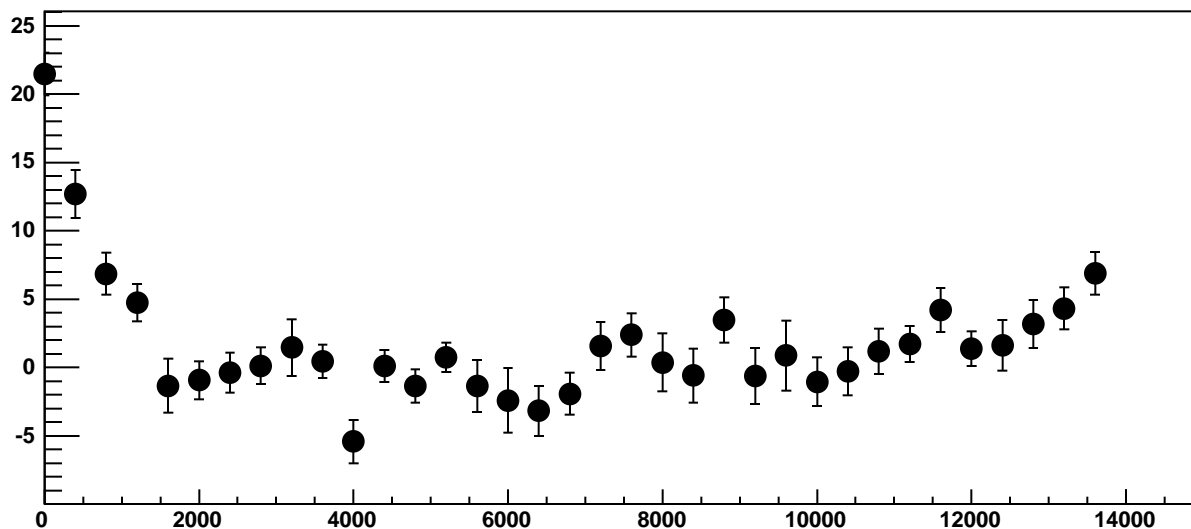
Chip 7, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC



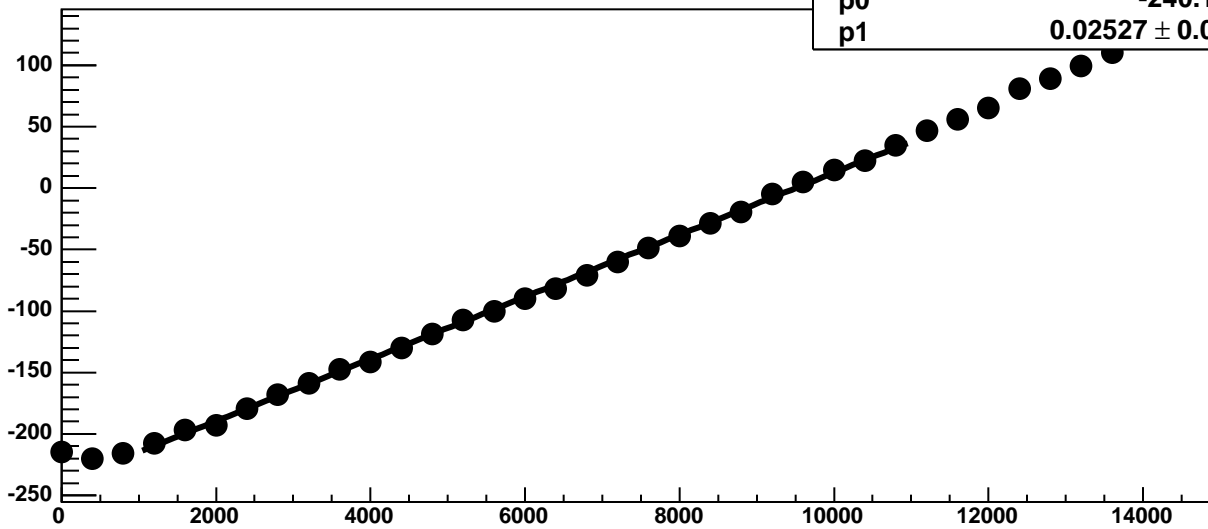
Chip 7, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

28.87 / 23

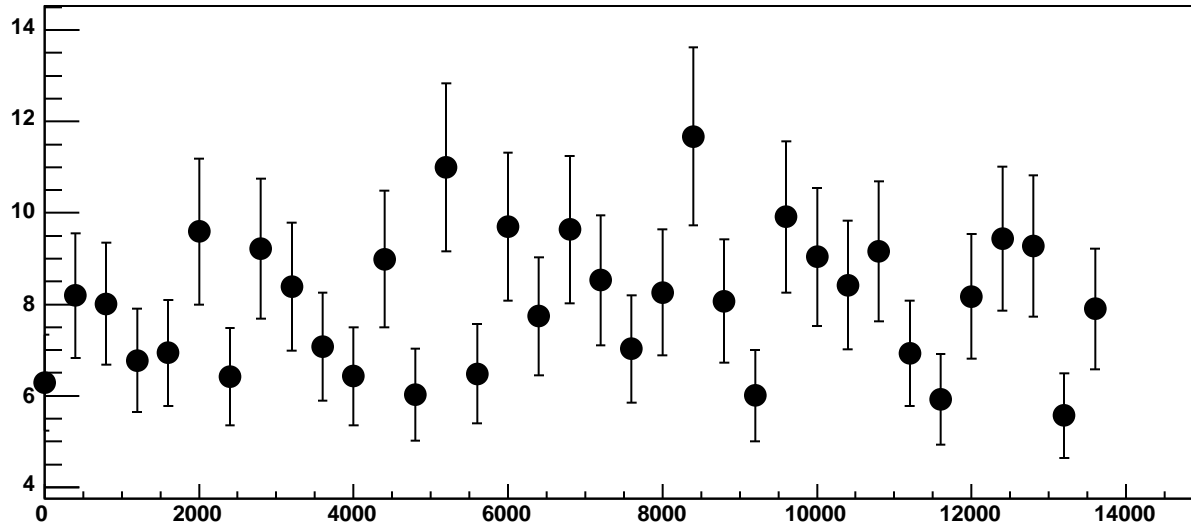
p0

-240.1 ± 0.801

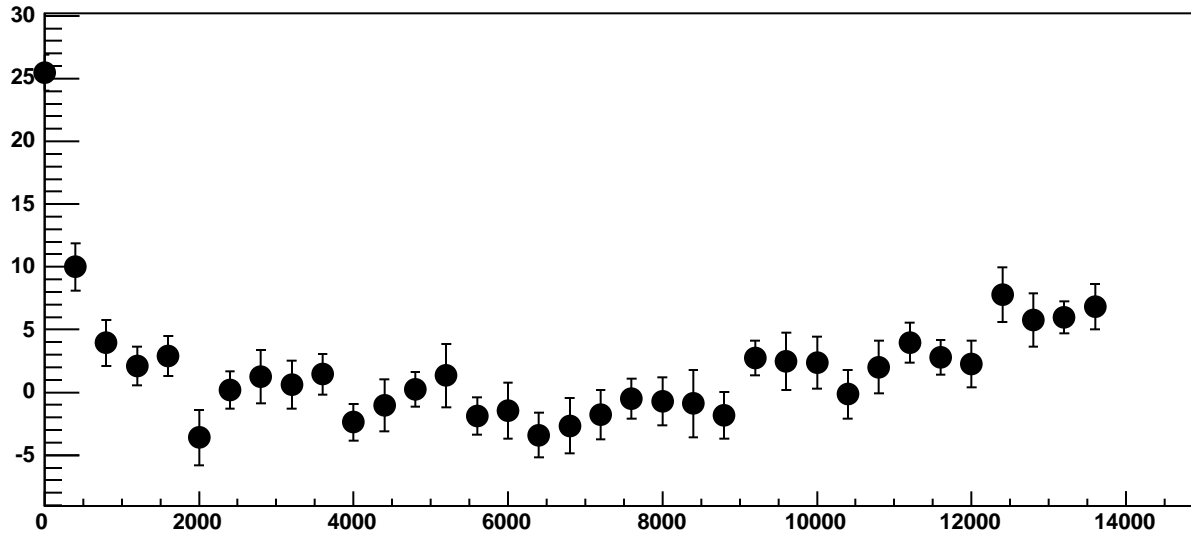
p1

0.02527 ± 0.0001255

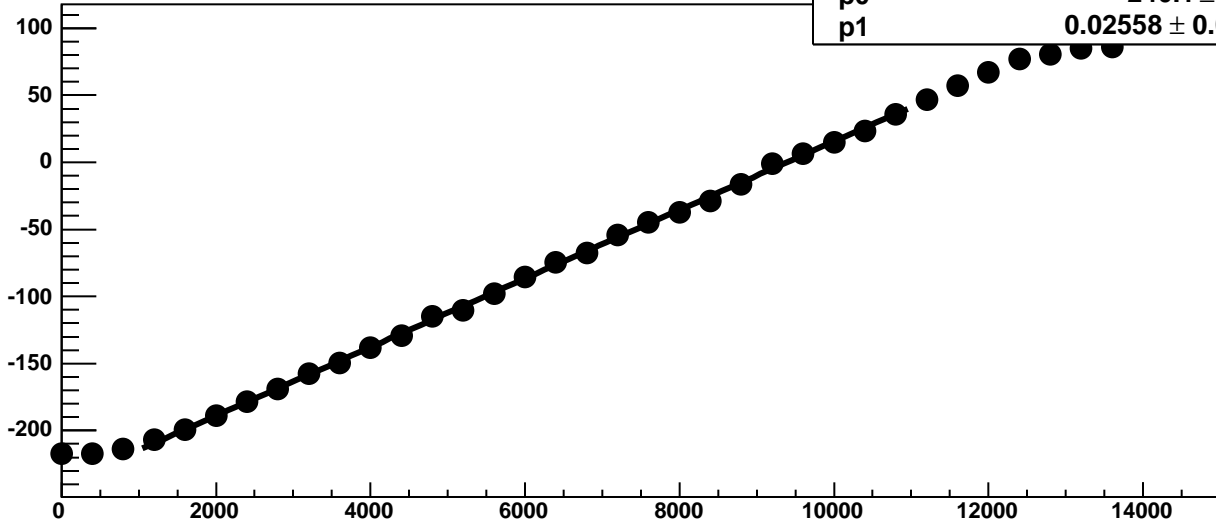
Chip 7, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



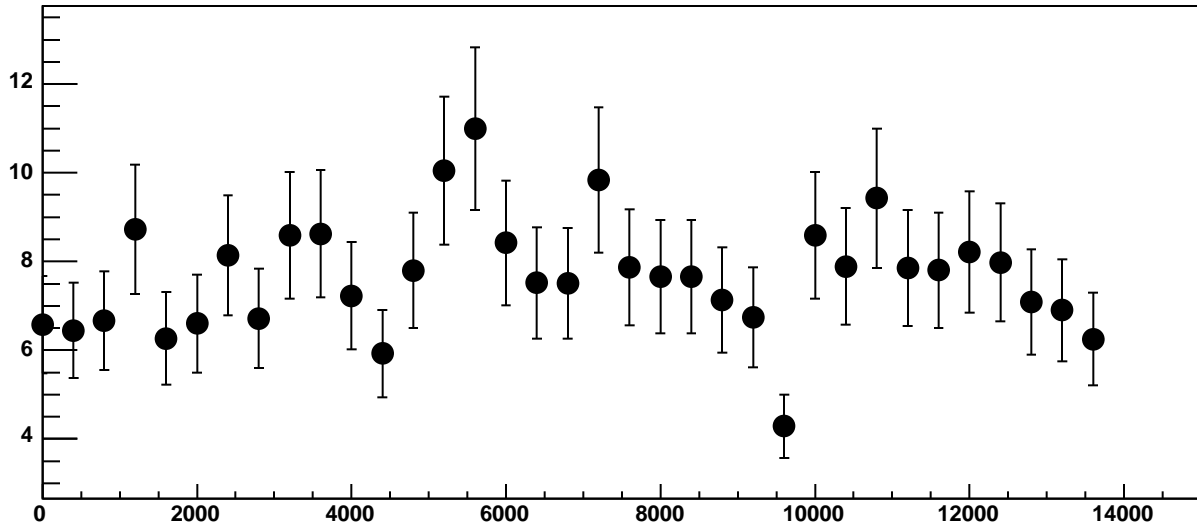
Chip 7, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



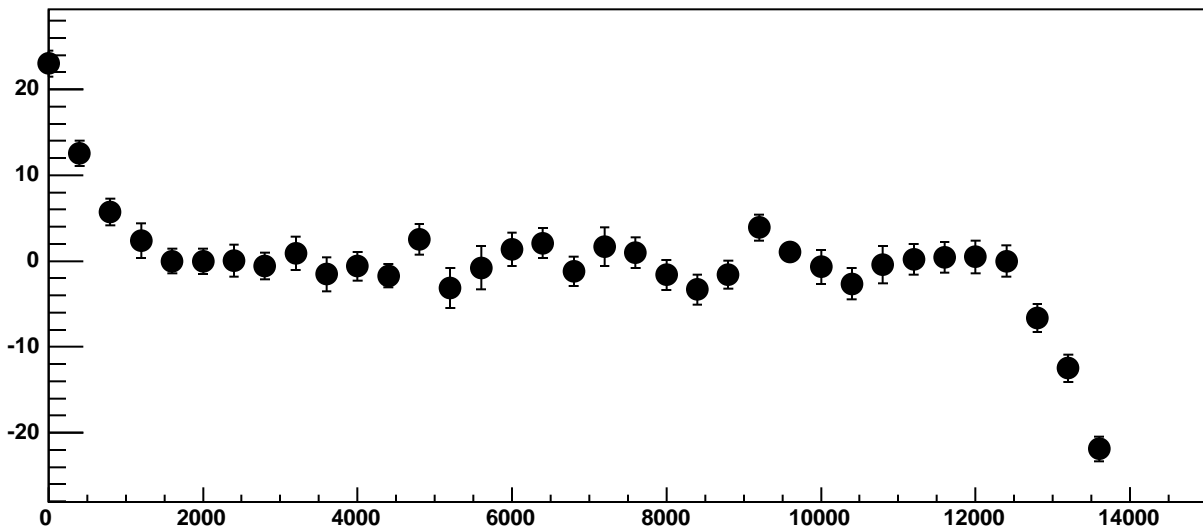
Chip 7, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



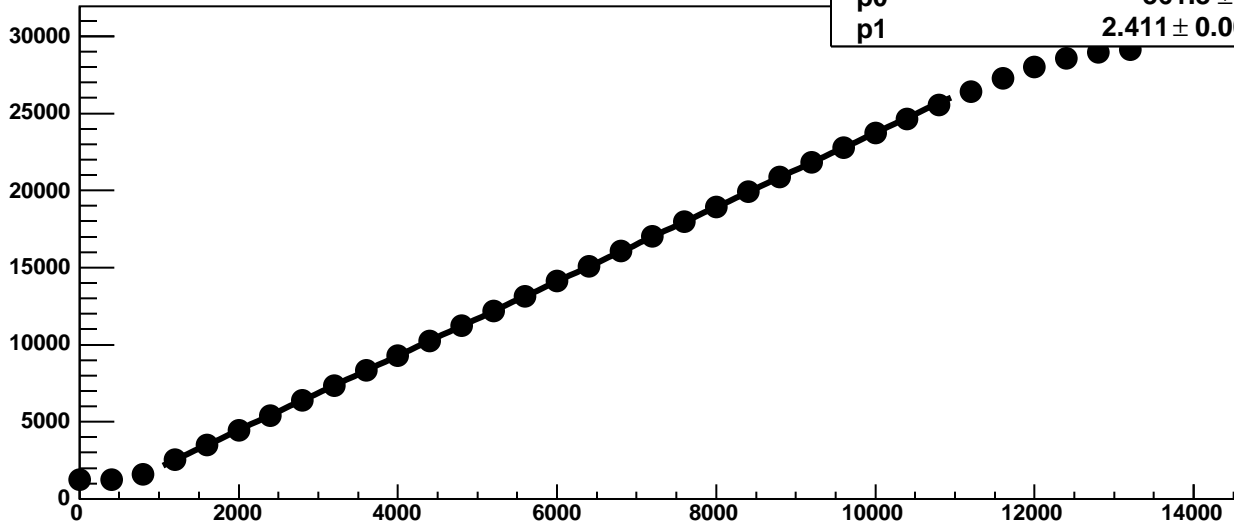
Chip 7, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 7, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

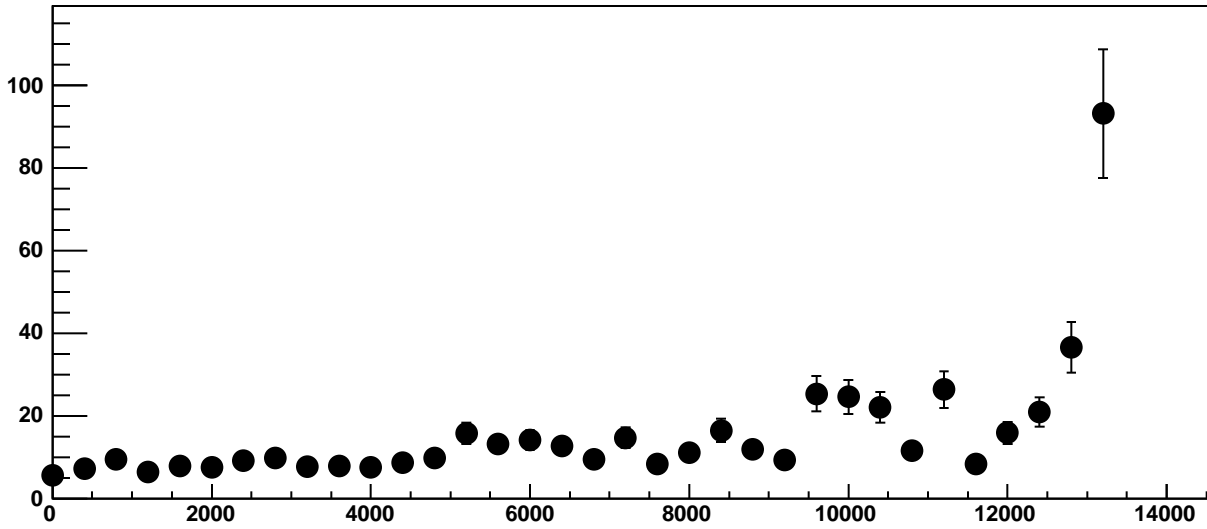


Chip 7, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC

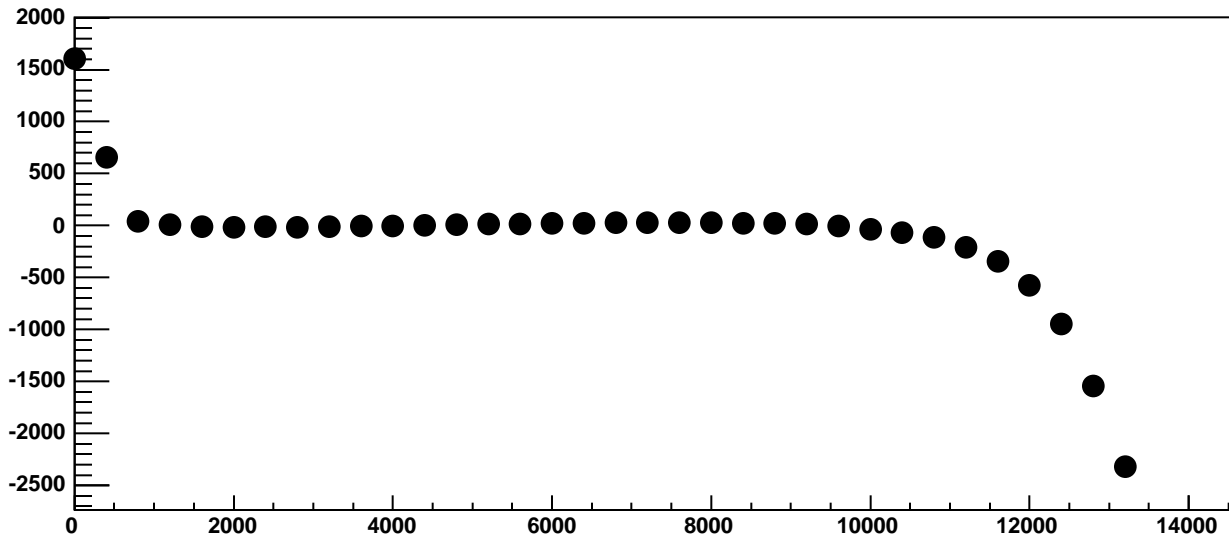


χ^2 / ndf 2965 / 23
p0 -361.8 ± 0.9229
p1 2.411 ± 0.0001683

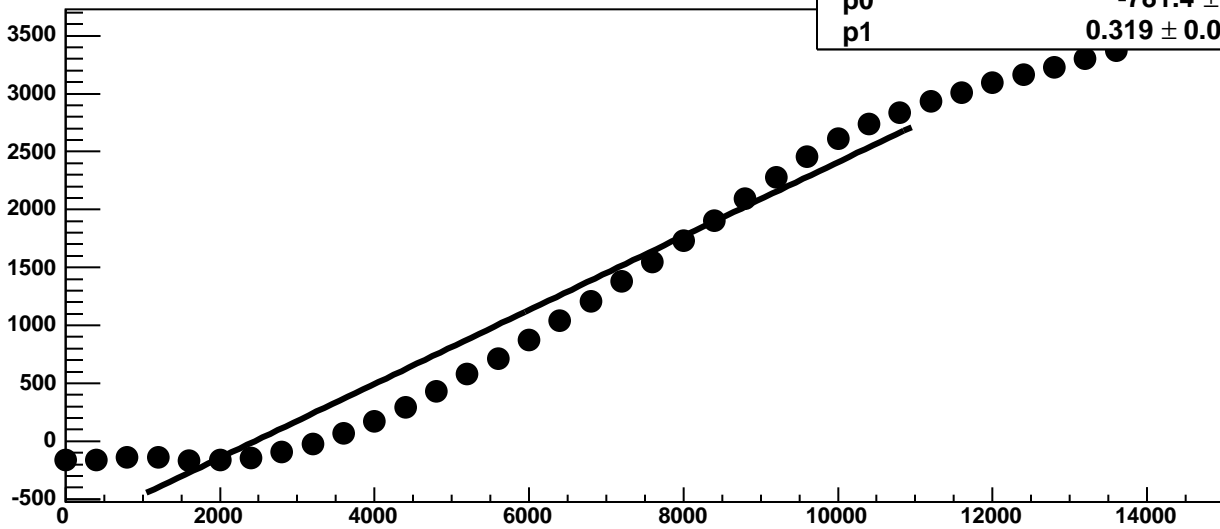
Chip 7, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC



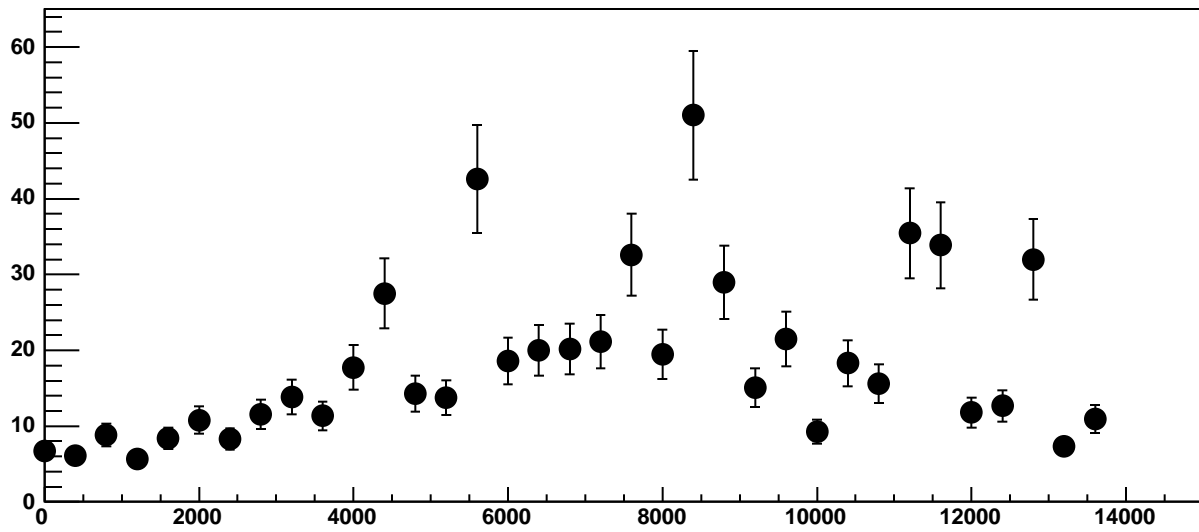
Chip 7, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC



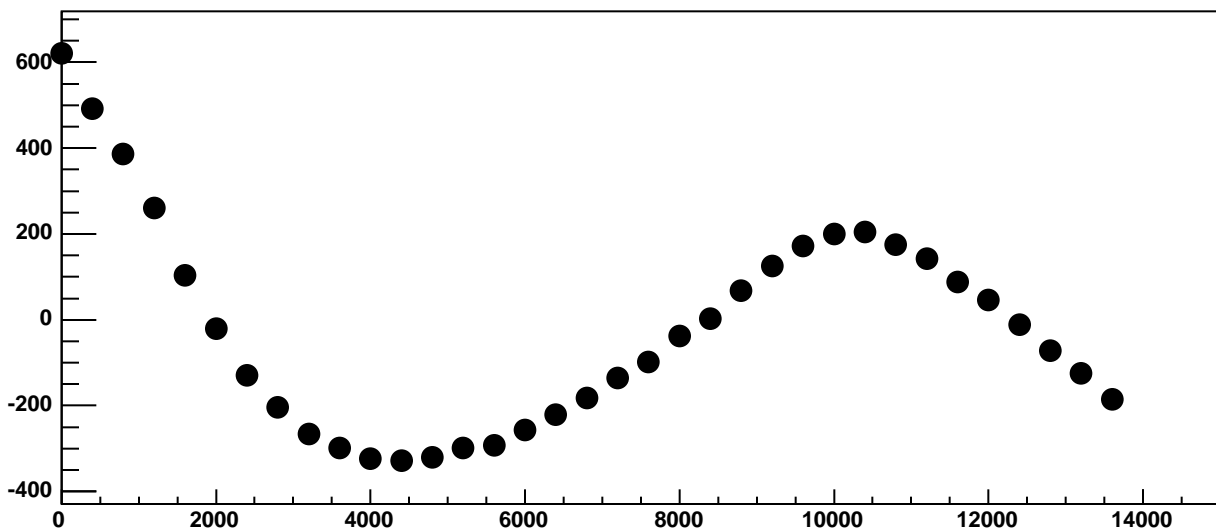
Chip 7, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC



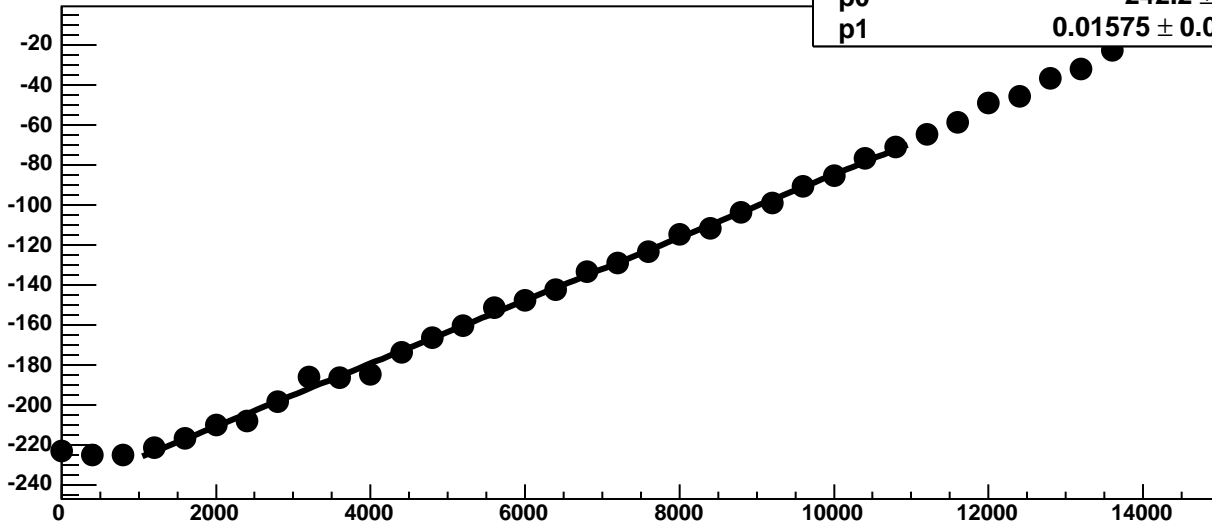
Chip 7, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

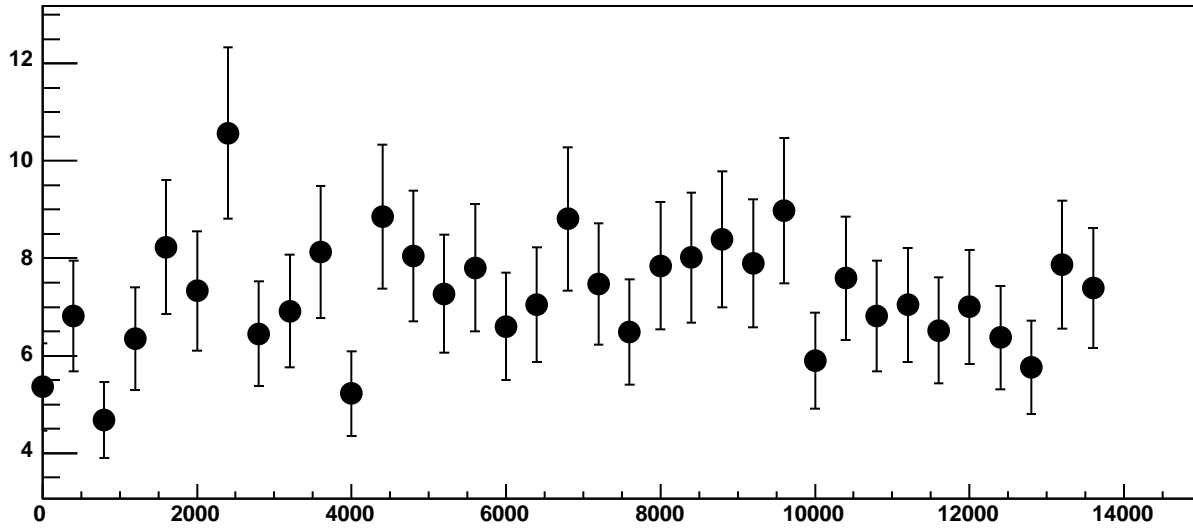


Chip 7, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

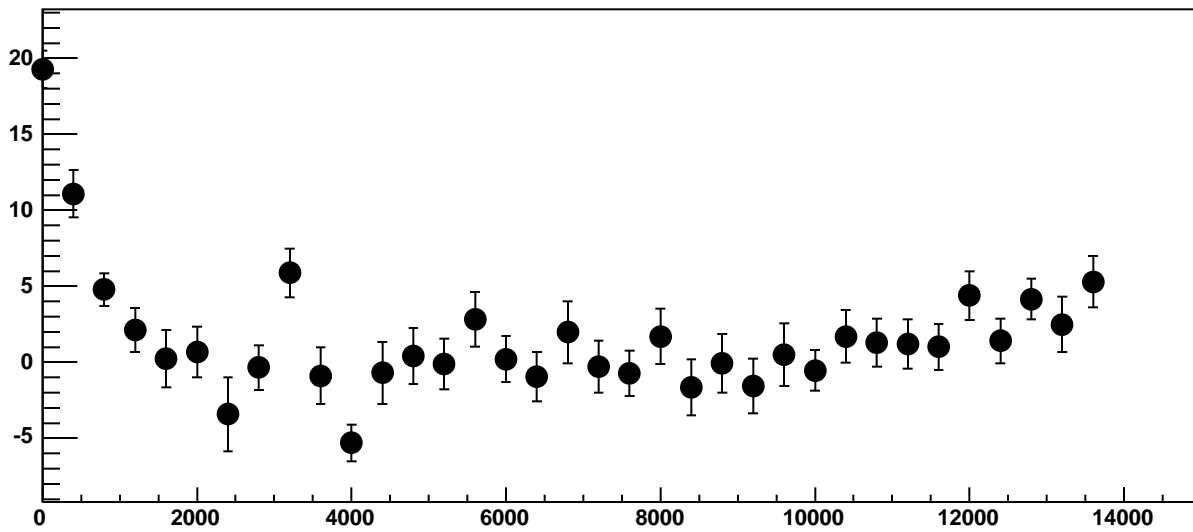


χ^2 / ndf 46.37 / 23
p0 -242.2 ± 0.7643
p1 0.01575 ± 0.0001152

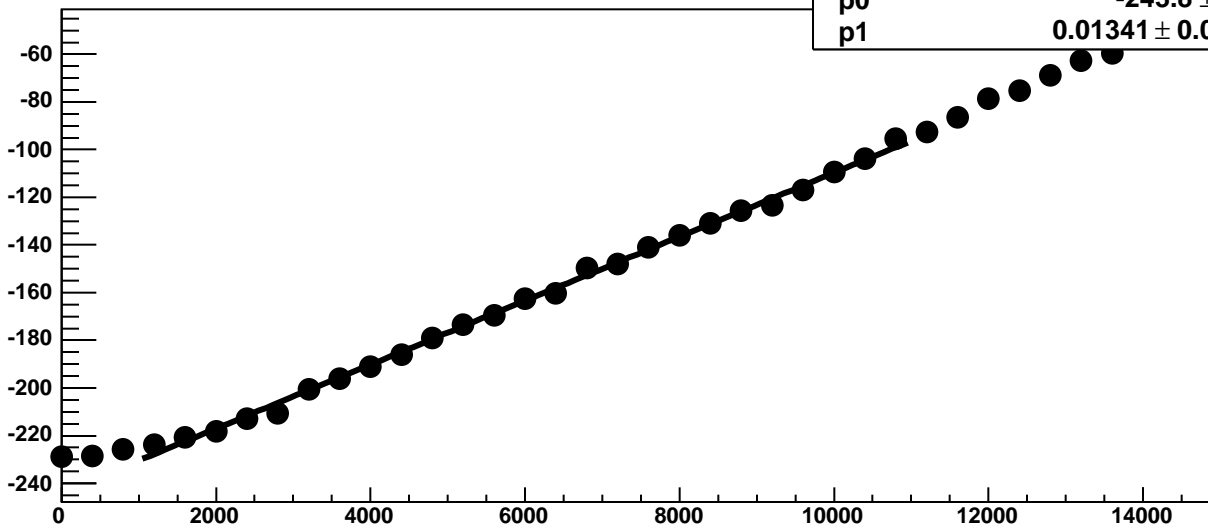
Chip 7, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

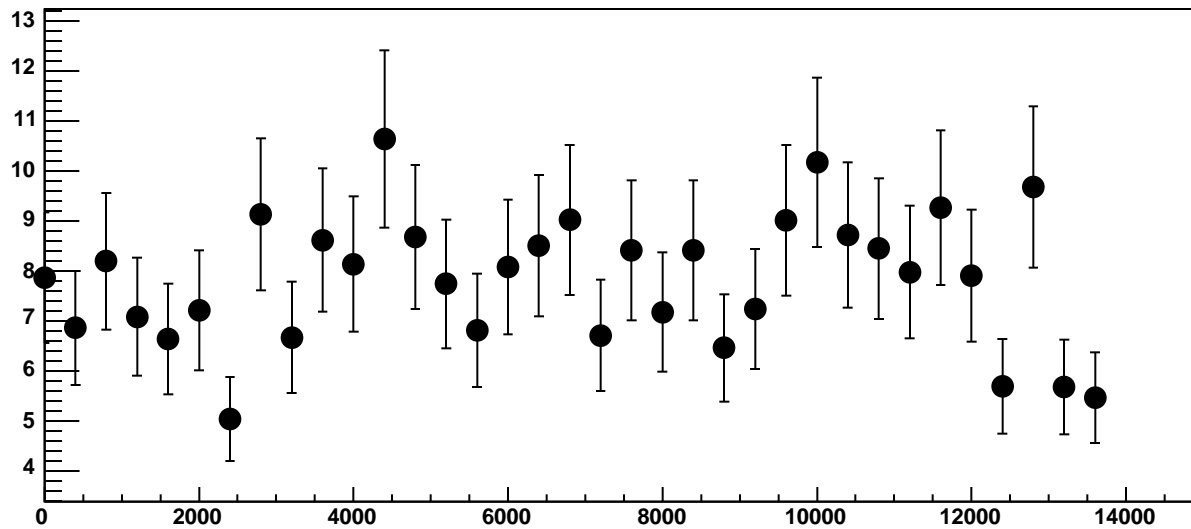


Chip 7, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

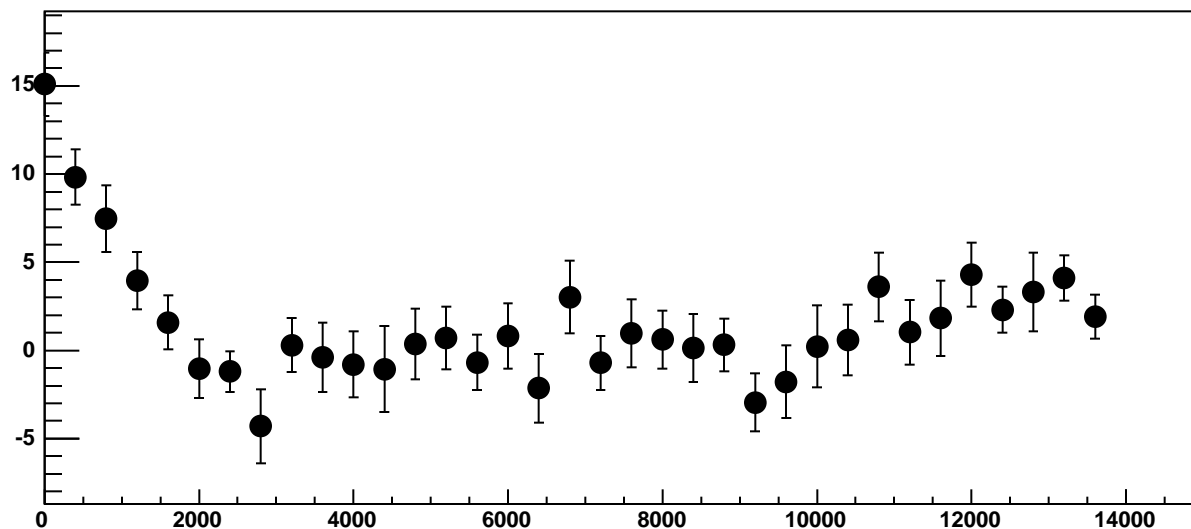


χ^2 / ndf 25.1 / 23
p0 -243.8 ± 0.7608
p1 0.01341 ± 0.0001198

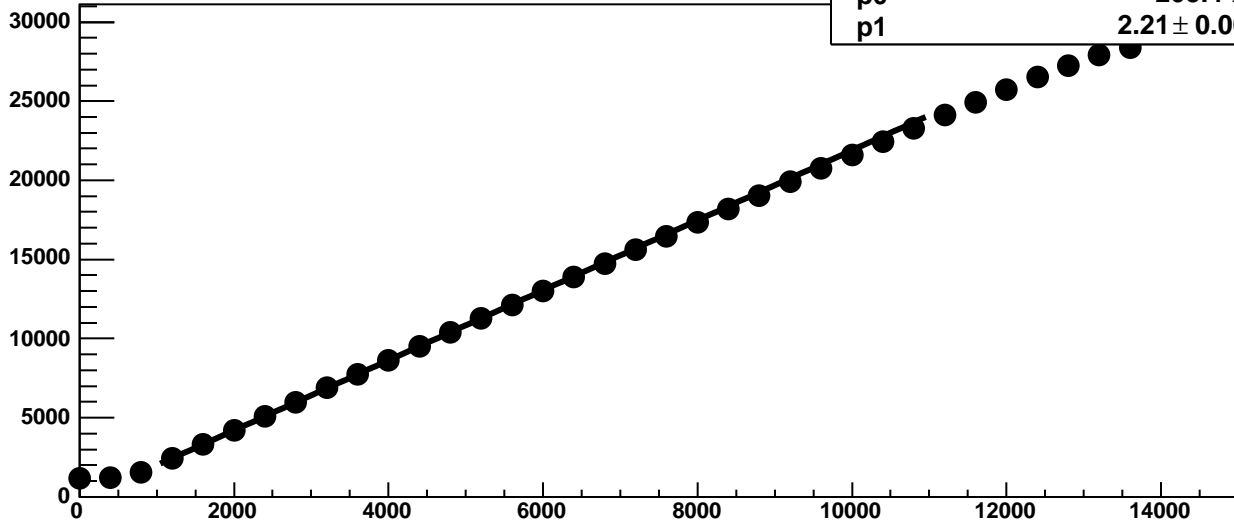
Chip 7, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



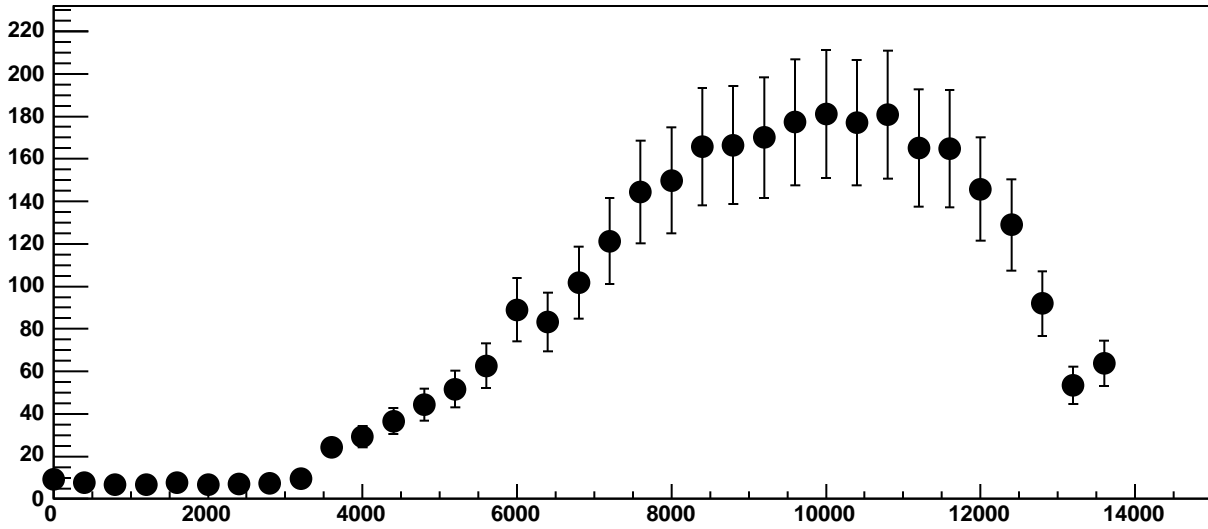
Chip 7, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



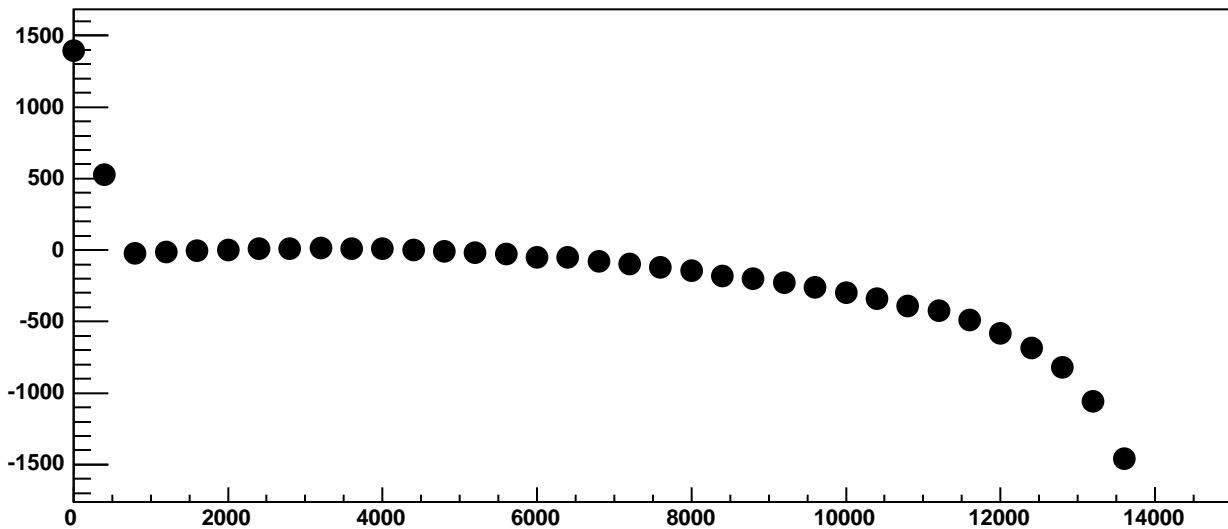
Chip 7, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC



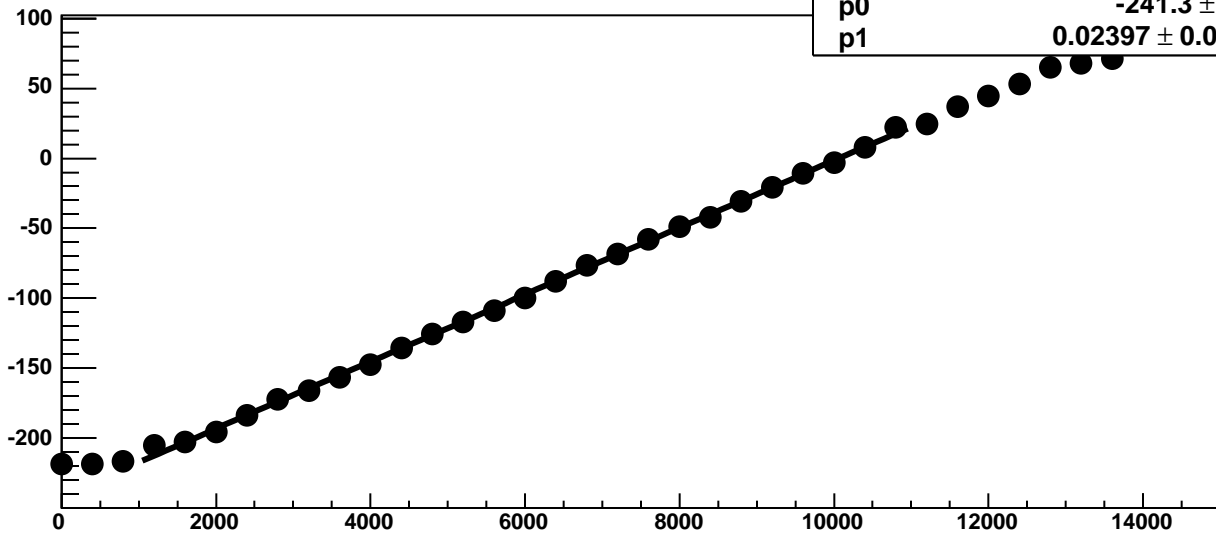
Chip 7, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC

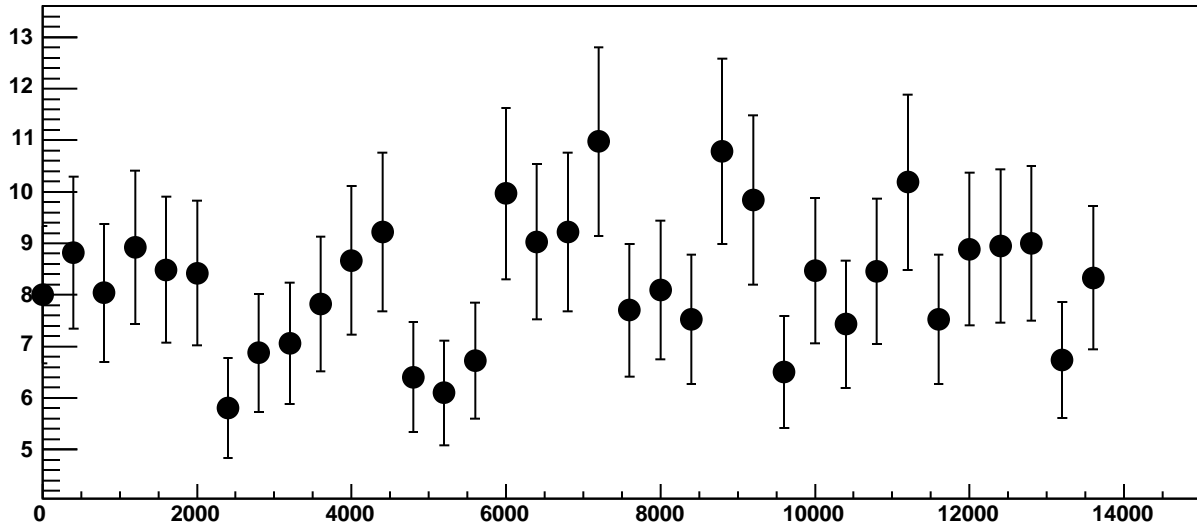


Chip 7, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

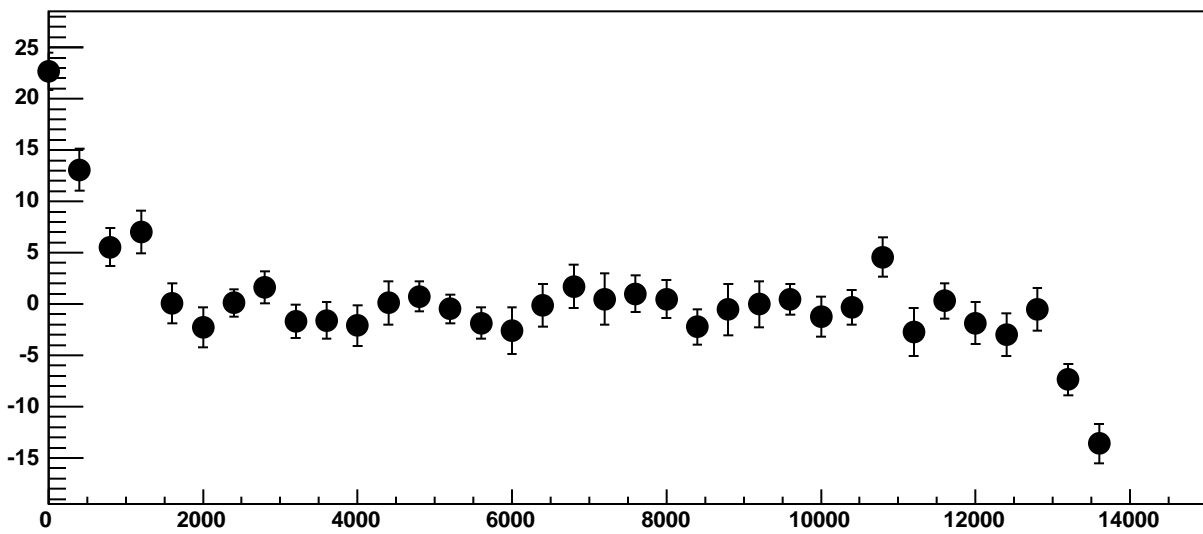


χ^2 / ndf 29.18 / 23
p0 -241.3 ± 0.8093
p1 0.02397 ± 0.0001251

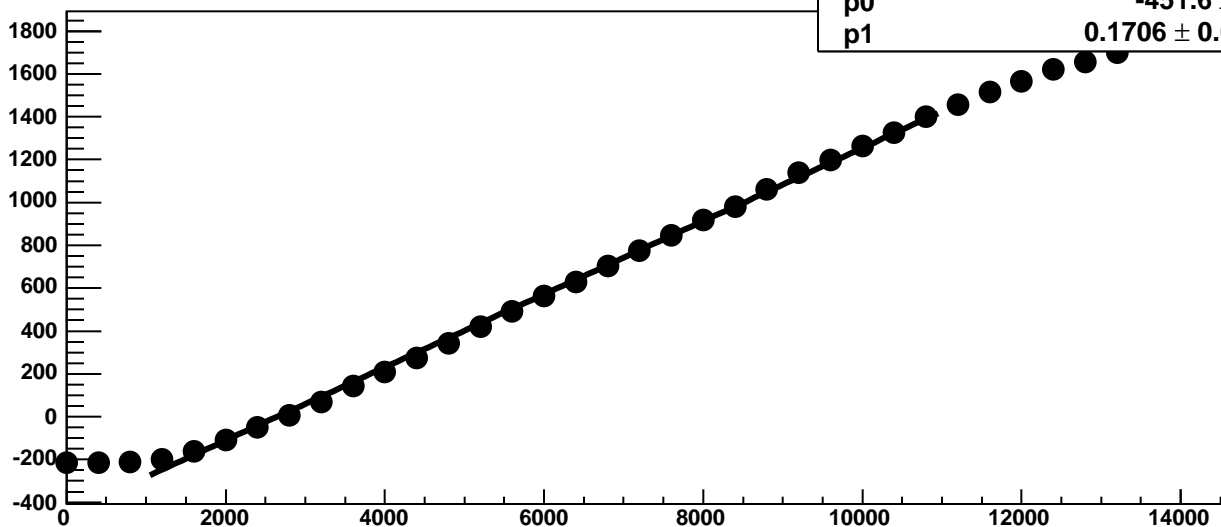
Chip 7, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



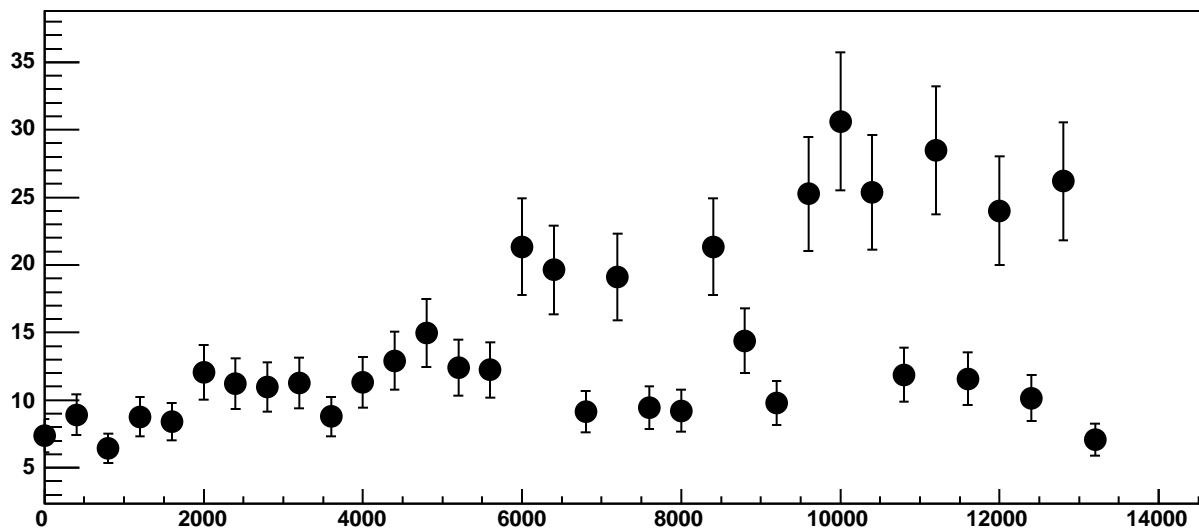
Chip 7, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



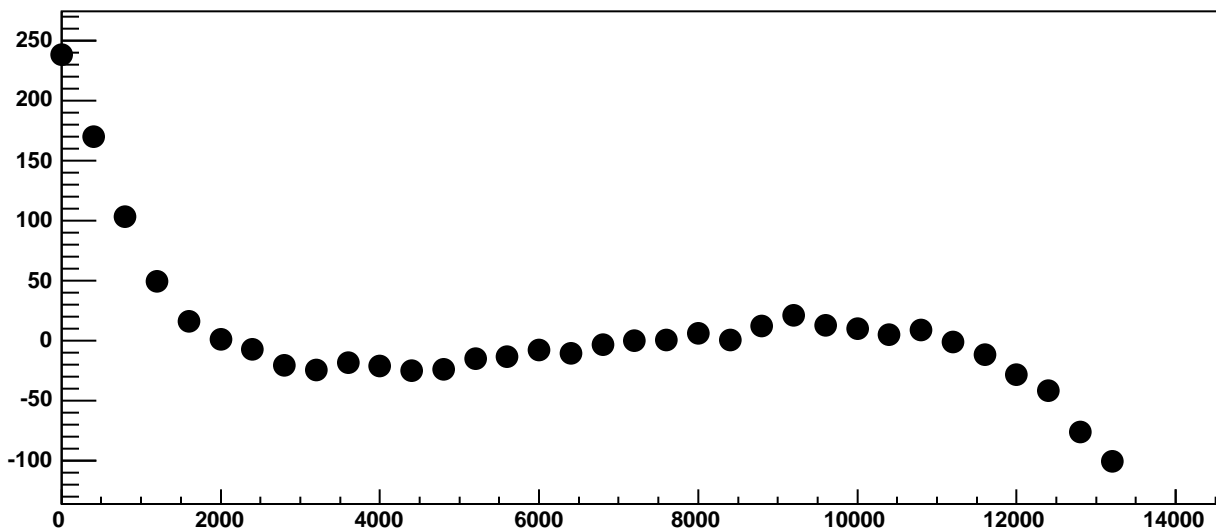
Chip 7, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



Chip 7, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



Chip 7, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

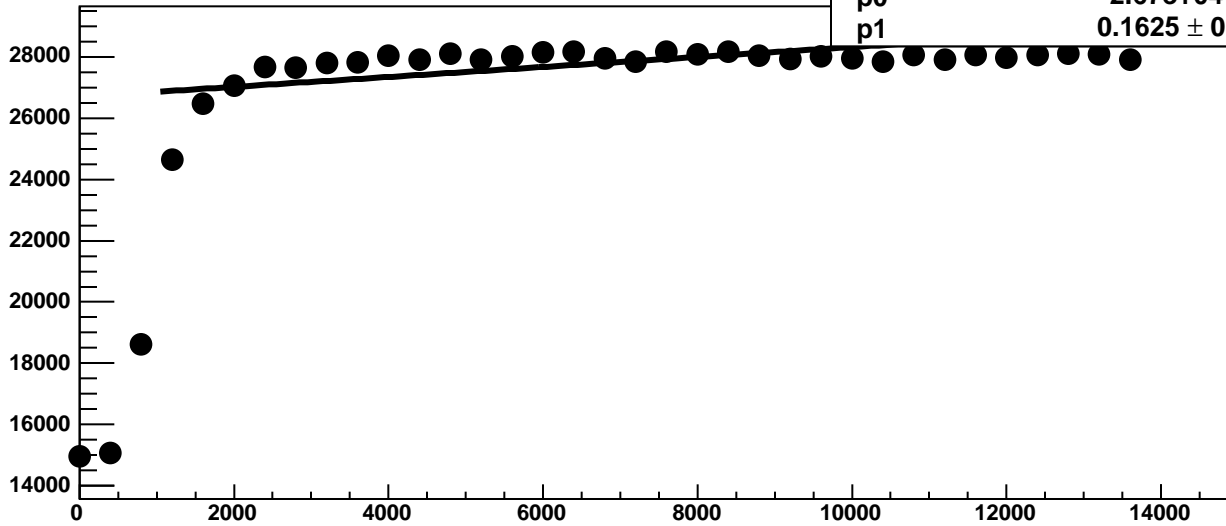
908.5 / 23

p0

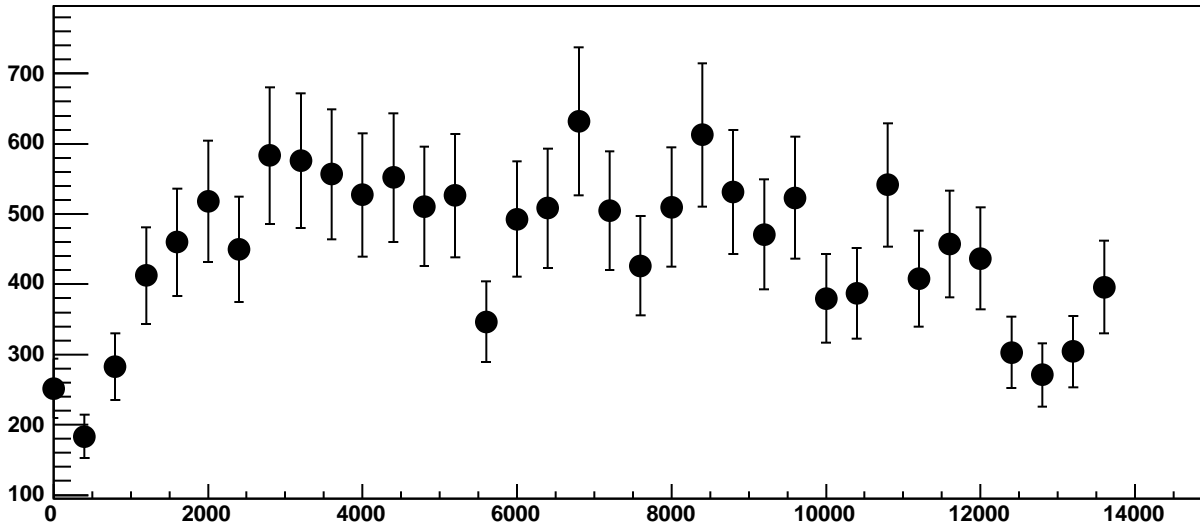
$2.67\text{e}+04 \pm 50.66$

p1

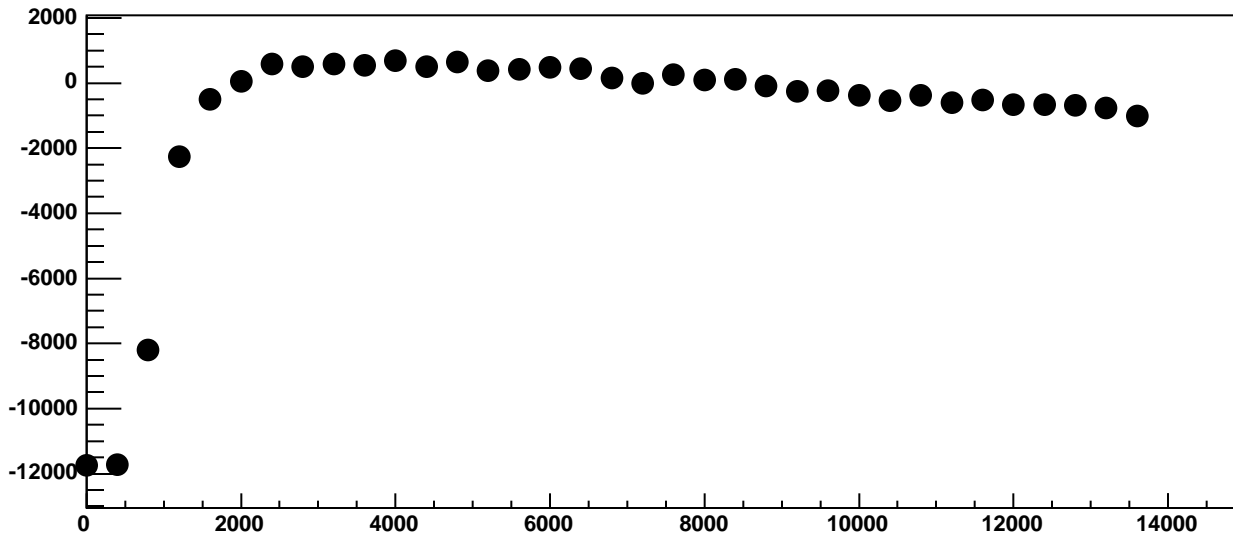
0.1625 ± 0.007443



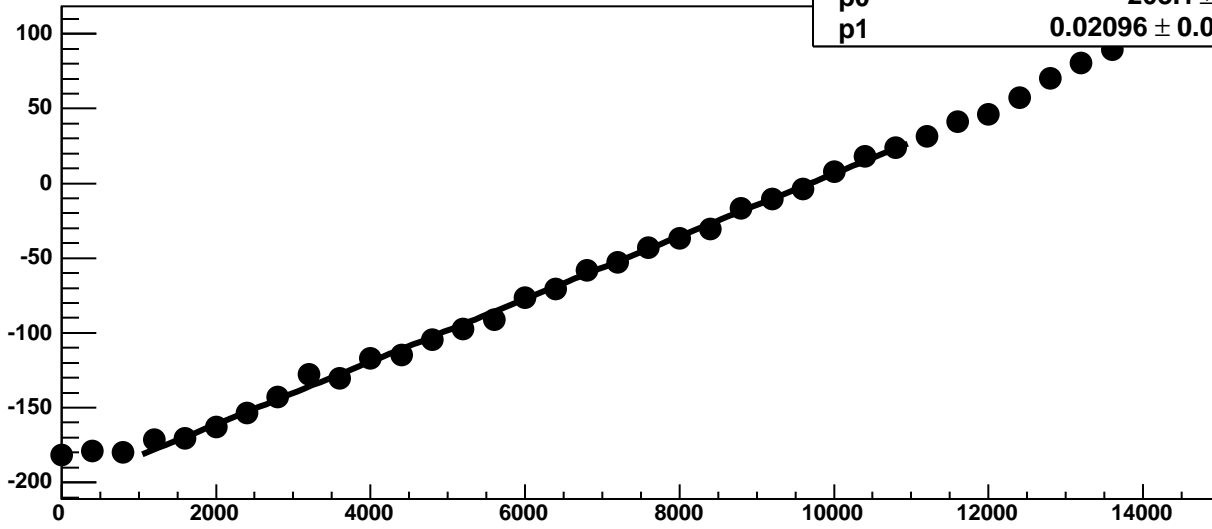
Chip 7, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC

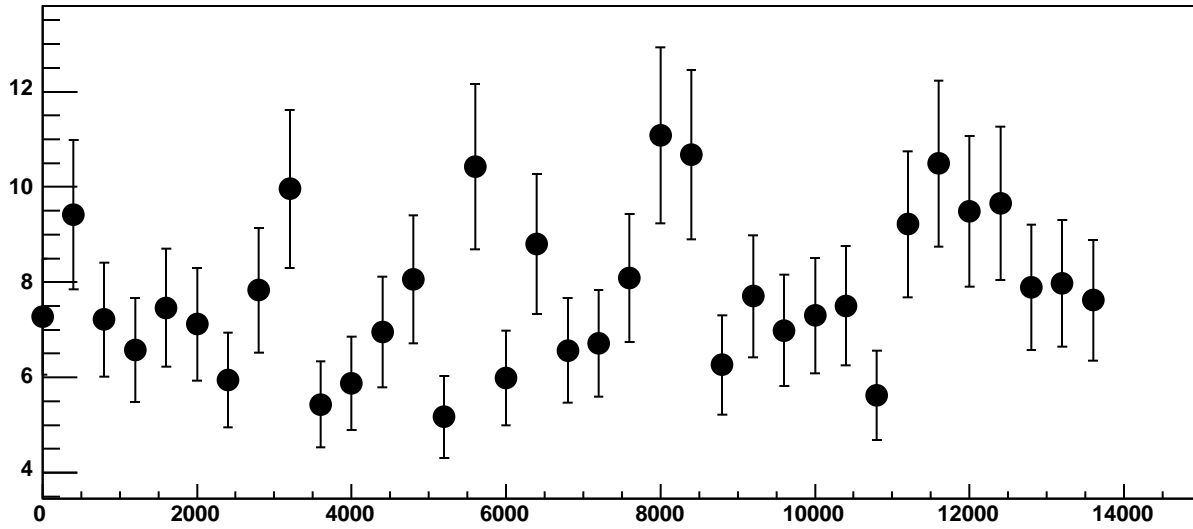


Chip 7, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

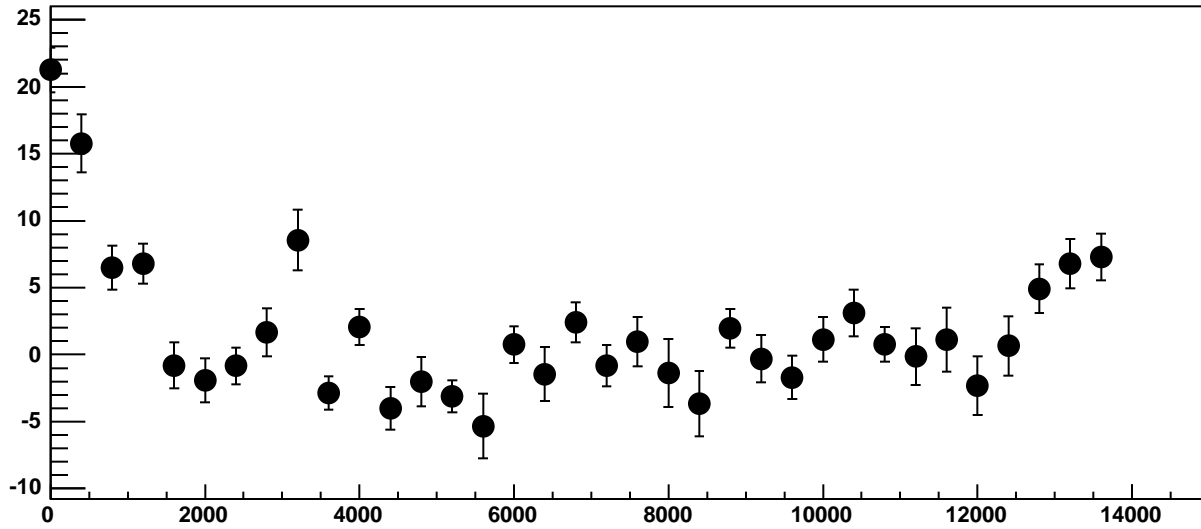


χ^2 / ndf 77.75 / 23
p0 -203.1 ± 0.7194
p1 0.02096 ± 0.0001094

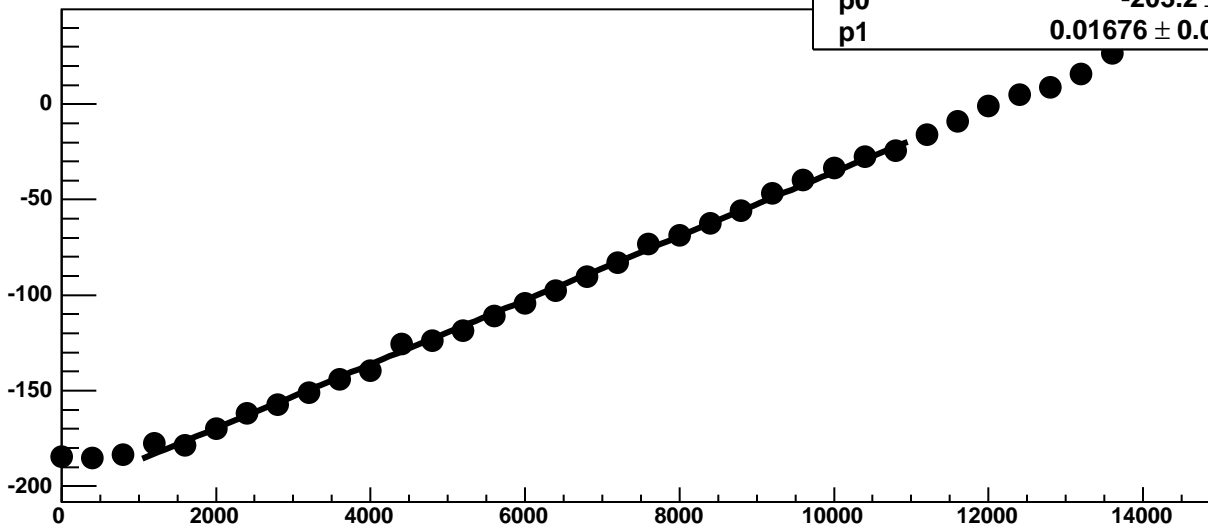
Chip 7, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

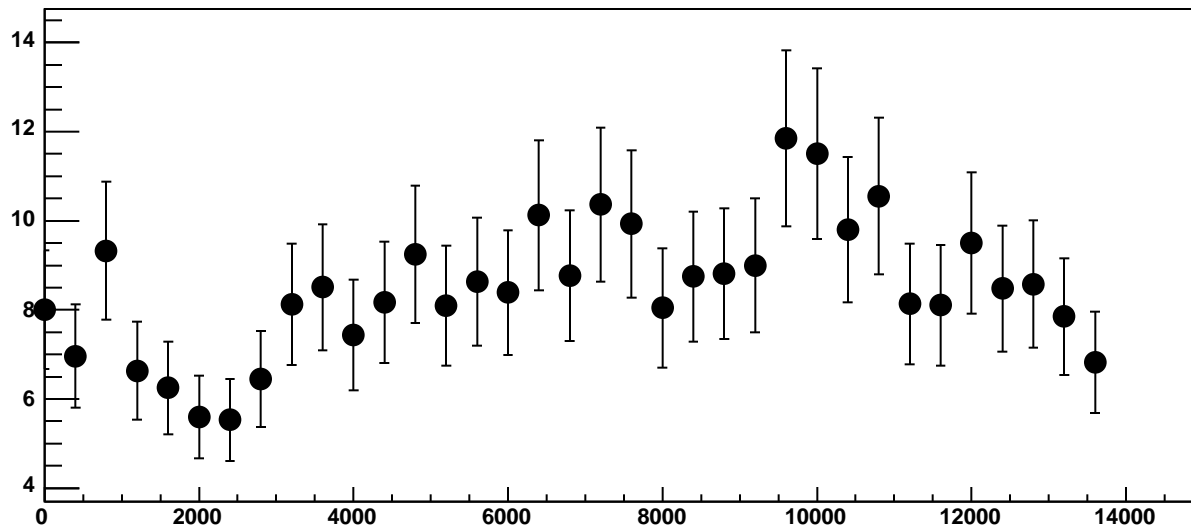


Chip 7, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

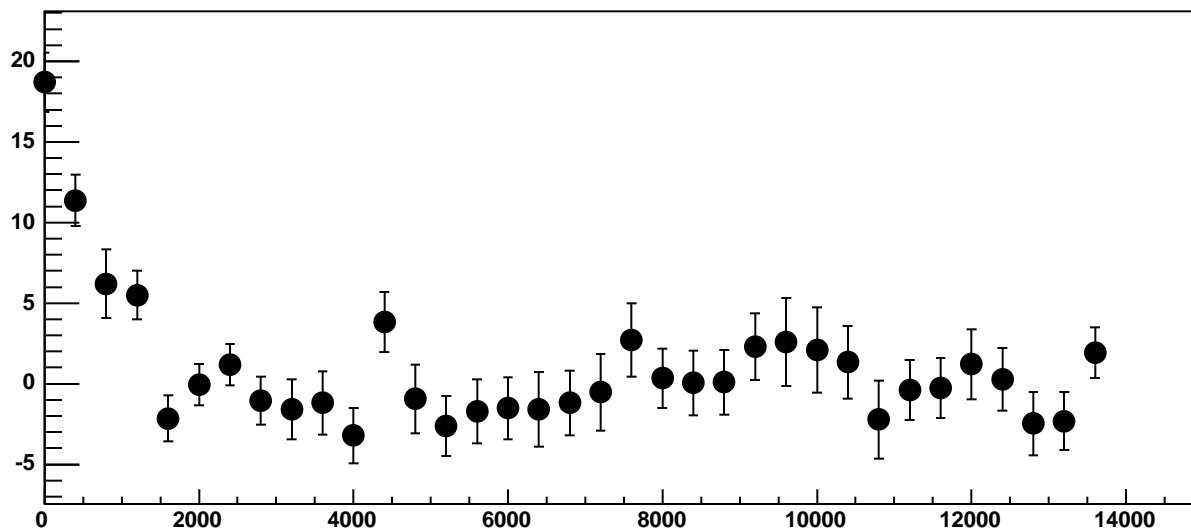


χ^2 / ndf 35.31 / 23
p0 -203.2 ± 0.7411
p1 0.01676 ± 0.0001286

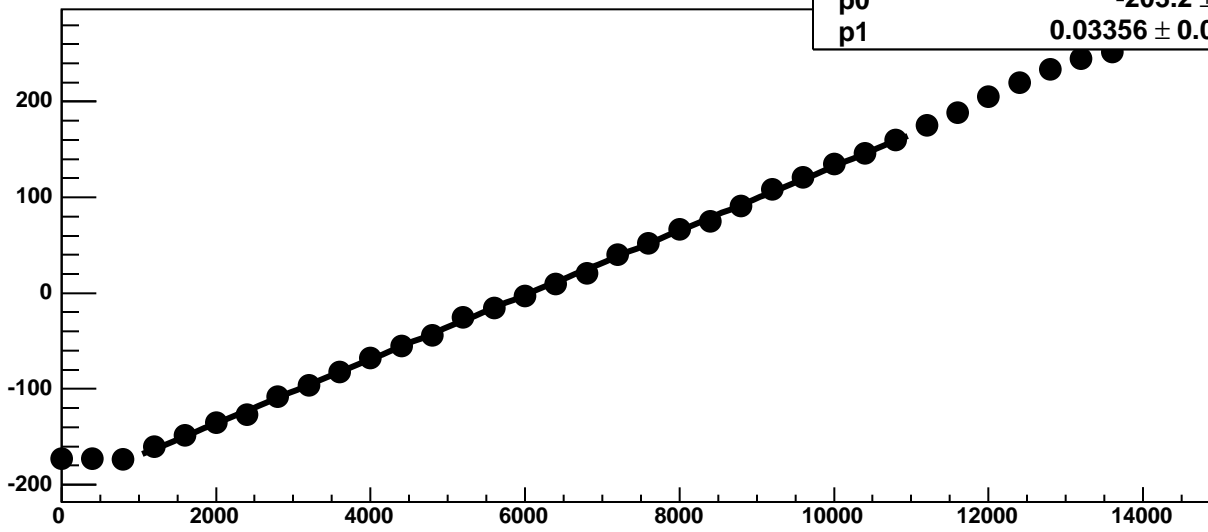
Chip 7, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



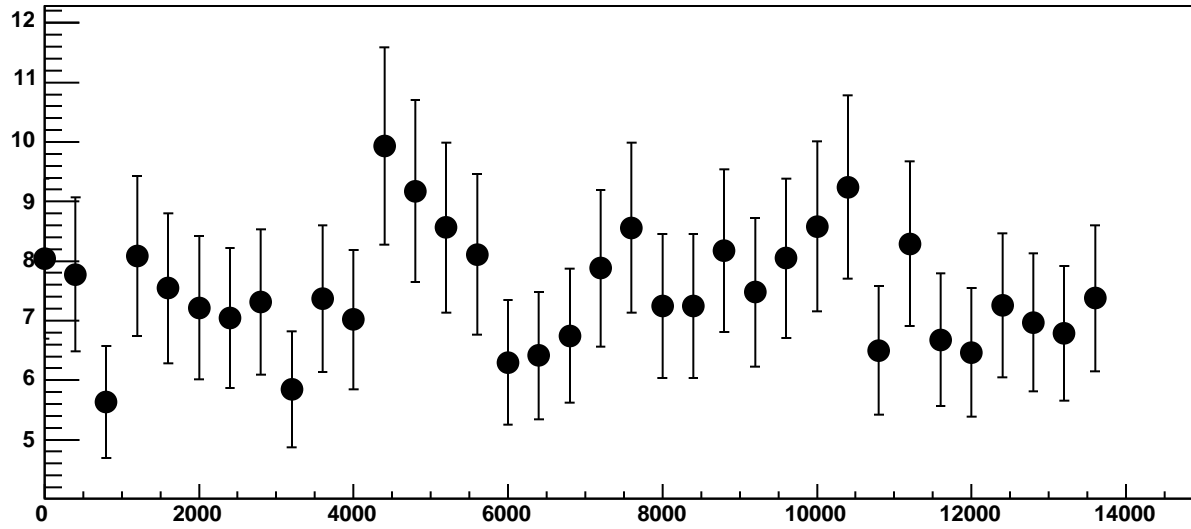
Chip 7, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



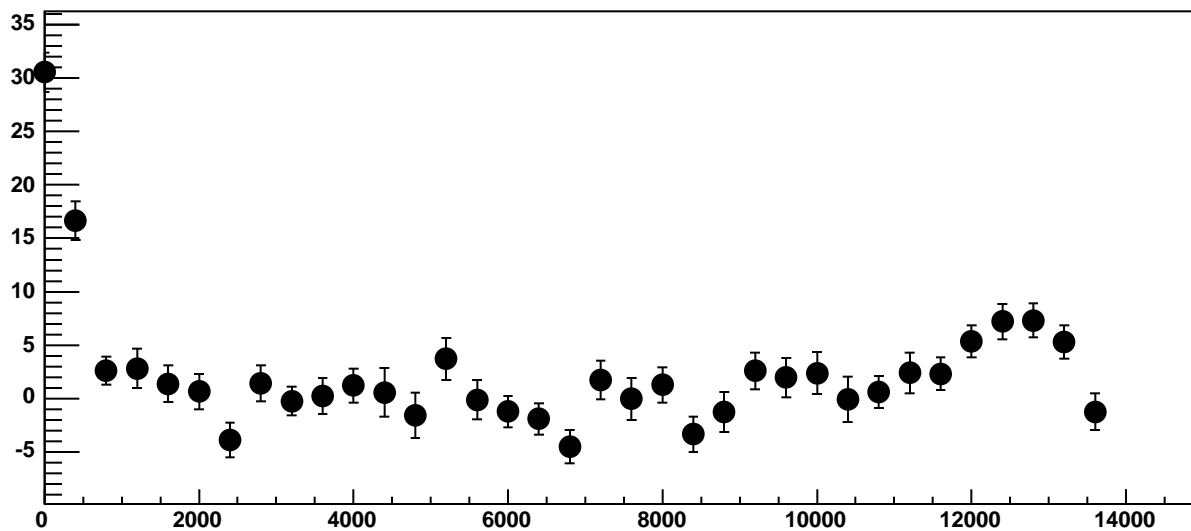
Chip 7, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC



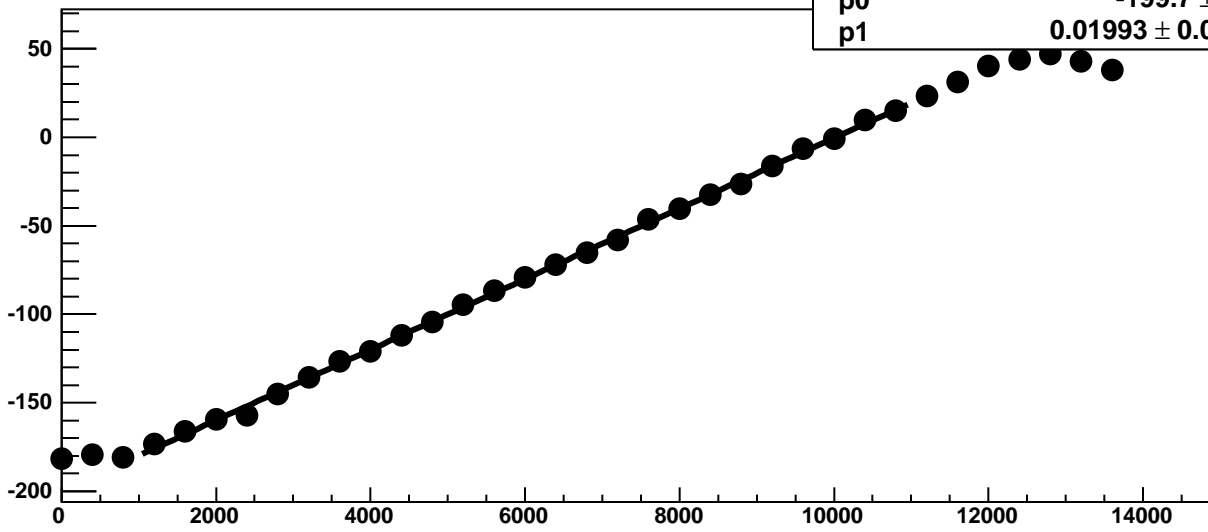
Chip 7, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

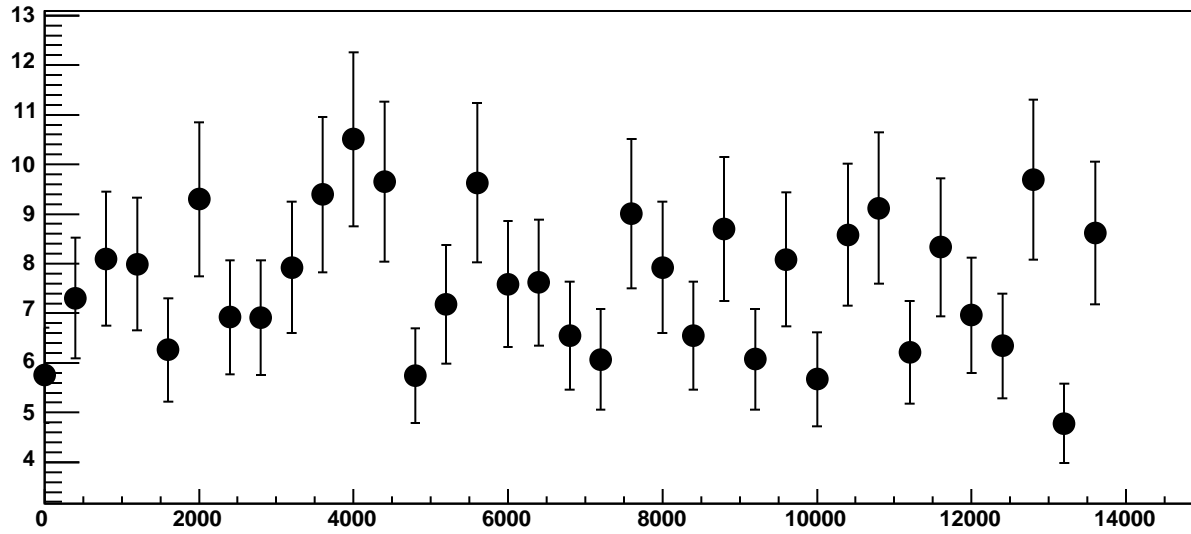


Chip 7, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

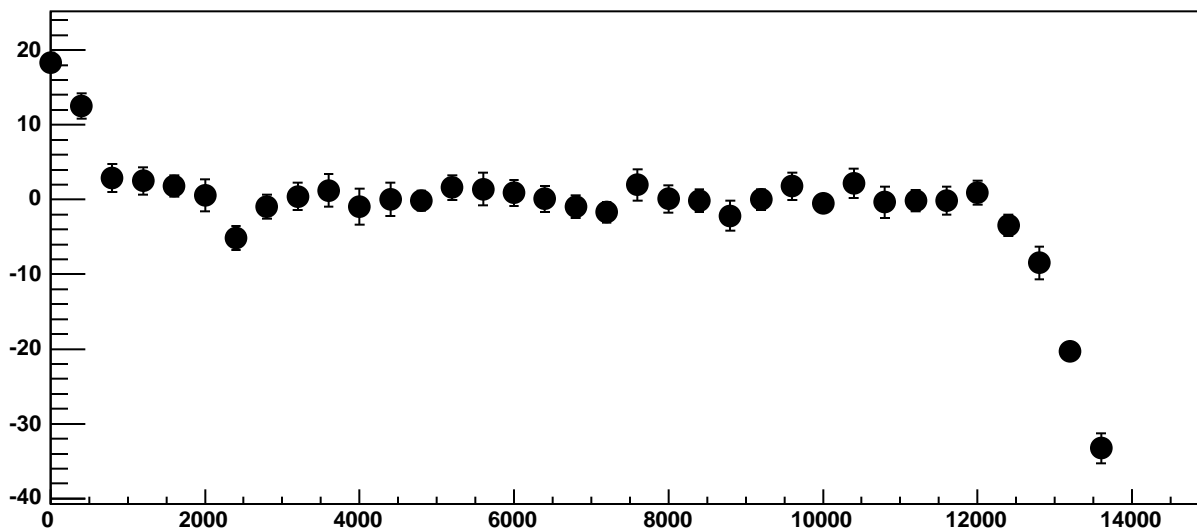


χ^2 / ndf 22.74 / 23
p0 -199.7 ± 0.8013
p1 0.01993 ± 0.0001187

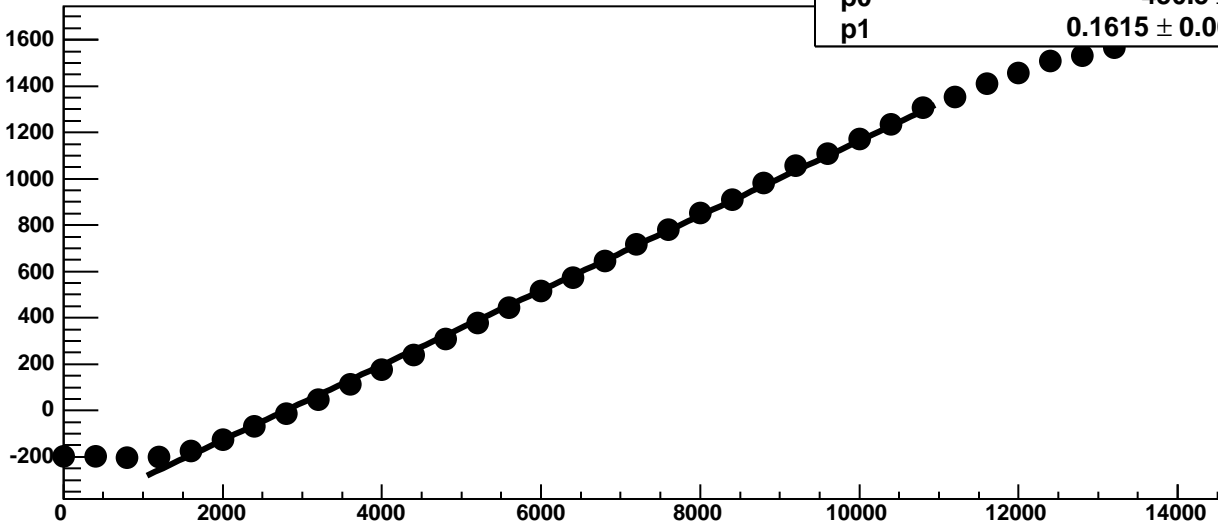
Chip 7, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



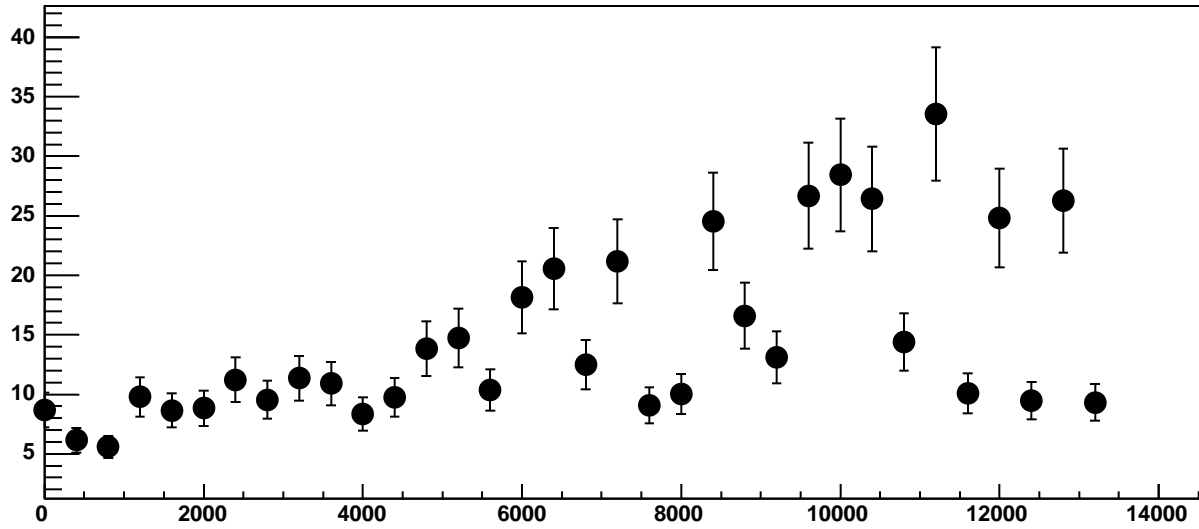
Chip 7, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



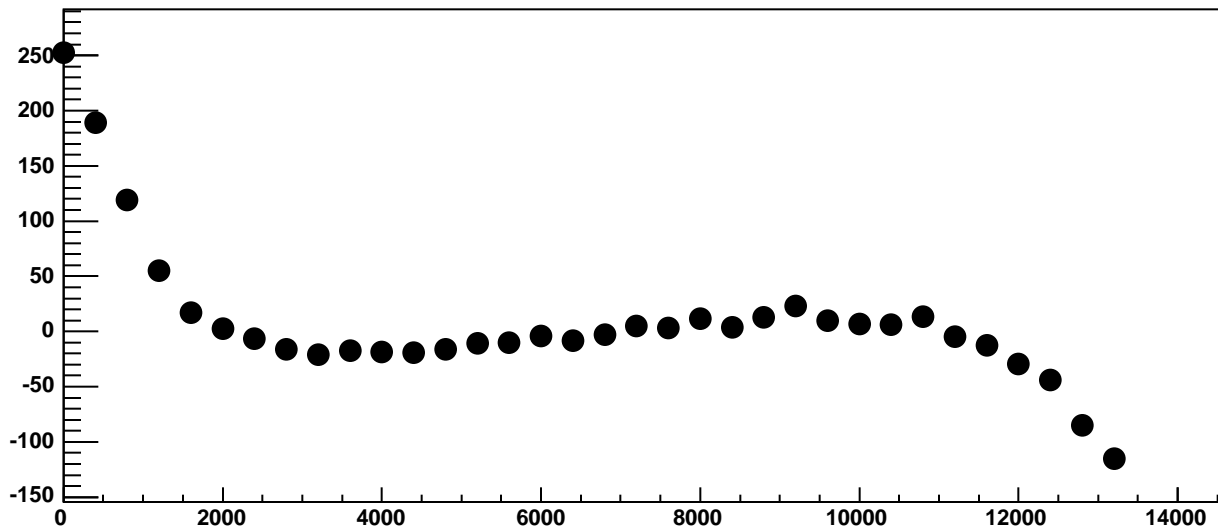
Chip 7, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



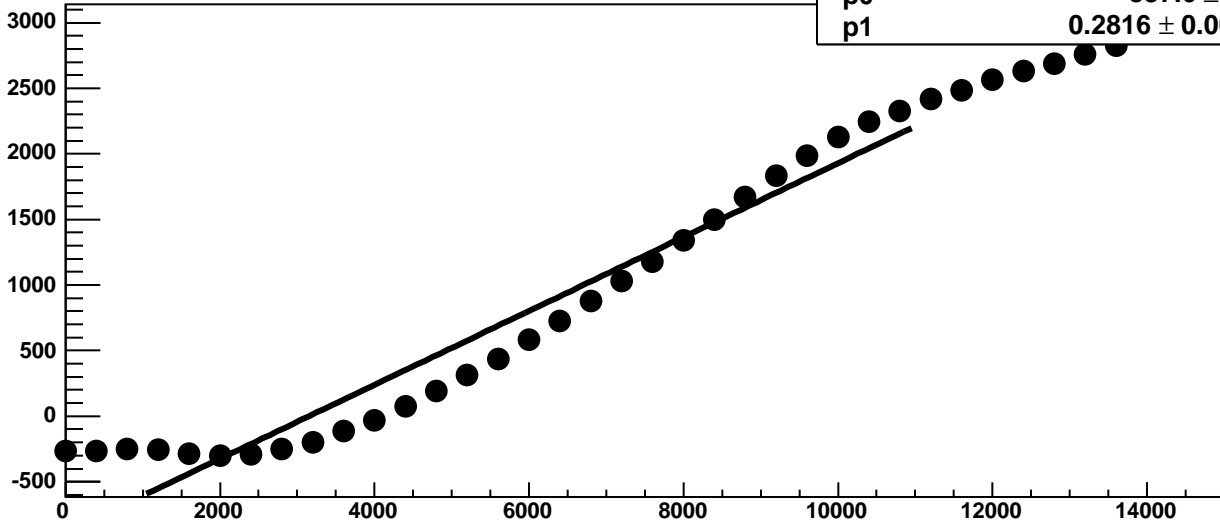
Chip 7, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



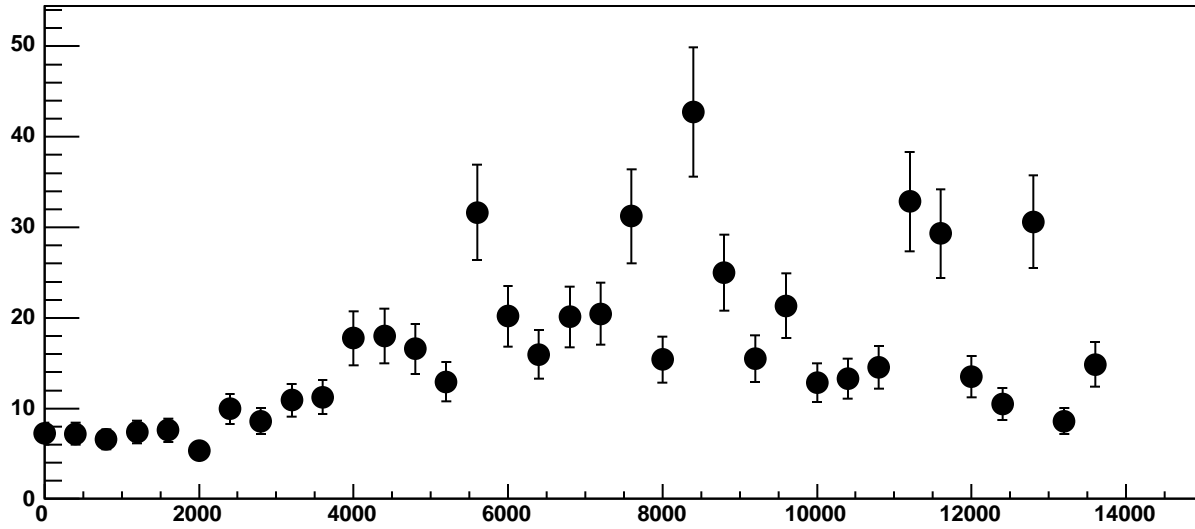
Chip 7, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



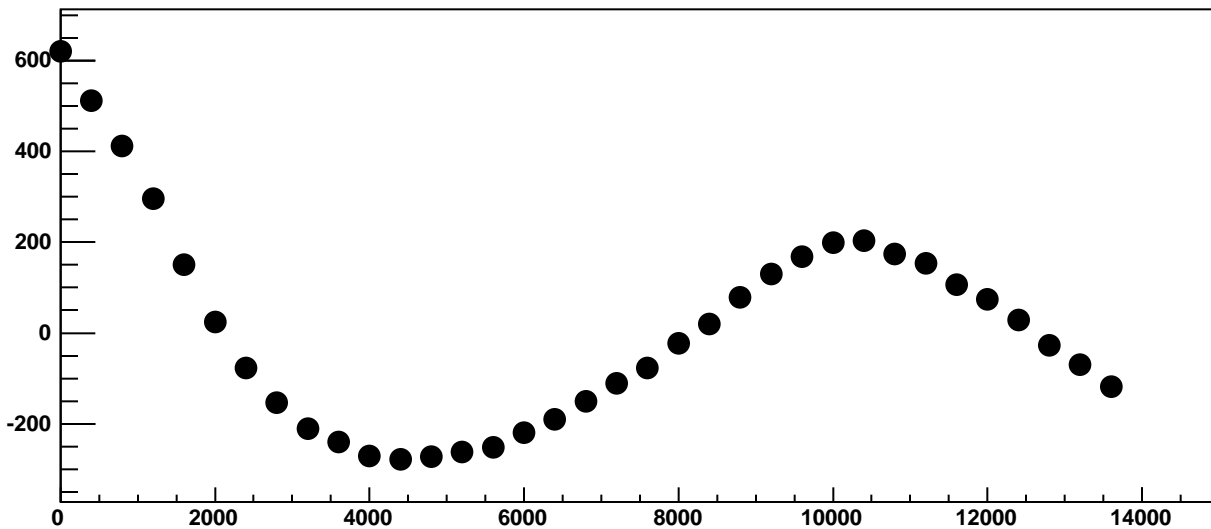
Chip 7, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC



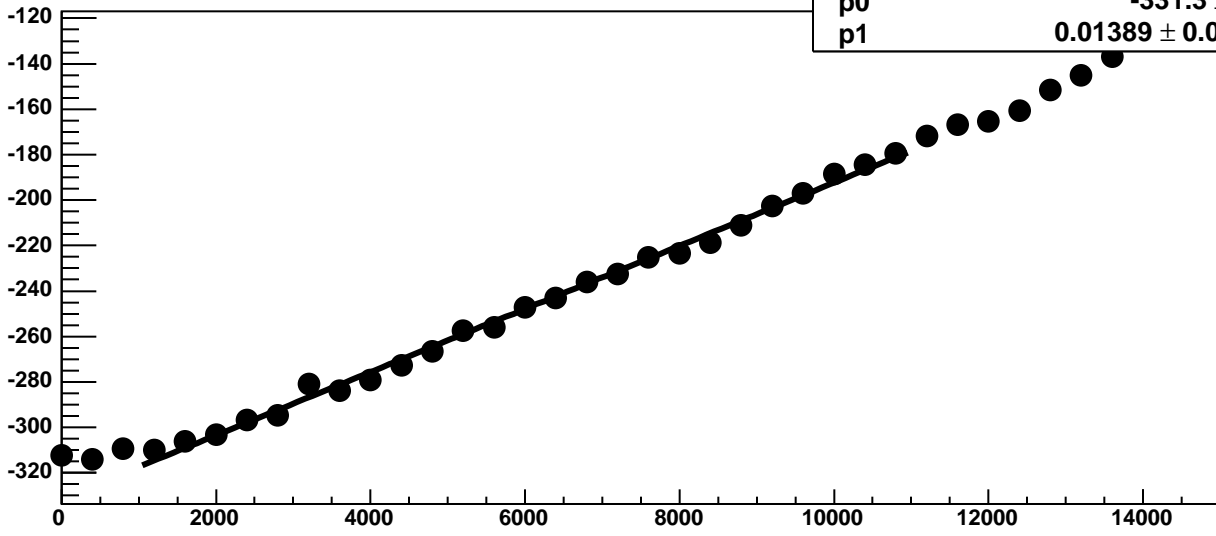
Chip 7, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



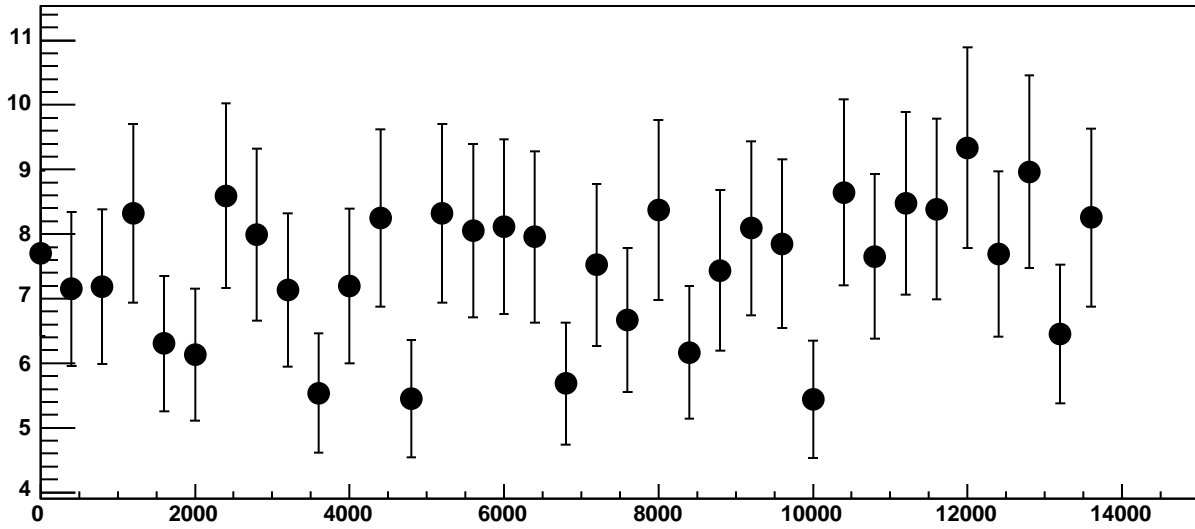
Chip 7, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC



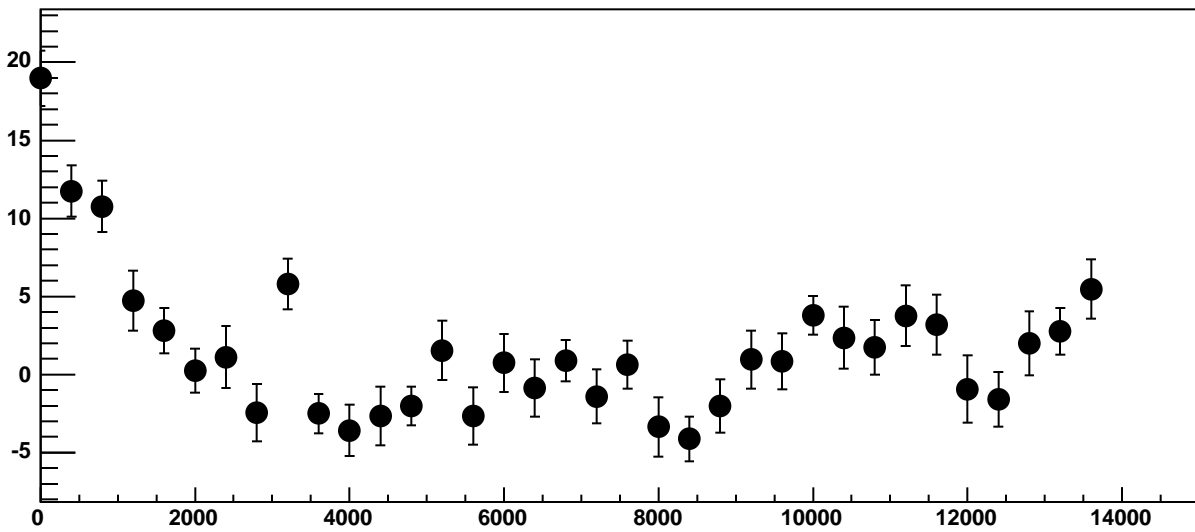
Chip 7, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



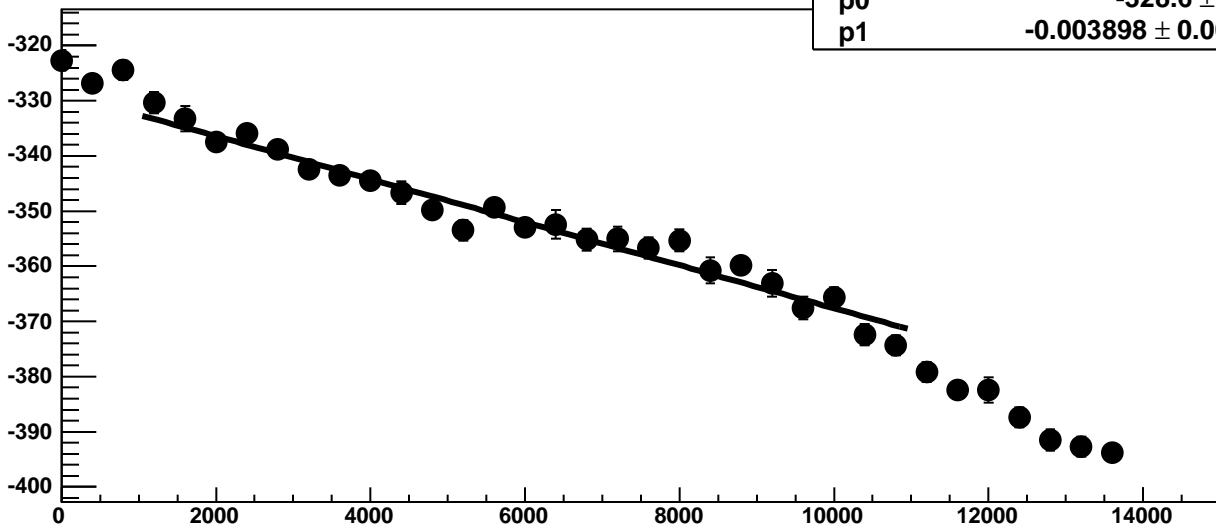
Chip 7, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

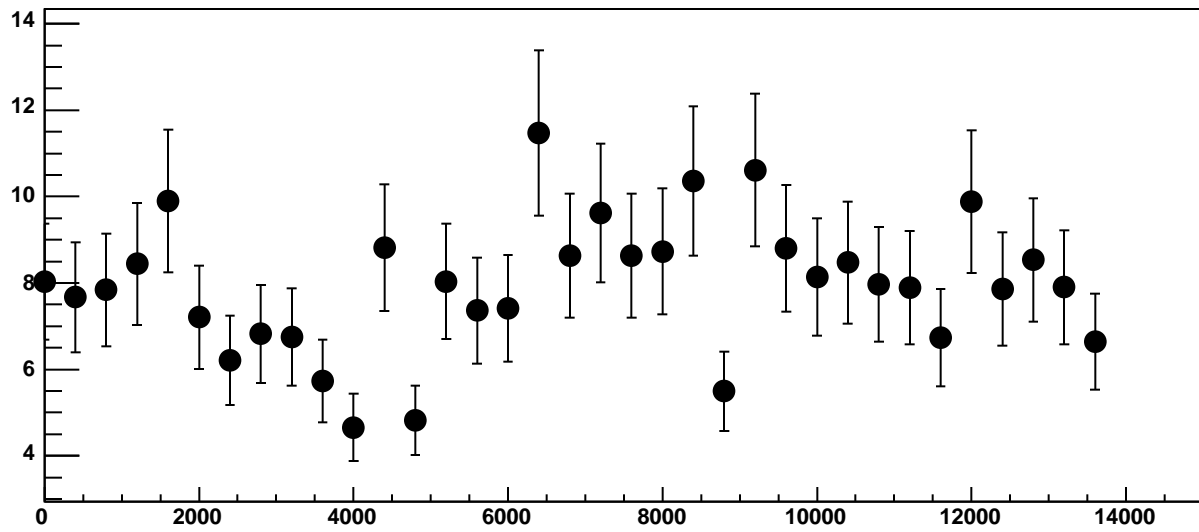


Chip 7, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

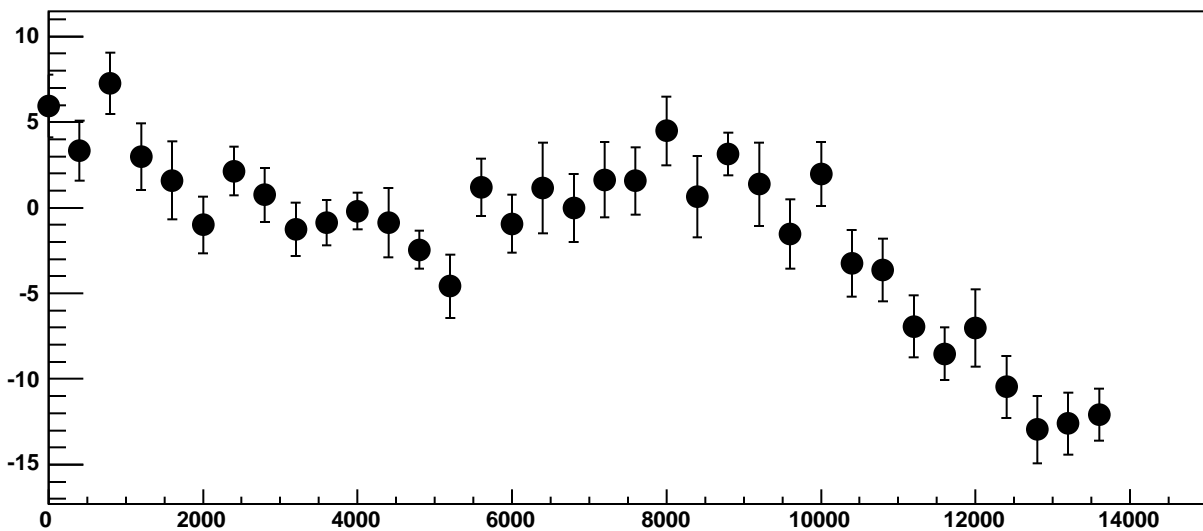


χ^2 / ndf 40.39 / 23
p0 -328.6 ± 0.7605
p1 -0.003898 ± 0.0001228

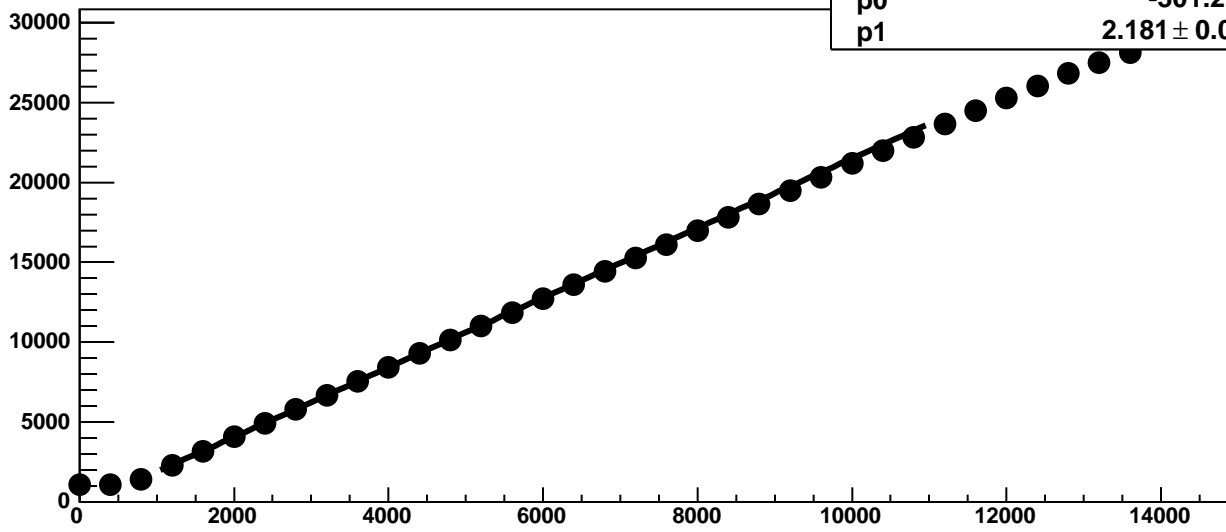
Chip 7, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



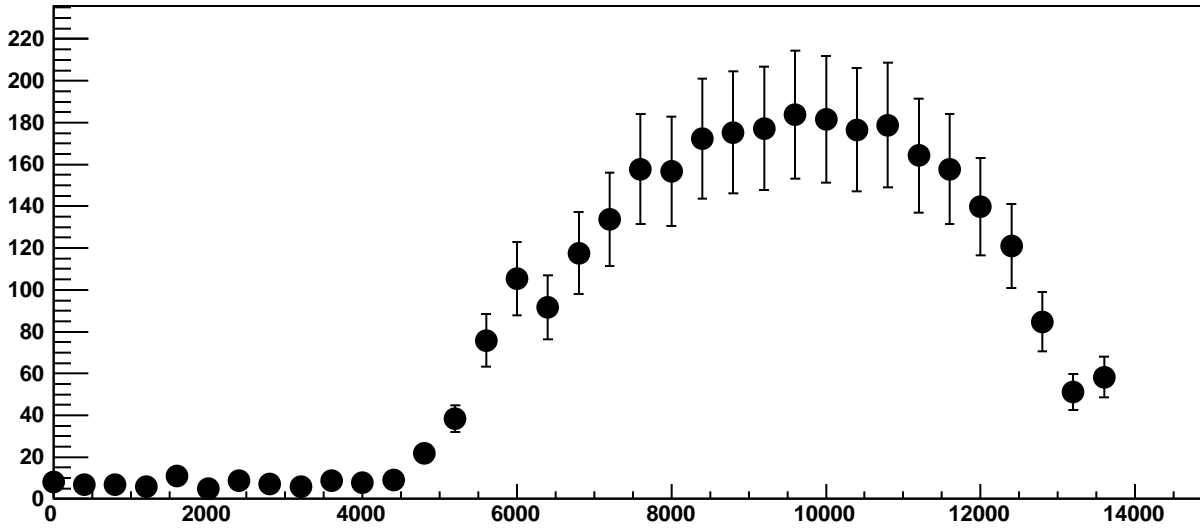
Chip 7, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



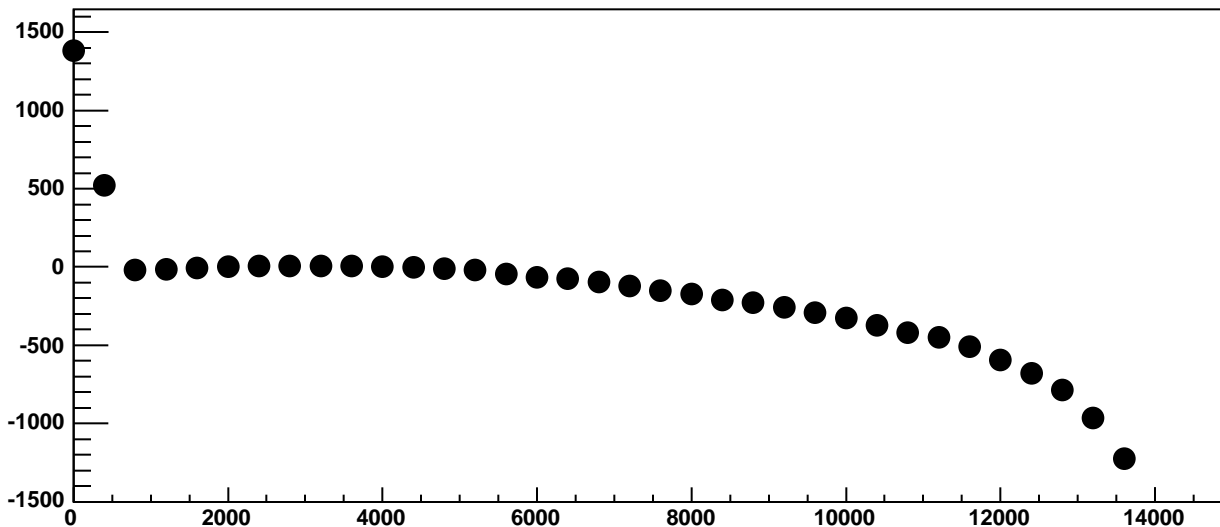
Chip 7, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC



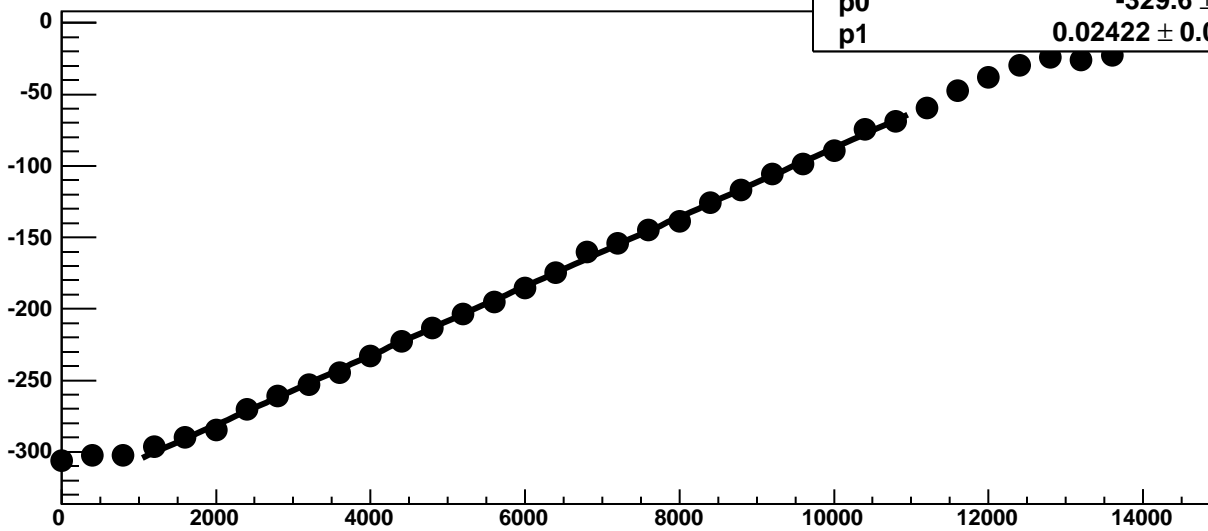
Chip 7, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC

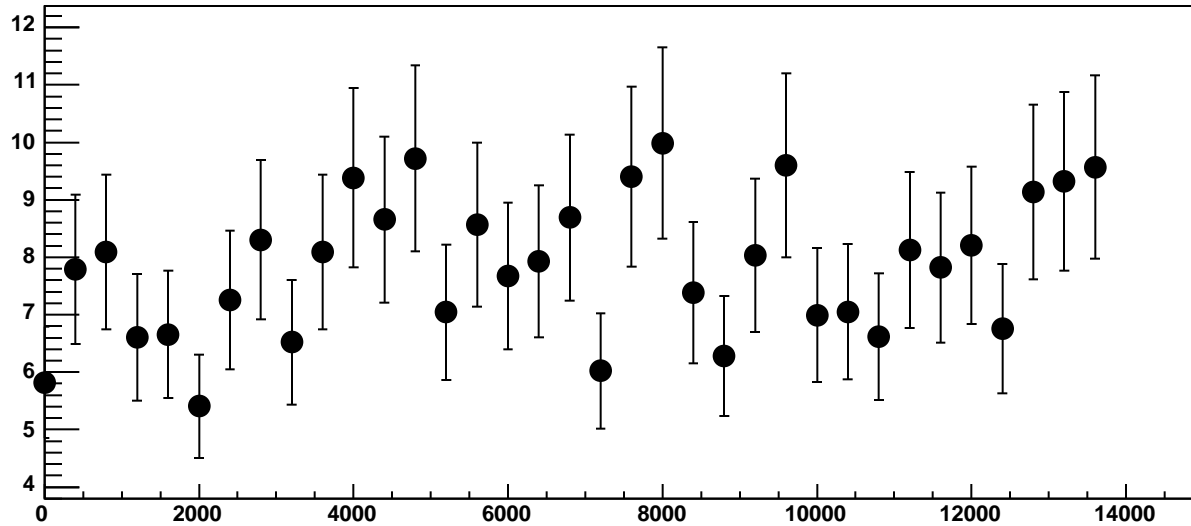


Chip 7, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

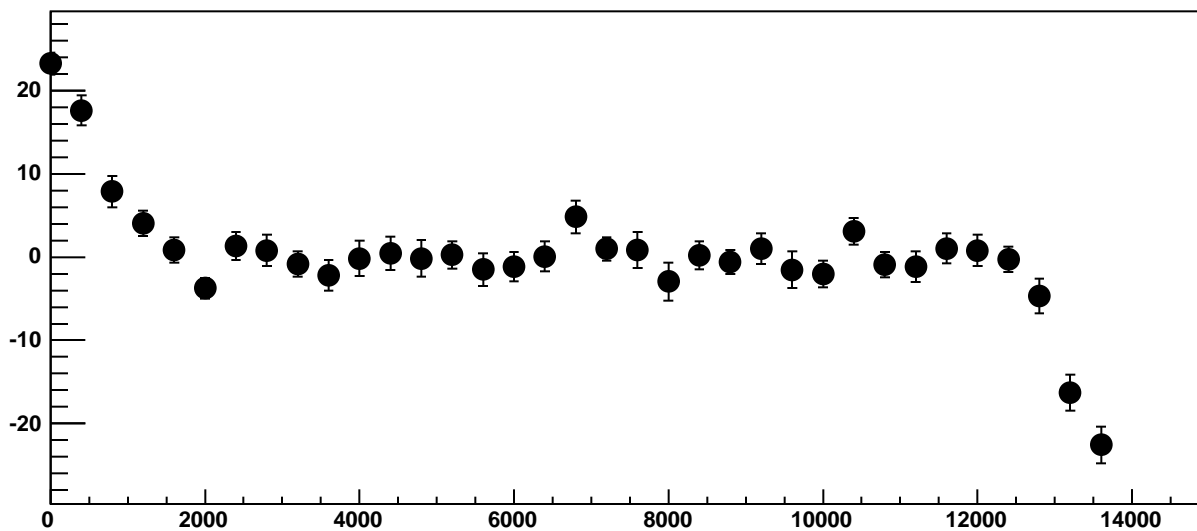


χ^2 / ndf 34.68 / 23
p0 -329.6 ± 0.7352
p1 0.02422 ± 0.0001113

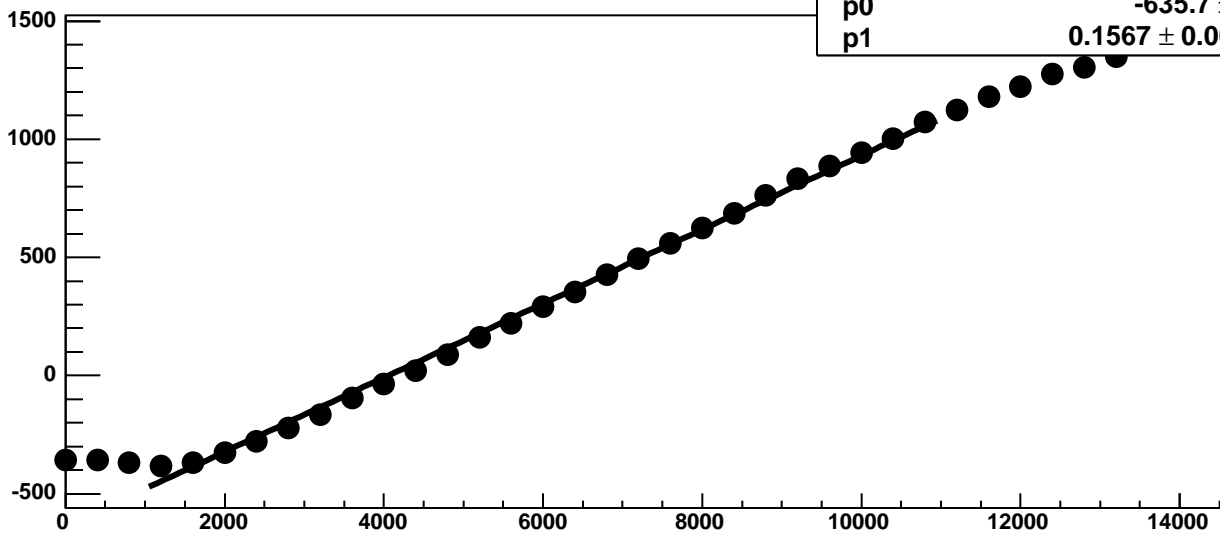
Chip 7, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



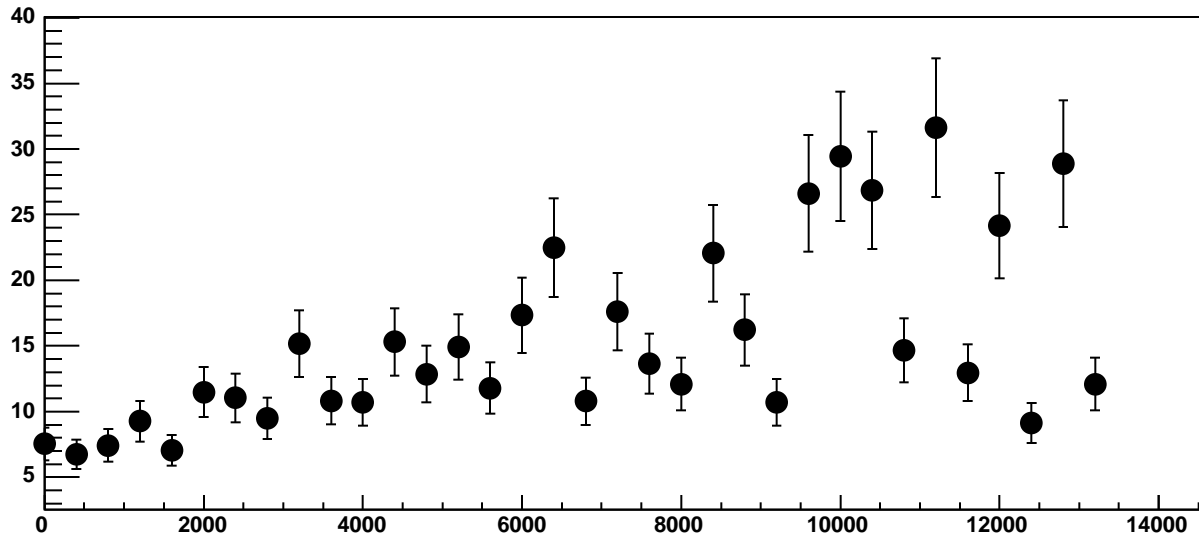
Chip 7, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



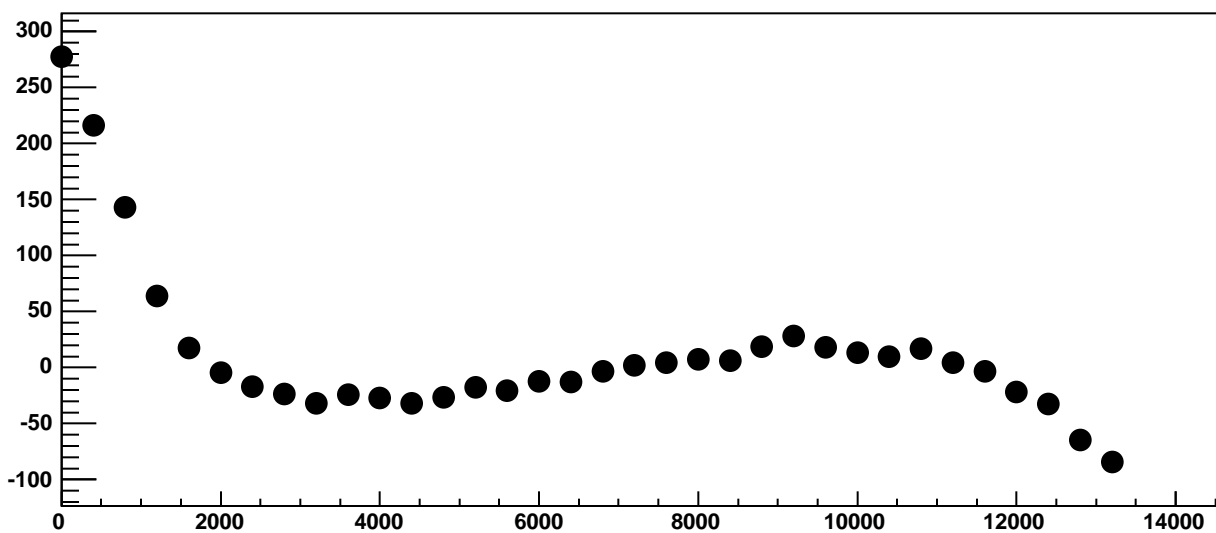
Chip 7, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



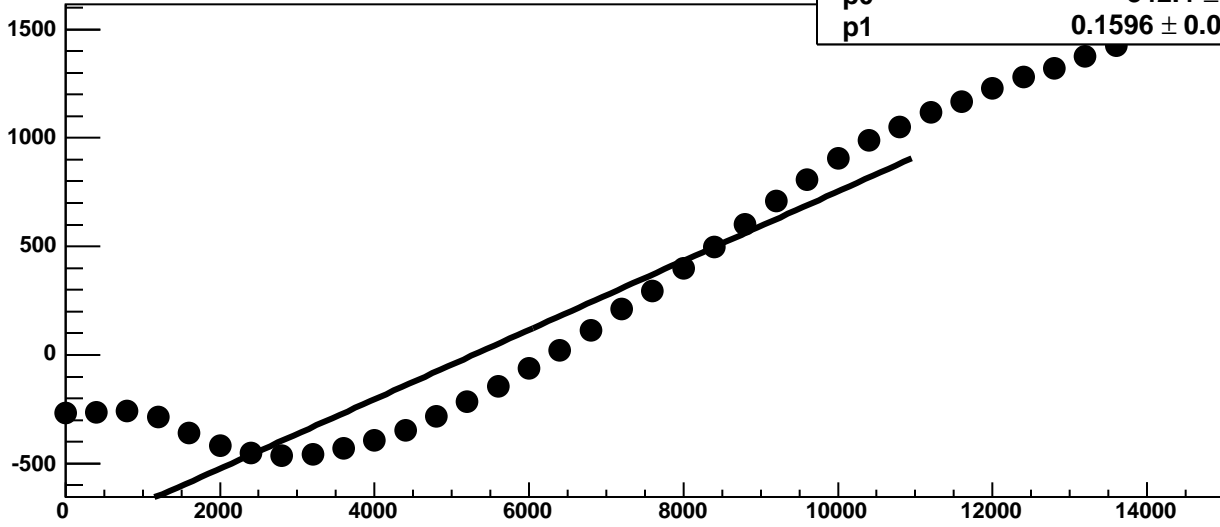
Chip 7, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 7, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

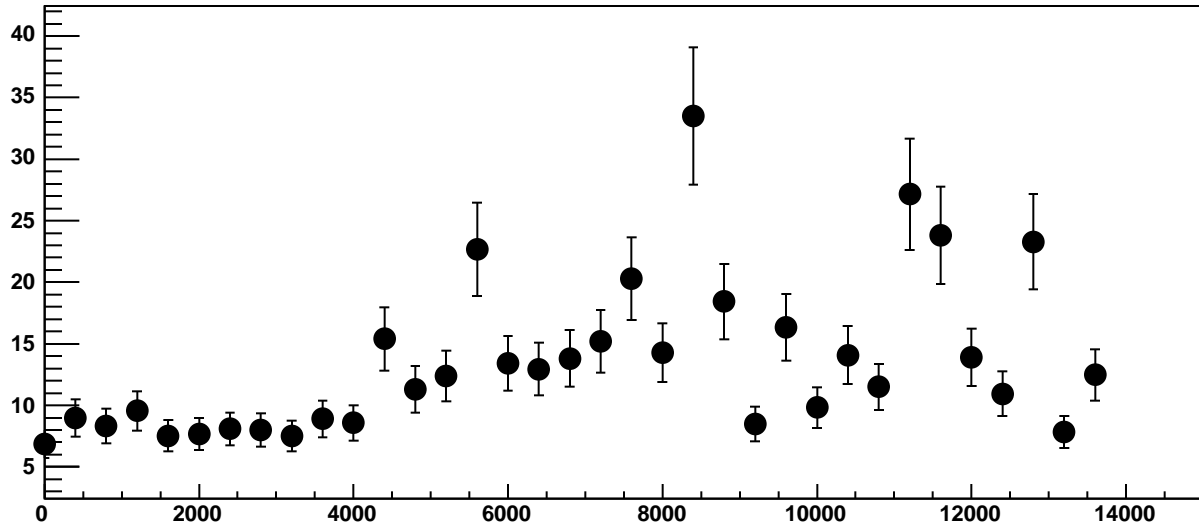


Chip 7, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

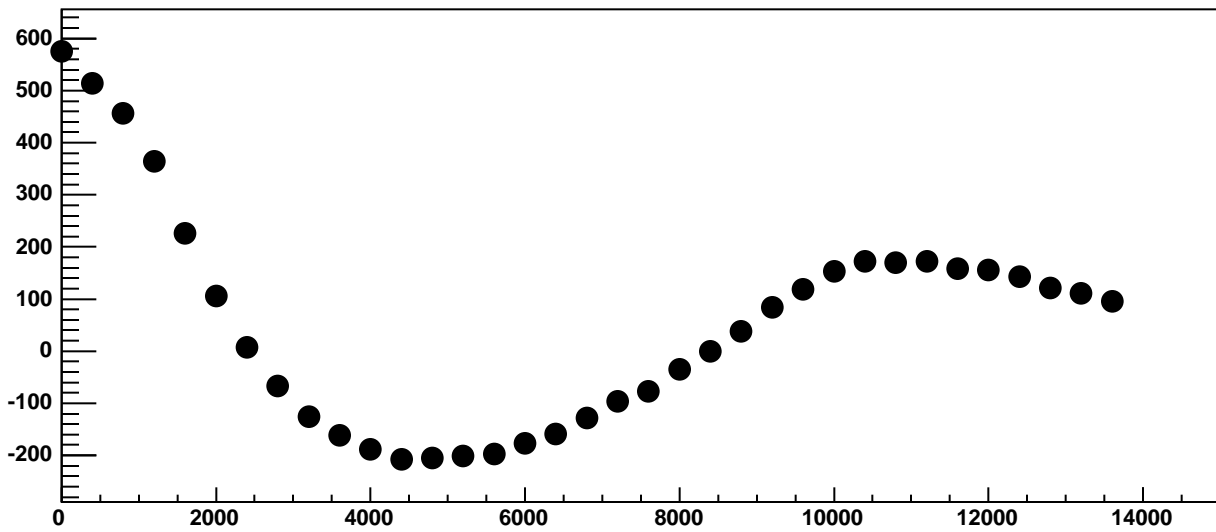


χ^2 / ndf 1.102e+05 / 23
p0 -842.4 ± 0.9435
p1 0.1596 ± 0.0001641

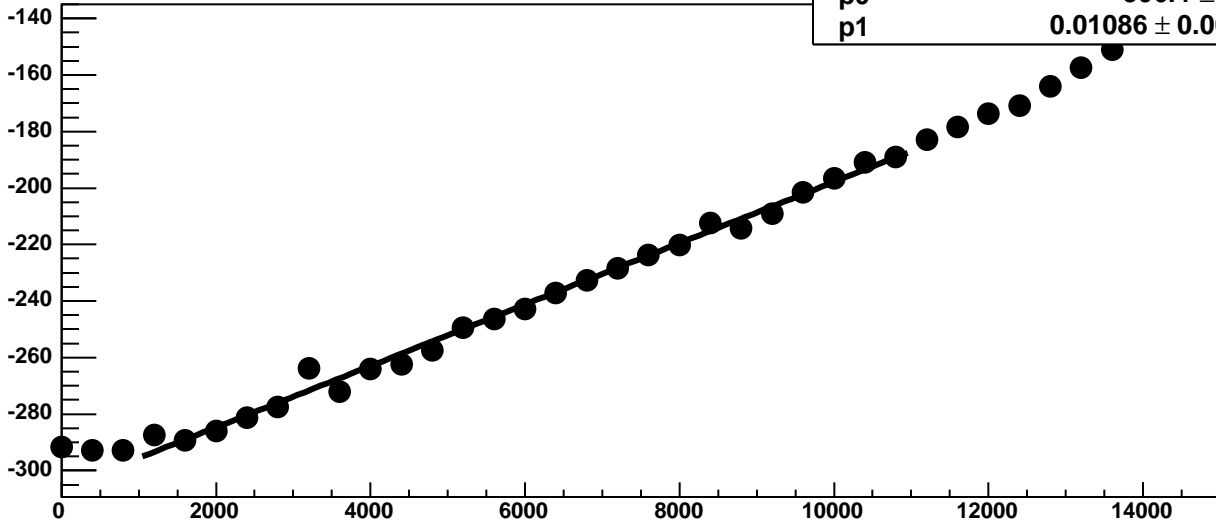
Chip 7, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



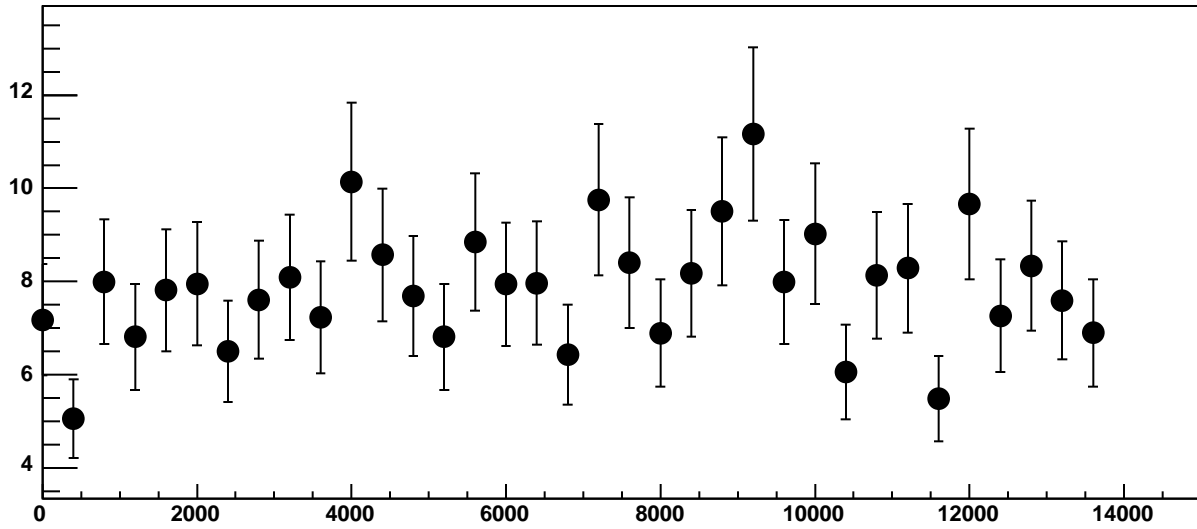
Chip 7, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



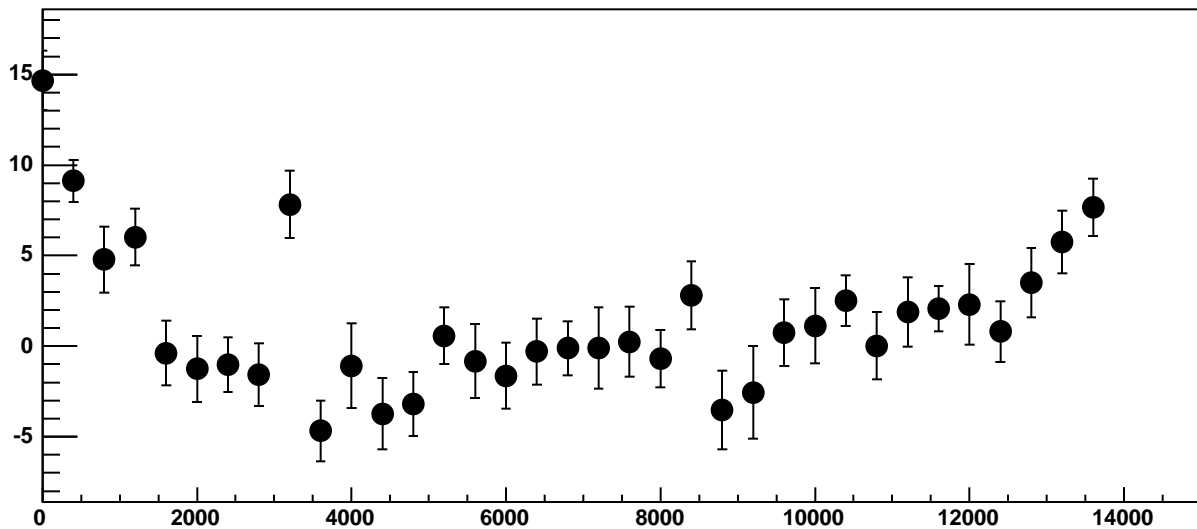
Chip 7, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC



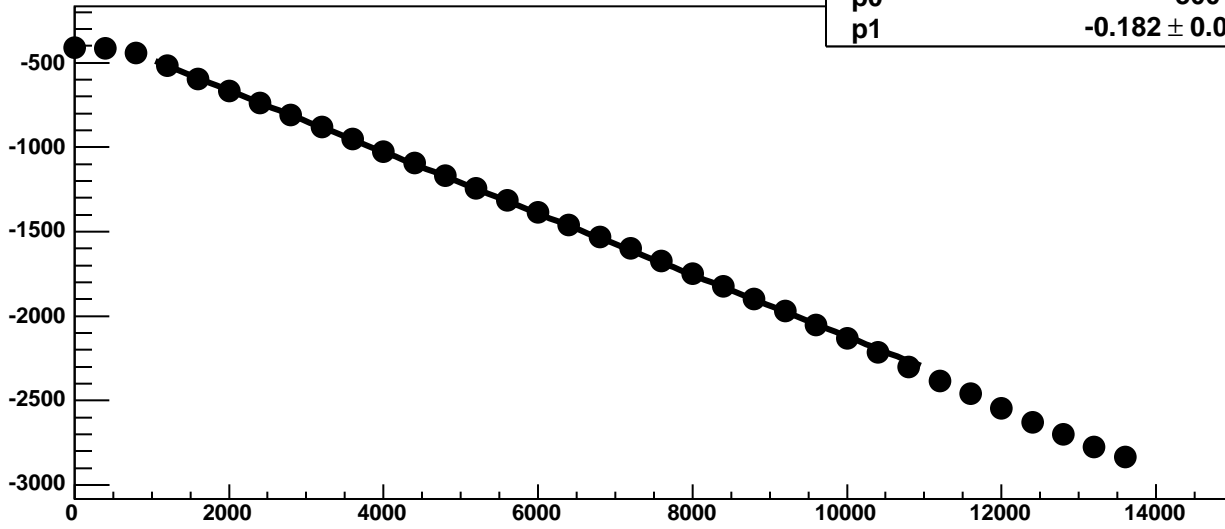
Chip 7, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

72.67 / 23

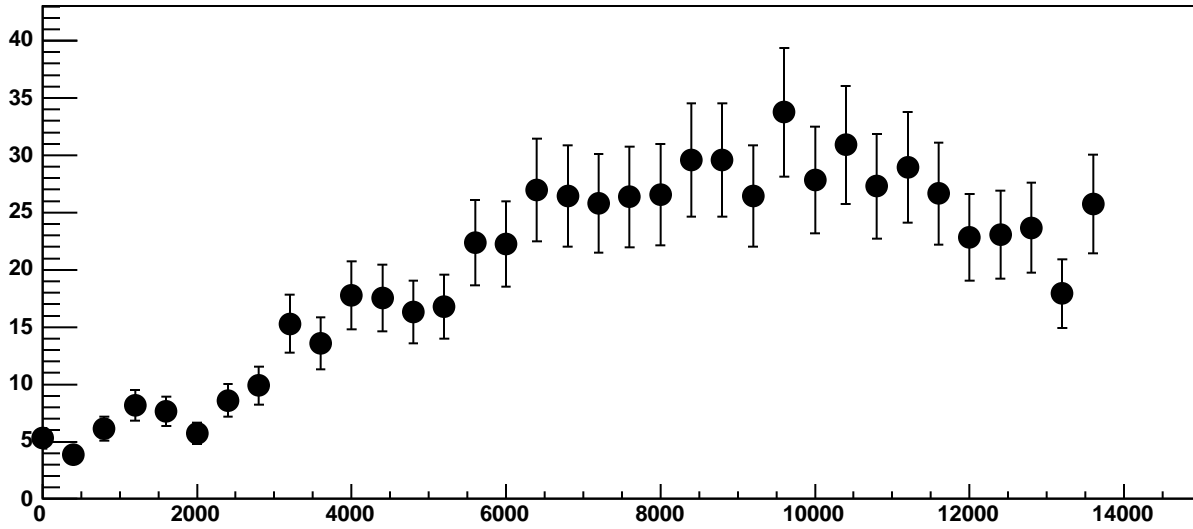
p0

-300 ± 1.105

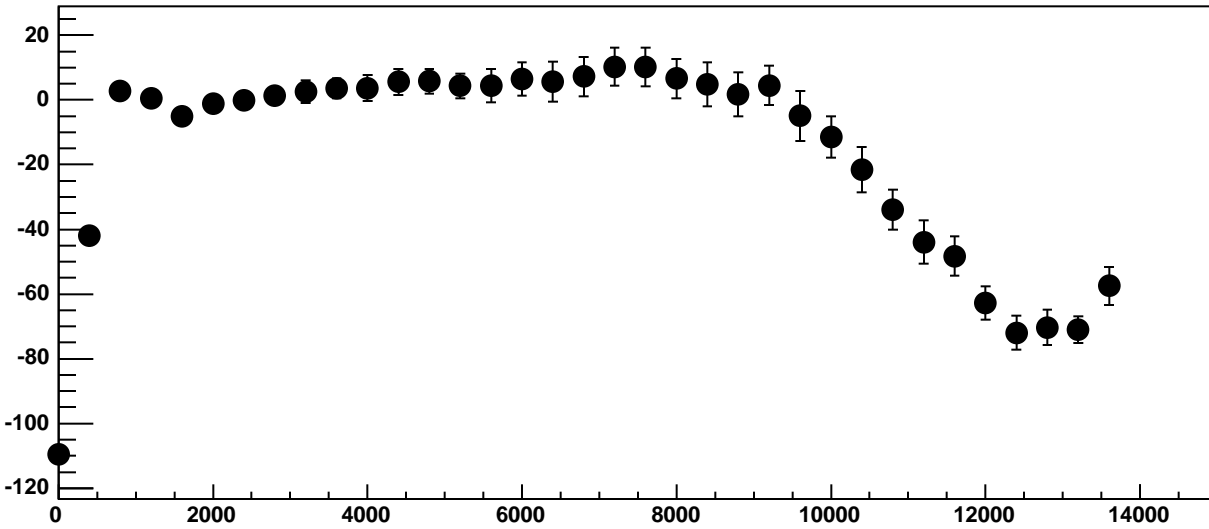
p1

-0.182 ± 0.0002784

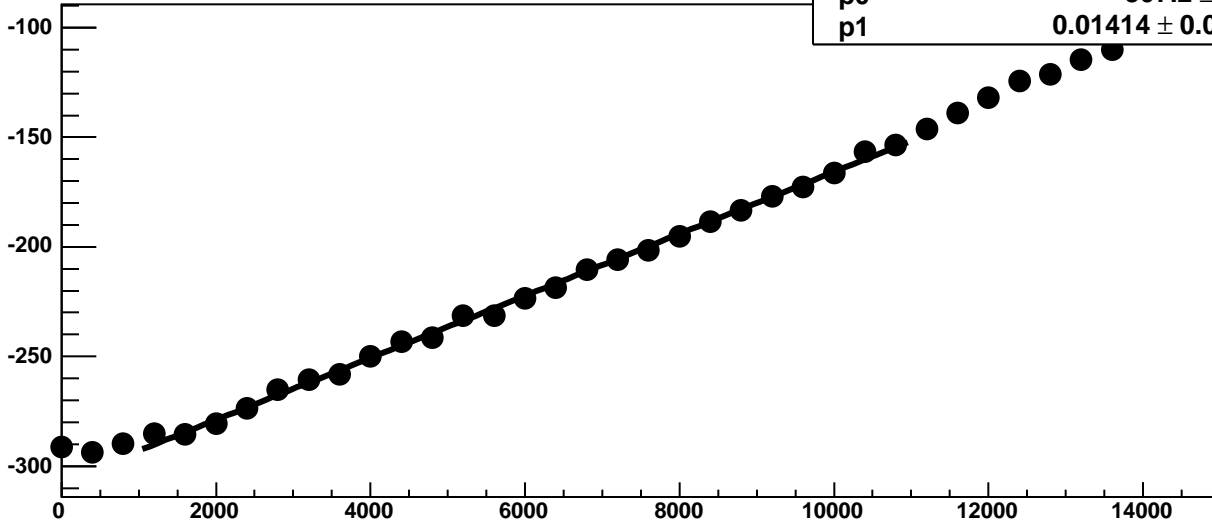
Chip 7, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

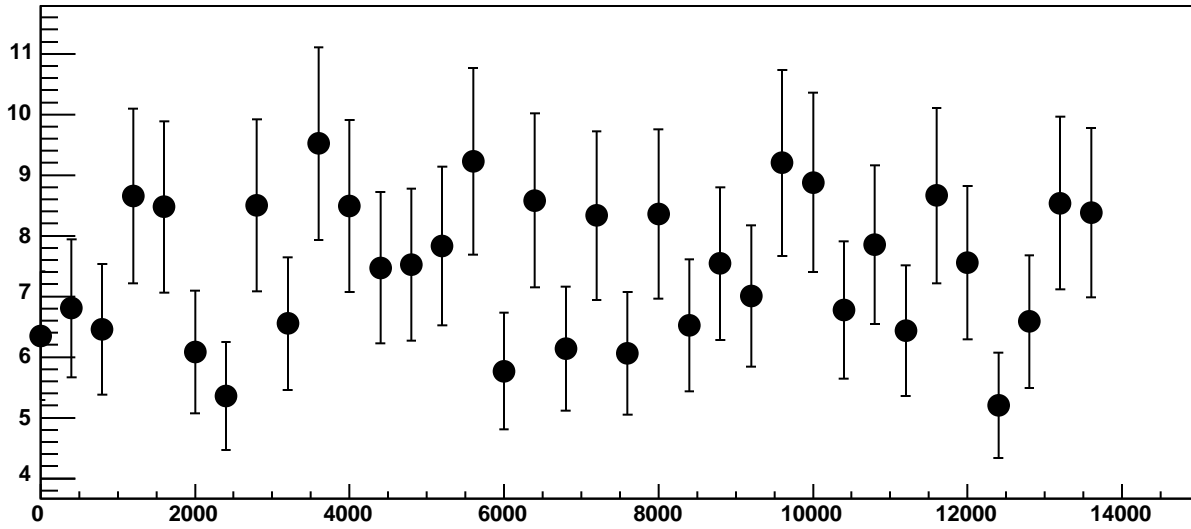


Chip 7, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

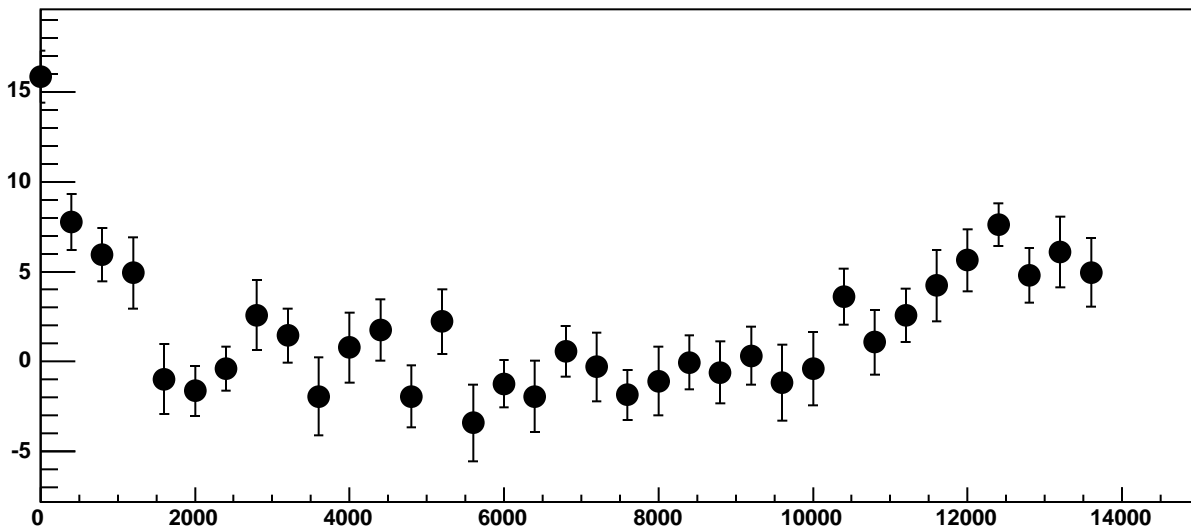


χ^2 / ndf 28.43 / 23
p0 -307.2 ± 0.7752
p1 0.01414 ± 0.0001179

Chip 7, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC

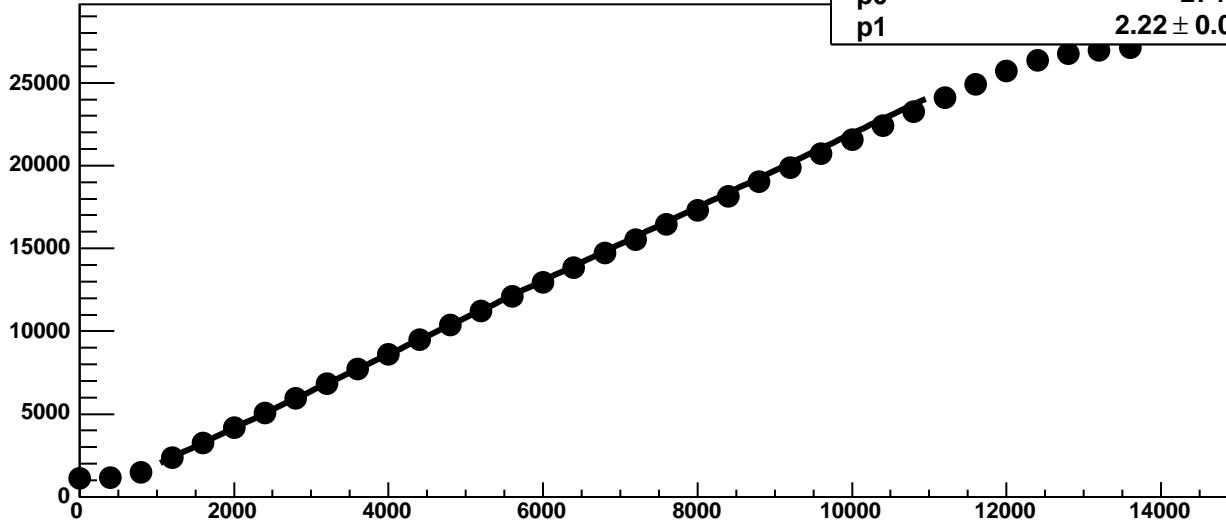


Chip 7, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

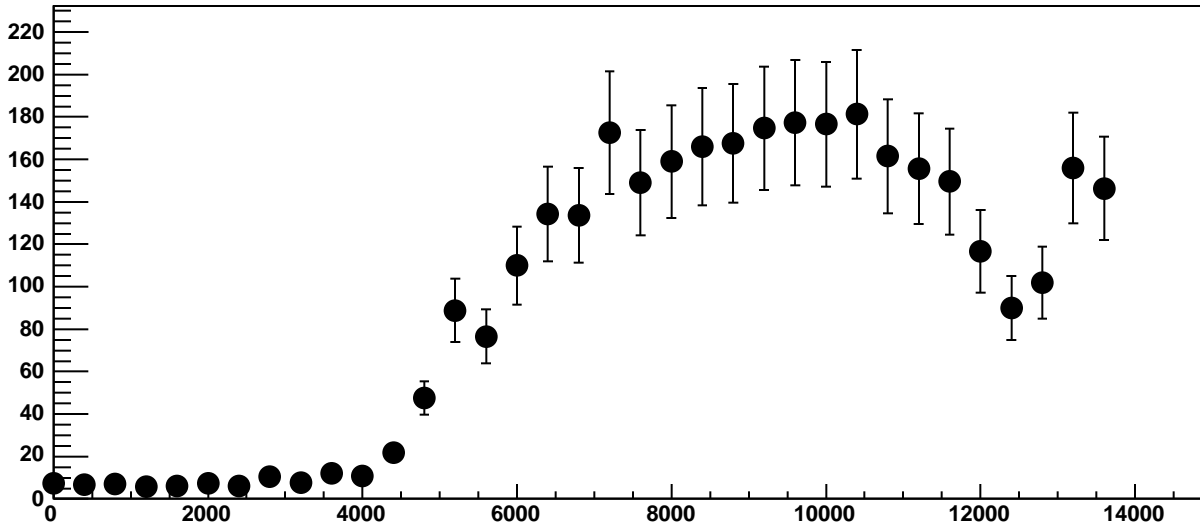


Chip 7, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC

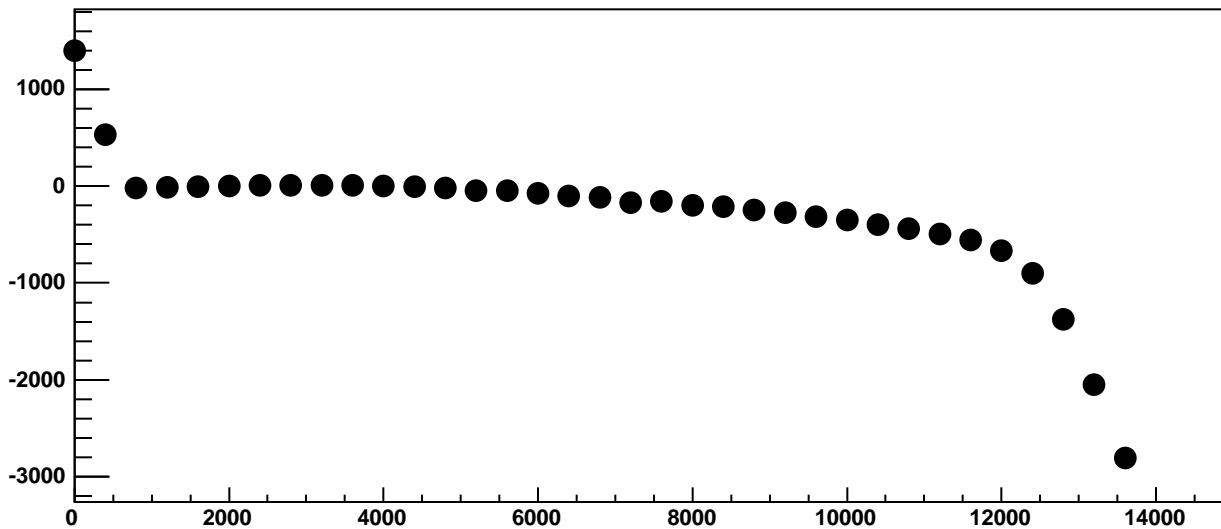
χ^2 / ndf 810.3 / 23
p0 -274 ± 1.547
p1 2.22 ± 0.0006218



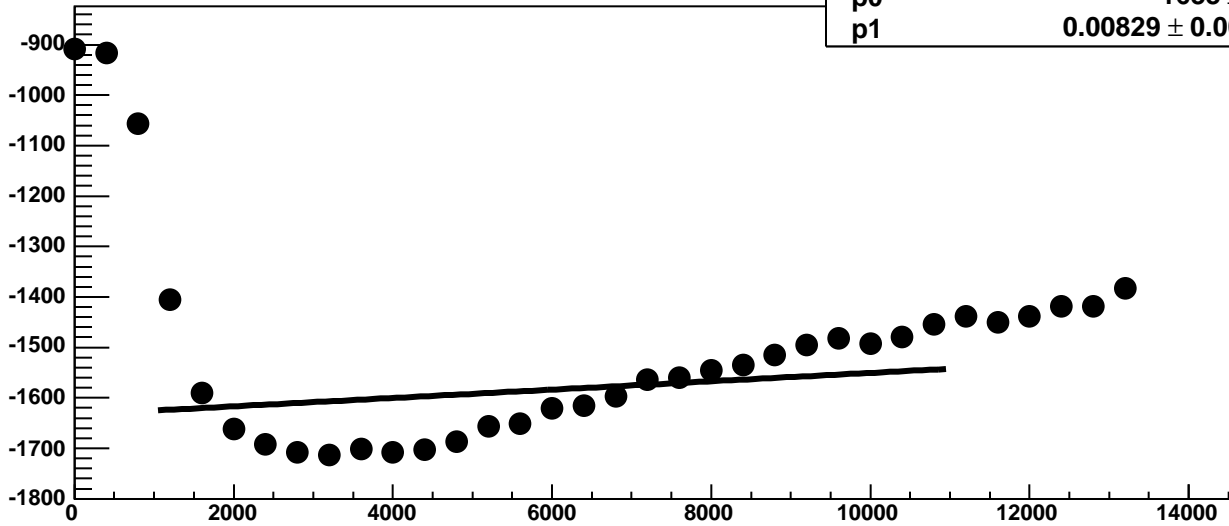
Chip 7, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

6736 / 23

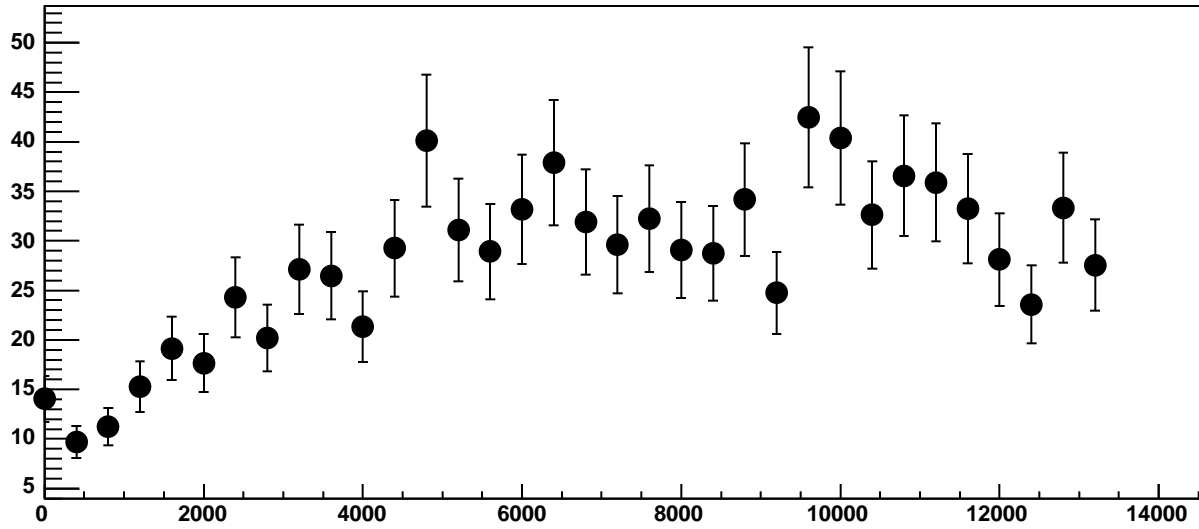
p0

-1633 ± 2.277

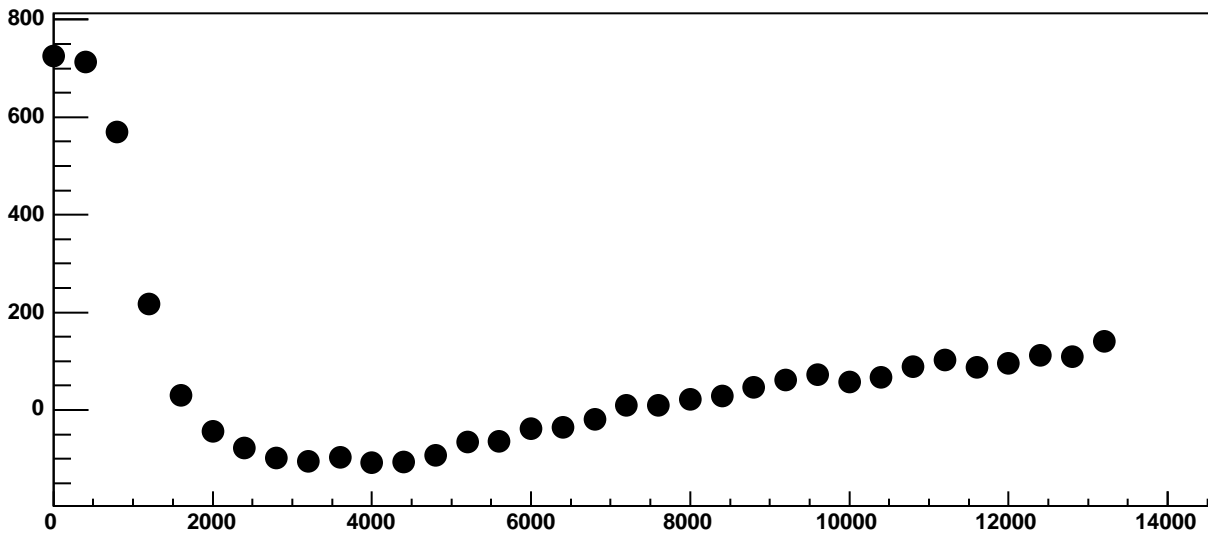
p1

0.00829 ± 0.0004082

Chip 7, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

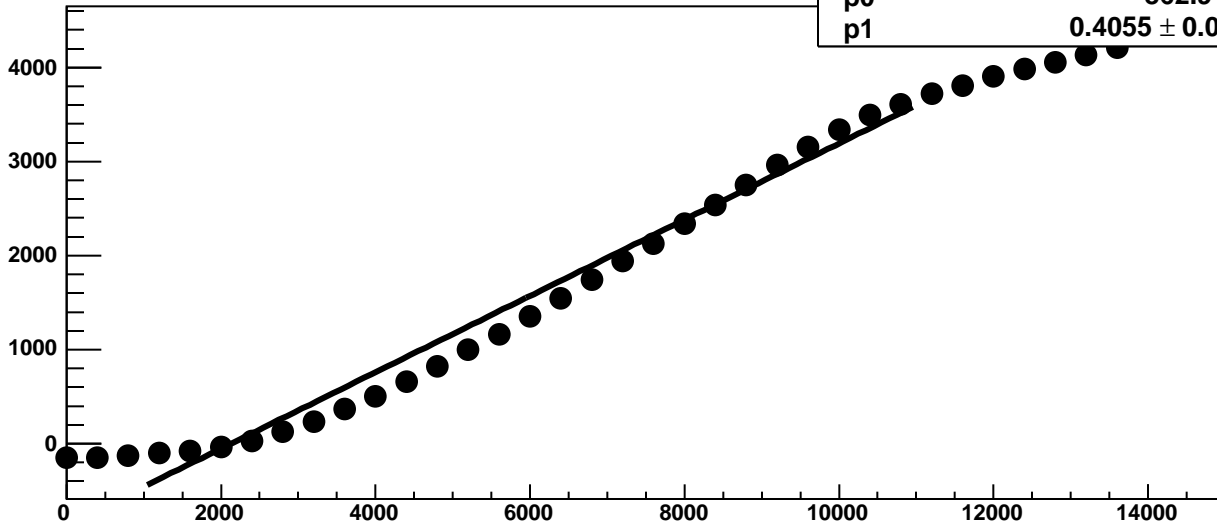


Chip 7, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

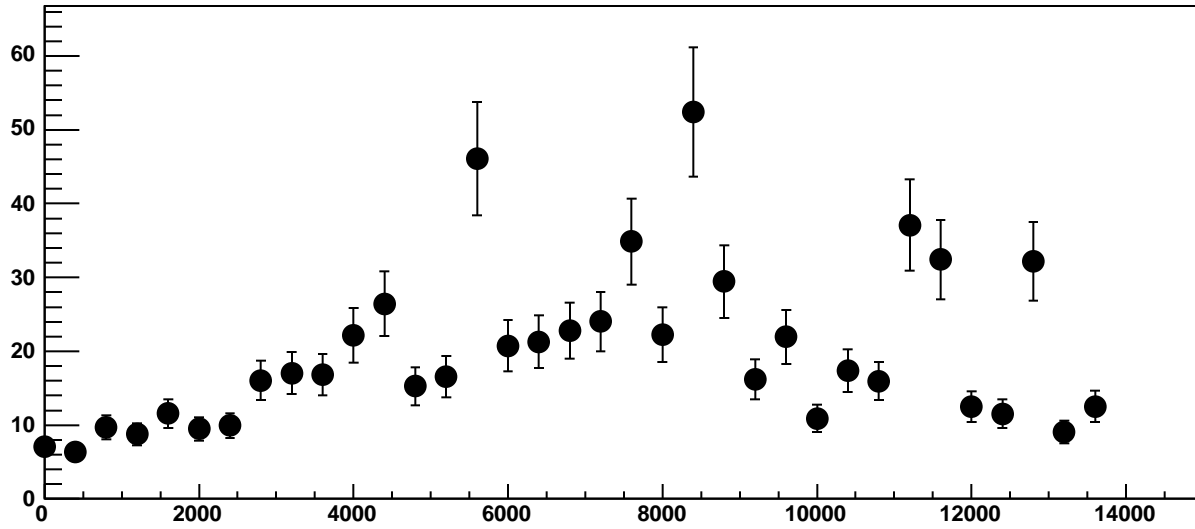


Chip 7, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

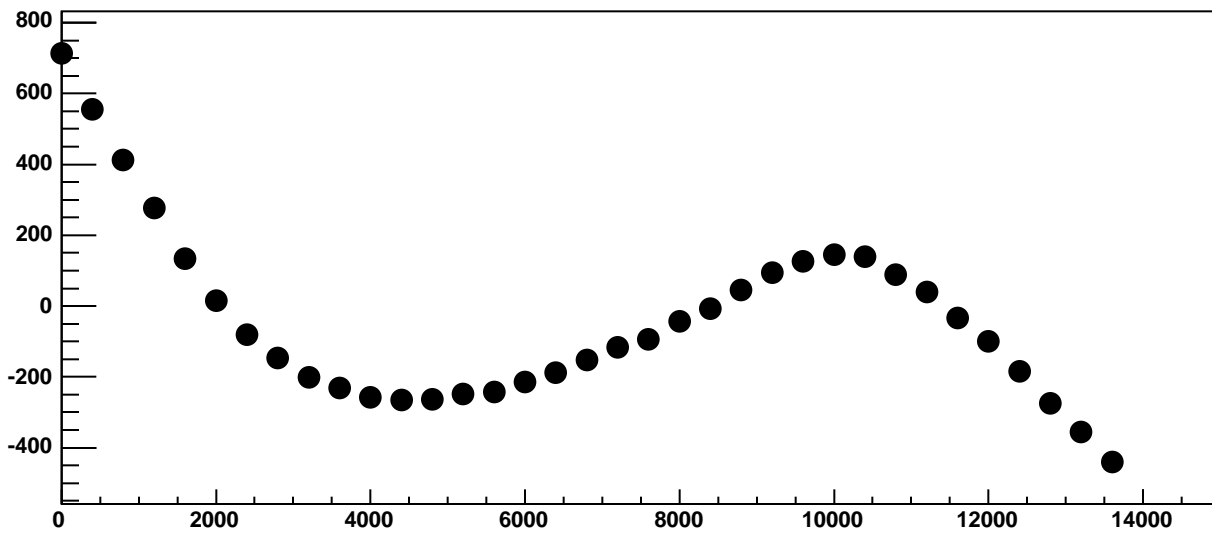
χ^2 / ndf 5.696e+04 / 23
p0 -862.9 ± 1.277
p1 0.4055 ± 0.0002187



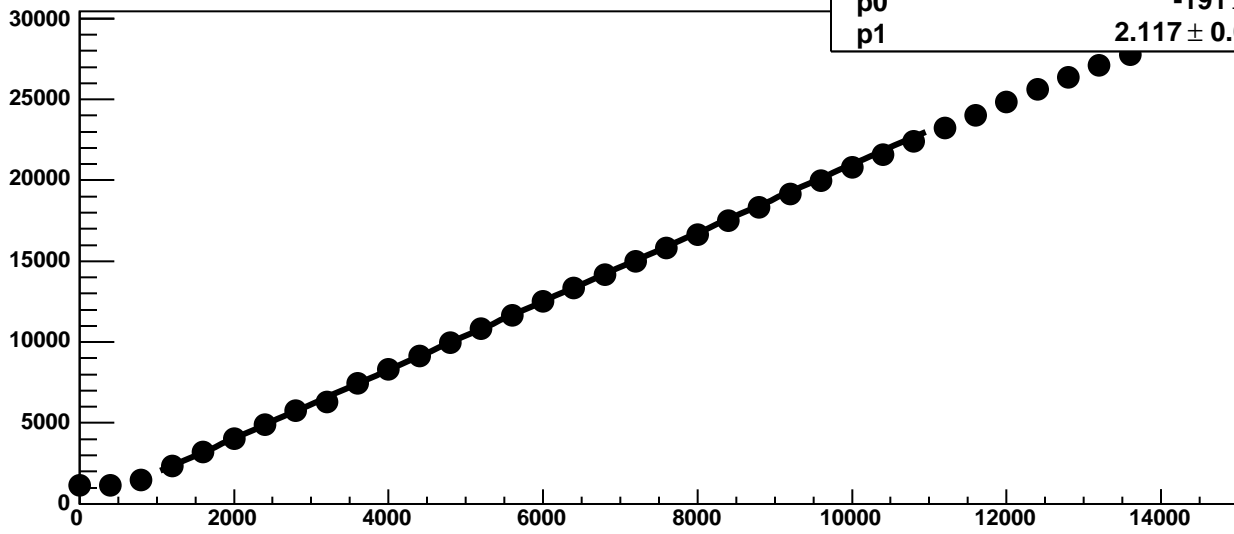
Chip 7, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

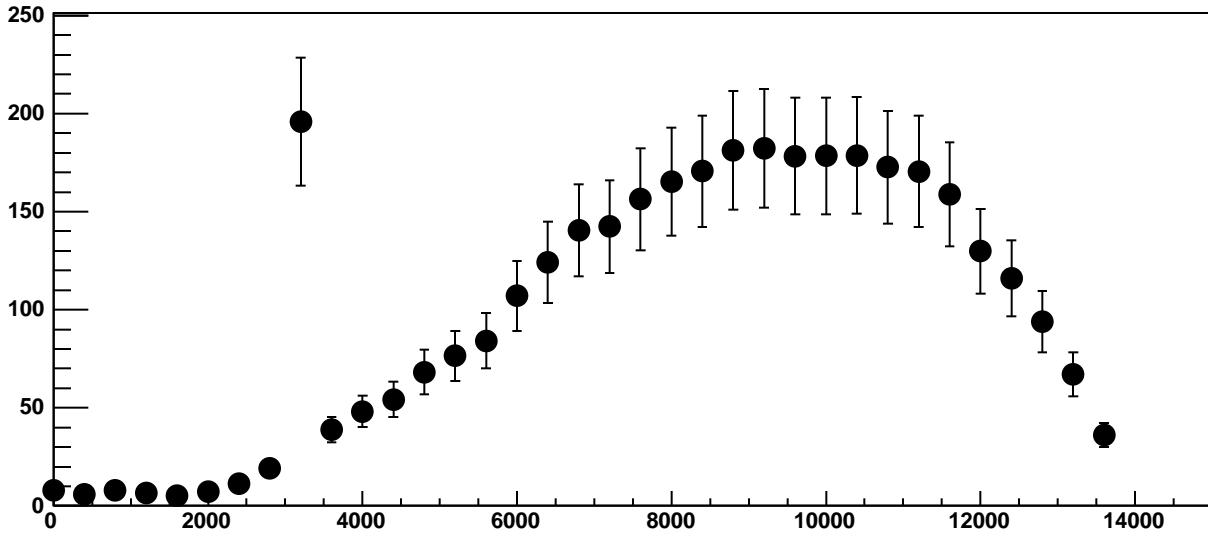


Chip 7, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC

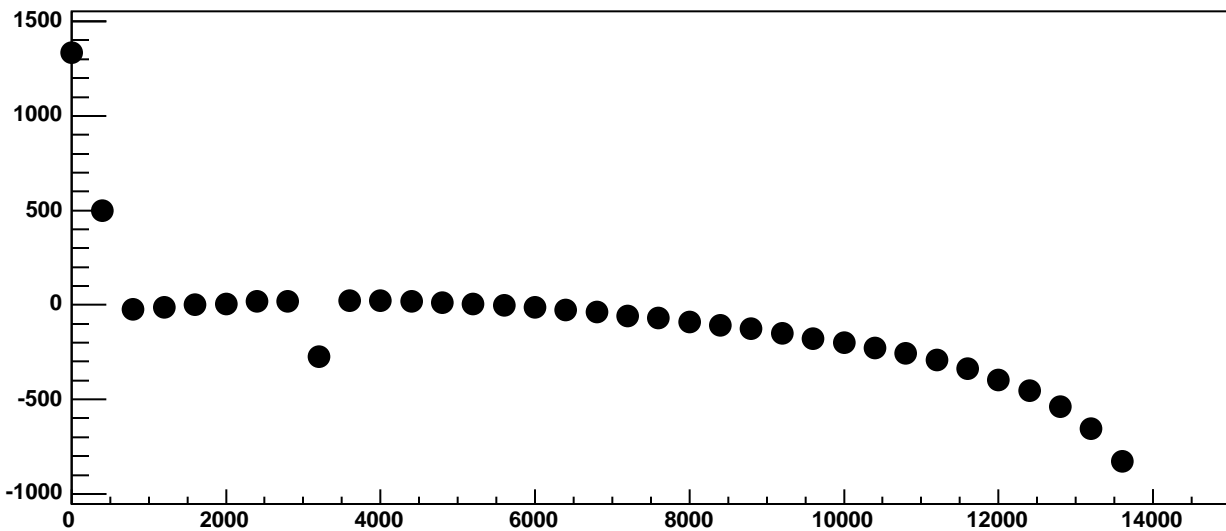


χ^2 / ndf 357.6 / 23
p0 -191 ± 1.954
p1 2.117 ± 0.001017

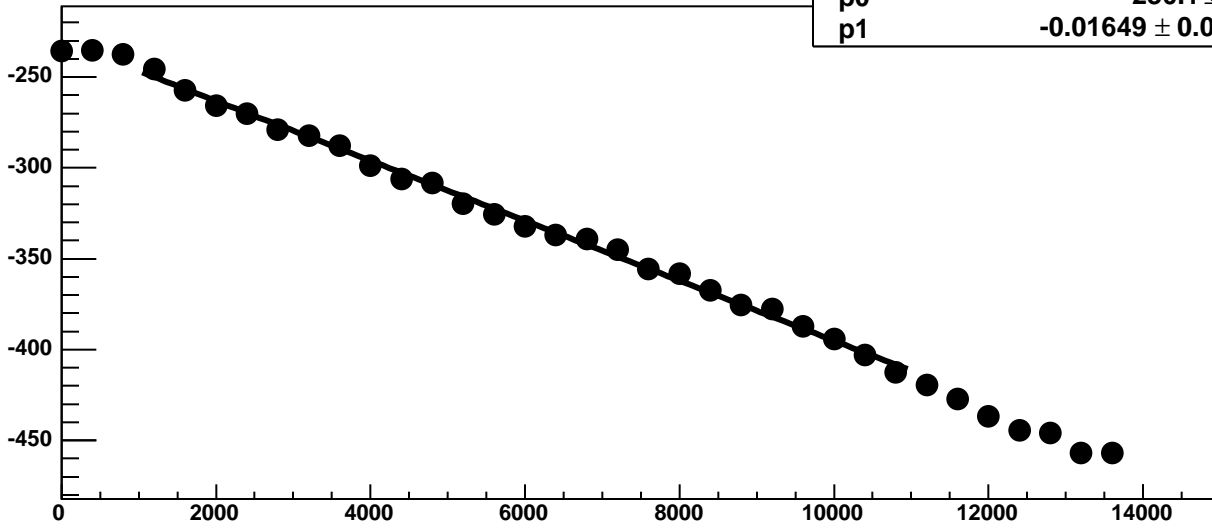
Chip 7, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC

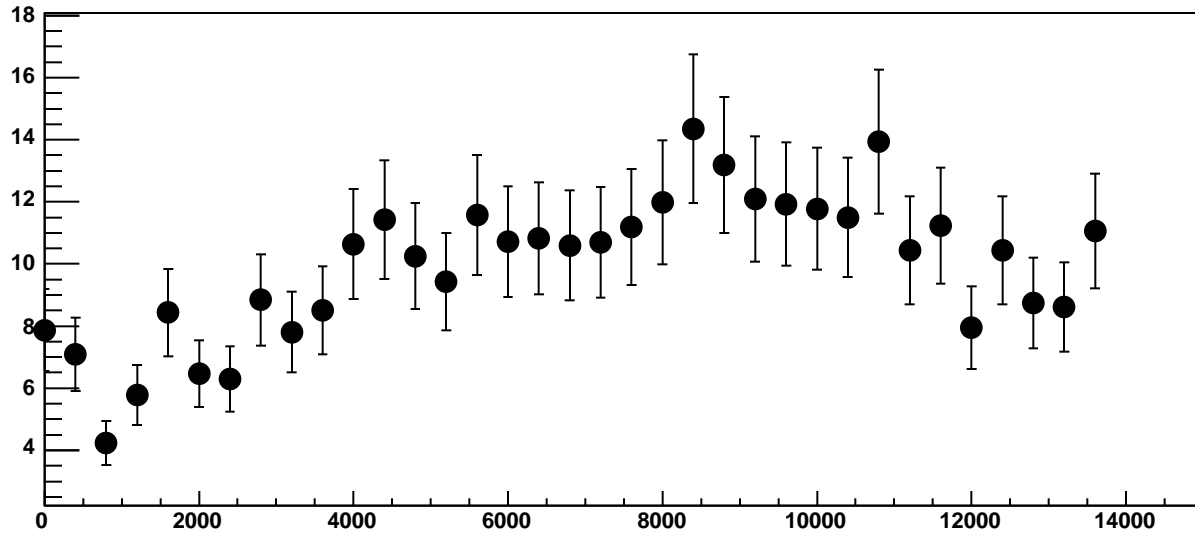


Chip 7, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

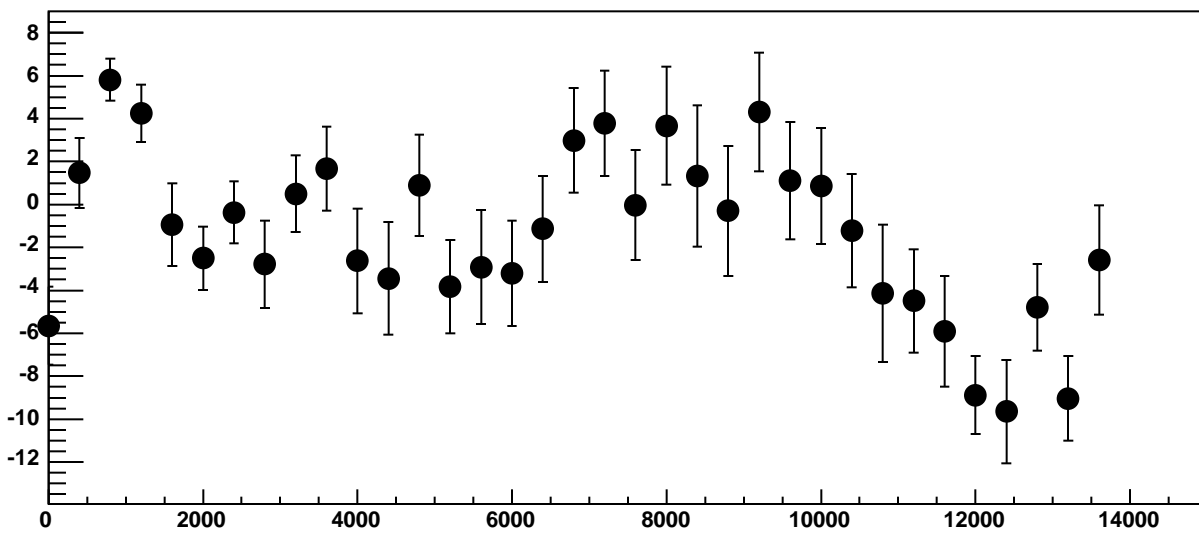


χ^2 / ndf	35.82 / 23
p0	-230.1 ± 0.8341
p1	-0.01649 ± 0.0001509

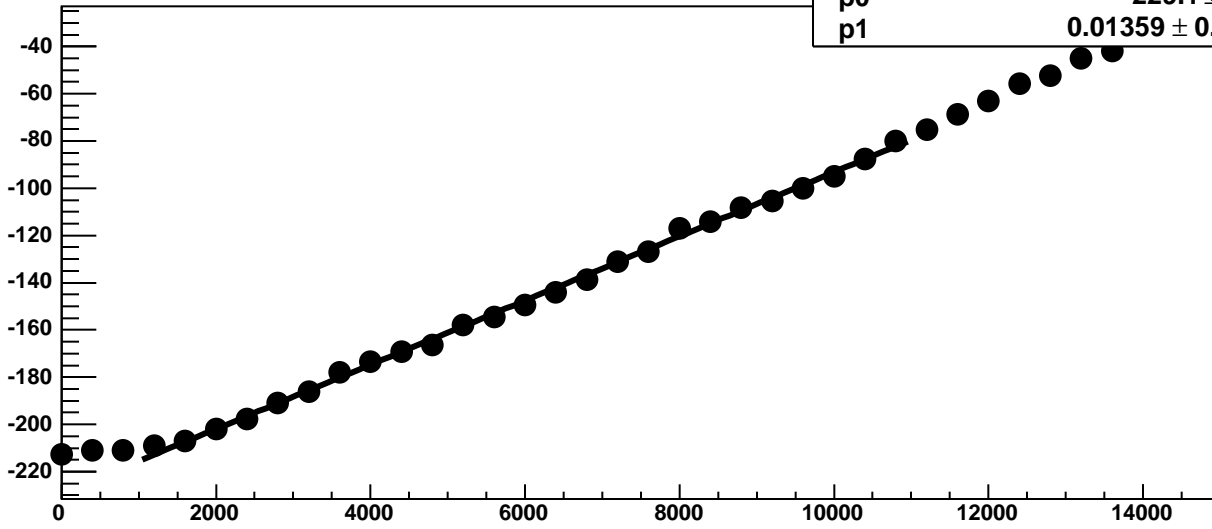
Chip 7, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

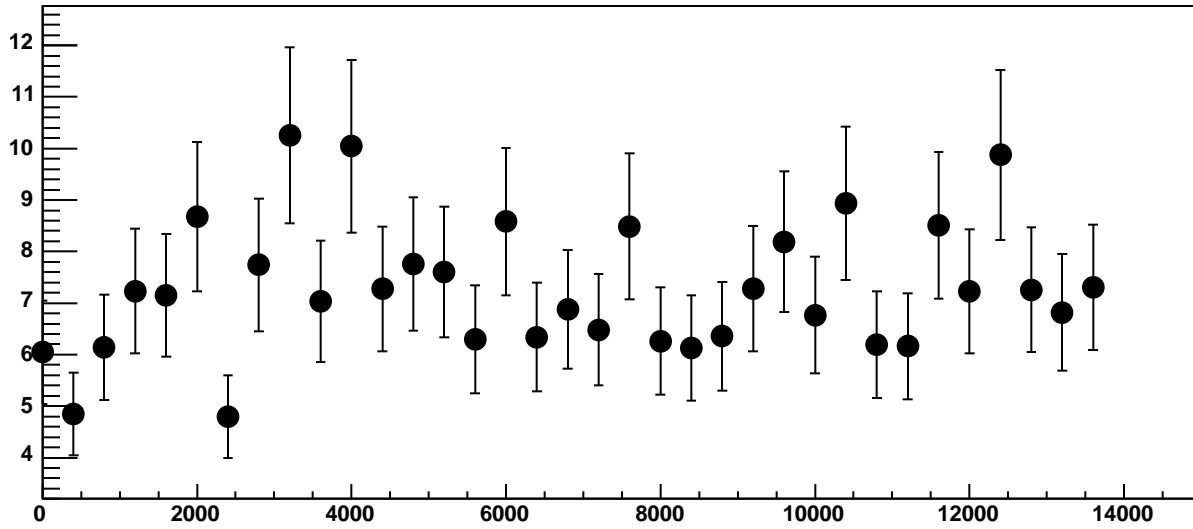


Chip 7, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

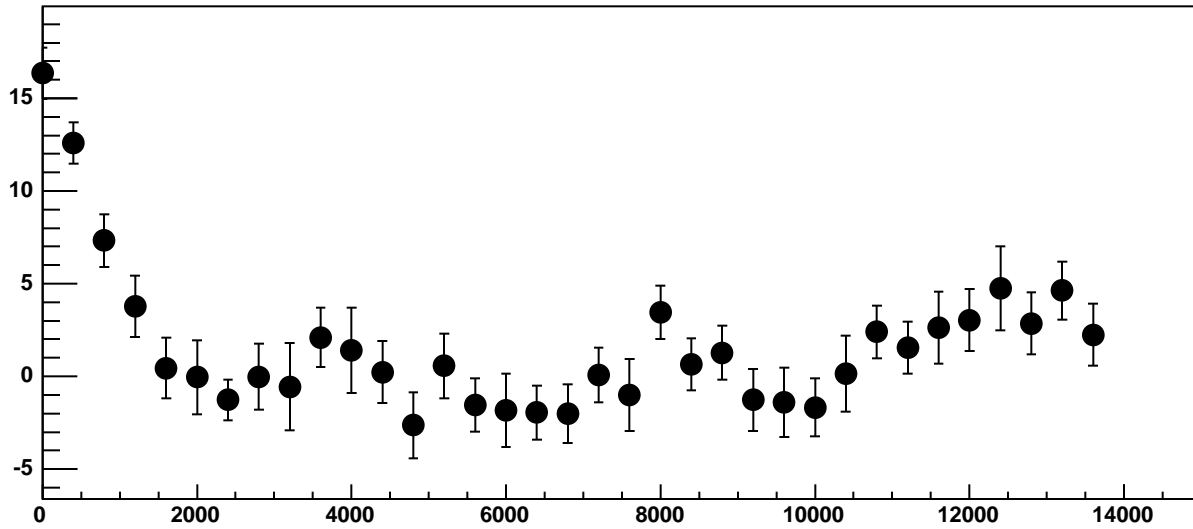


χ^2 / ndf 28.68 / 23
p0 -229.1 ± 0.7531
p1 0.01359 ± 0.000112

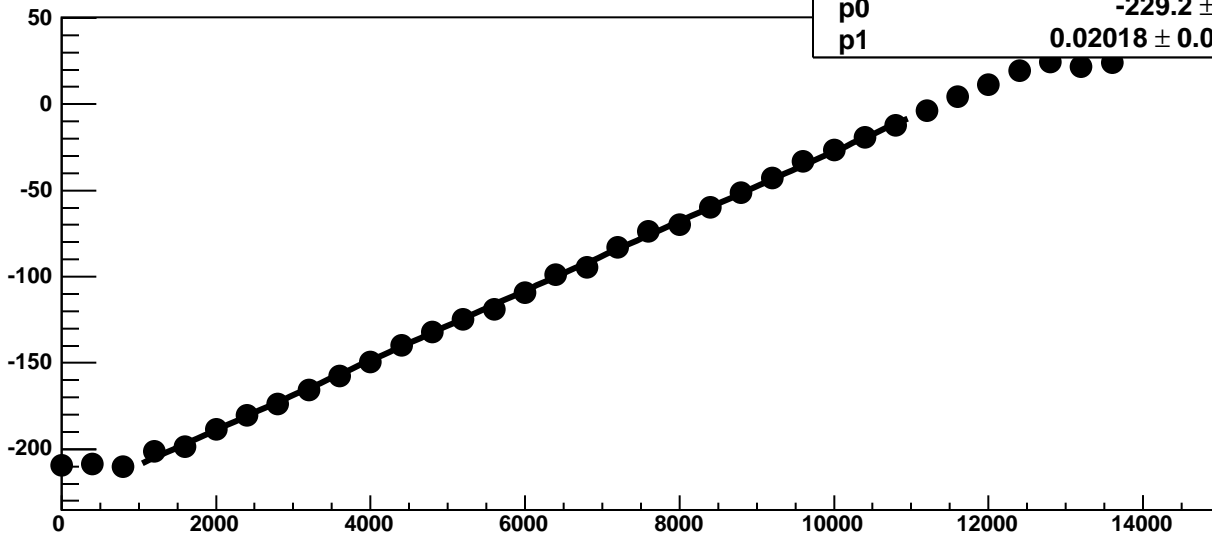
Chip 7, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



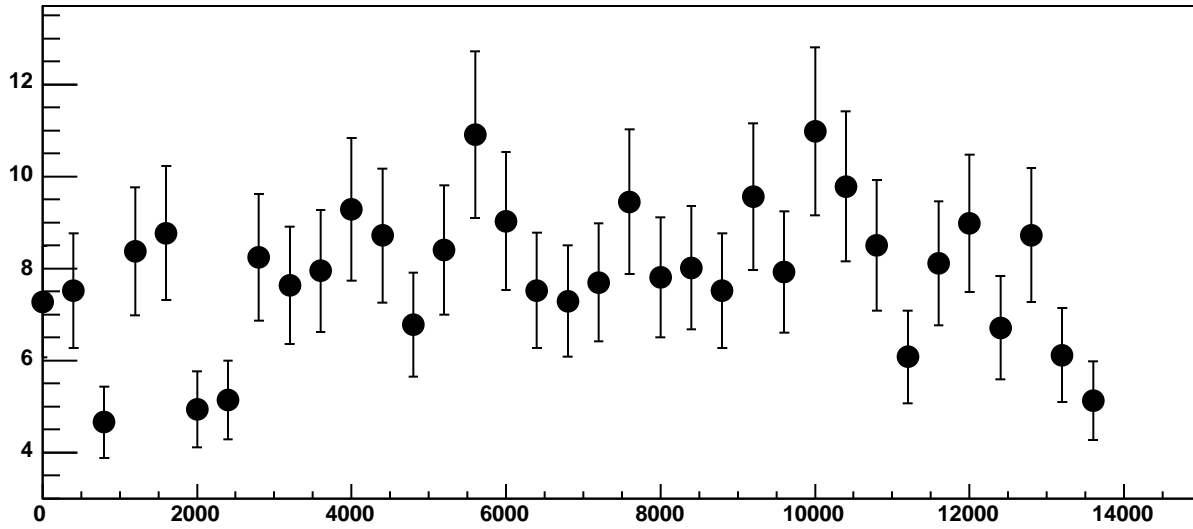
Chip 7, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



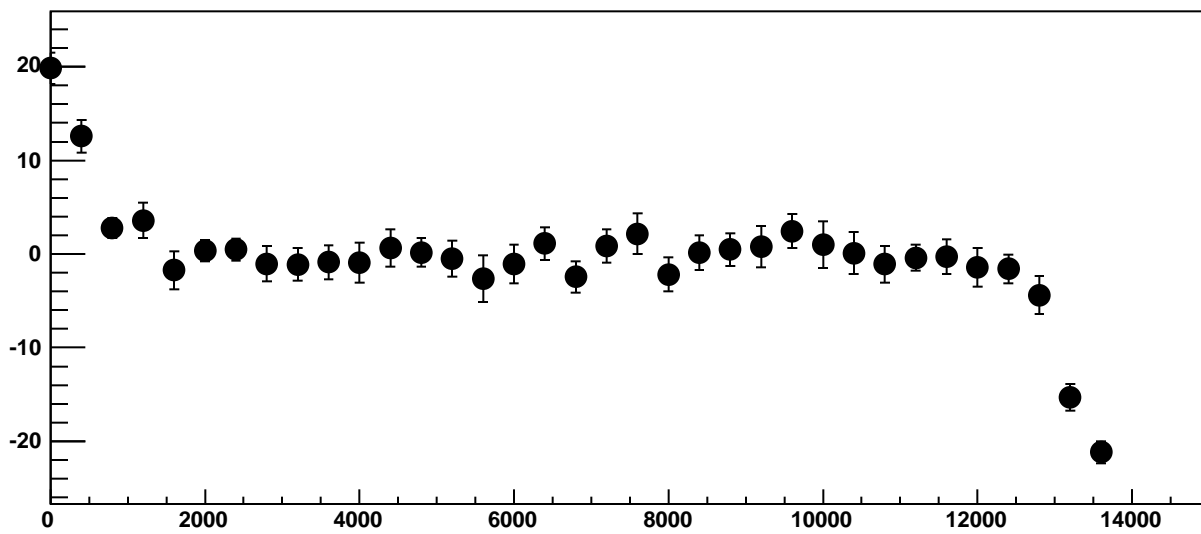
Chip 7, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC



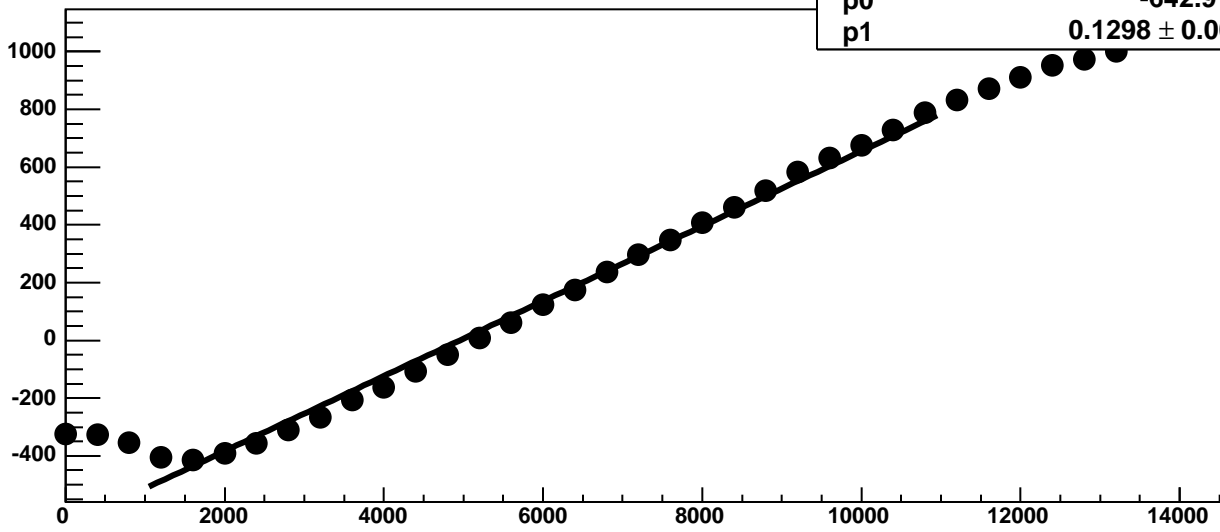
Chip 7, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



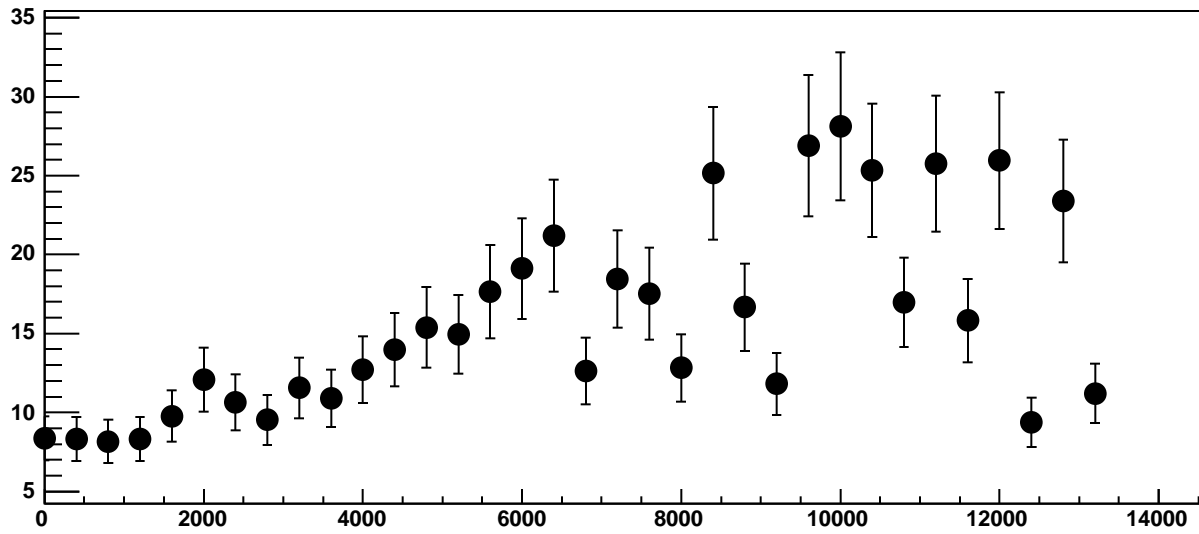
Chip 7, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC



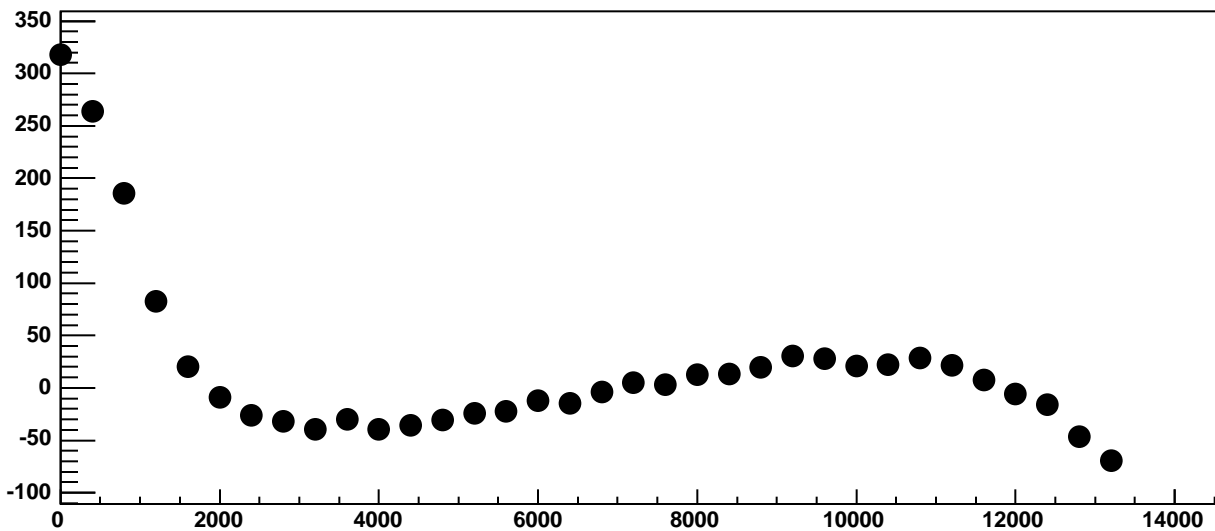
Chip 7, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



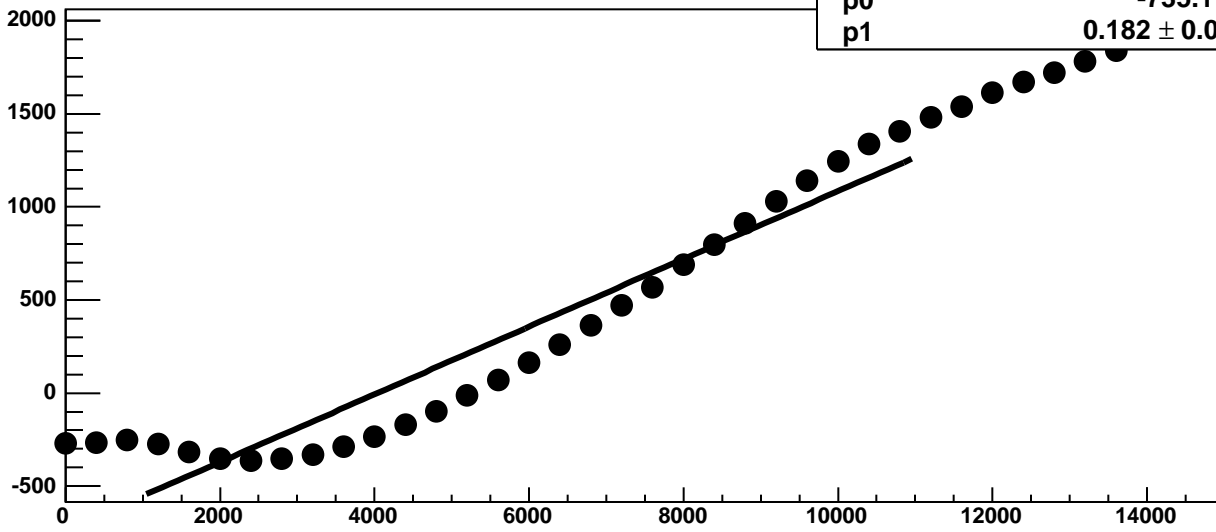
Chip 7, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



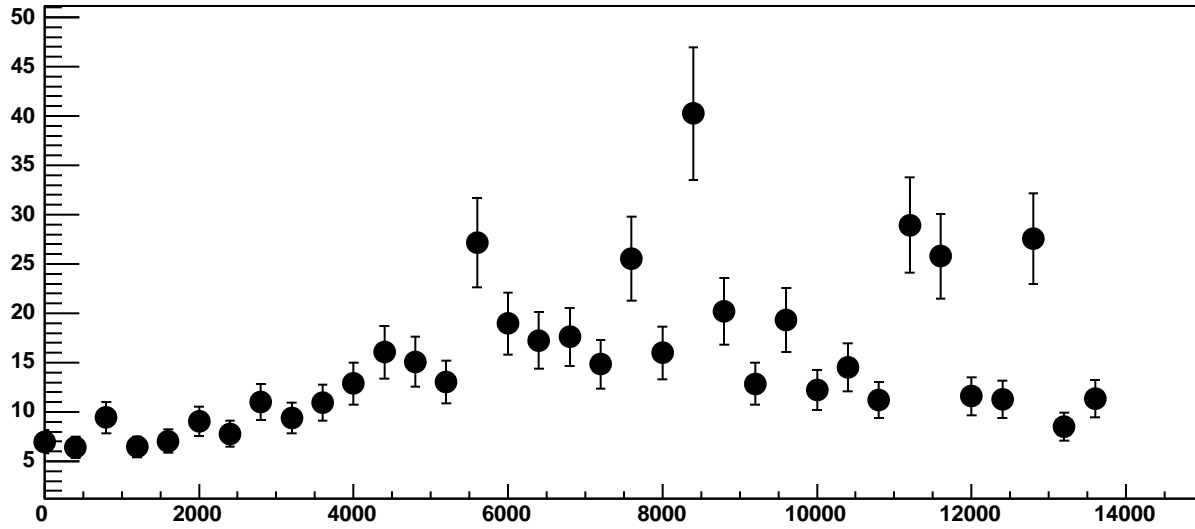
Chip 7, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



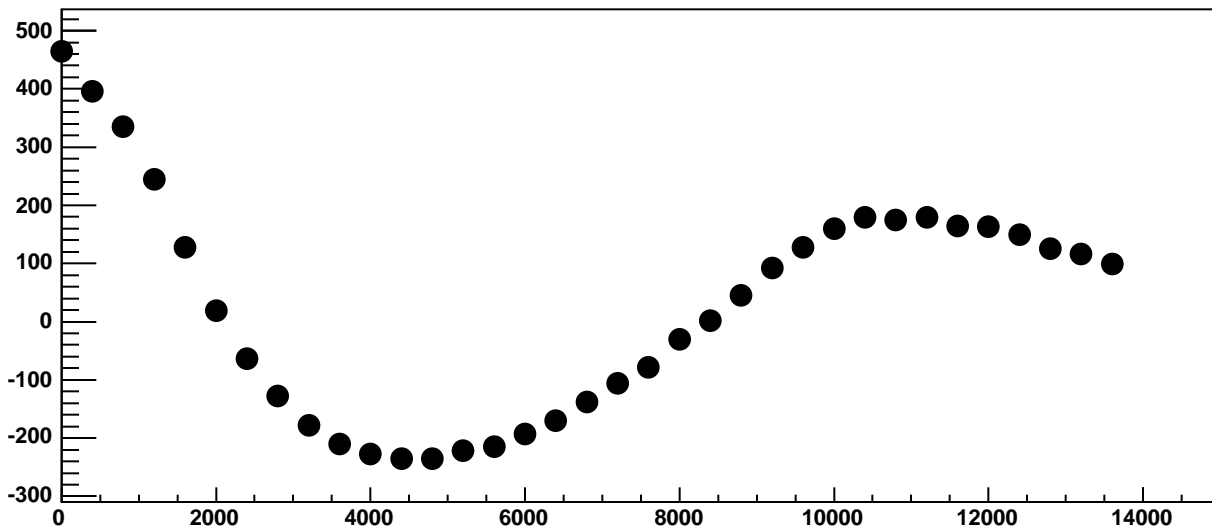
Chip 7, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



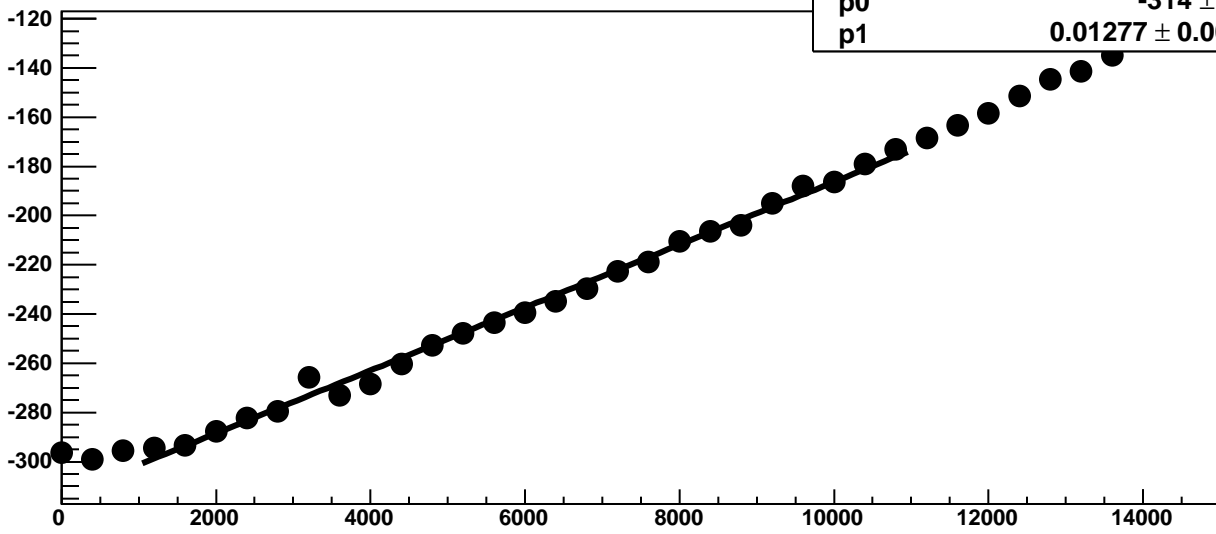
Chip 7, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



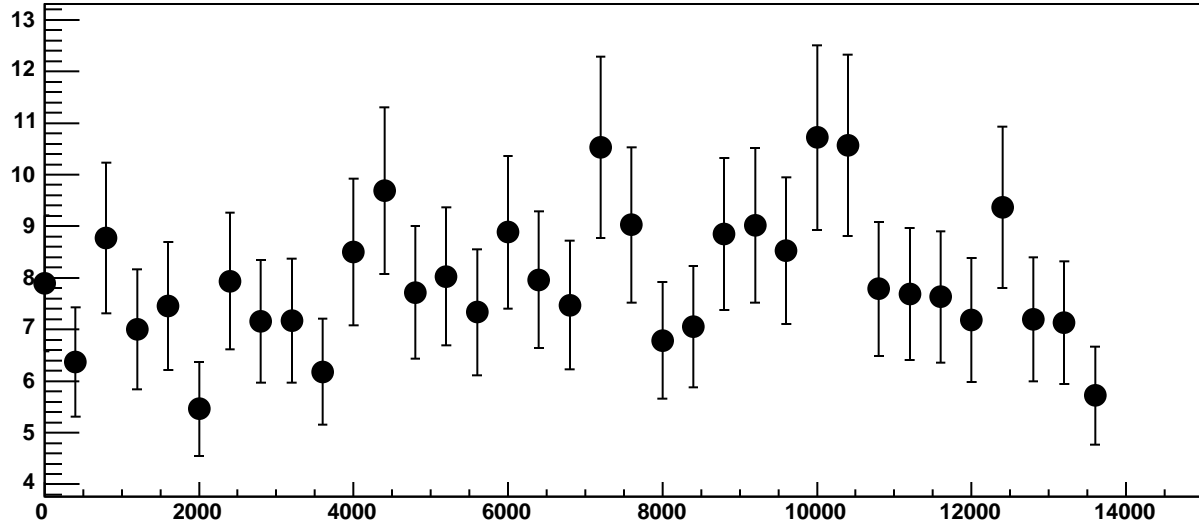
Chip 7, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



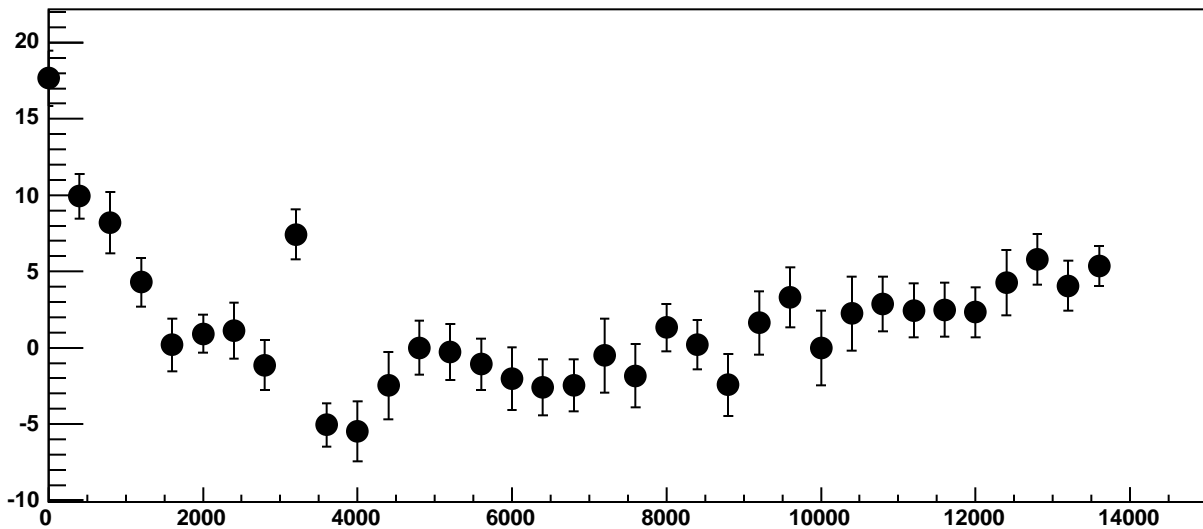
Chip 7, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC



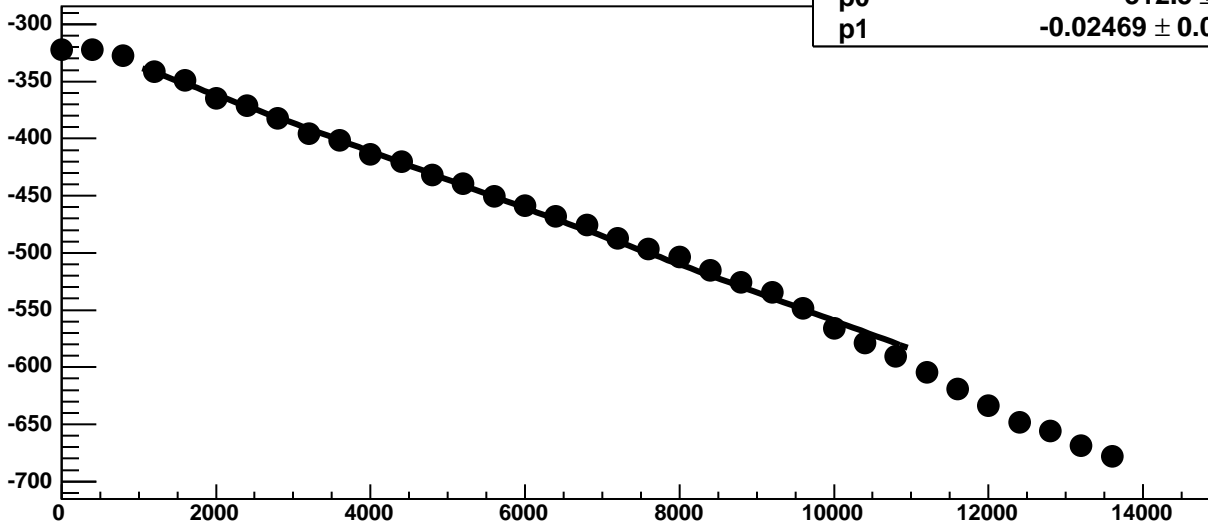
Chip 7, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC

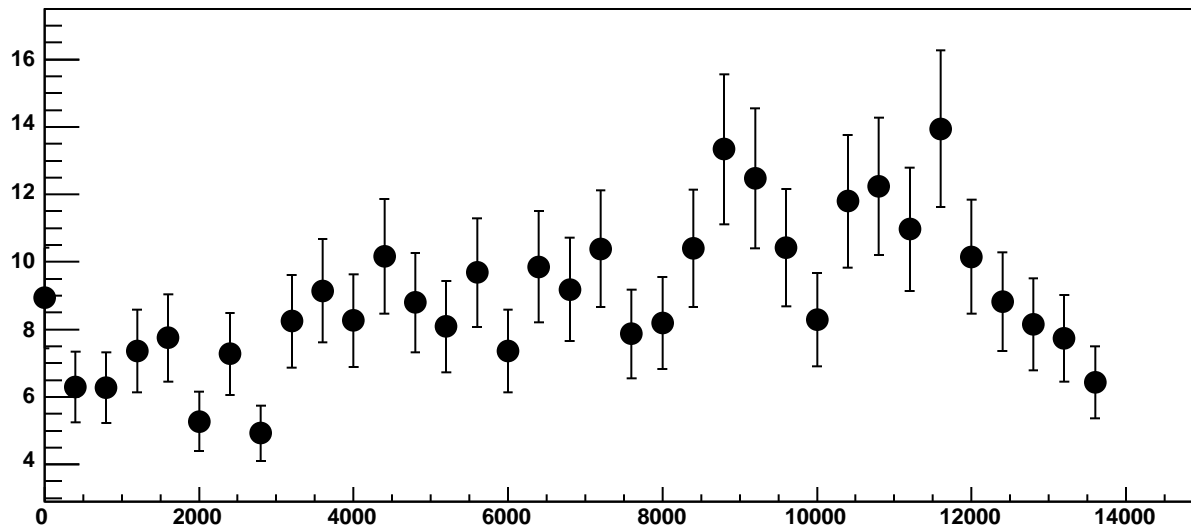


Chip 7, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

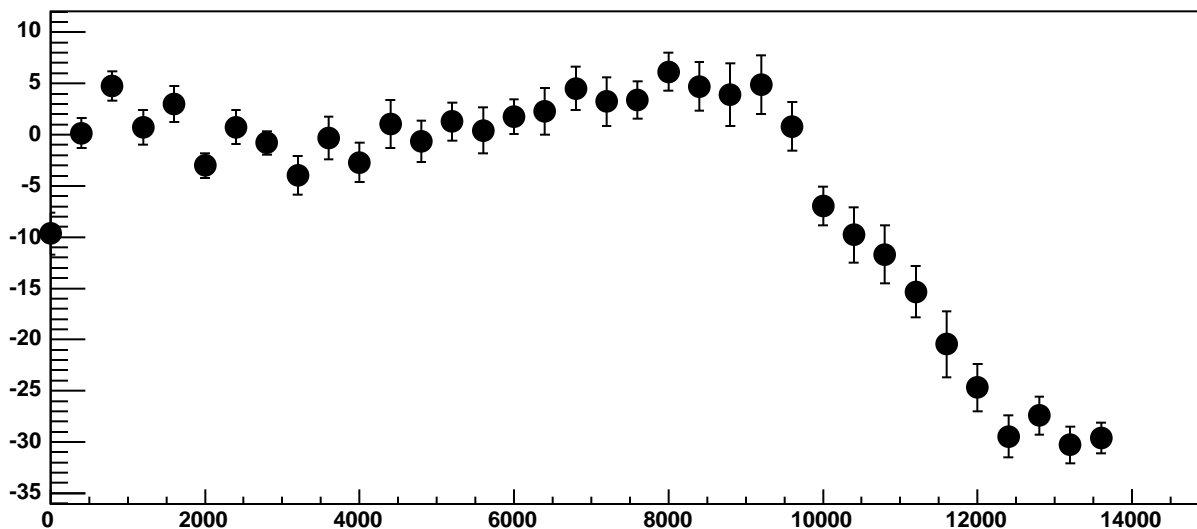


χ^2 / ndf	92.26 / 23
p0	-312.5 ± 0.7789
p1	-0.02469 ± 0.0001356

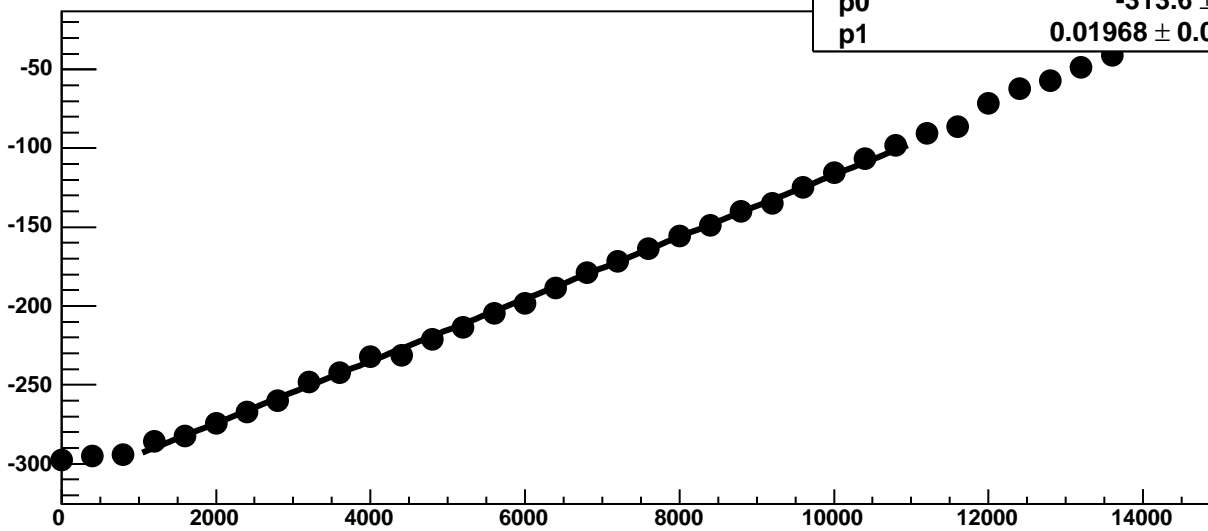
Chip 7, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

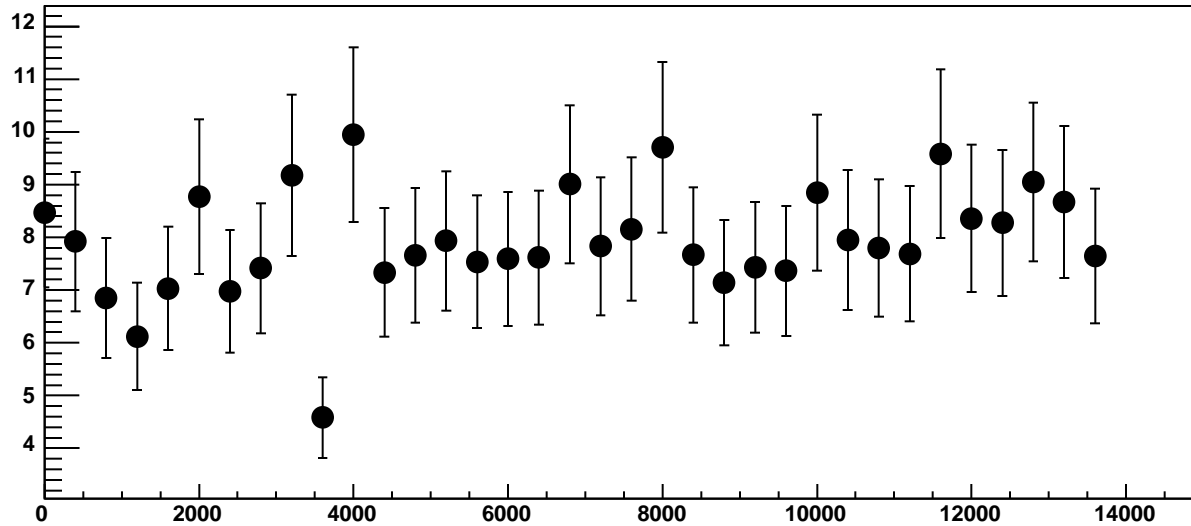


Chip 7, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

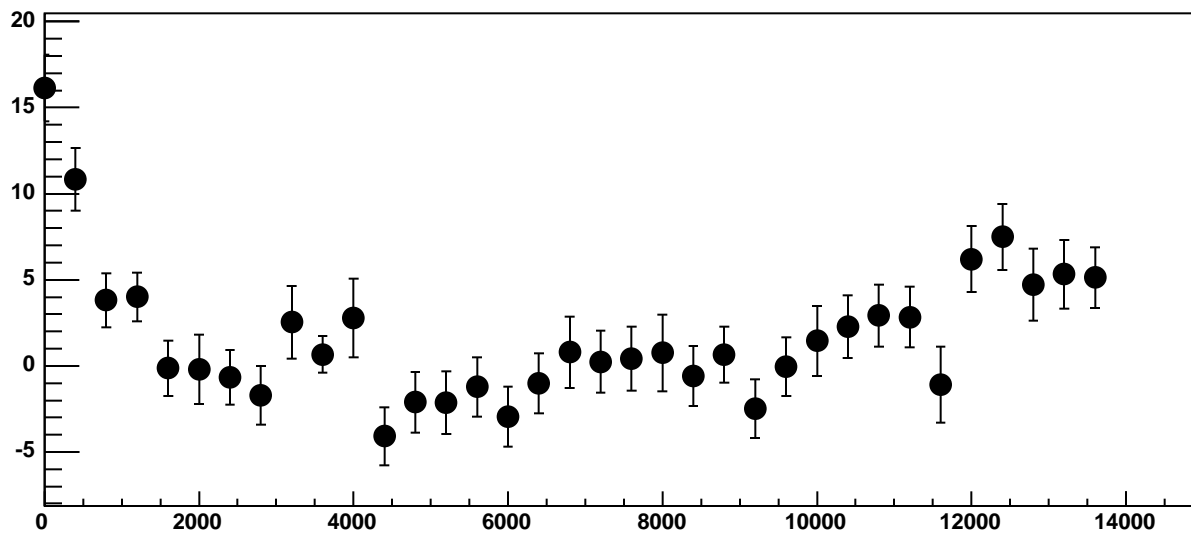


χ^2 / ndf 32.49 / 23
p0 -313.6 ± 0.7529
p1 0.01968 ± 0.0001177

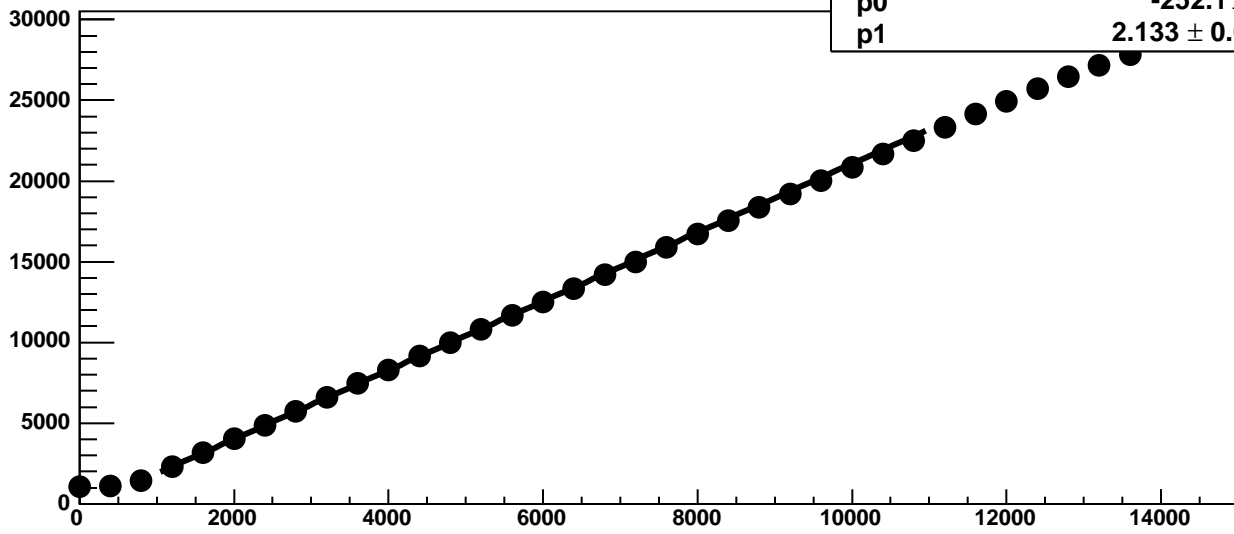
Chip 7, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



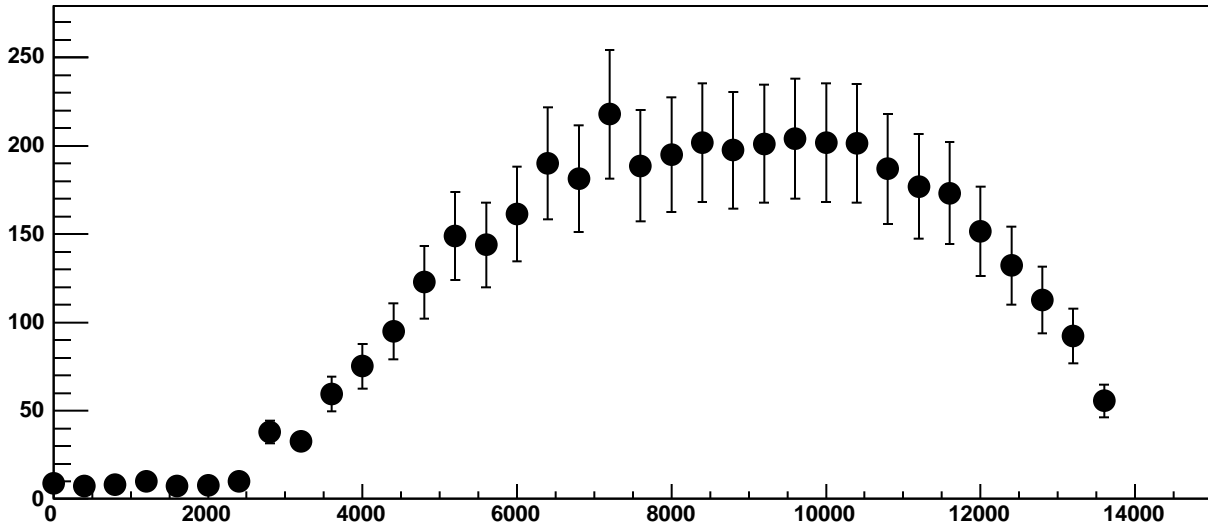
Chip 7, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



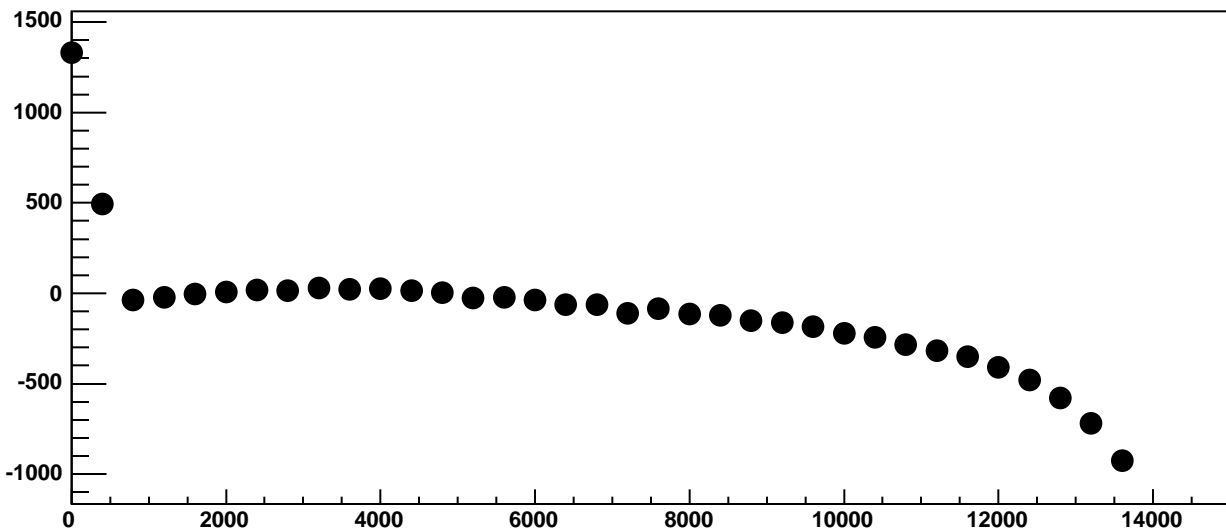
Chip 7, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC



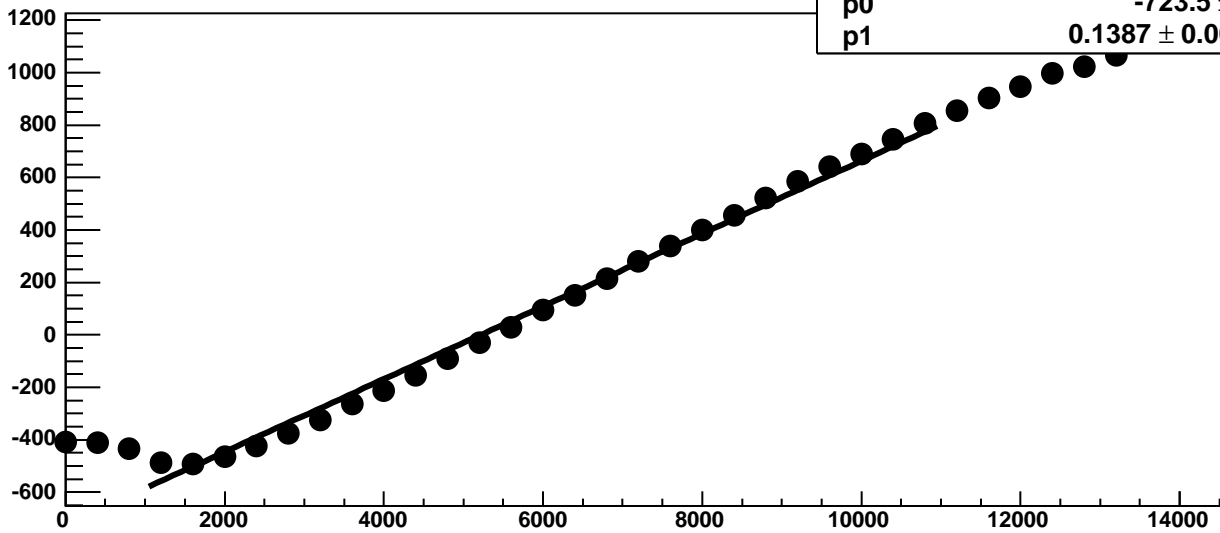
Chip 7, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

4056 / 23

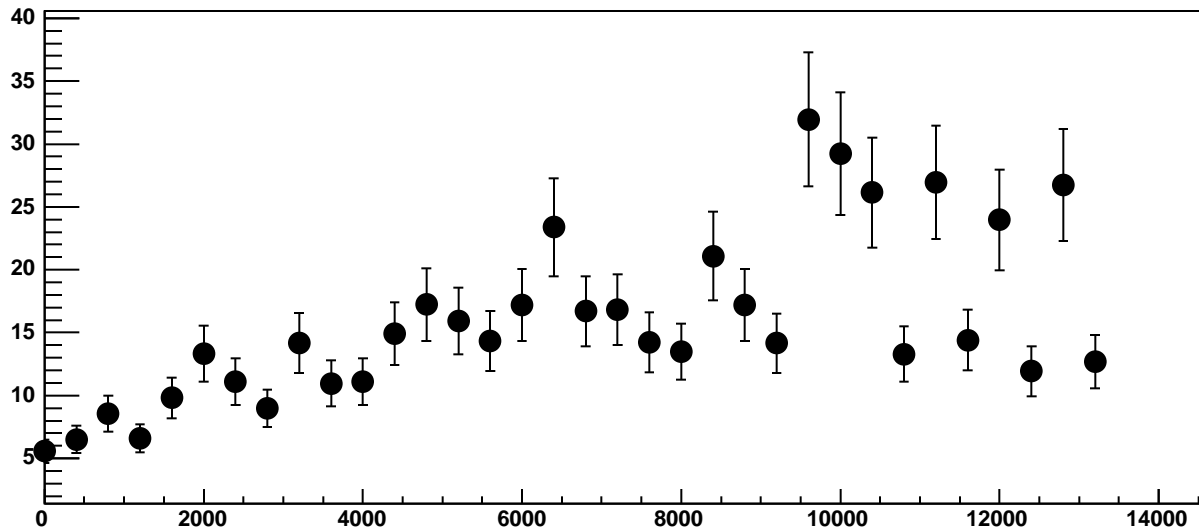
p0

-723.5 ± 1.124

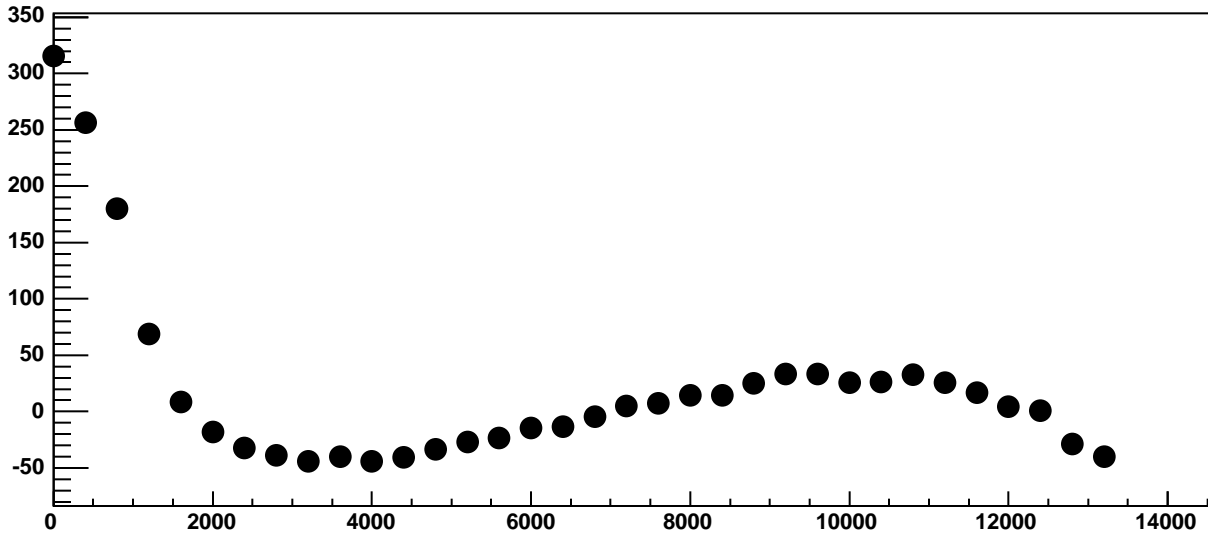
p1

0.1387 ± 0.0002107

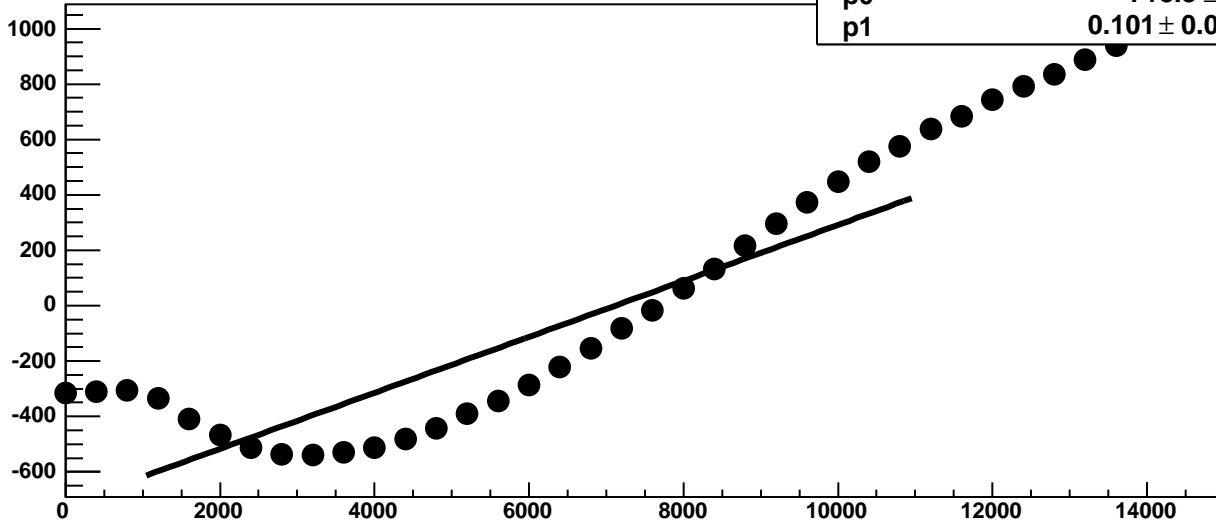
Chip 7, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



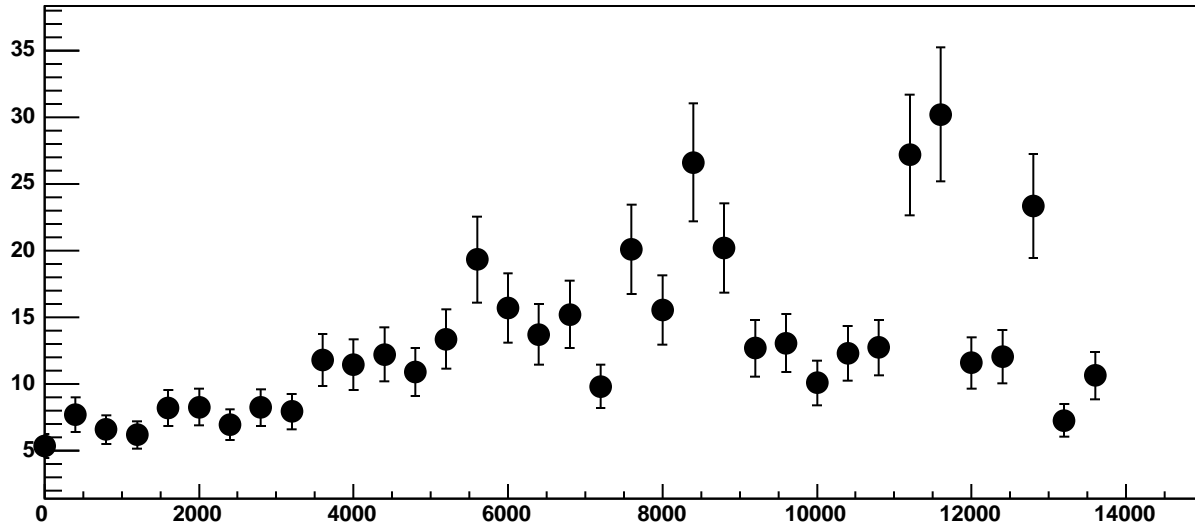
Chip 7, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



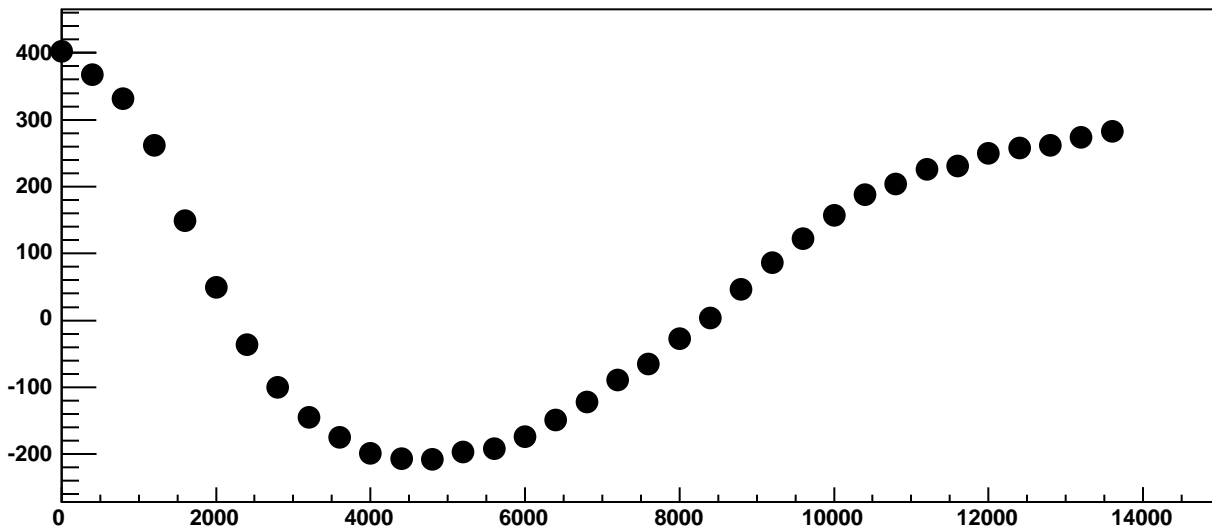
Chip 7, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



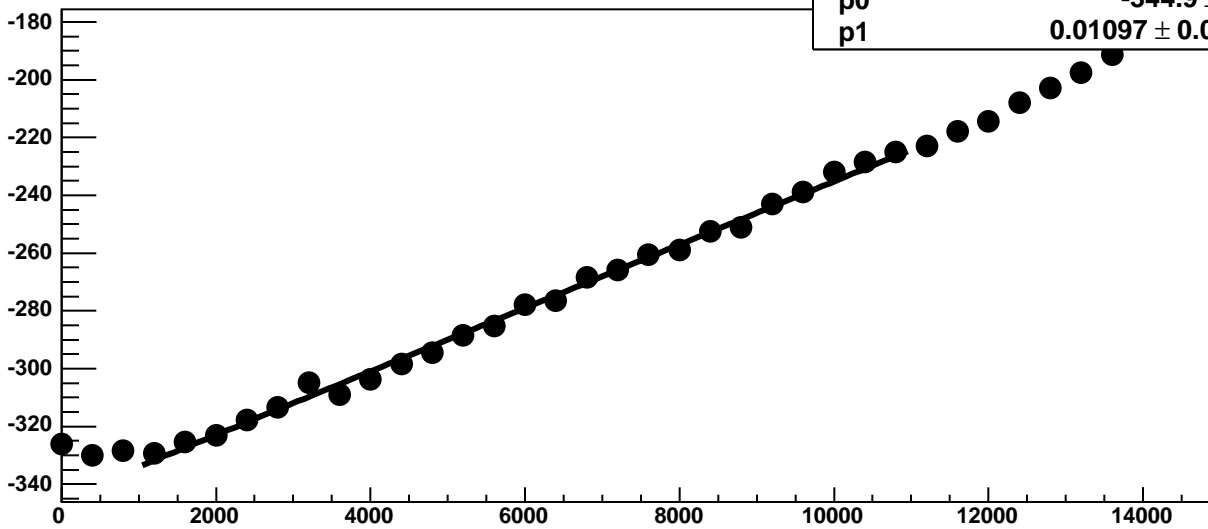
Chip 7, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

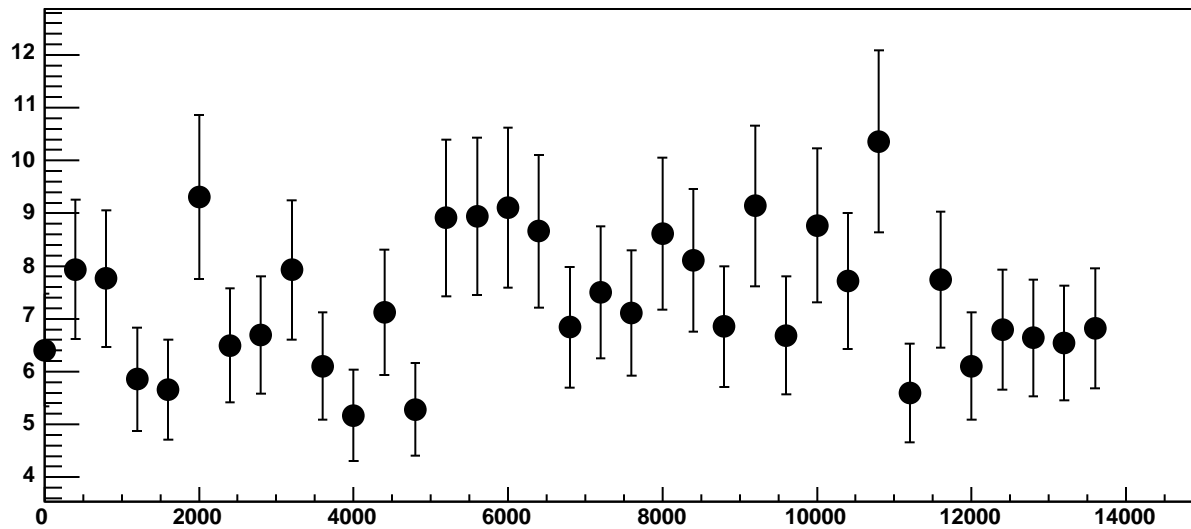


Chip 7, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

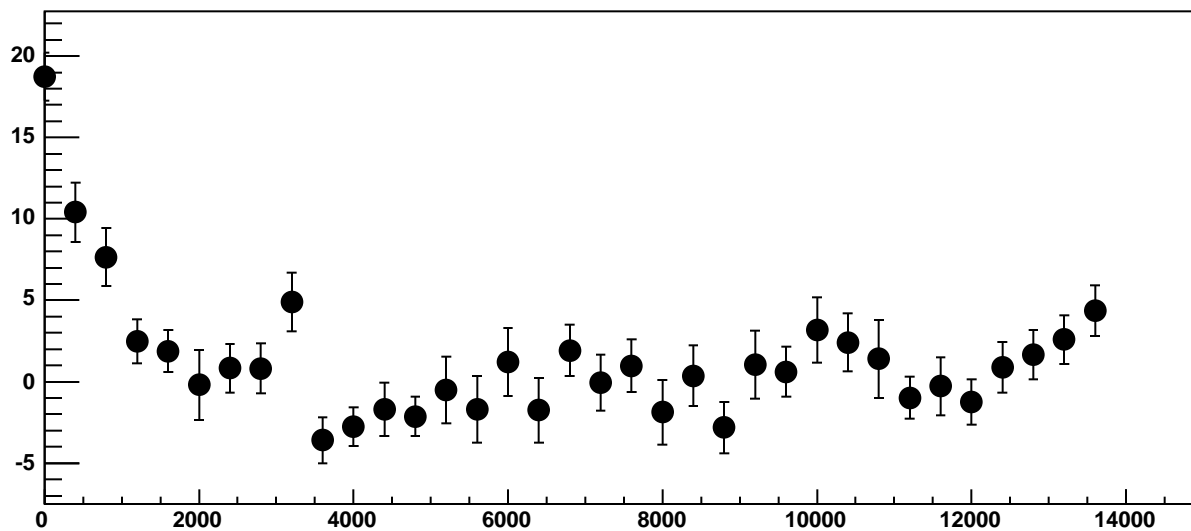


χ^2 / ndf 42.56 / 23
p0 -344.9 ± 0.7101
p1 0.01097 ± 0.0001154

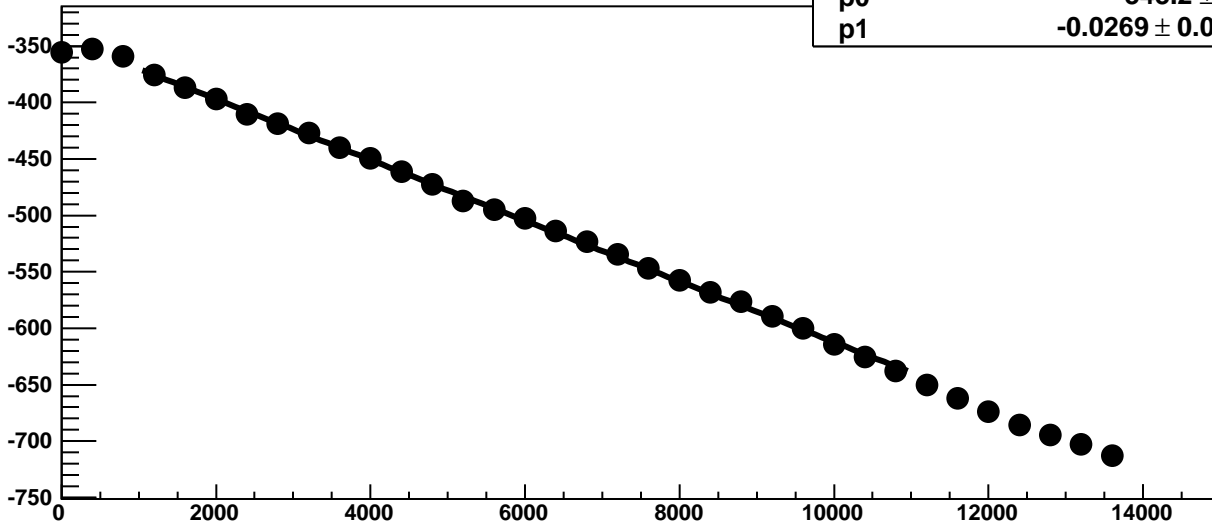
Chip 7, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

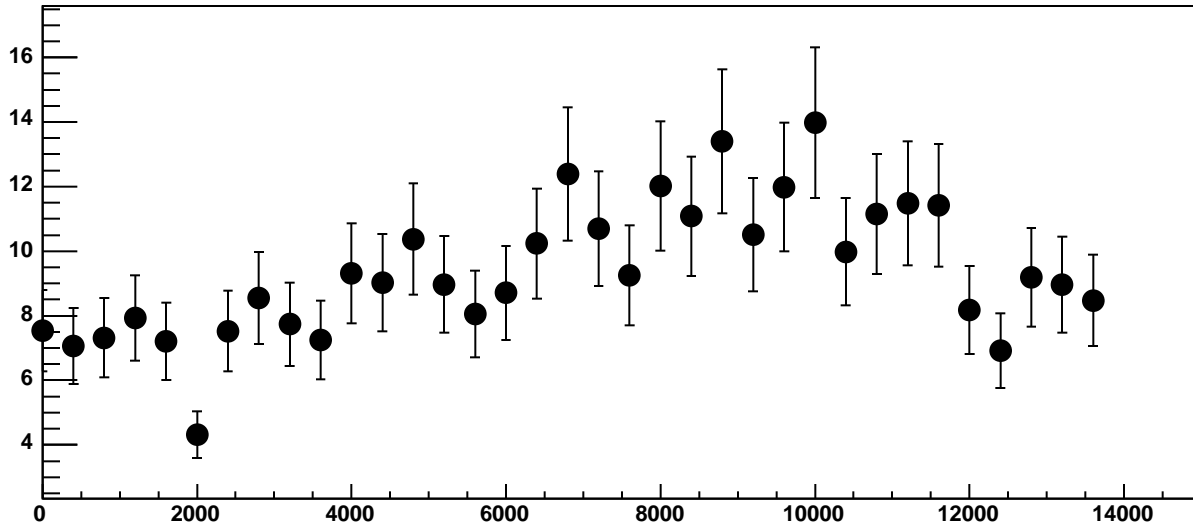


Chip 7, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

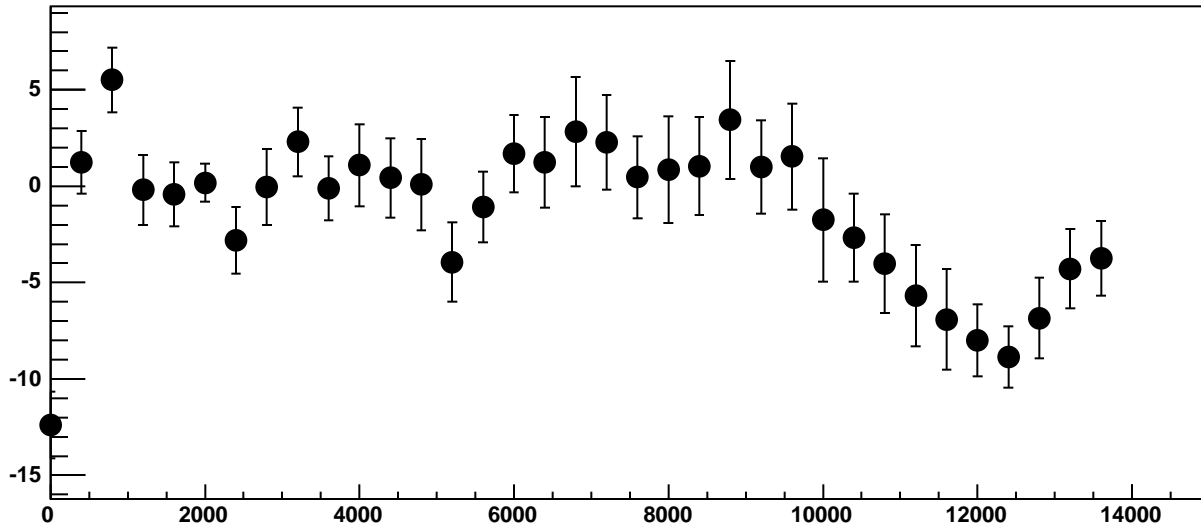


χ^2 / ndf	17.77 / 23
p0	-343.2 ± 0.7858
p1	-0.0269 ± 0.0001416

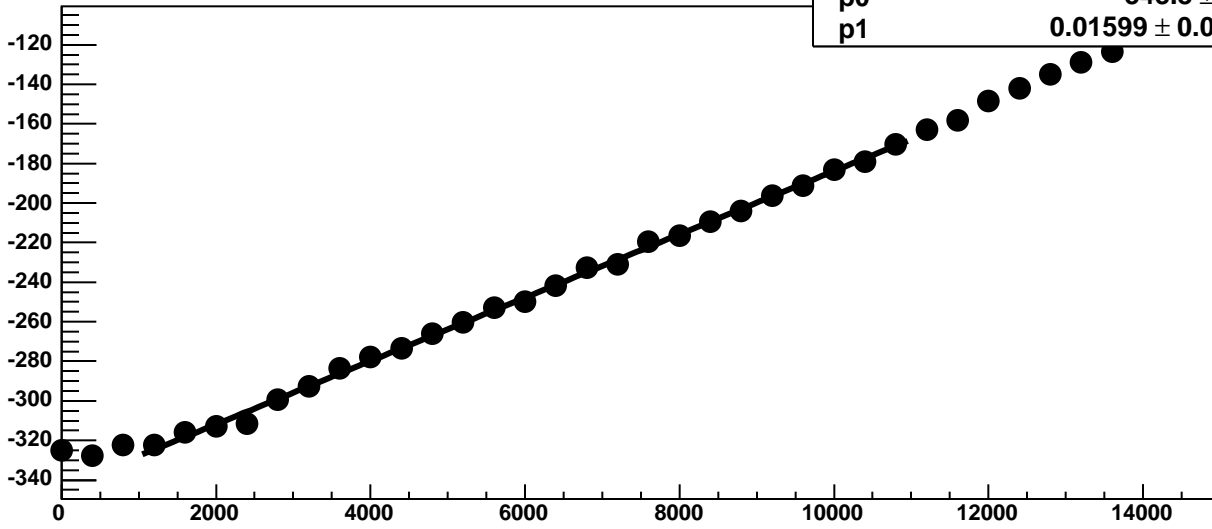
Chip 7, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



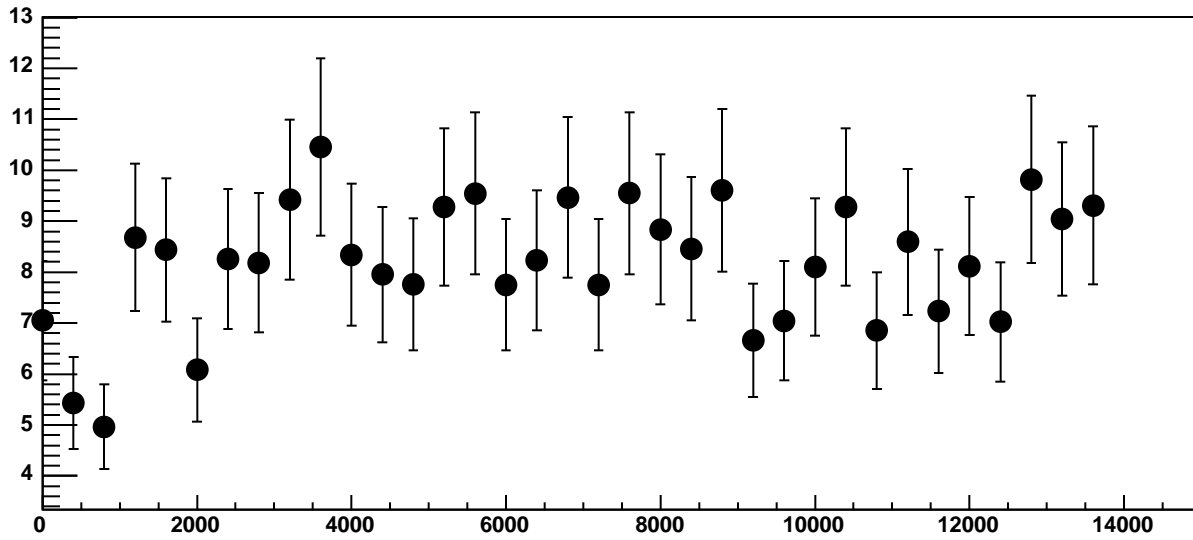
Chip 7, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC



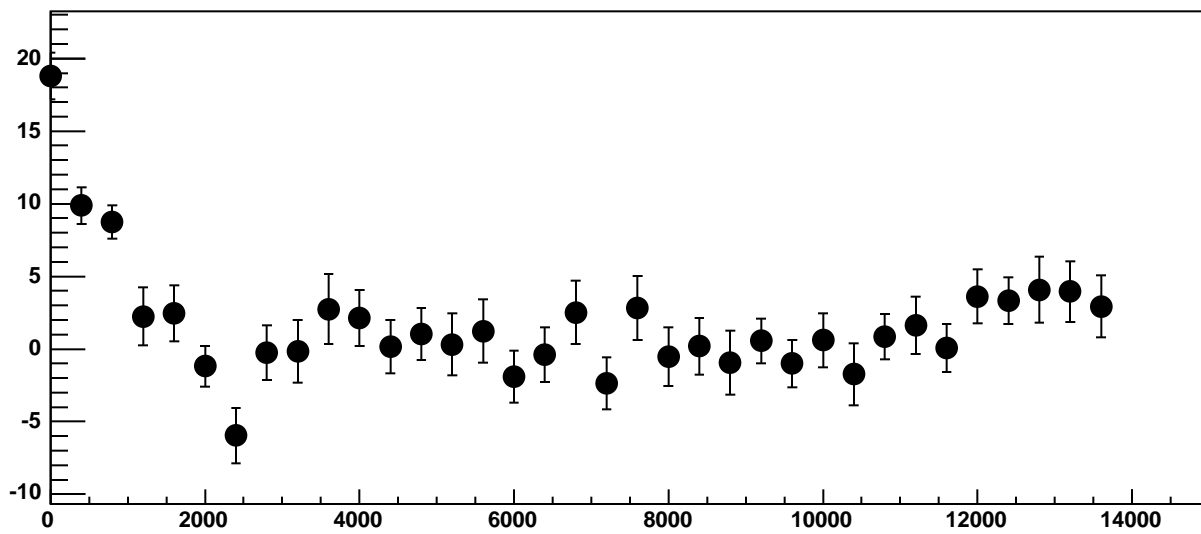
Chip 7, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



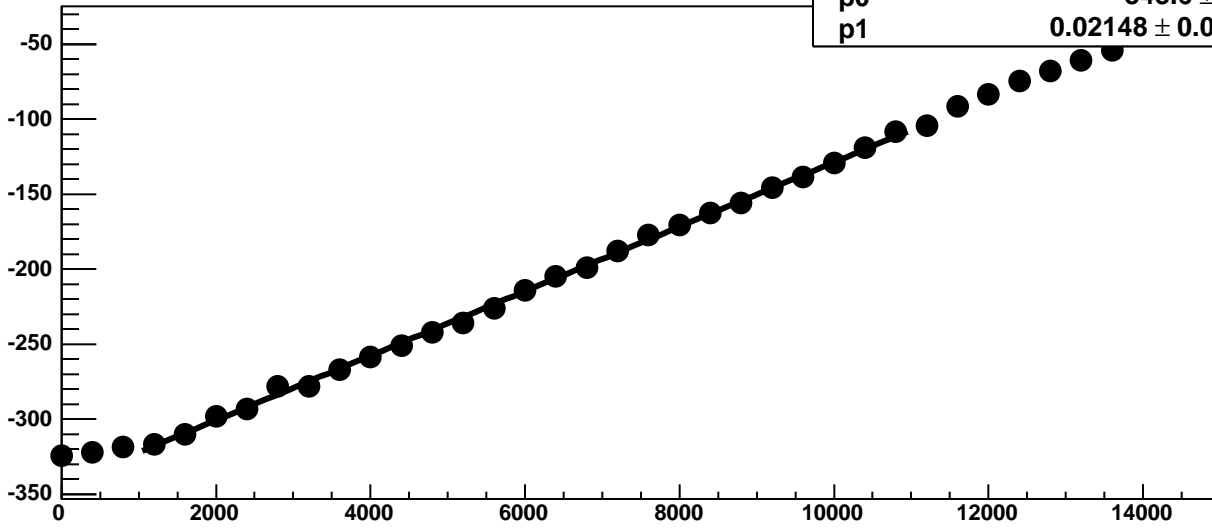
Chip 7, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

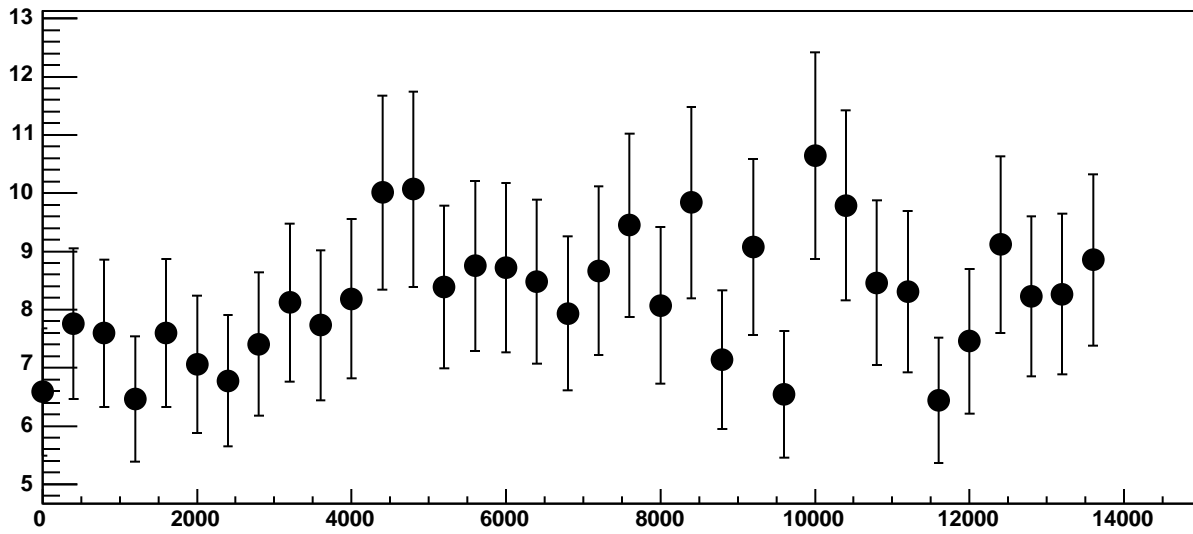


Chip 7, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

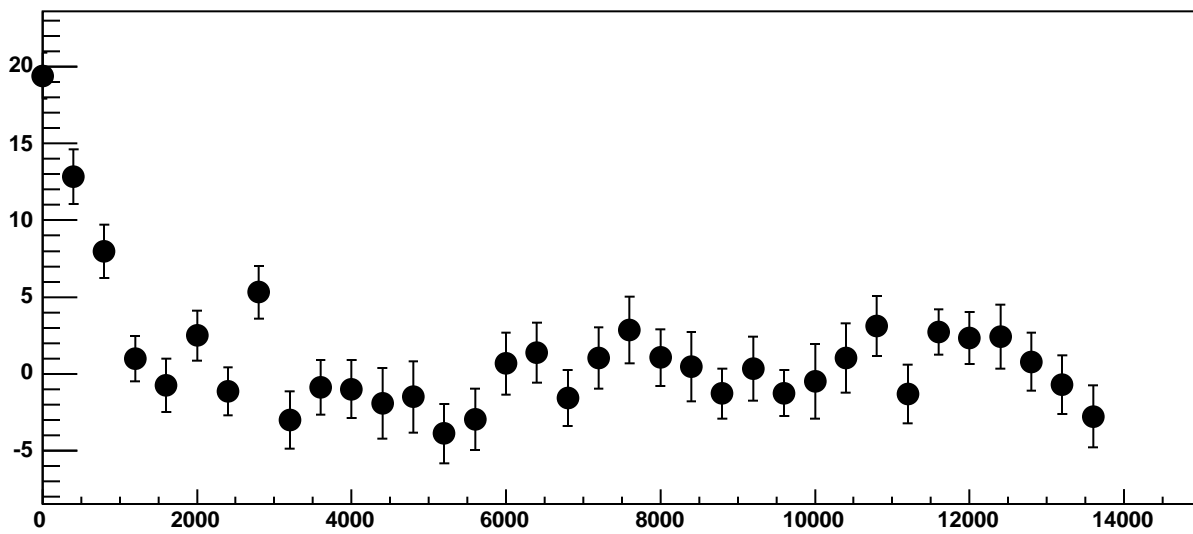


χ^2 / ndf 31.77 / 23
p0 -343.6 ± 0.7977
p1 0.02148 ± 0.0001247

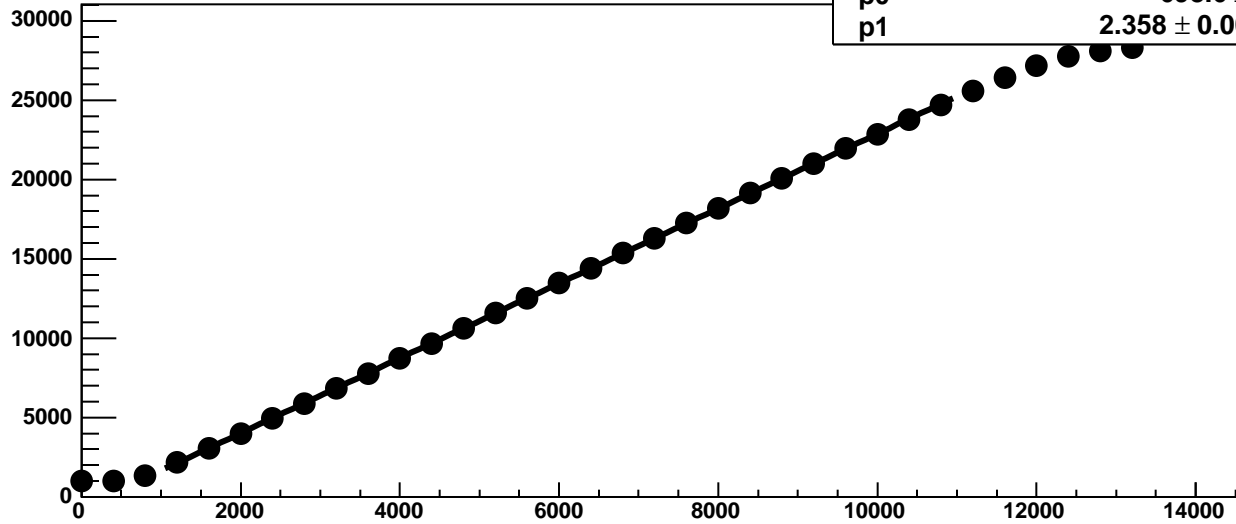
Chip 7, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 7, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1995 / 23

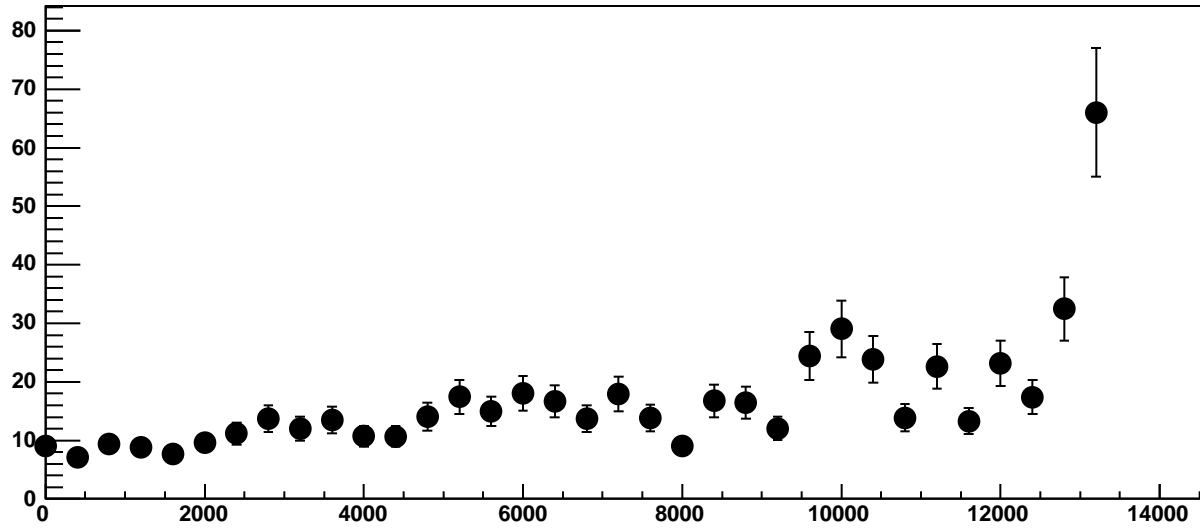
p0

-698.6 ± 1.143

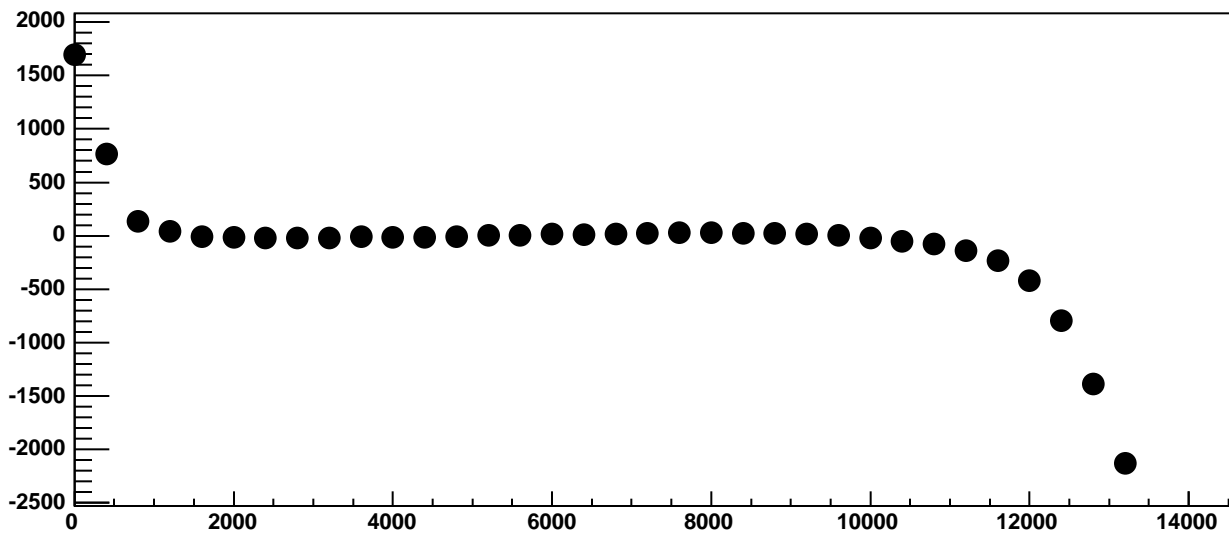
p1

2.358 ± 0.0002014

Chip 7, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC

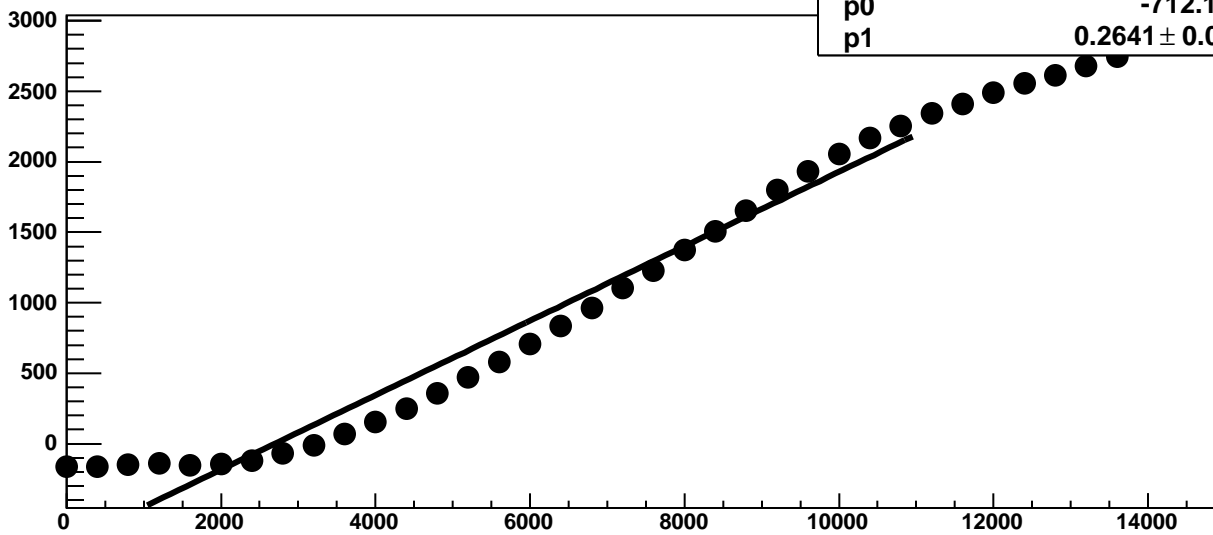


Chip 7, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC

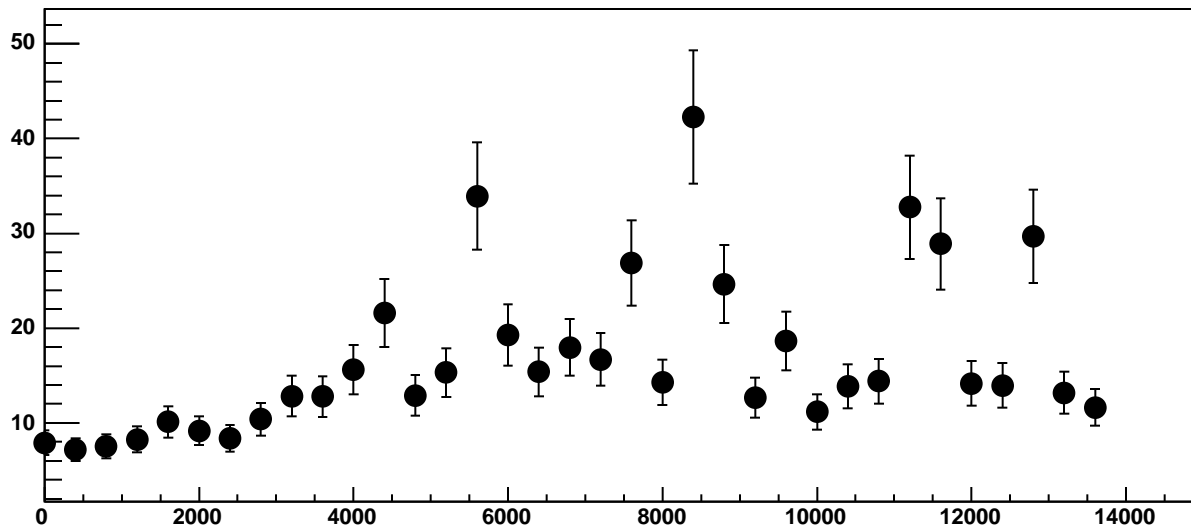


Chip 7, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

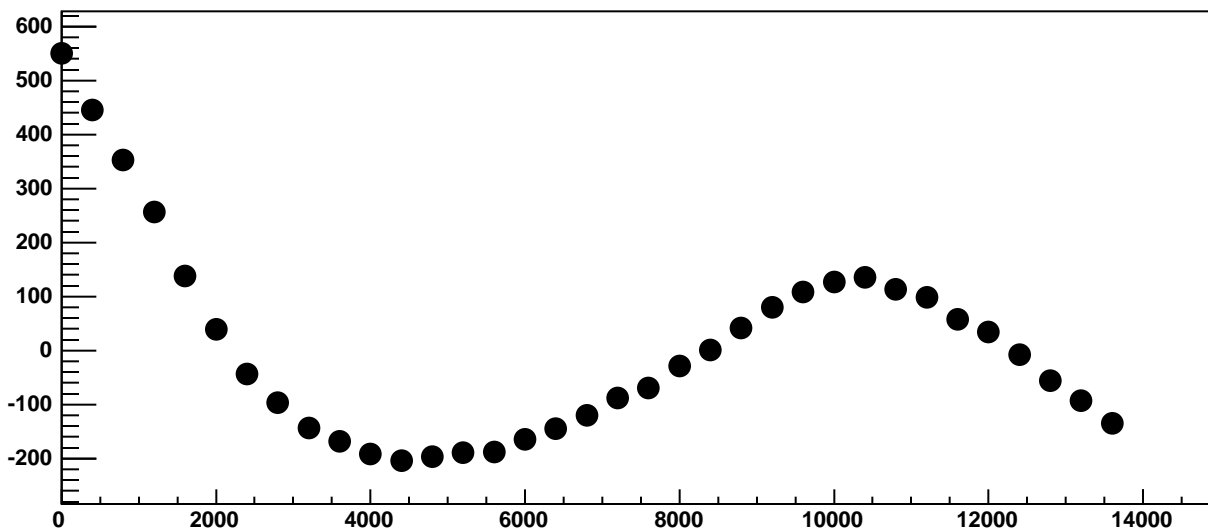
χ^2 / ndf 5.408e+04 / 23
p0 -712.1 ± 1.117
p1 0.2641 ± 0.0001923



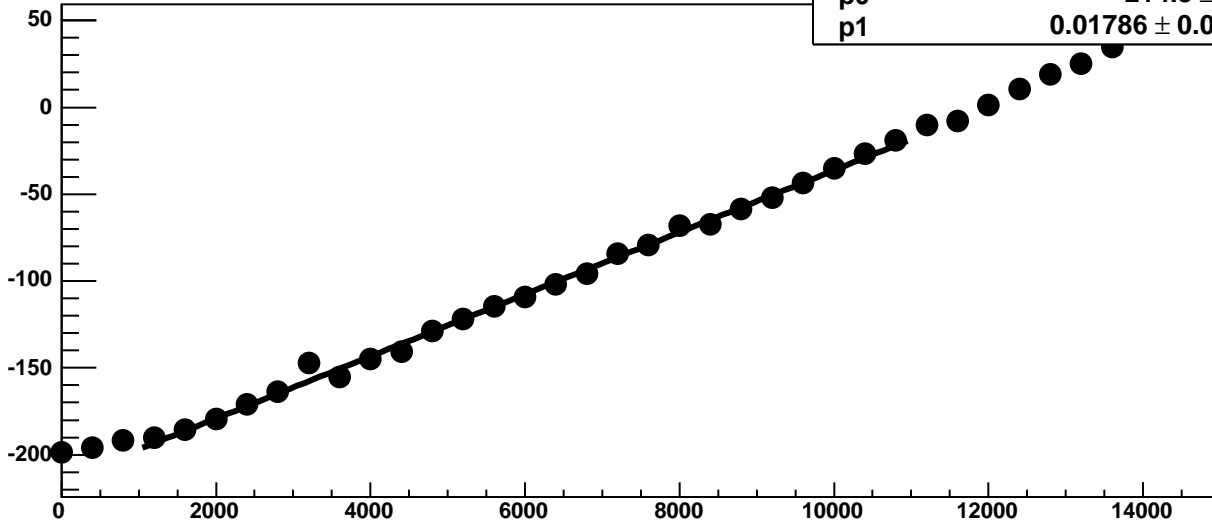
Chip 7, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



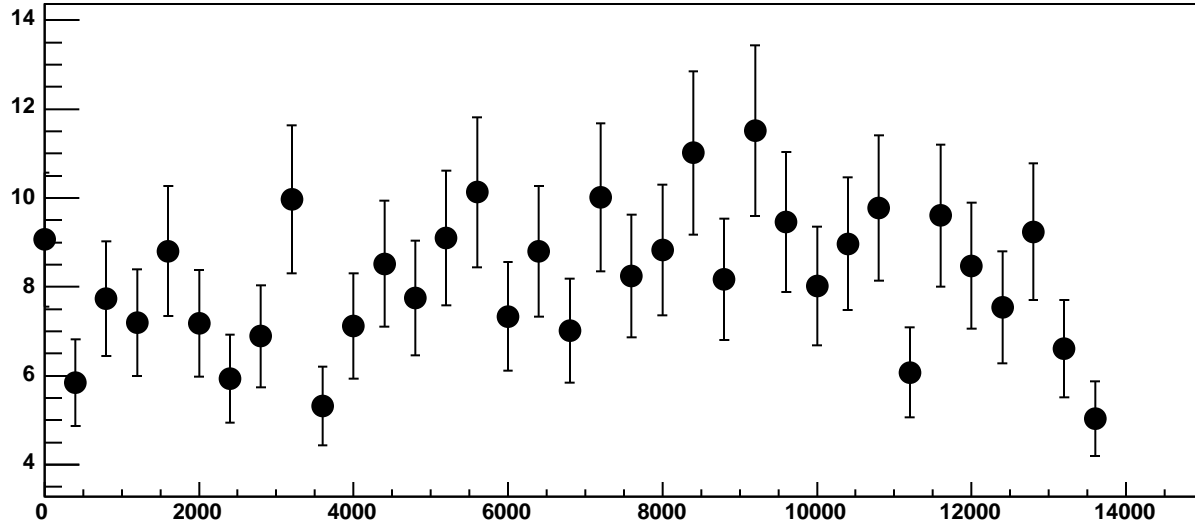
Chip 7, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



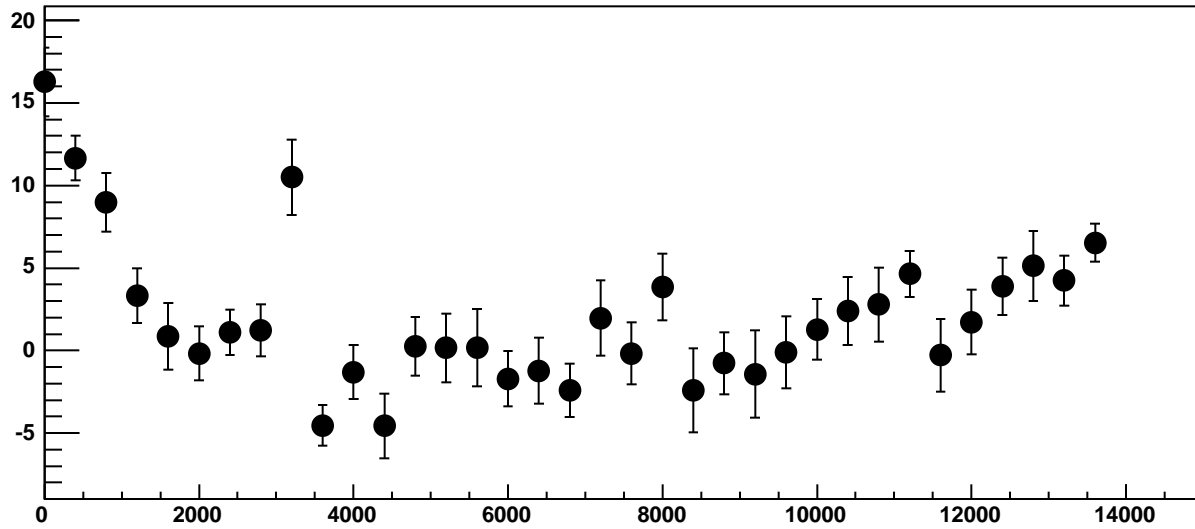
Chip 7, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



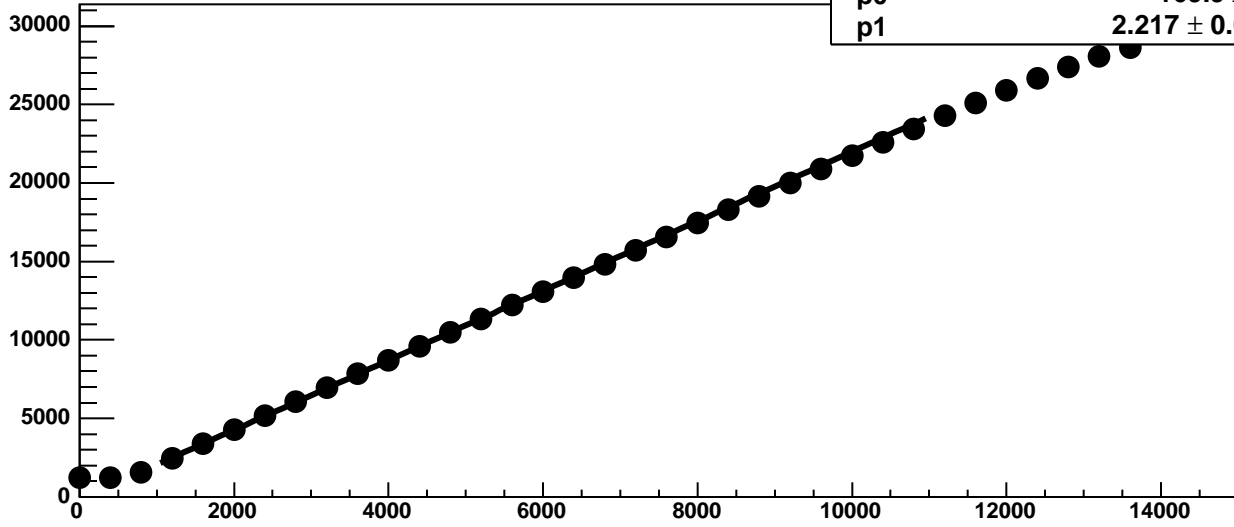
Chip 7, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

495.8 / 23

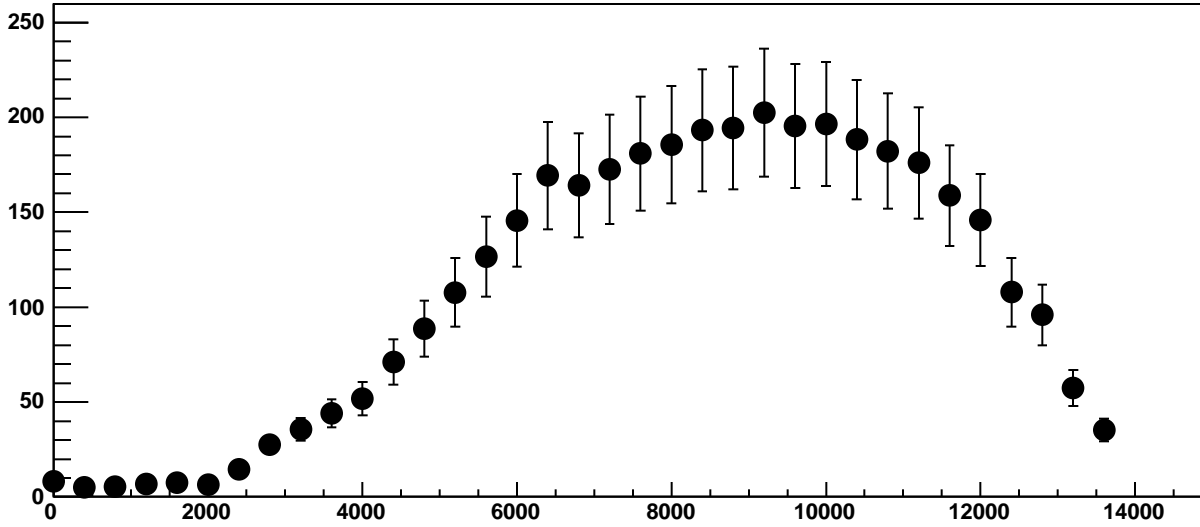
p0

-169.9 ± 2.264

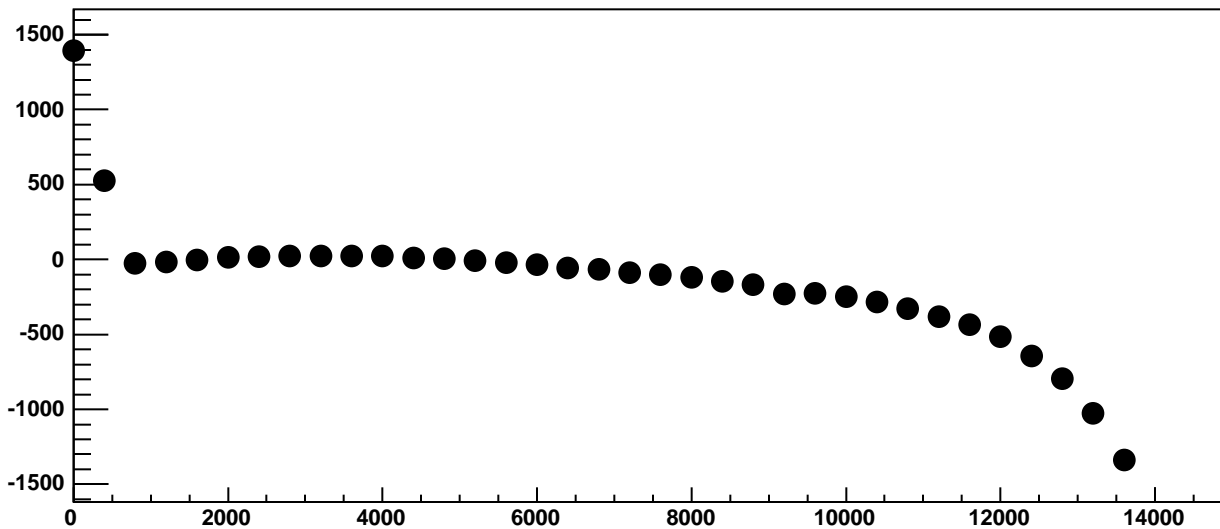
p1

2.217 ± 0.001165

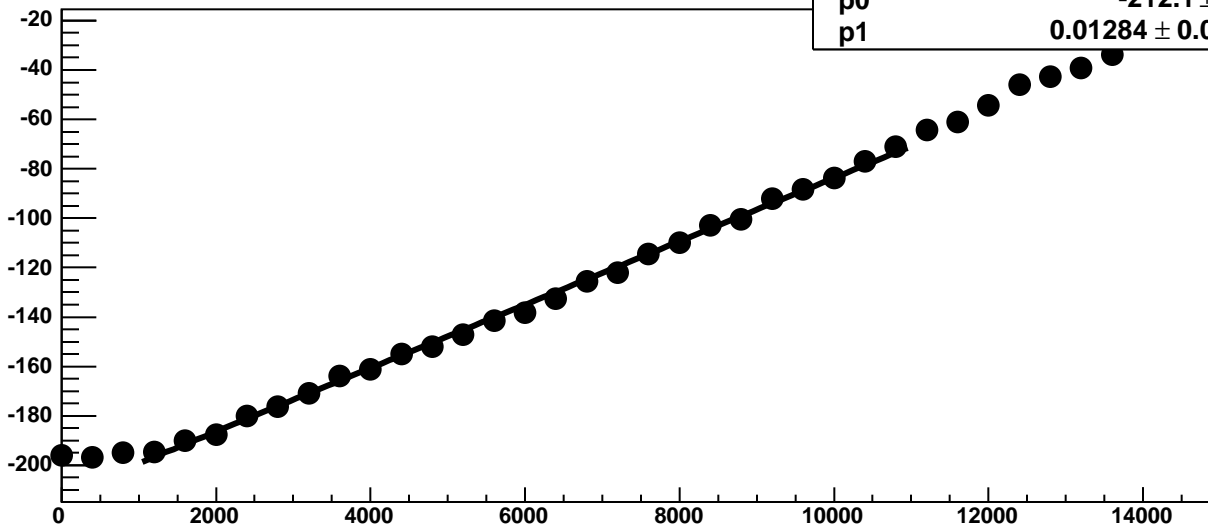
Chip 7, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC



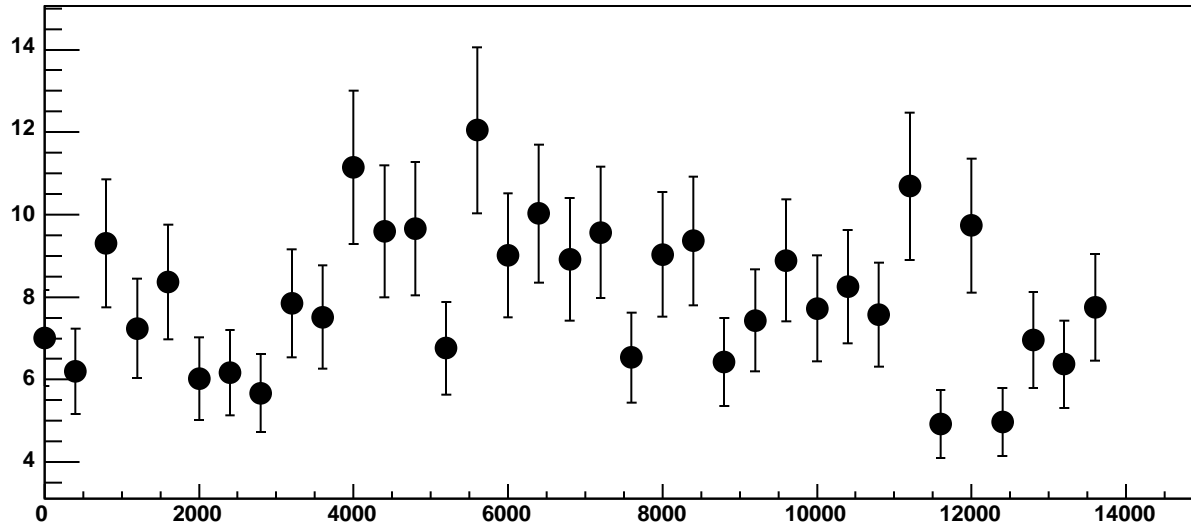
Chip 7, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC



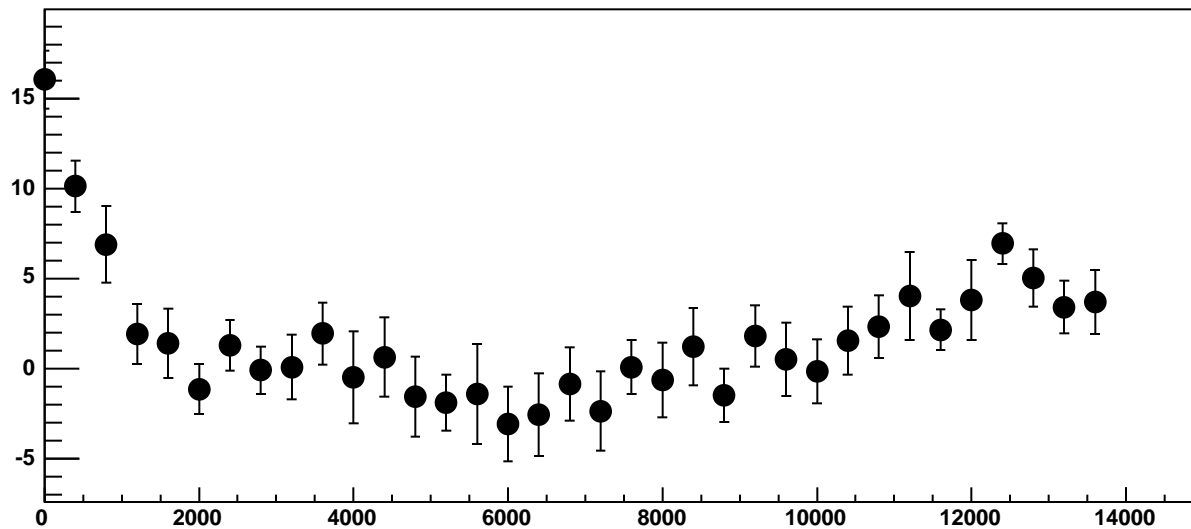
Chip 7, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



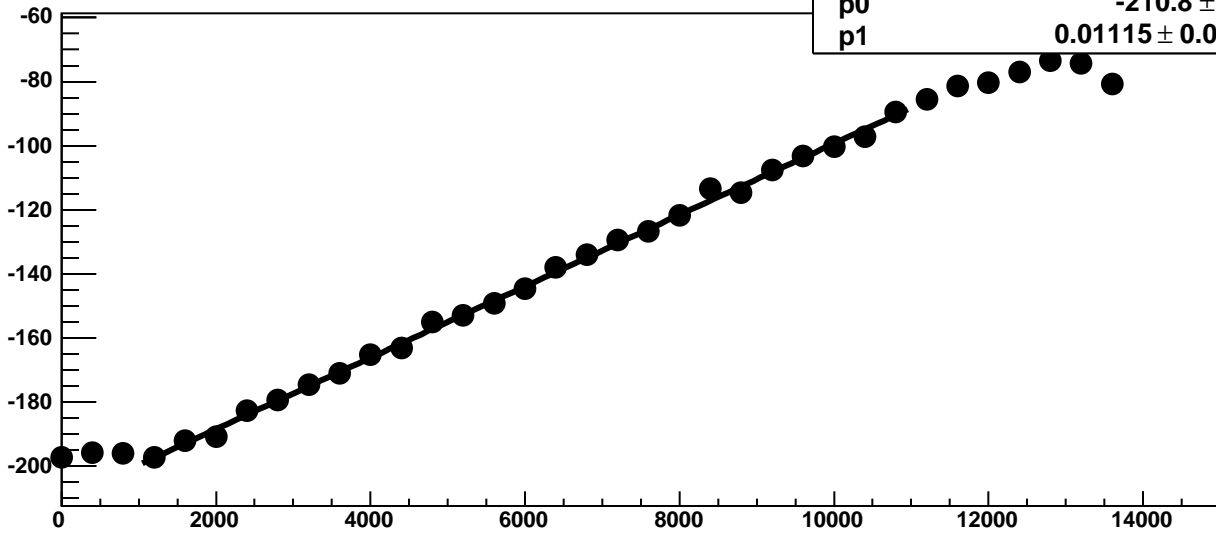
Chip 7, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

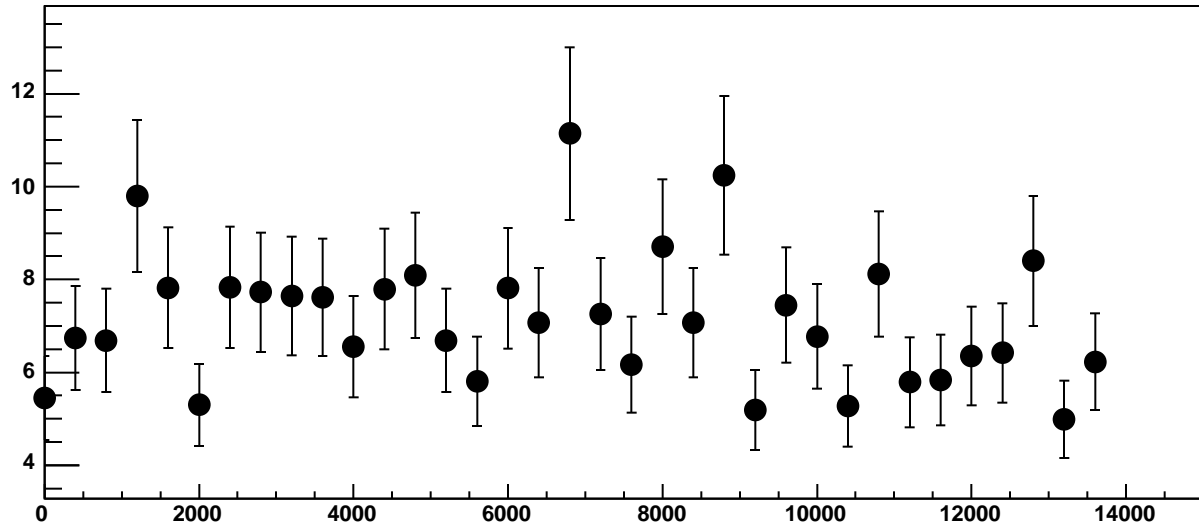


Chip 7, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

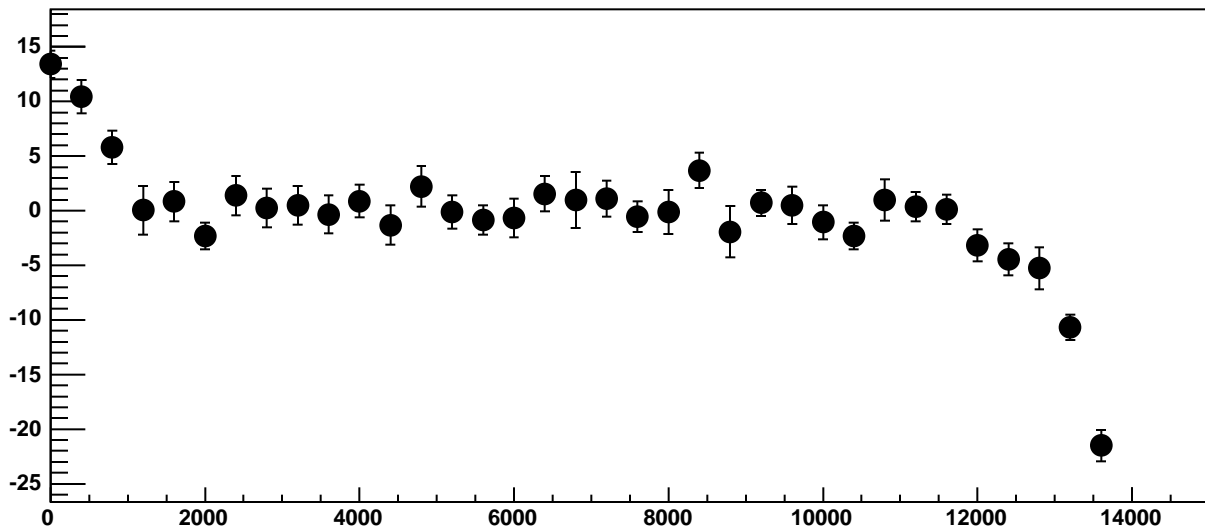


χ^2 / ndf 19.69 / 23
p0 -210.8 ± 0.7616
p1 0.01115 ± 0.0001112

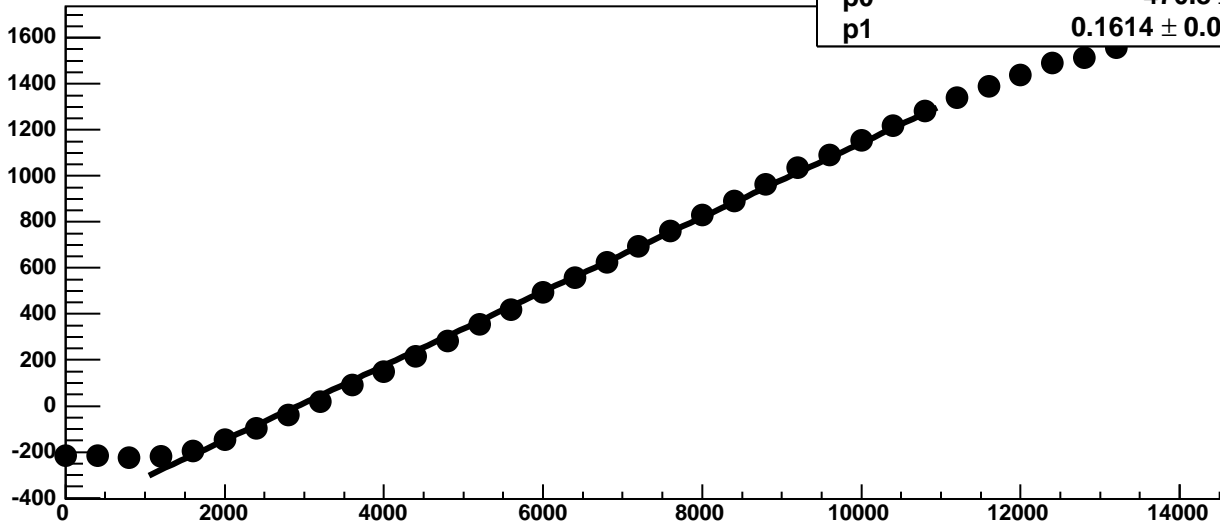
Chip 7, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



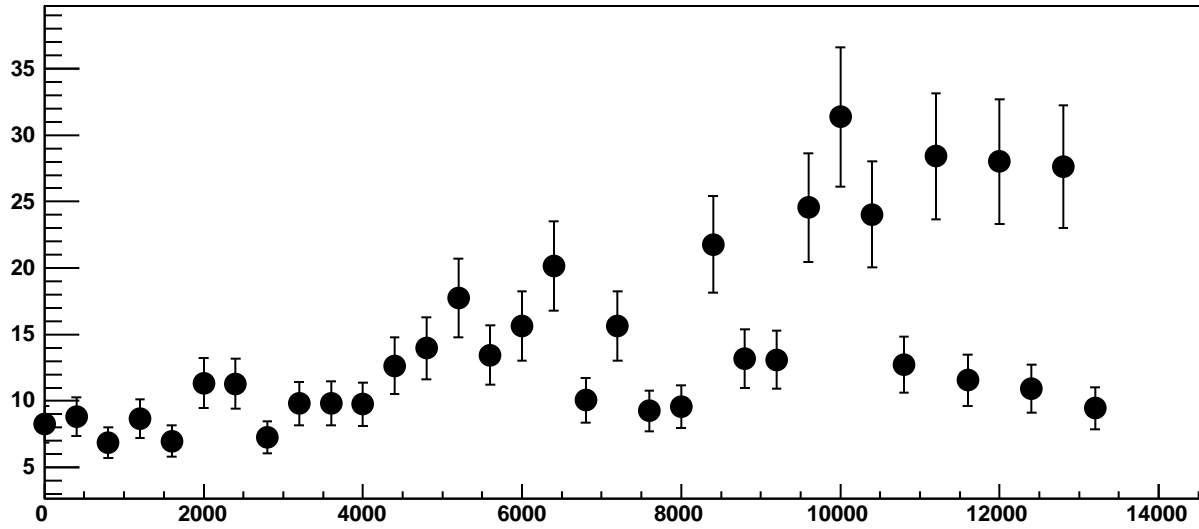
Chip 7, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



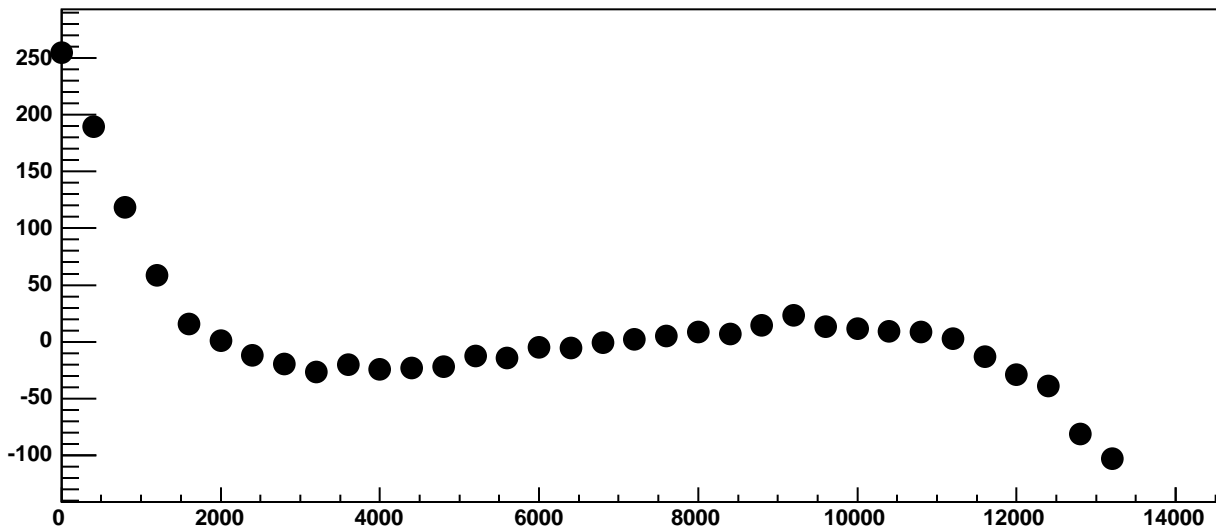
Chip 7, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC



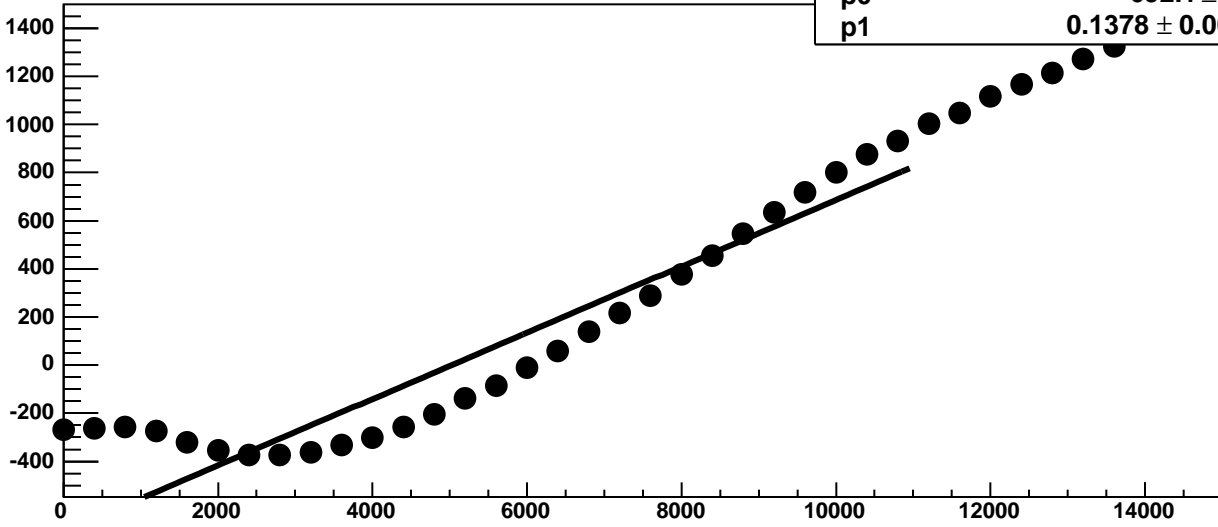
Chip 7, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



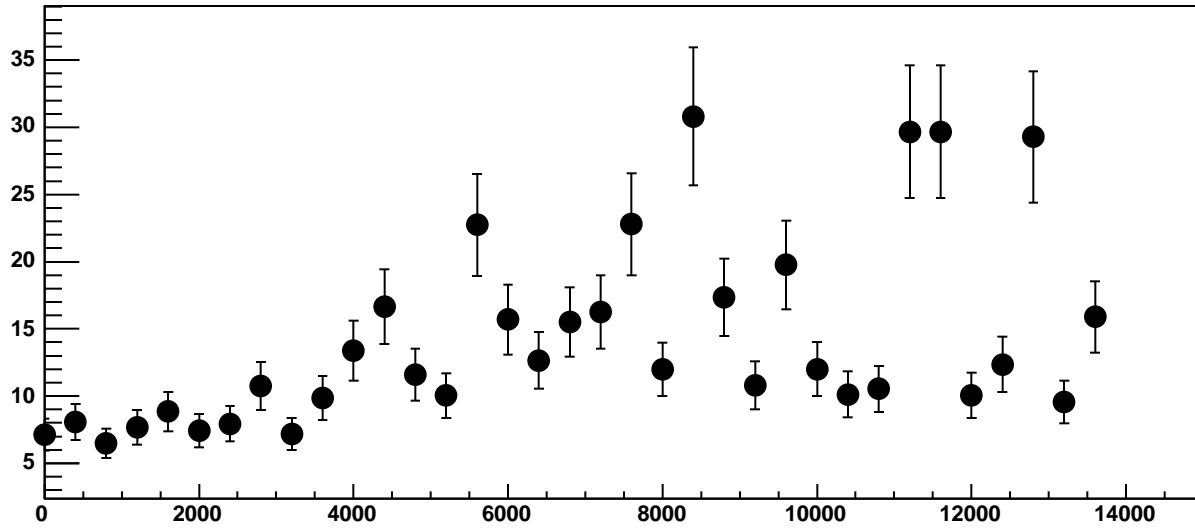
Chip 7, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC



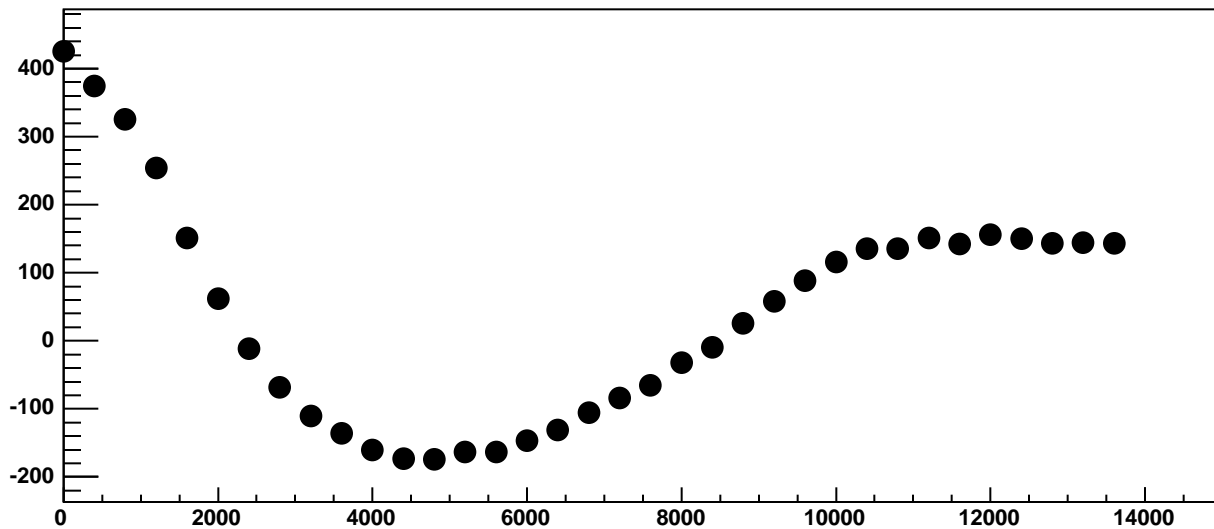
Chip 7, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



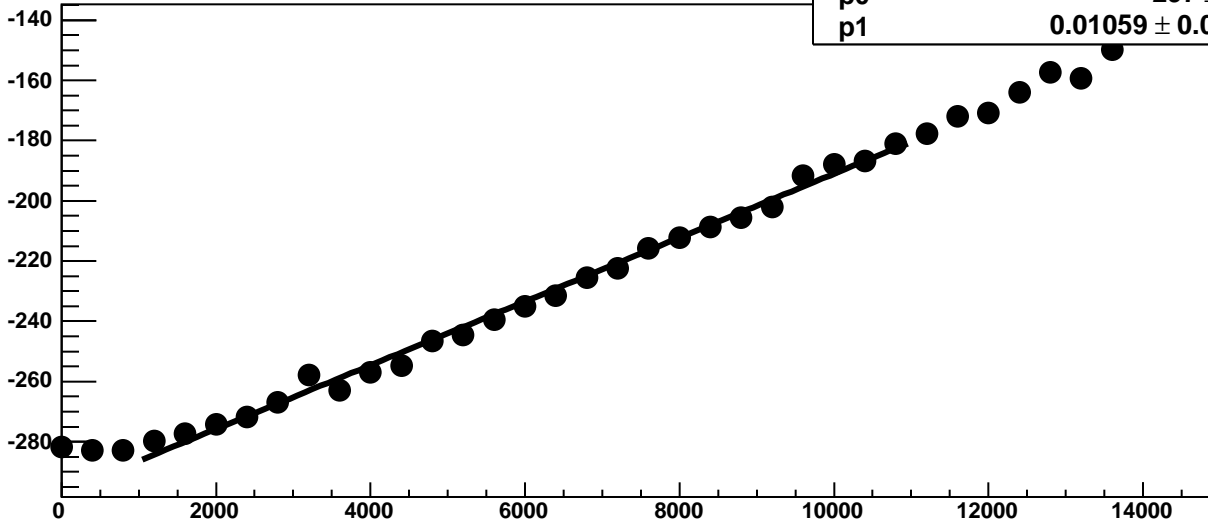
Chip 7, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

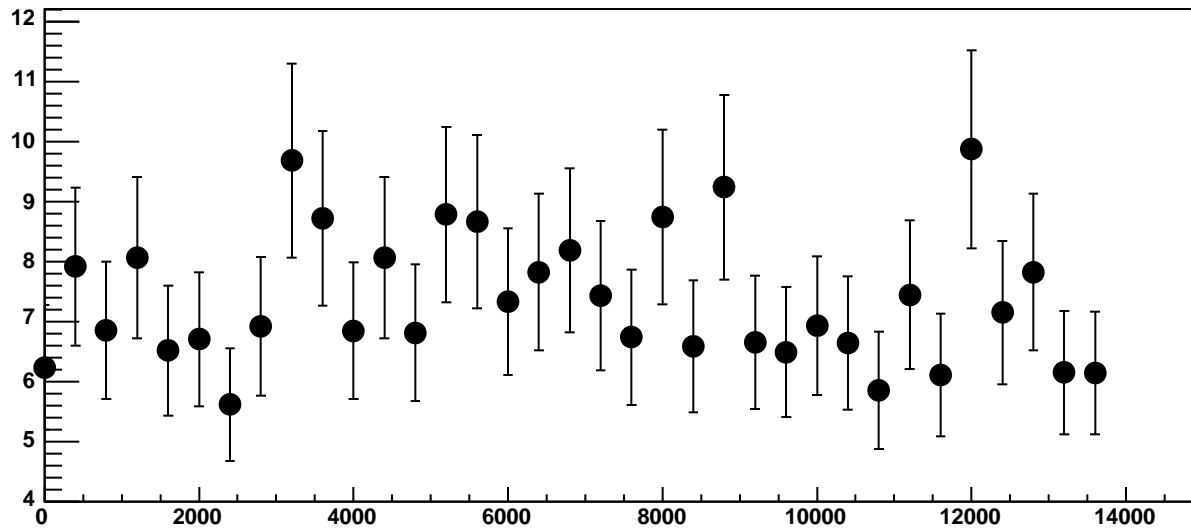


Chip 7, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

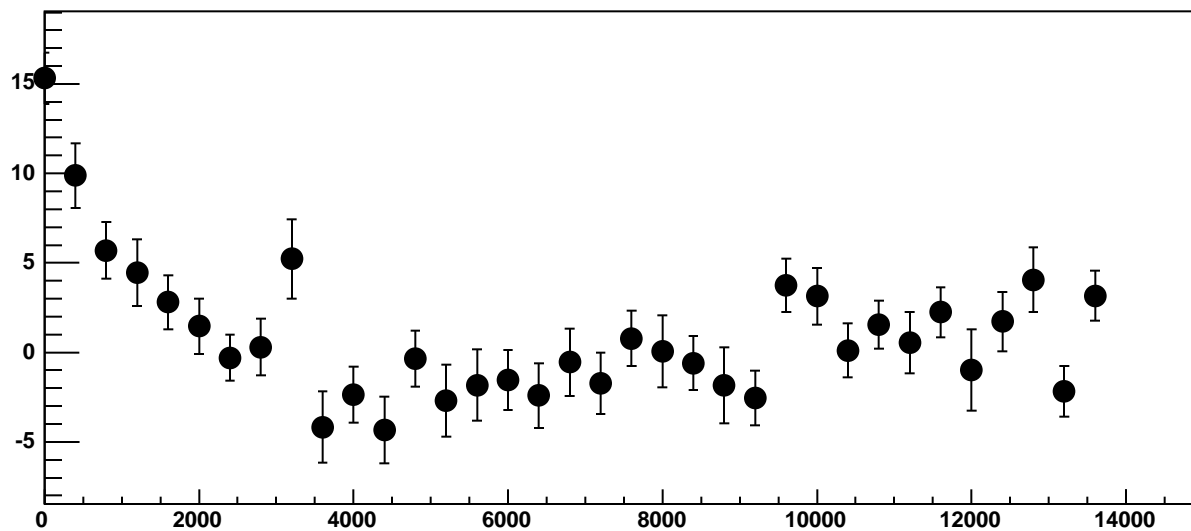


χ^2 / ndf 49.86 / 23
p0 -297 ± 0.7421
p1 0.01059 ± 0.0001086

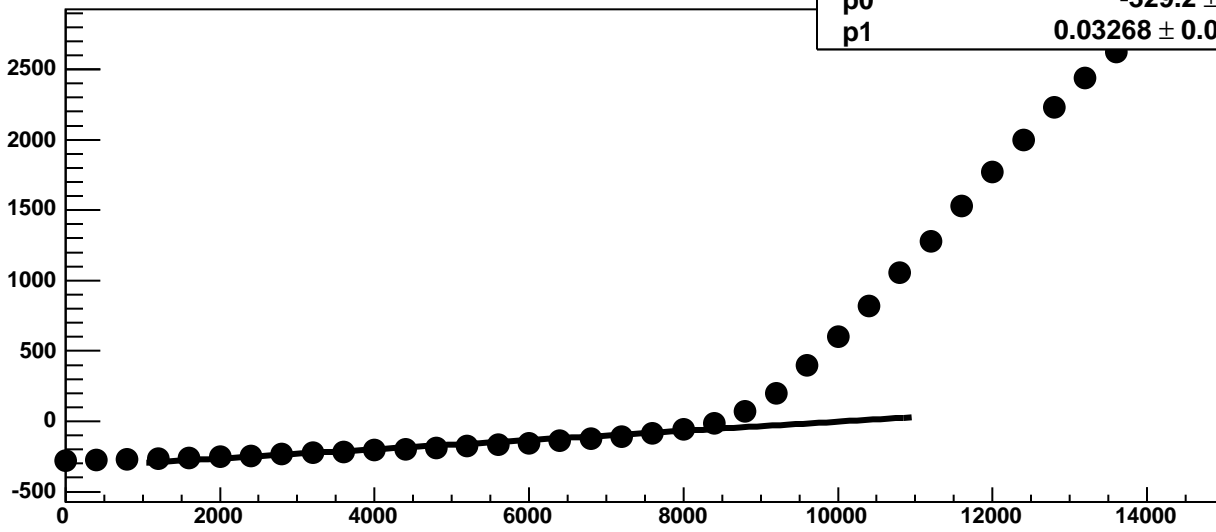
Chip 7, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



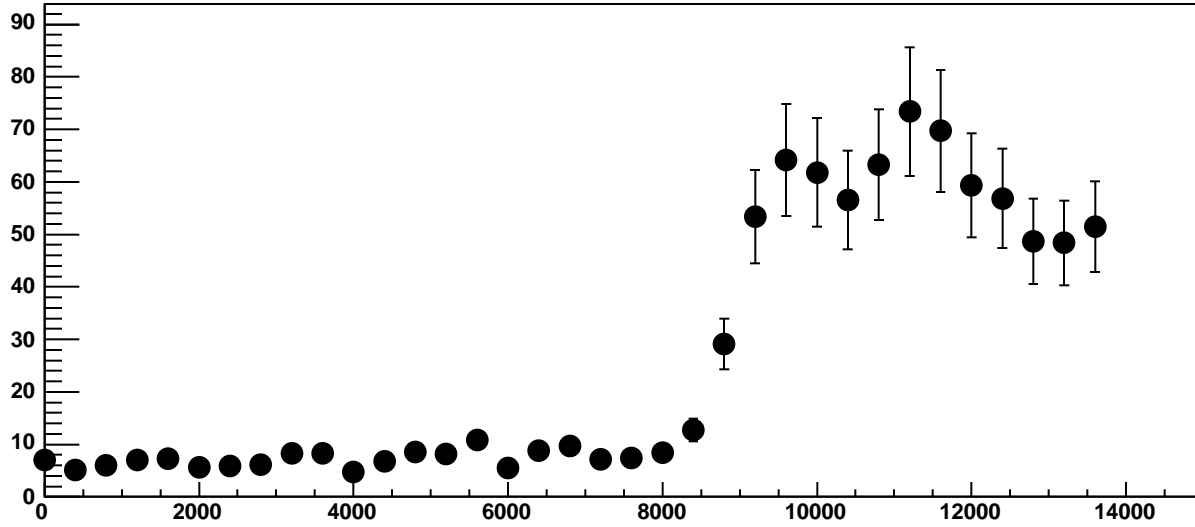
Chip 7, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



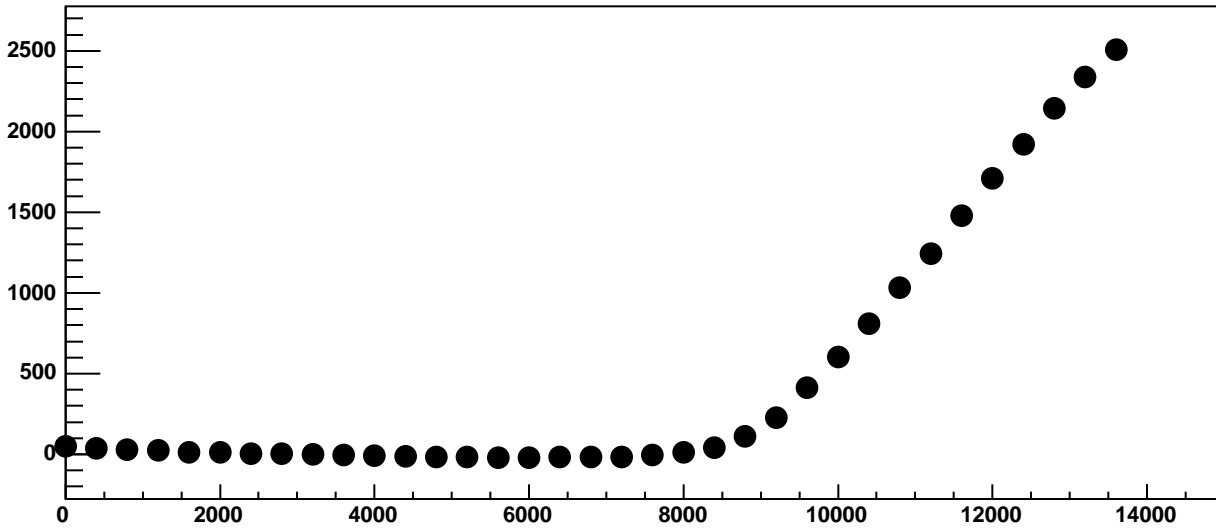
Chip 7, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC



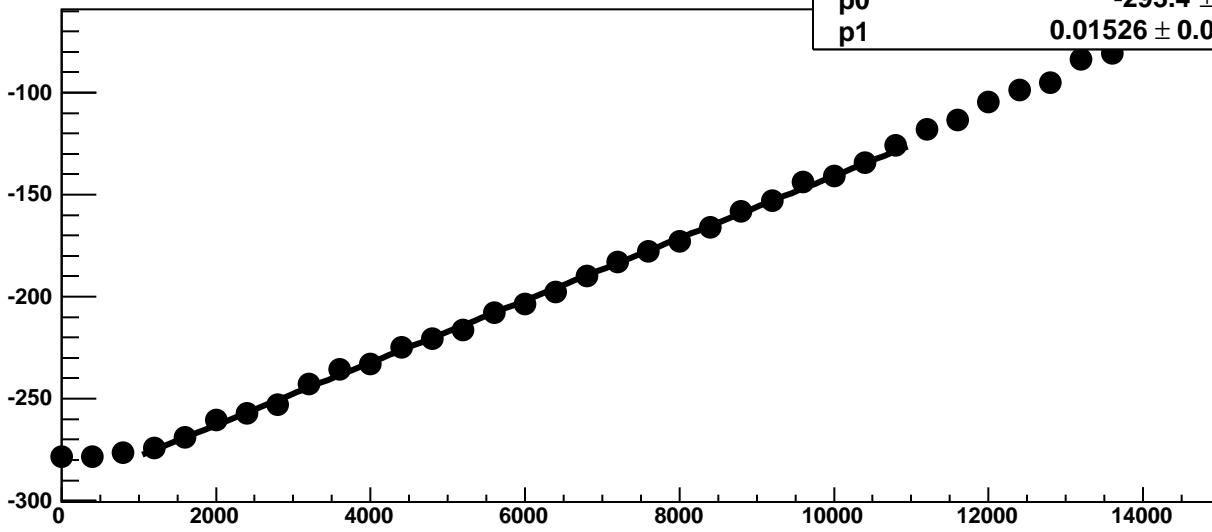
Chip 7, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC

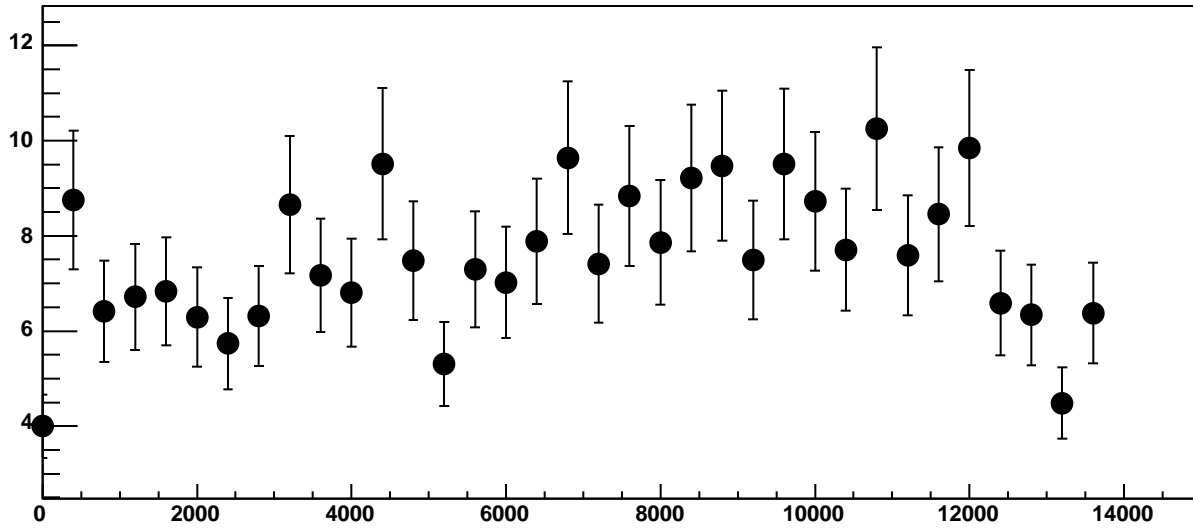


Chip 7, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

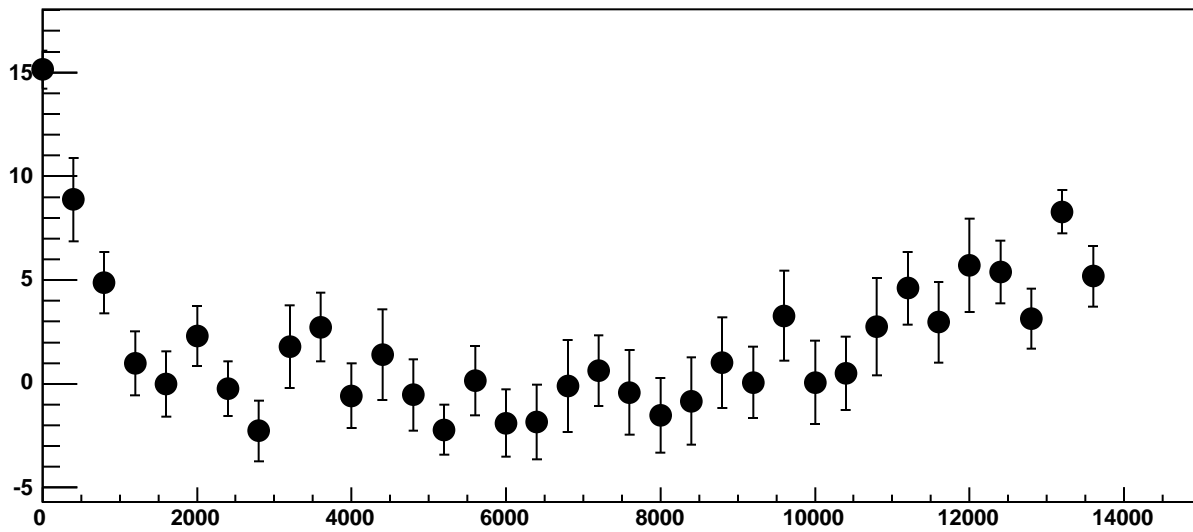


χ^2 / ndf 20.35 / 23
p0 -293.4 ± 0.7389
p1 0.01526 ± 0.0001215

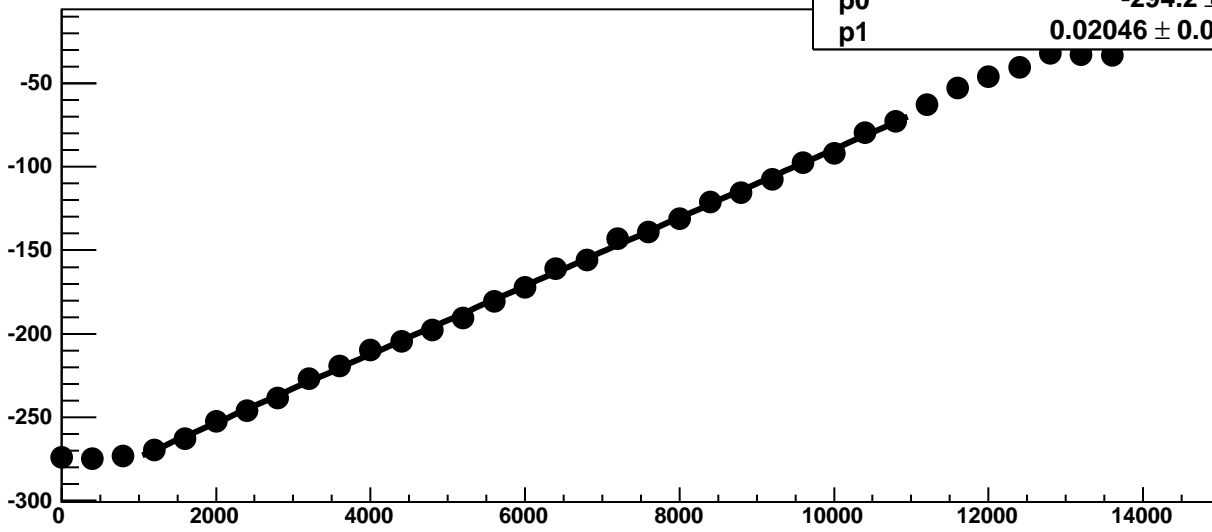
Chip 7, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



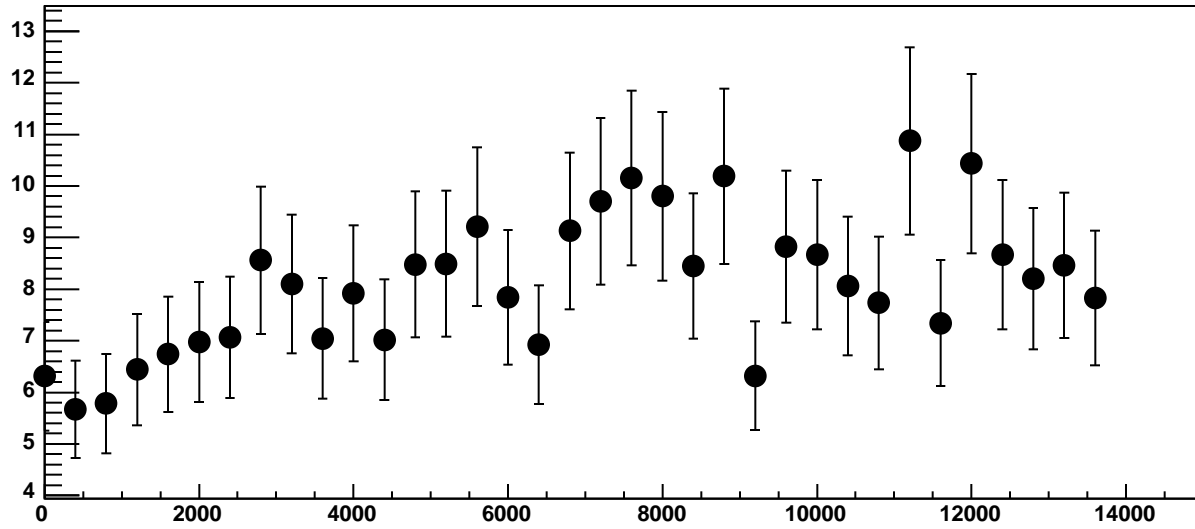
Chip 7, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



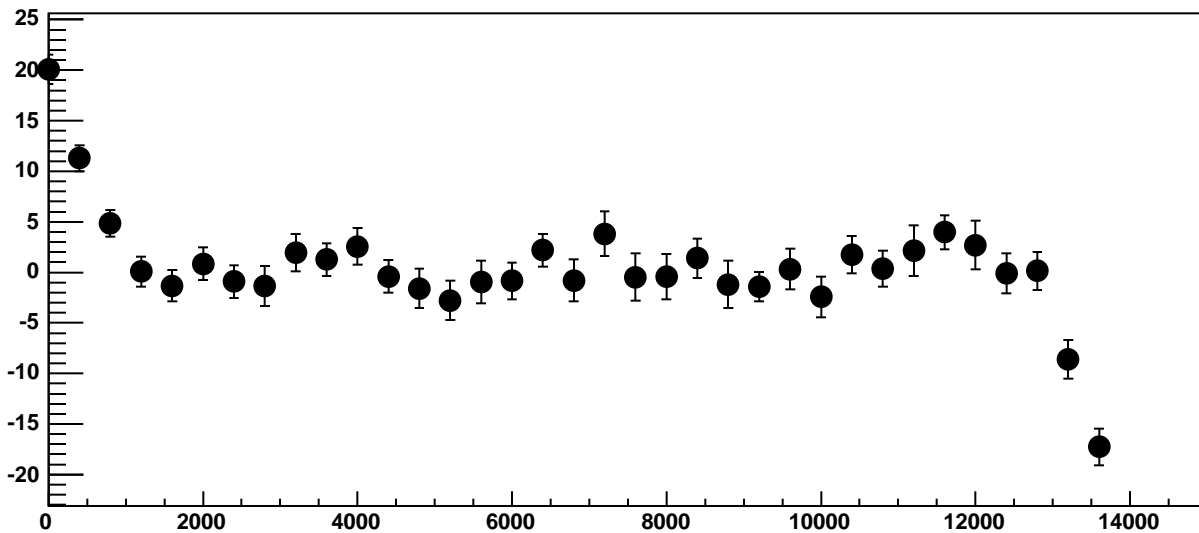
Chip 7, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



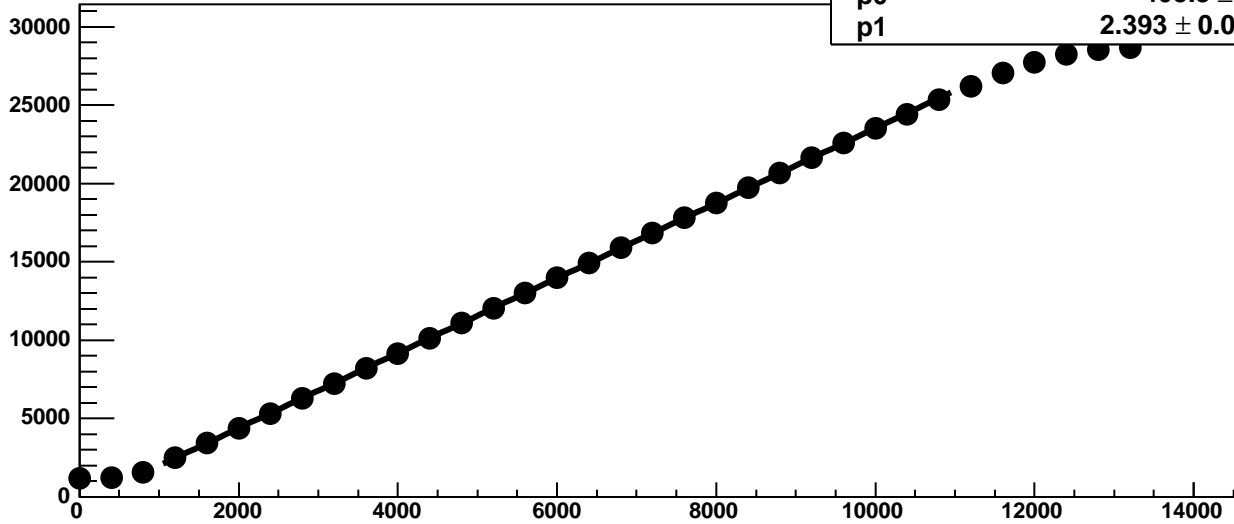
Chip 7, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



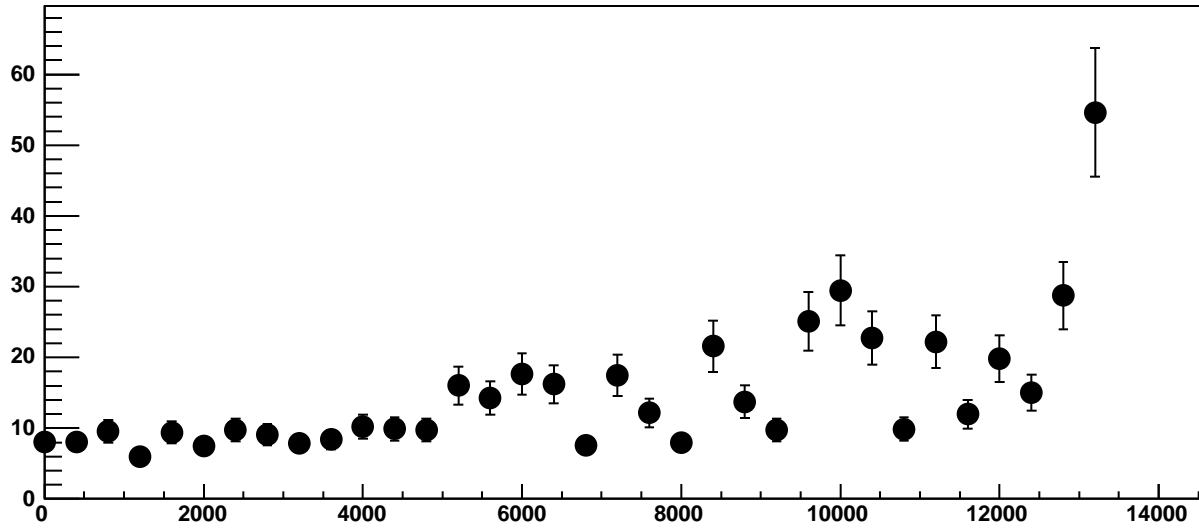
Chip 7, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



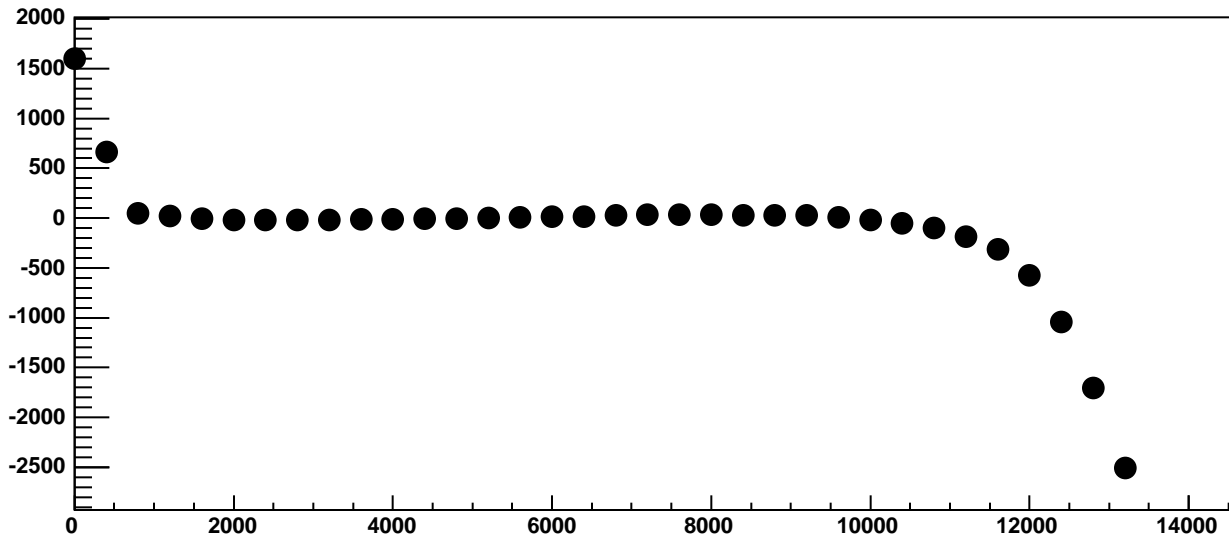
Chip 7, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC



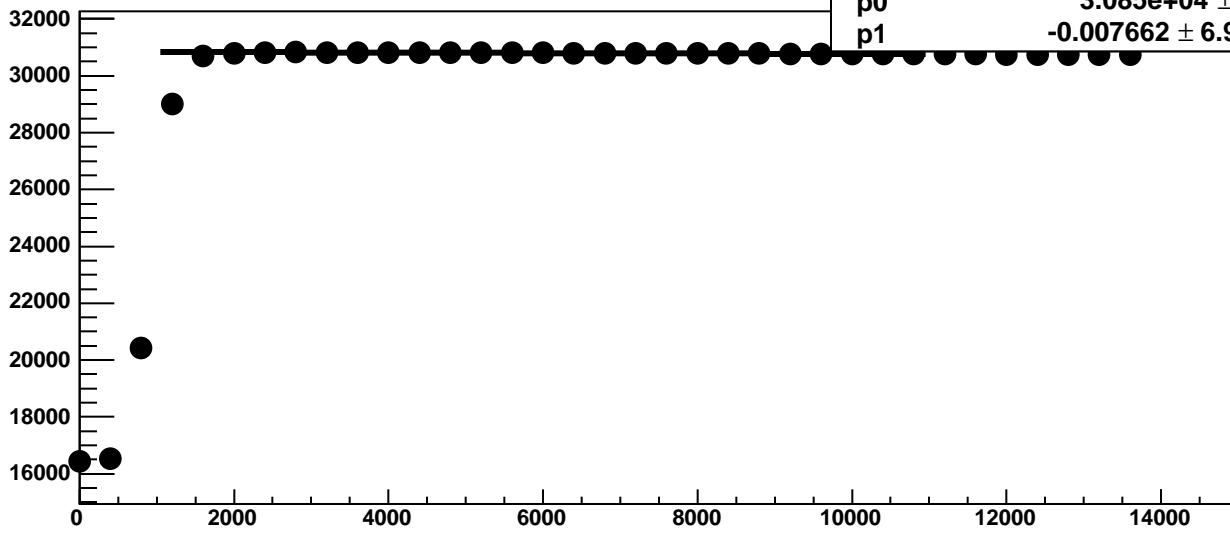
Chip 7, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC



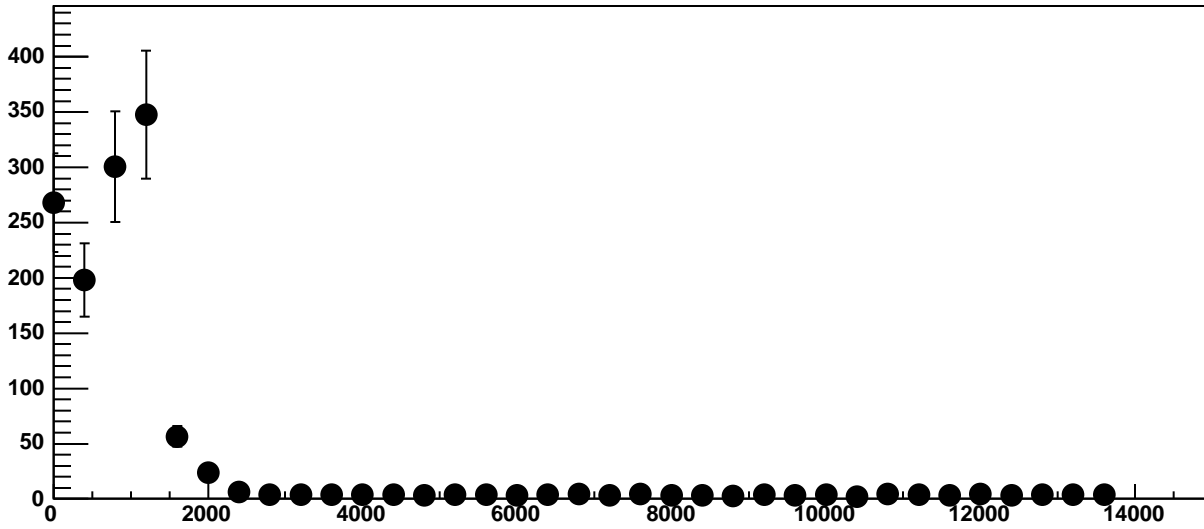
Chip 7, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC



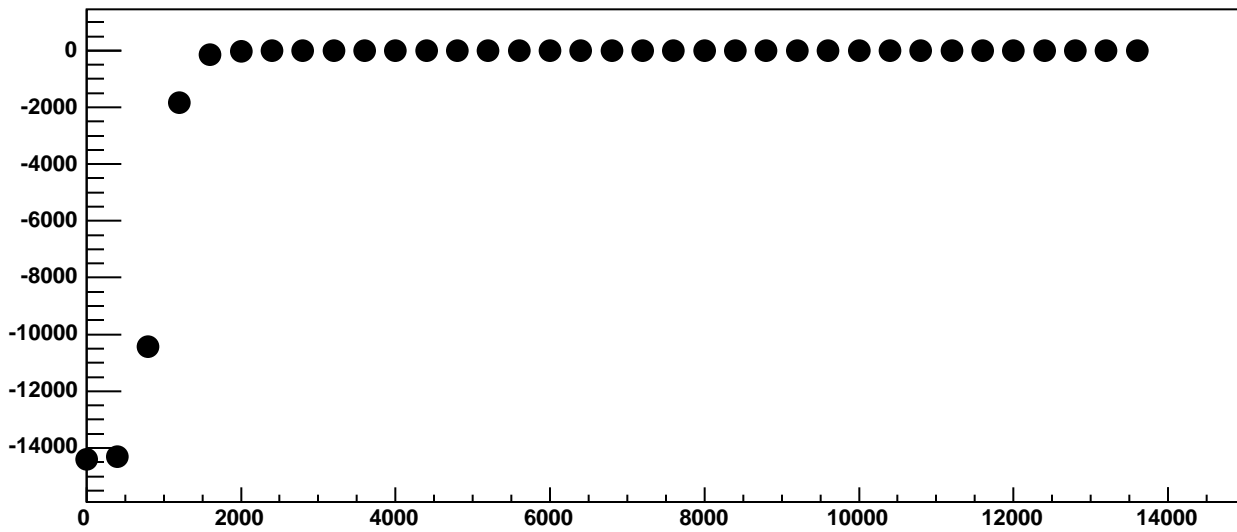
Chip 8, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



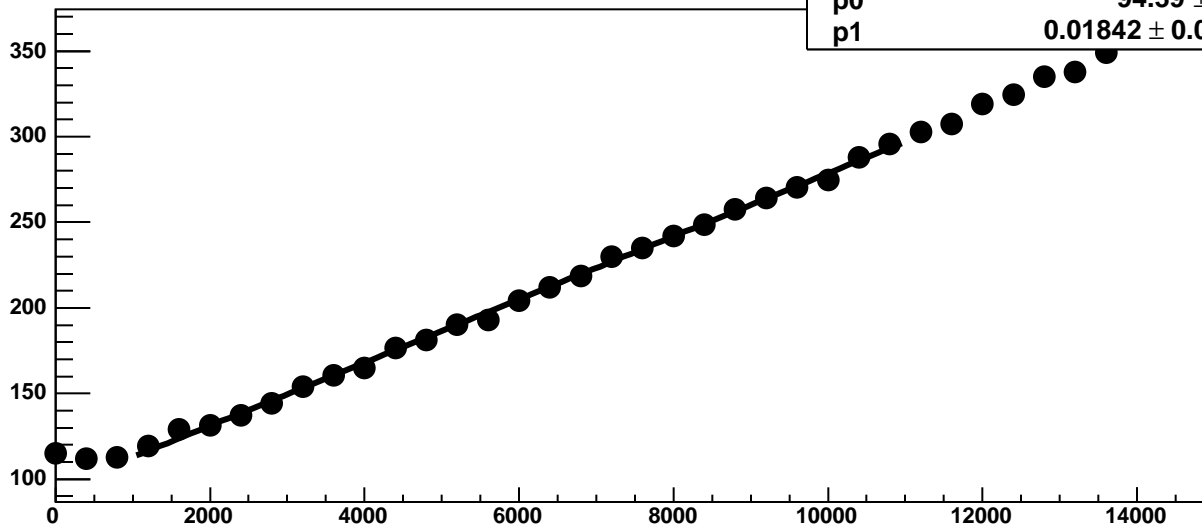
Chip 8, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC

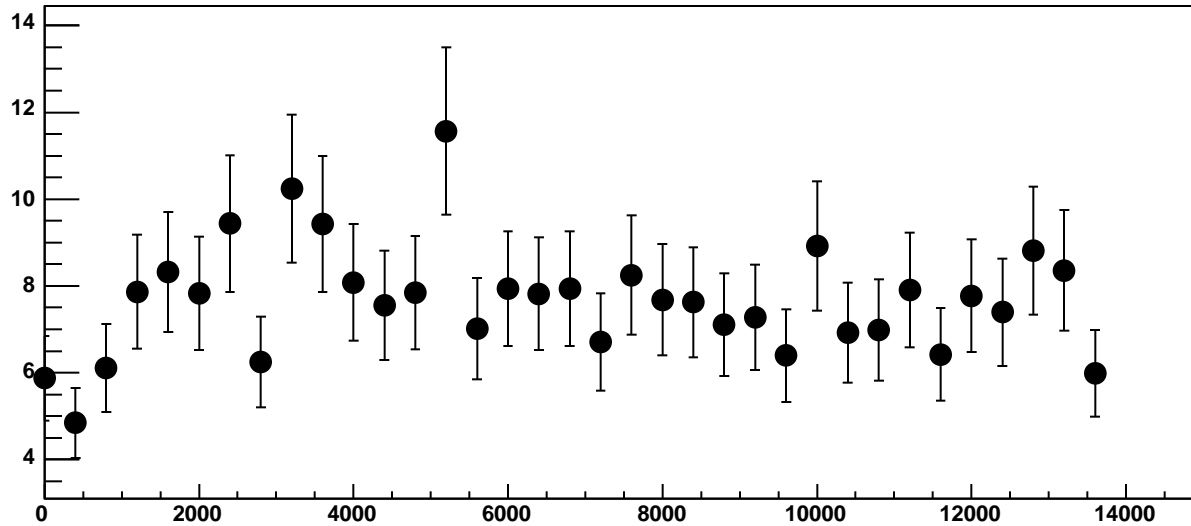


Chip 8, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

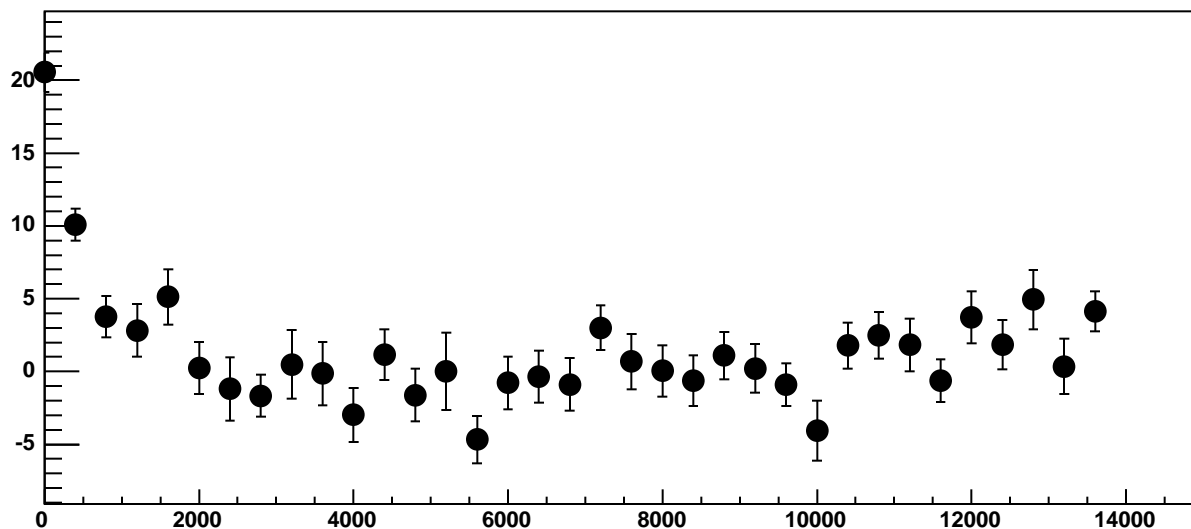


χ^2 / ndf 36.6 / 23
p0 94.39 ± 0.8414
p1 0.01842 ± 0.0001216

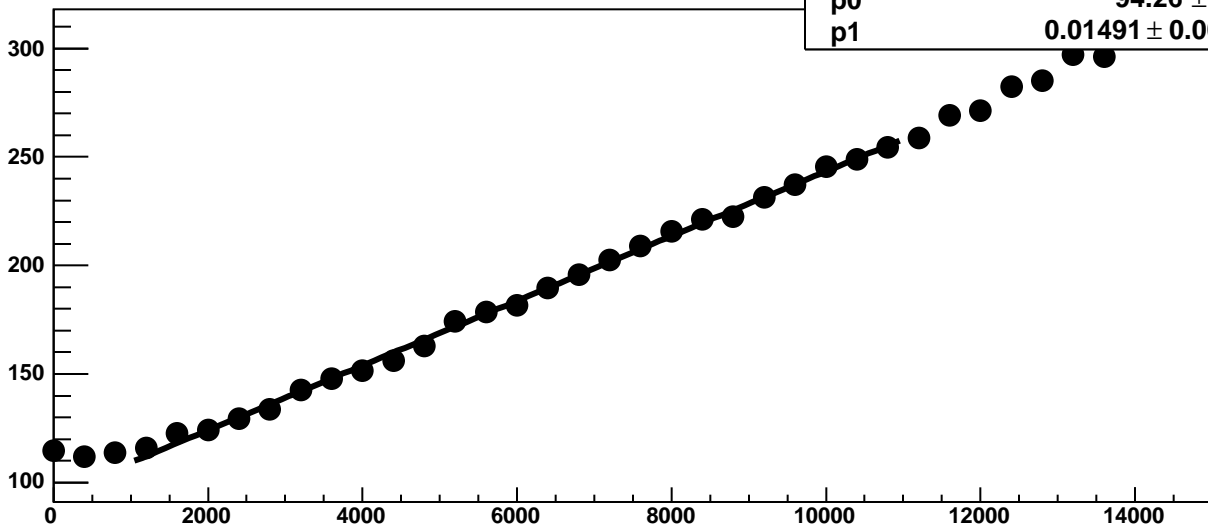
Chip 8, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

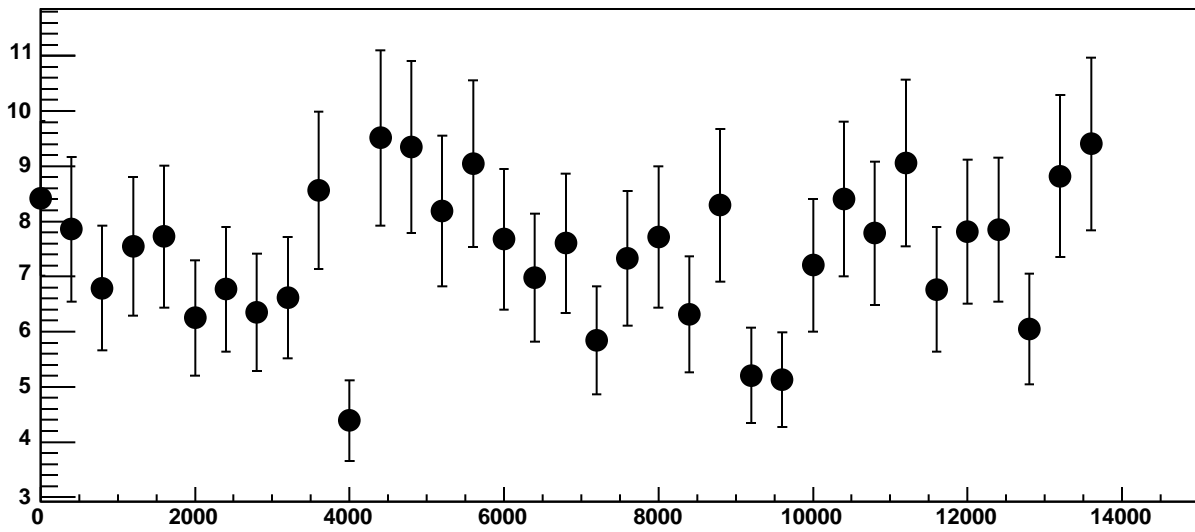


Chip 8, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

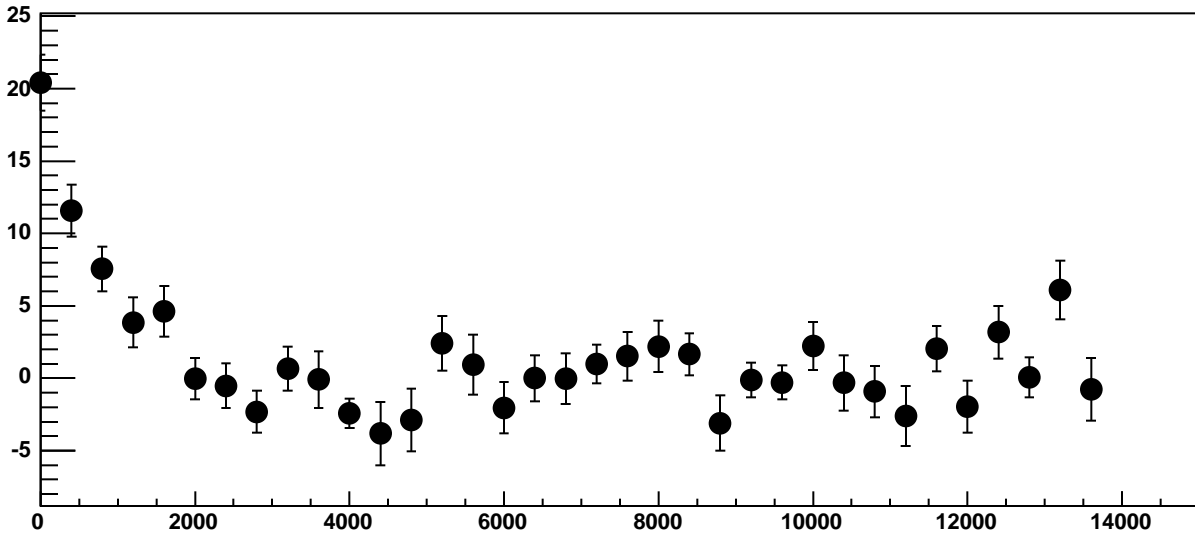


χ^2 / ndf 37.46 / 23
p0 94.26 ± 0.7294
p1 0.01491 ± 0.0001086

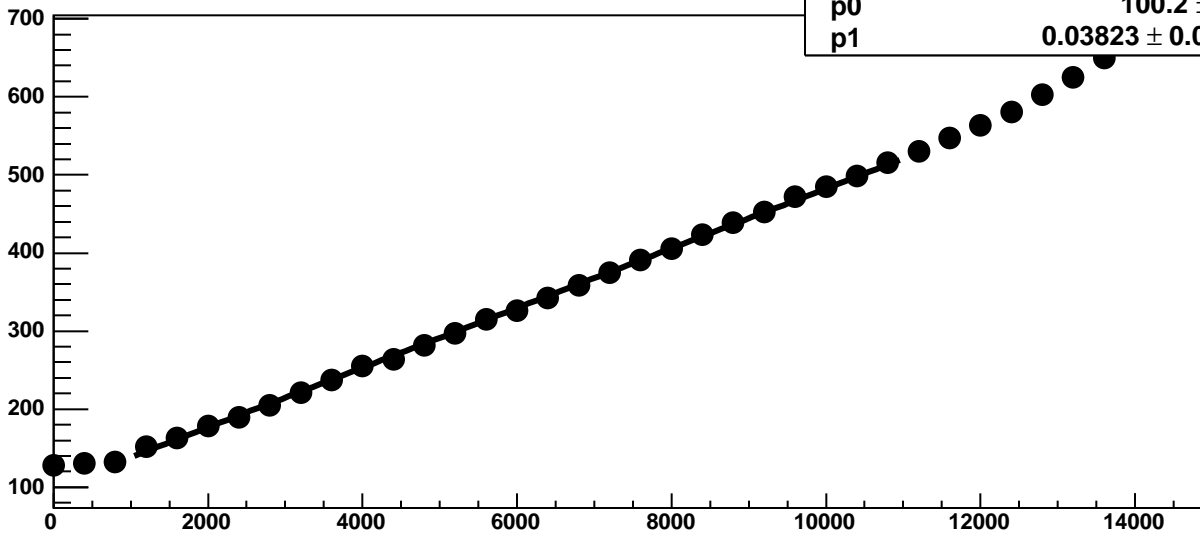
Chip 8, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

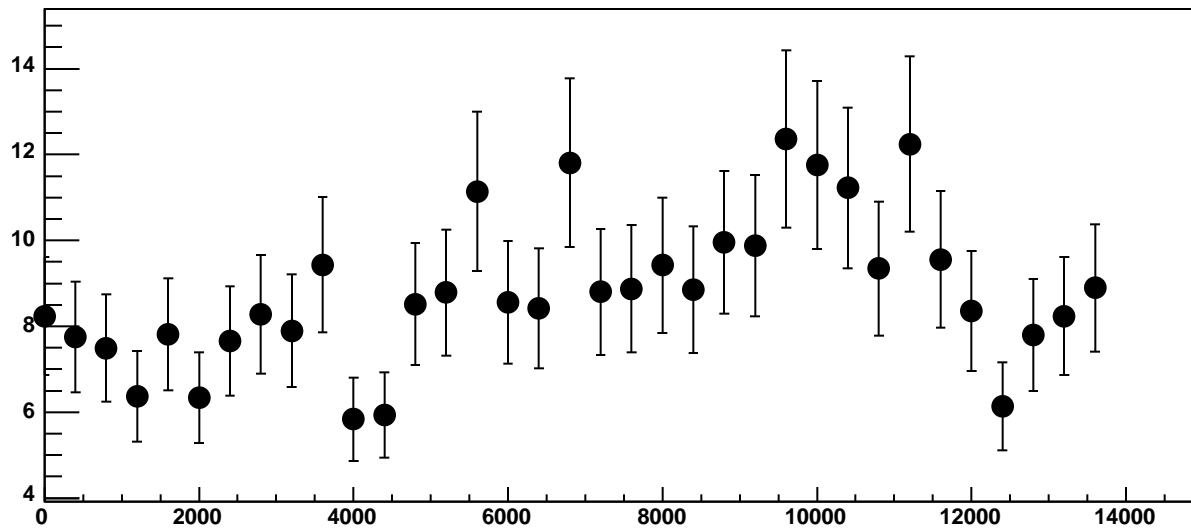


Chip 8, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

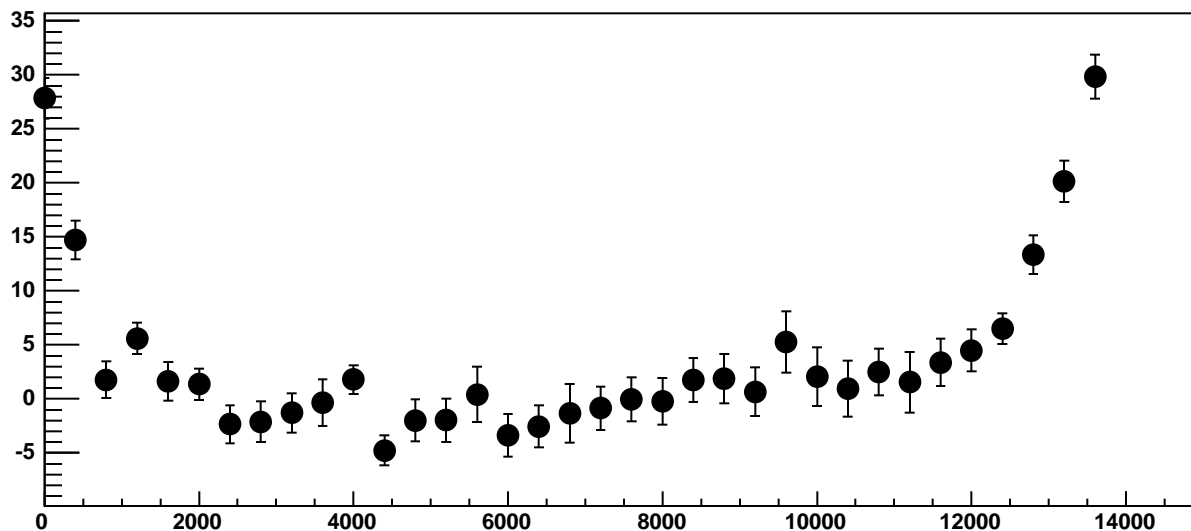


χ^2 / ndf 48.07 / 23
p0 100.2 ± 0.8061
p1 0.03823 ± 0.0001374

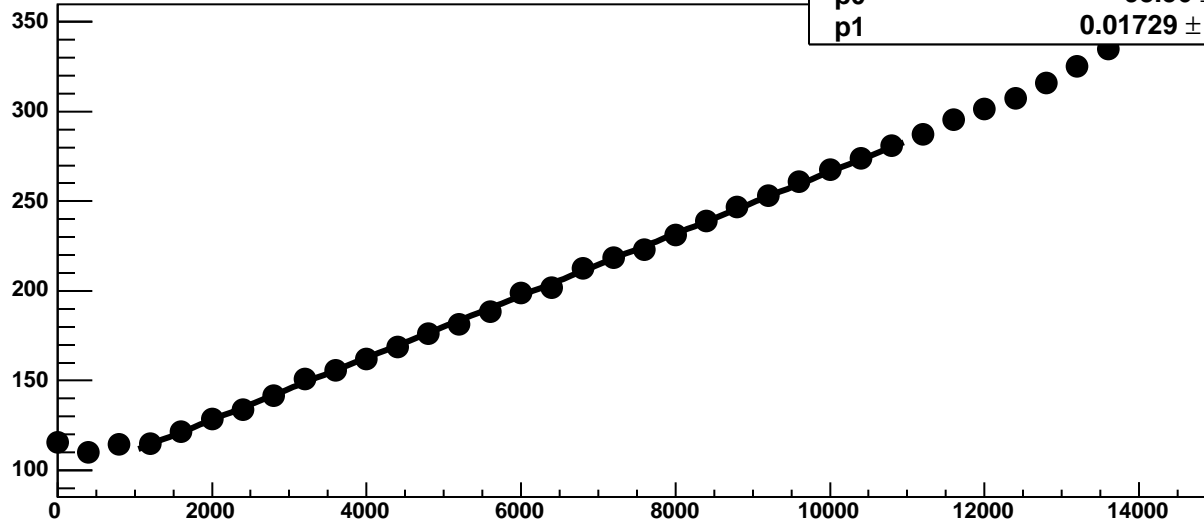
Chip 8, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

11.07 / 23

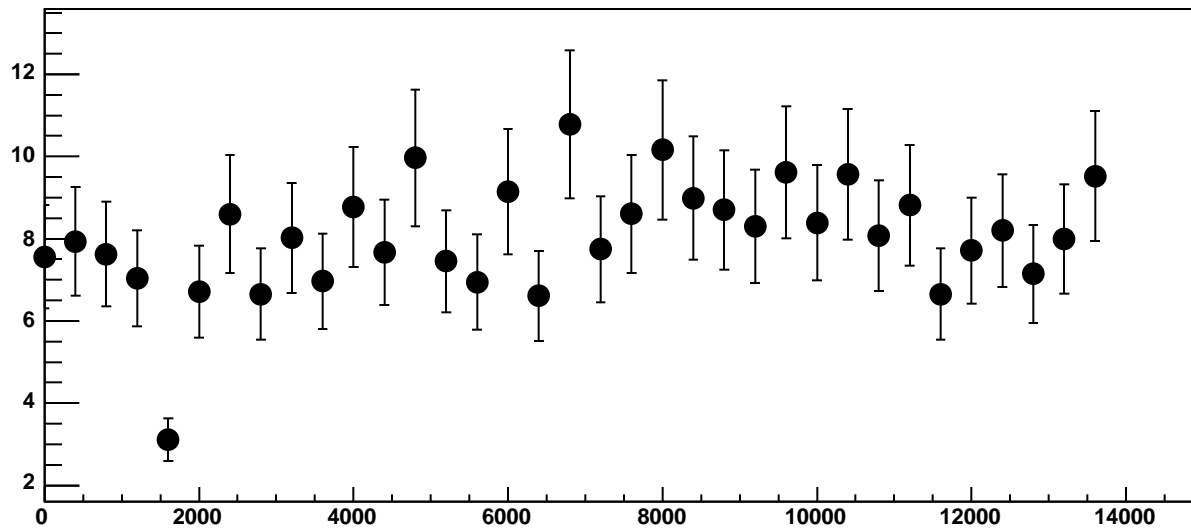
p0

93.56 ± 0.6311

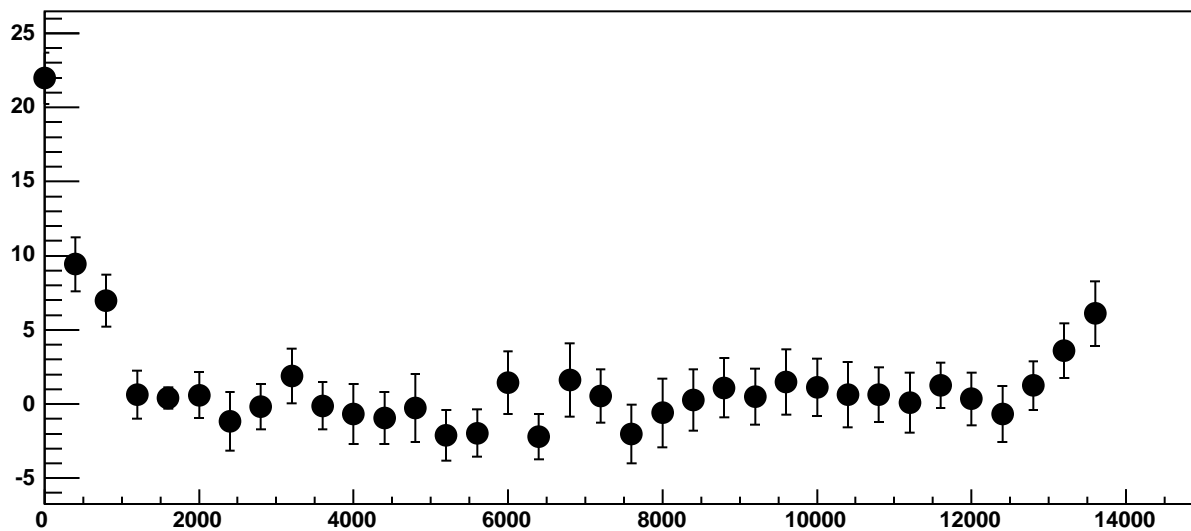
p1

0.01729 ± 0.00011

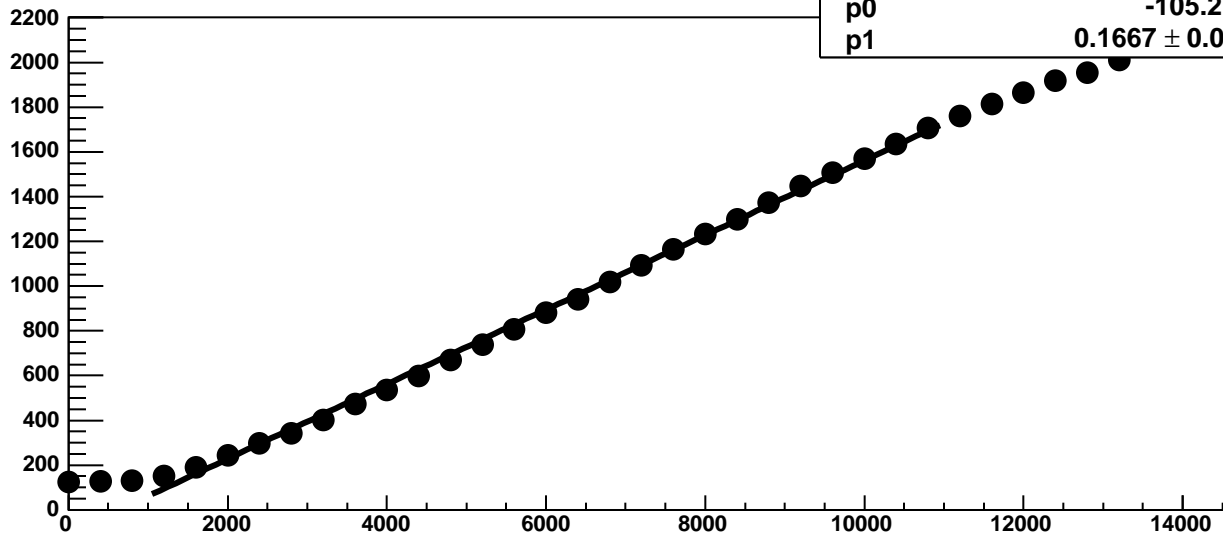
Chip 8, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

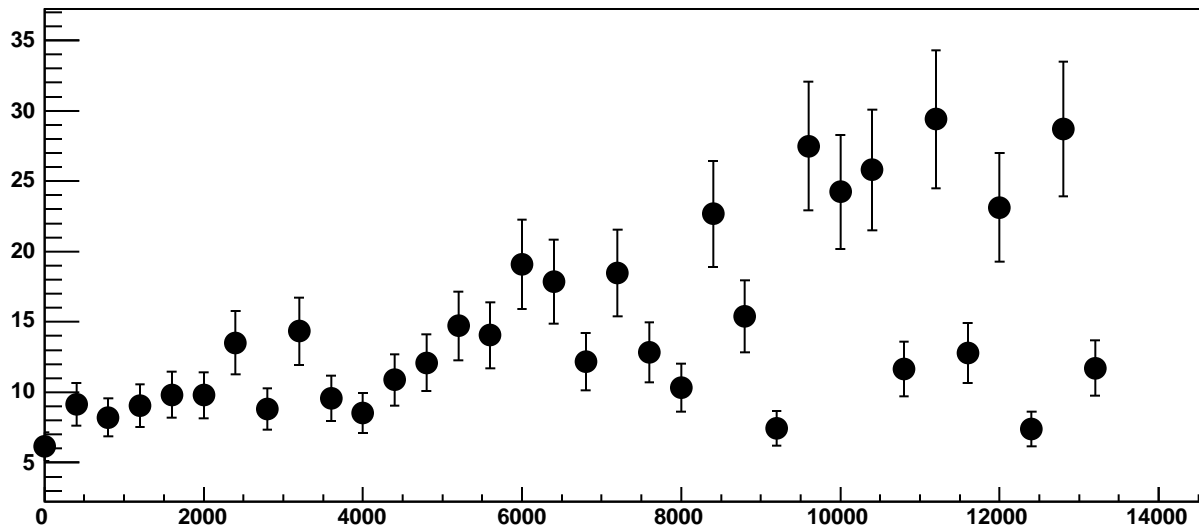


Chip 8, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

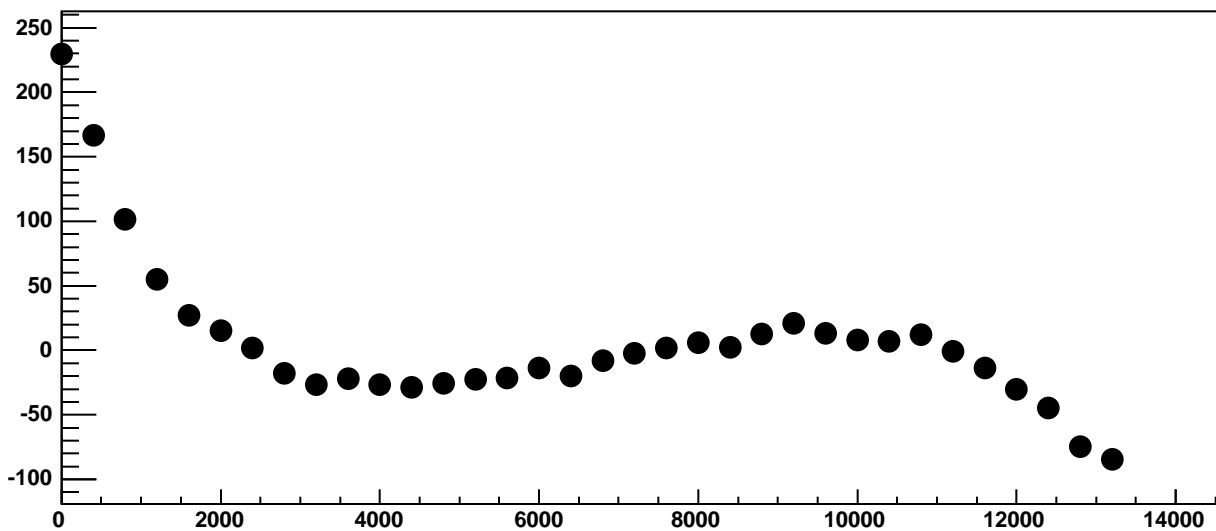


χ^2 / ndf 1872 / 23
p0 -105.2 ± 1.13
p1 0.1667 ± 0.0001891

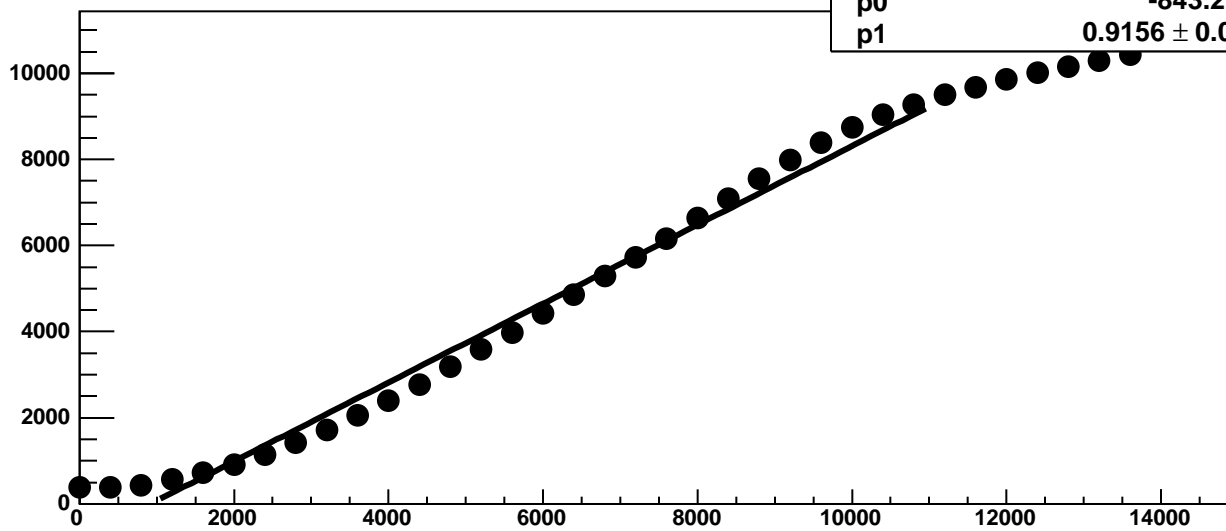
Chip 8, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



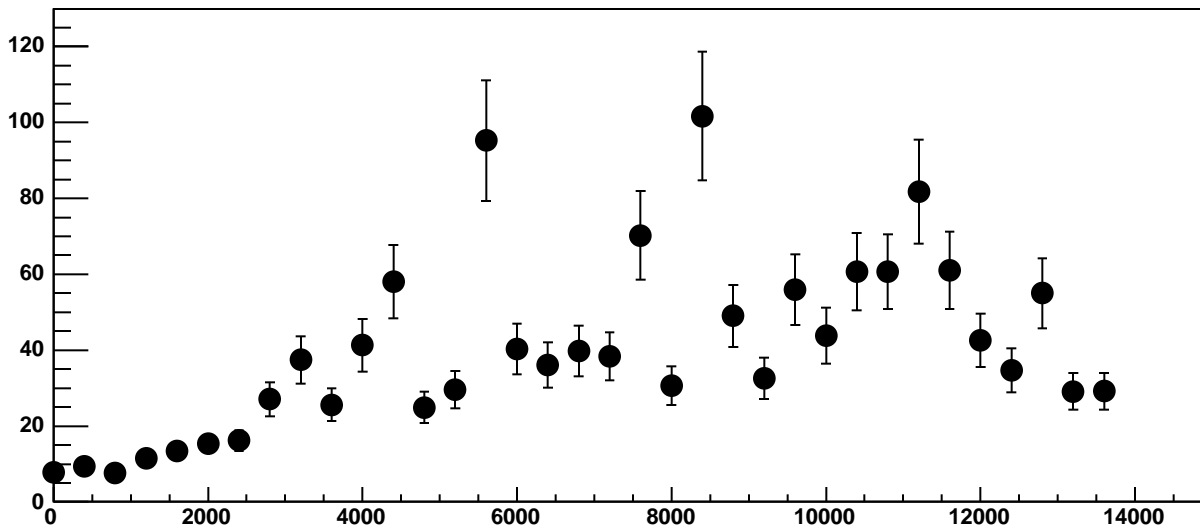
Chip 8, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



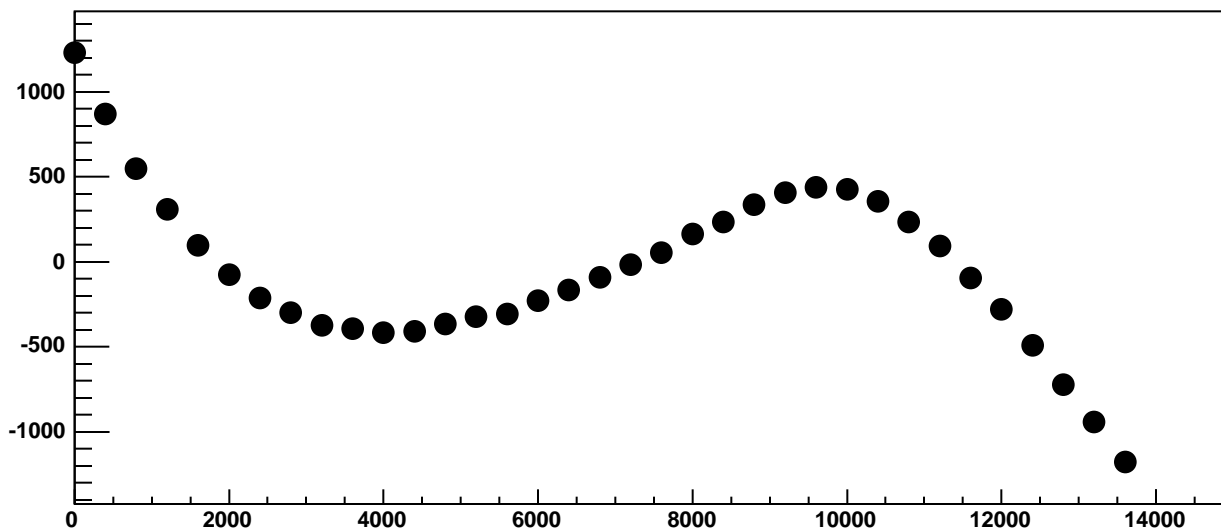
Chip 8, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC



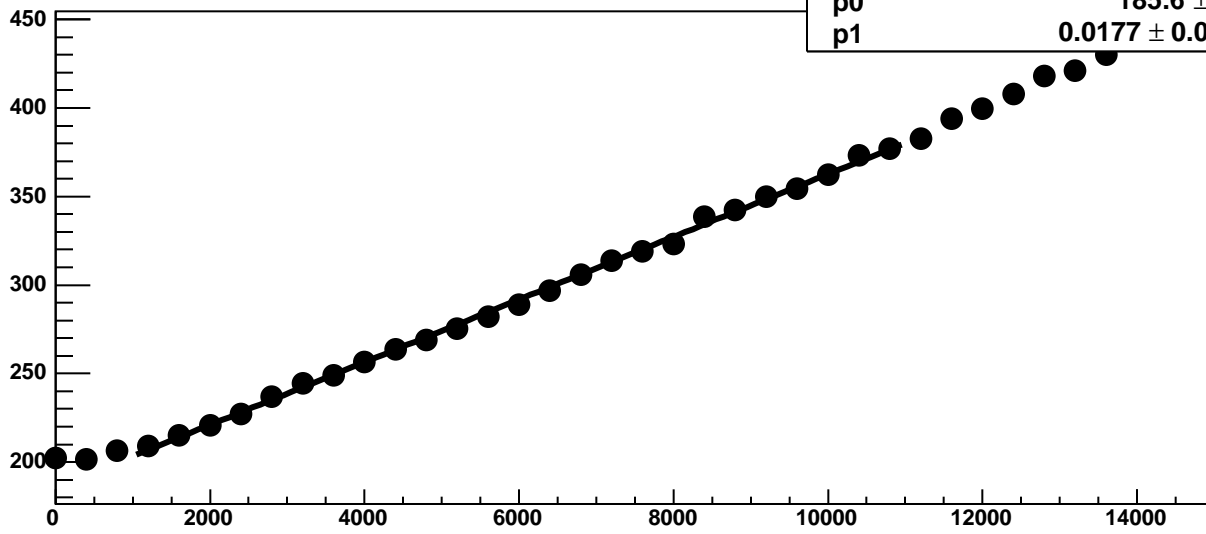
Chip 8, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC

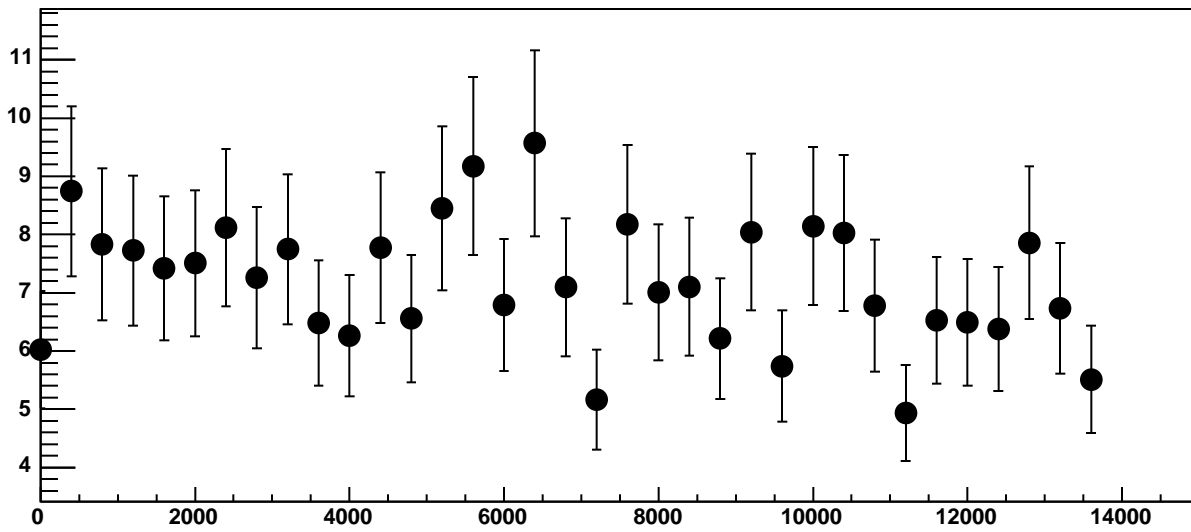


Chip 8, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

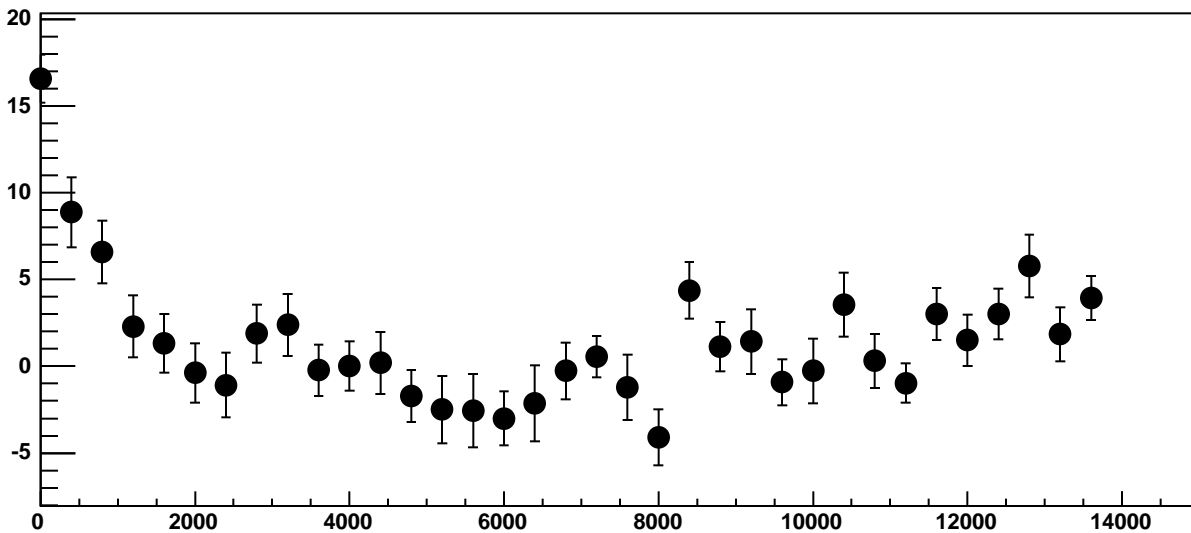


χ^2 / ndf 34.66 / 23
p0 185.6 ± 0.7809
p1 0.0177 ± 0.0001153

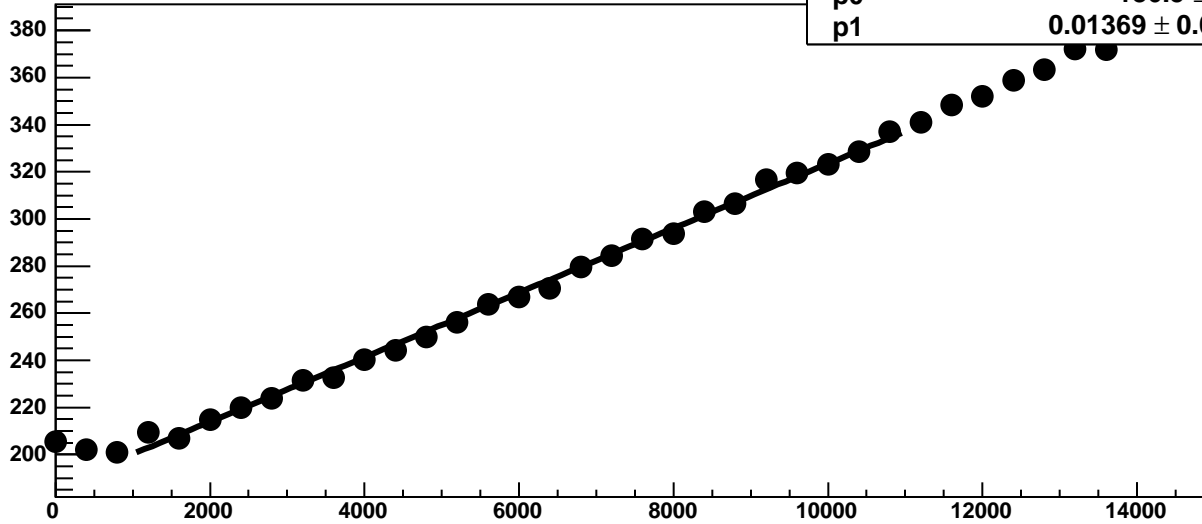
Chip 8, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

51.36 / 23

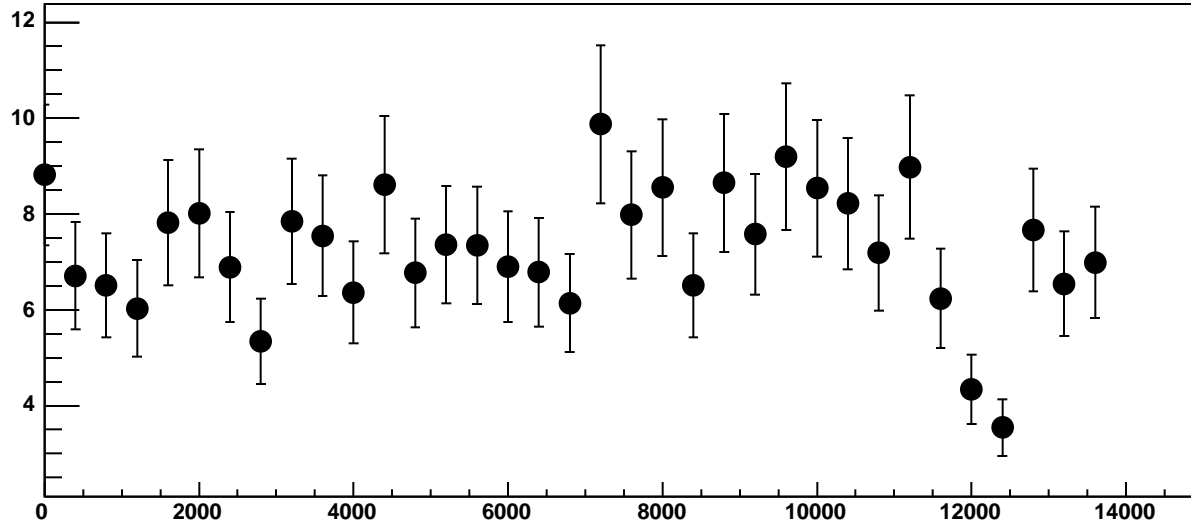
p0

186.5 ± 0.7424

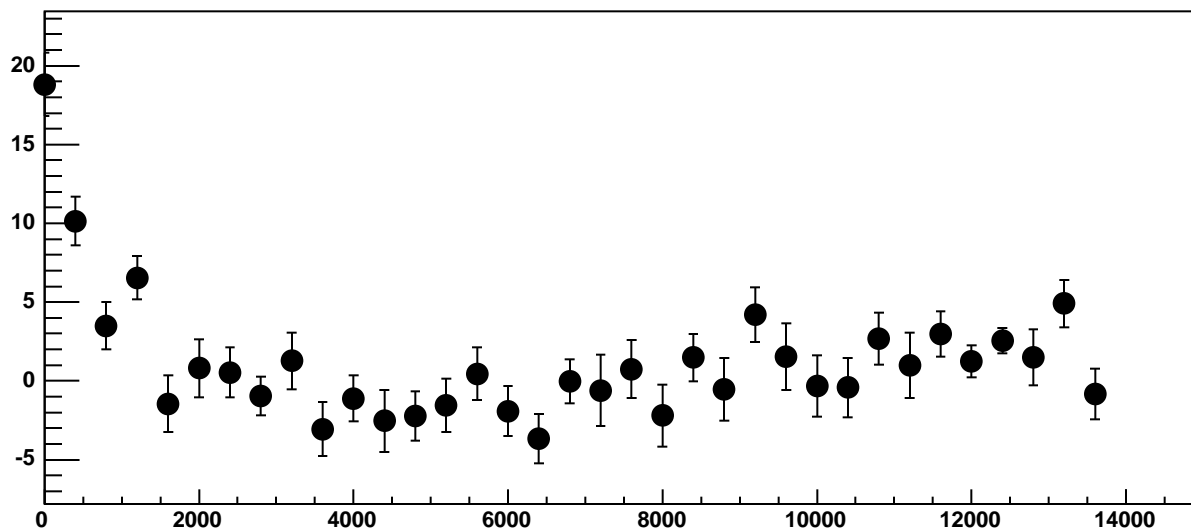
p1

0.01369 ± 0.0001171

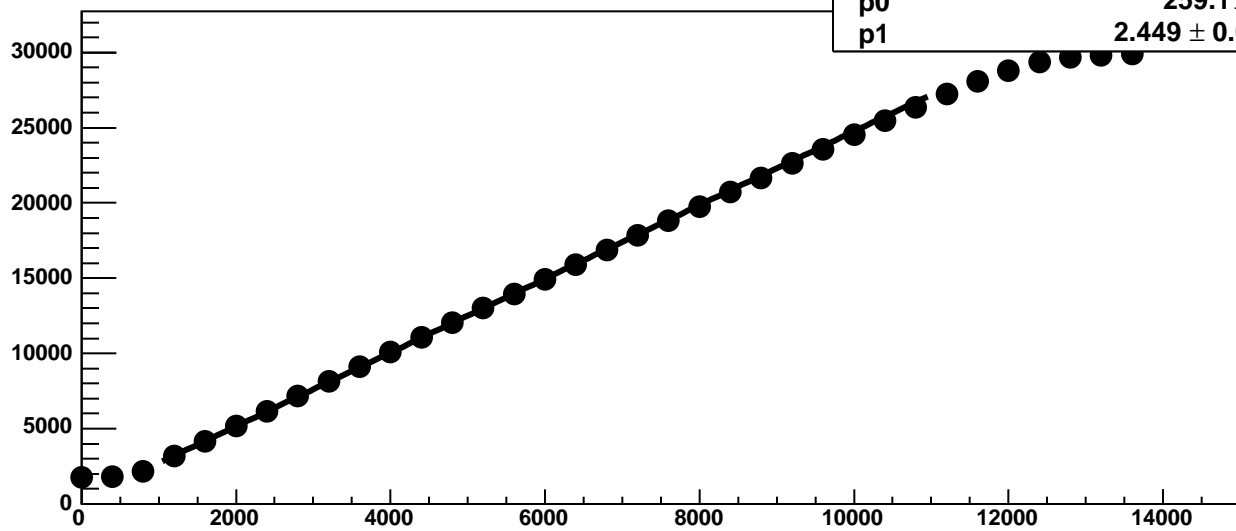
Chip 8, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



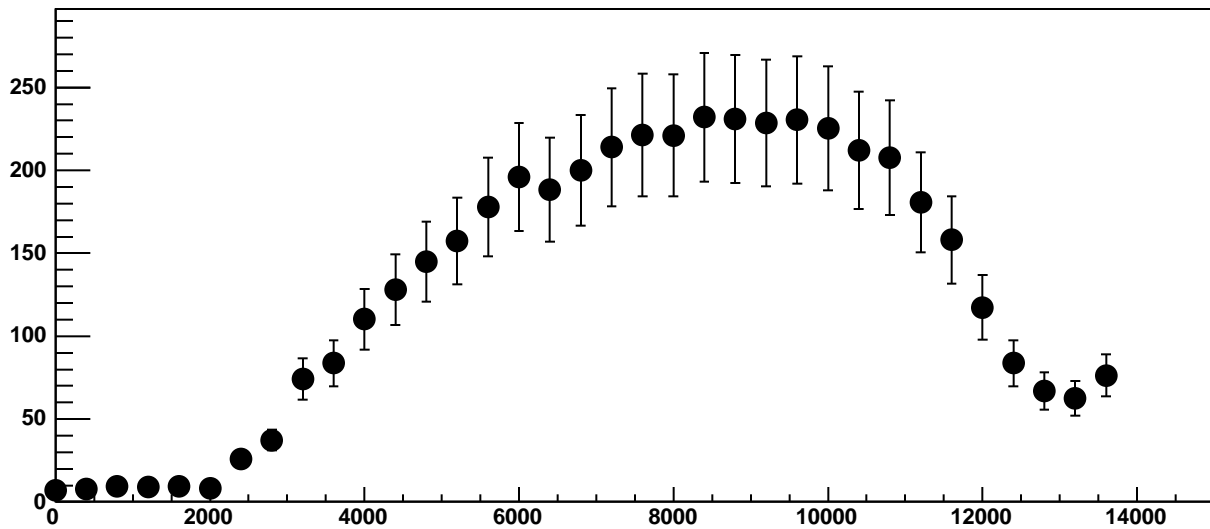
Chip 8, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



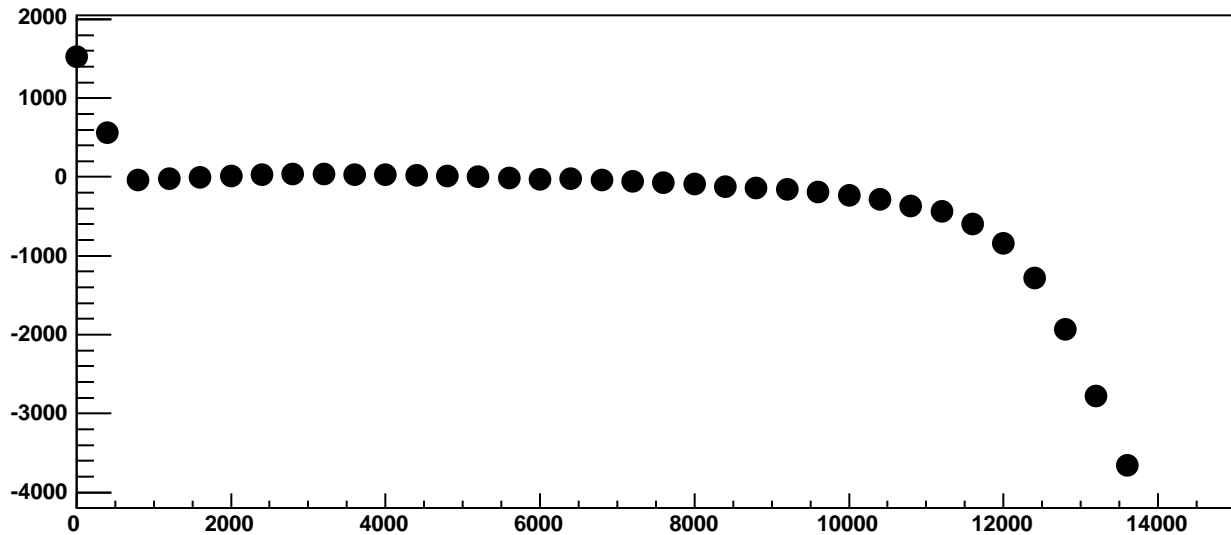
Chip 8, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC



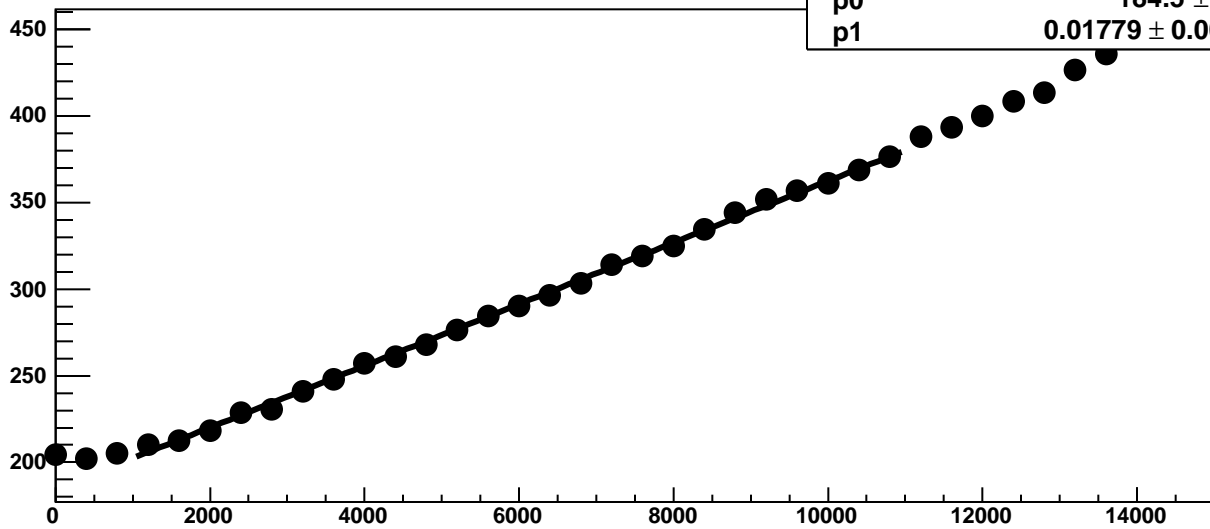
Chip 8, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC

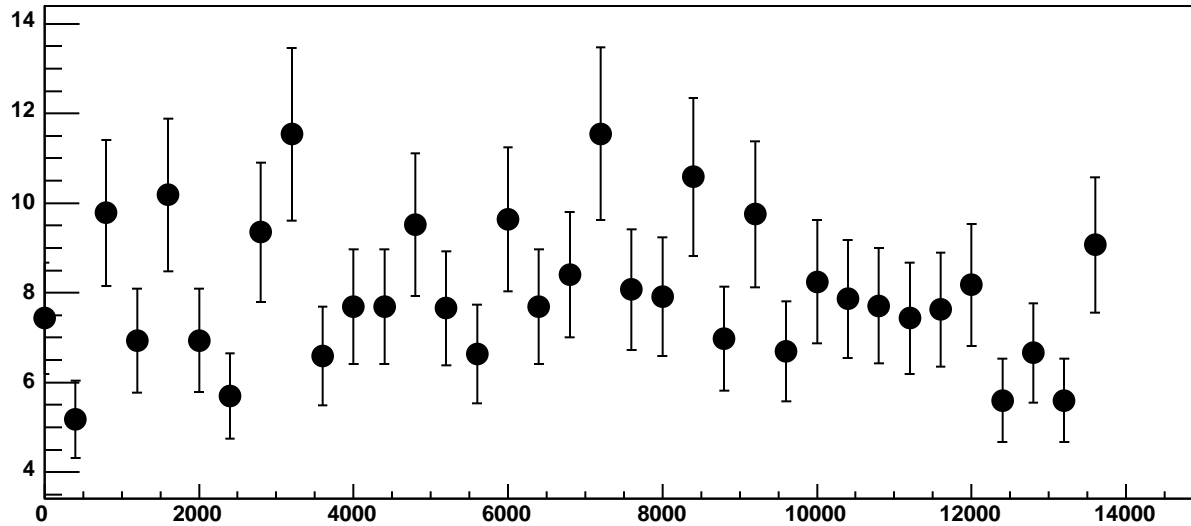


Chip 8, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

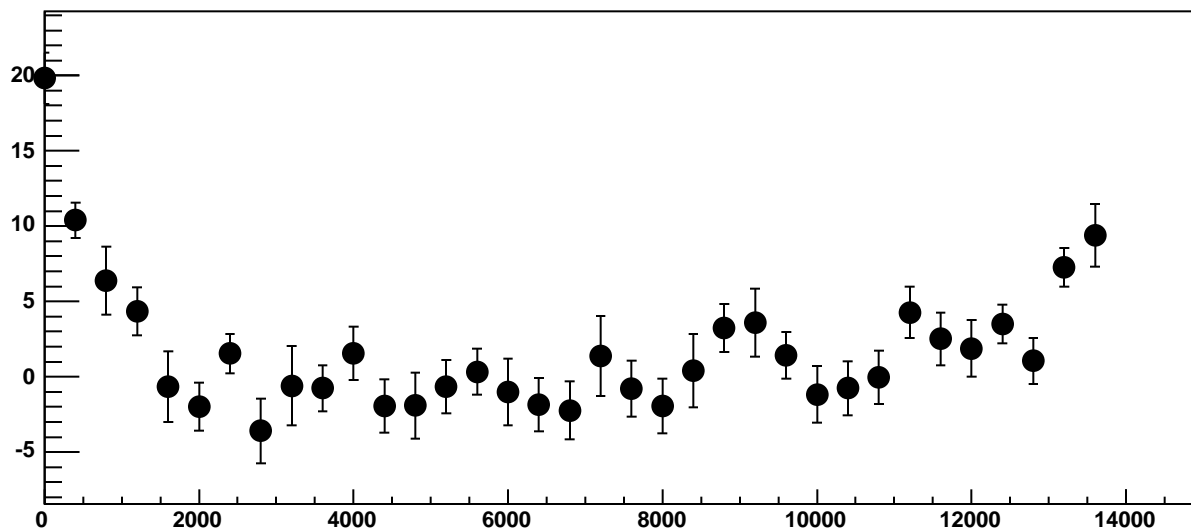


χ^2 / ndf 28.89 / 23
p0 184.5 ± 0.8036
p1 0.01779 ± 0.0001225

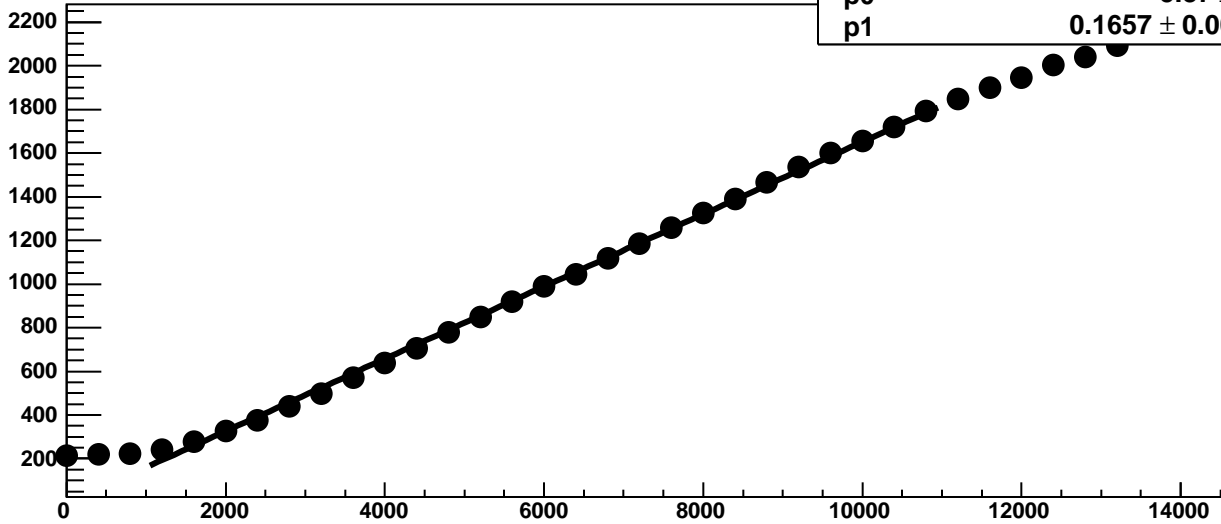
Chip 8, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



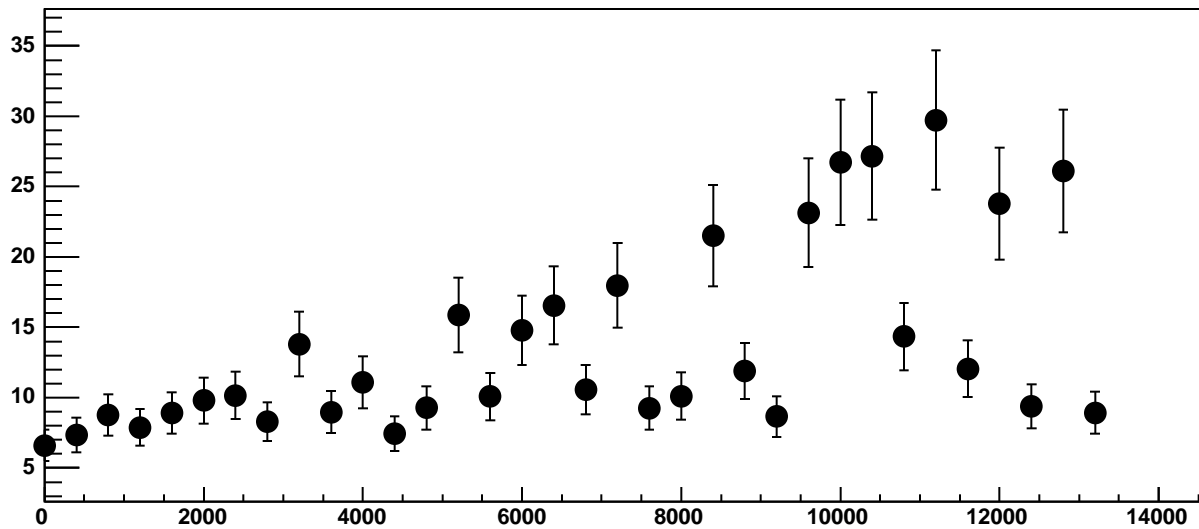
Chip 8, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



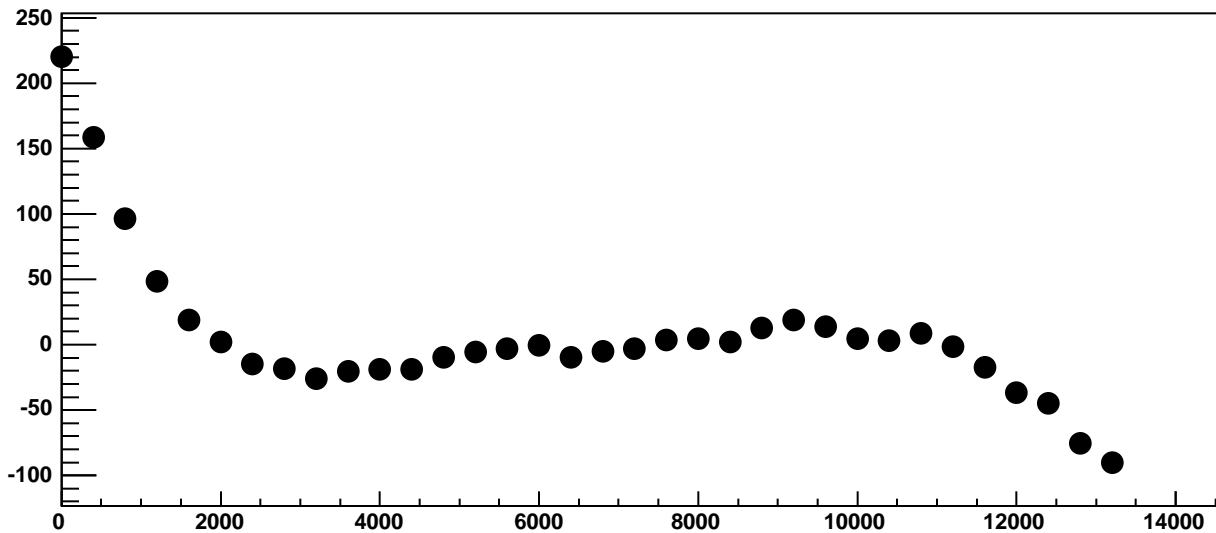
Chip 8, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



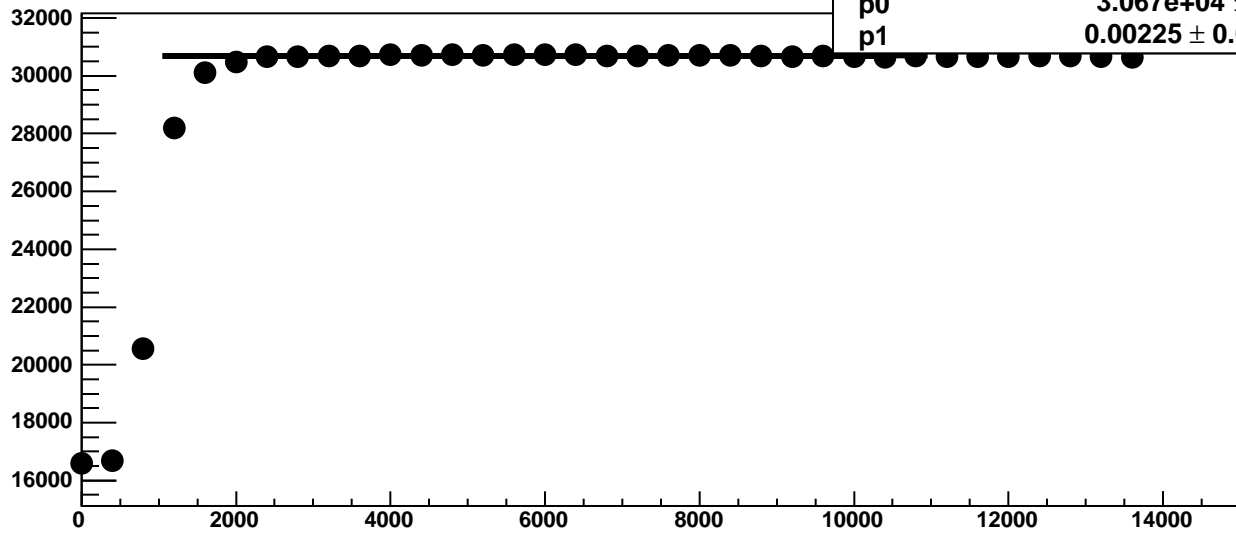
Chip 8, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



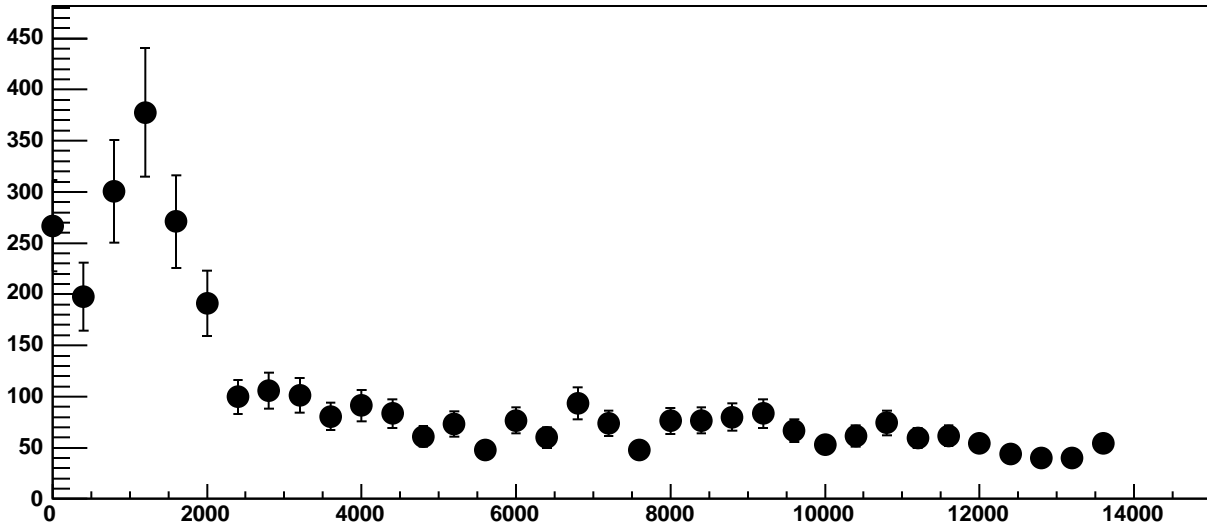
Chip 8, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



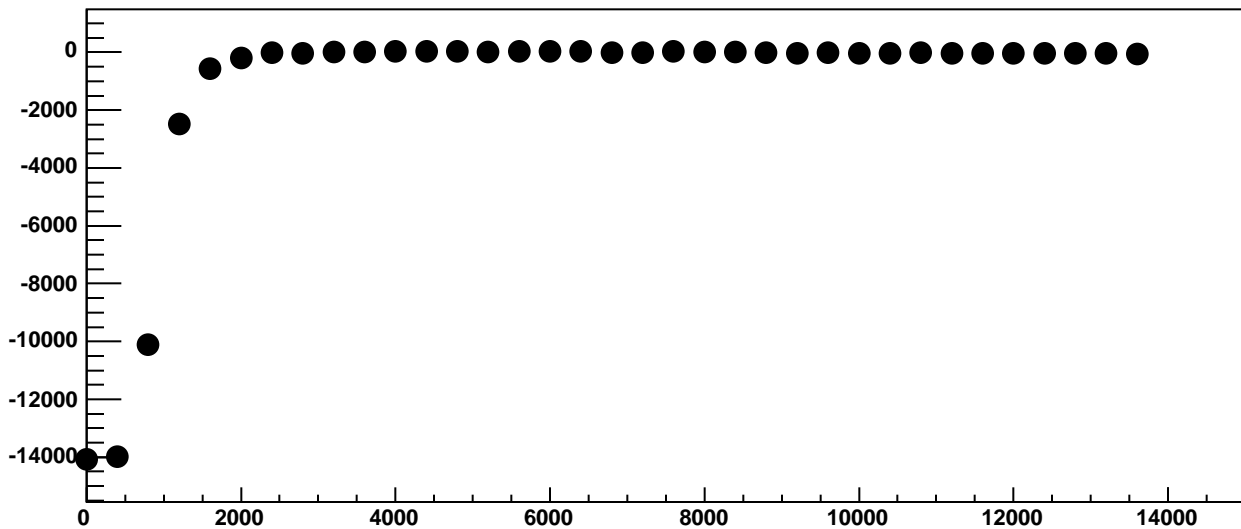
Chip 8, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC



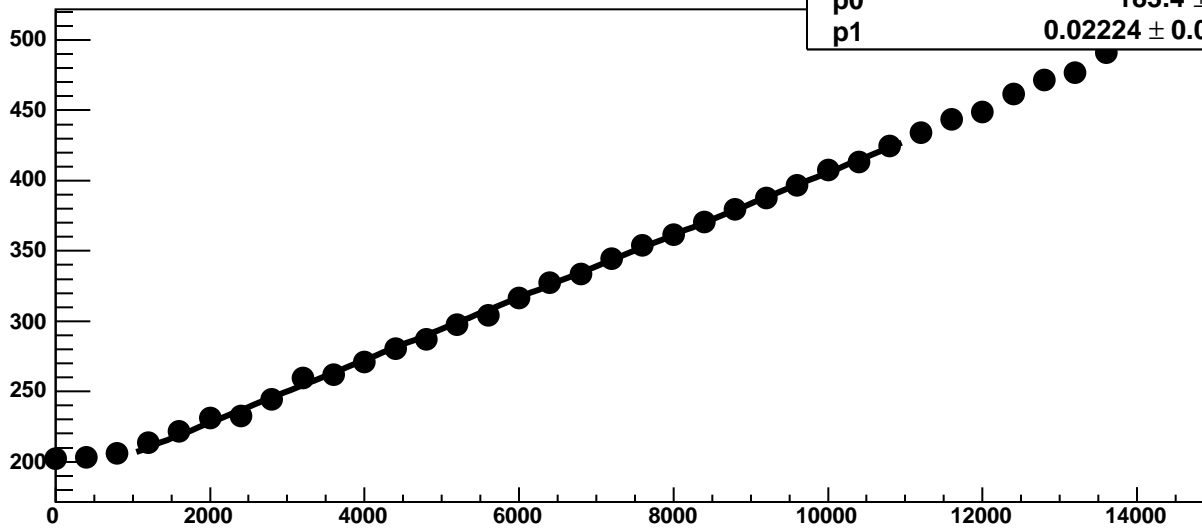
Chip 8, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC

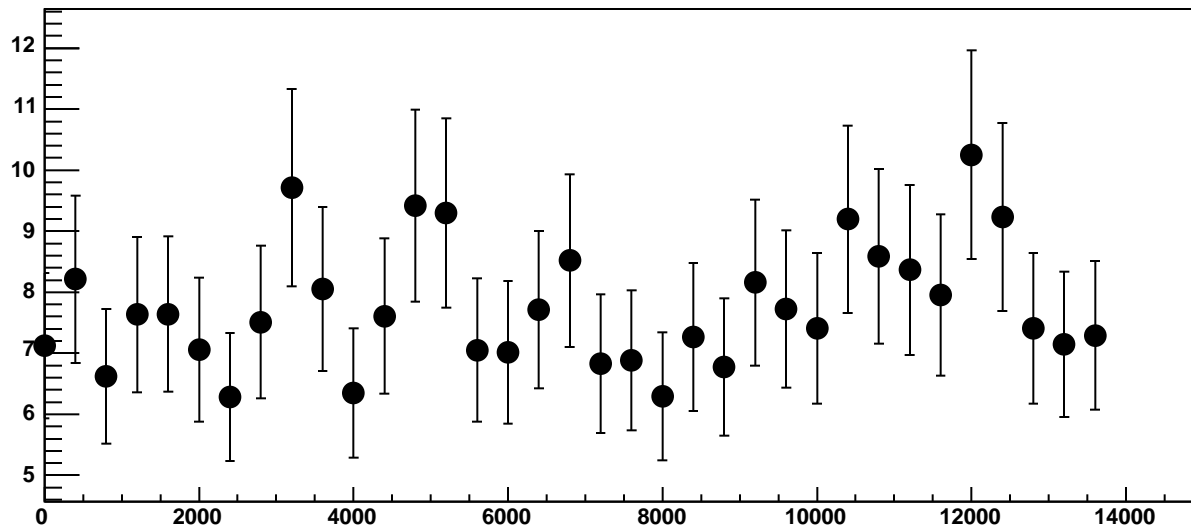


Chip 8, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

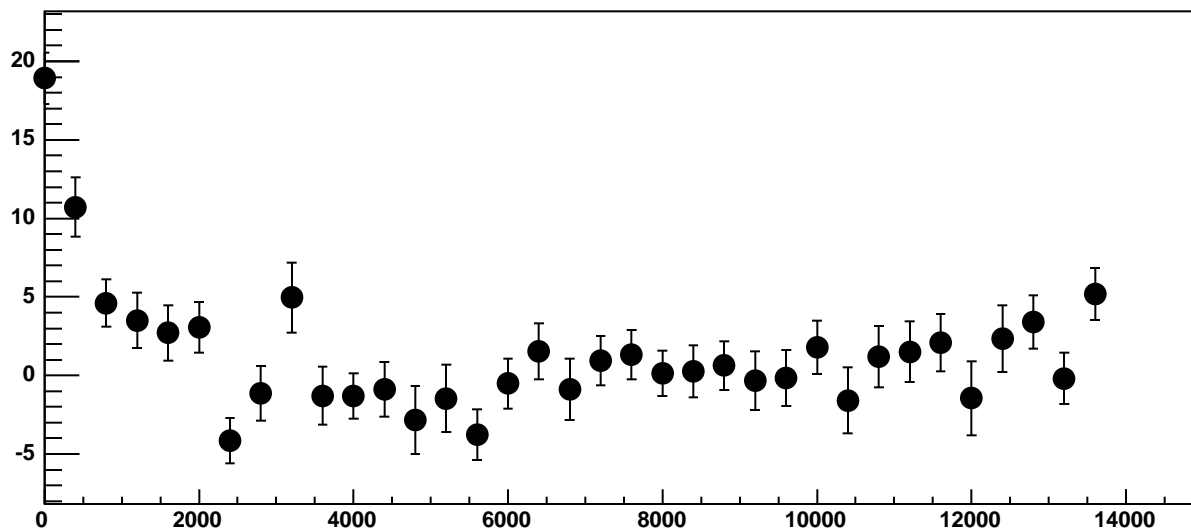


χ^2 / ndf 37.23 / 23
p0 183.4 ± 0.7979
p1 0.02224 ± 0.0001212

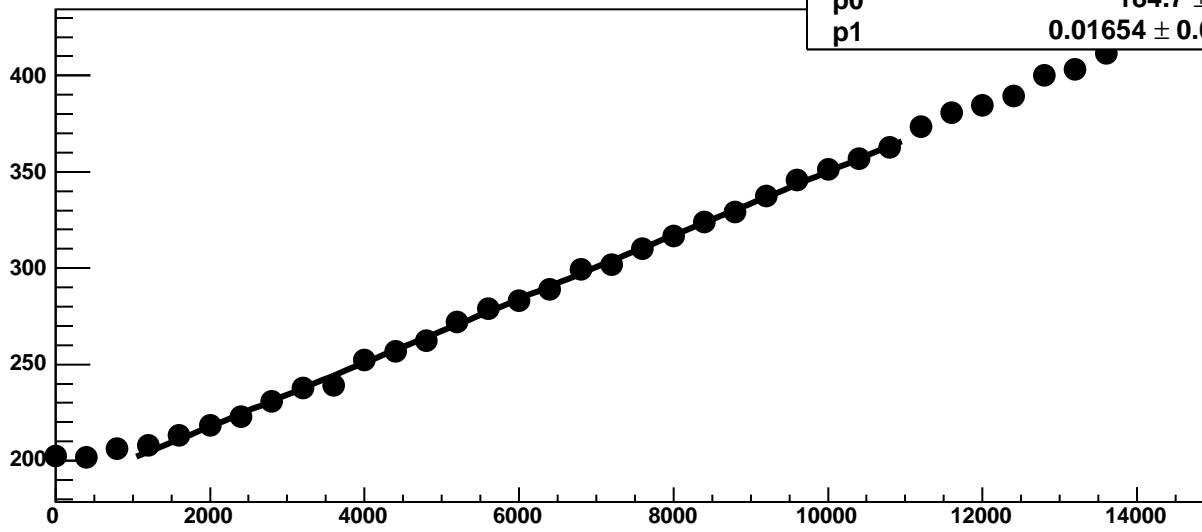
Chip 8, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

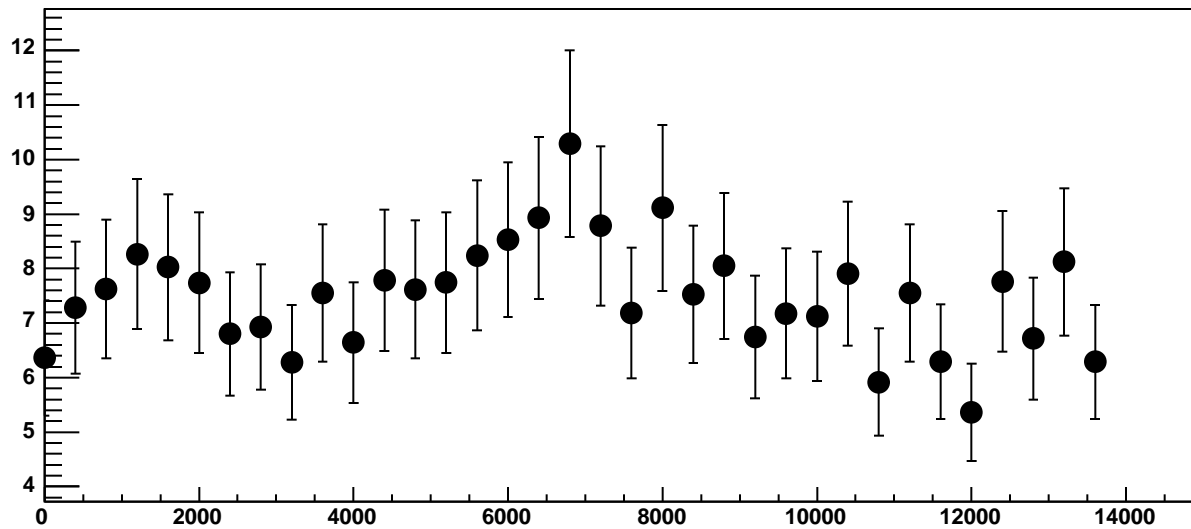


Chip 8, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

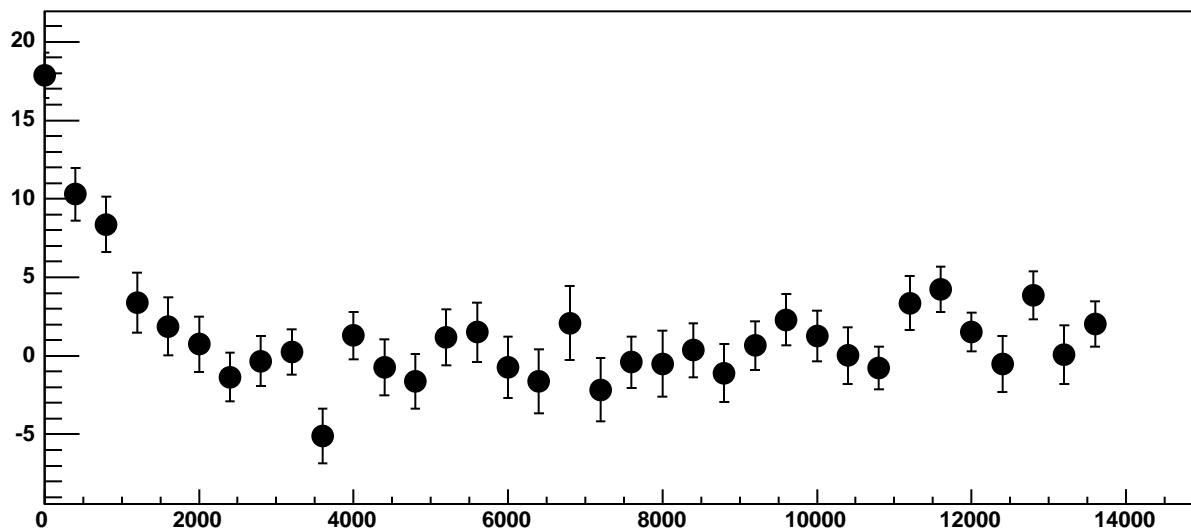


χ^2 / ndf 22.97 / 23
p0 184.7 ± 0.7773
p1 0.01654 ± 0.0001151

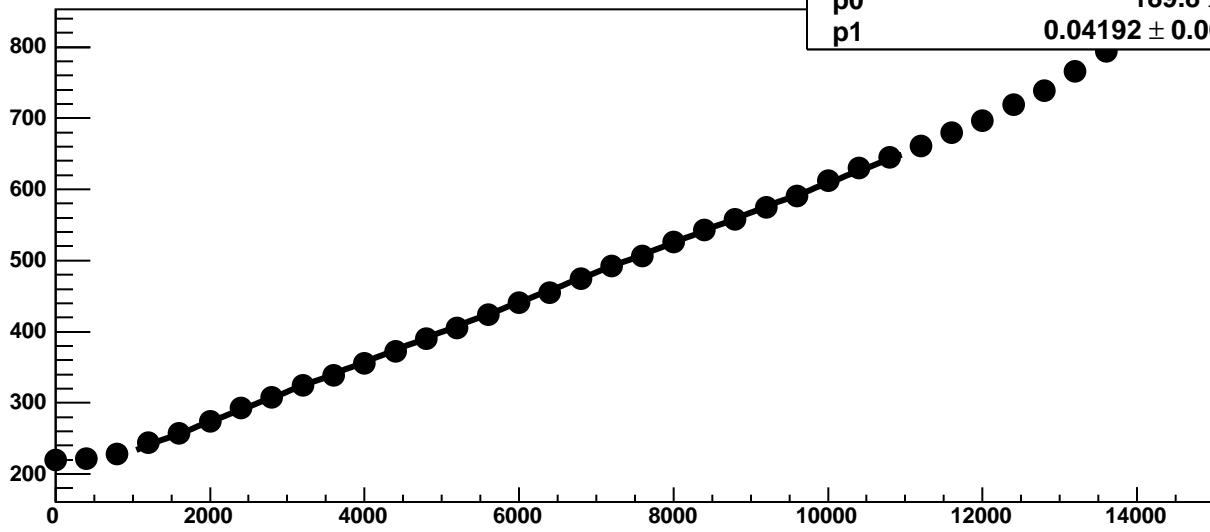
Chip 8, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

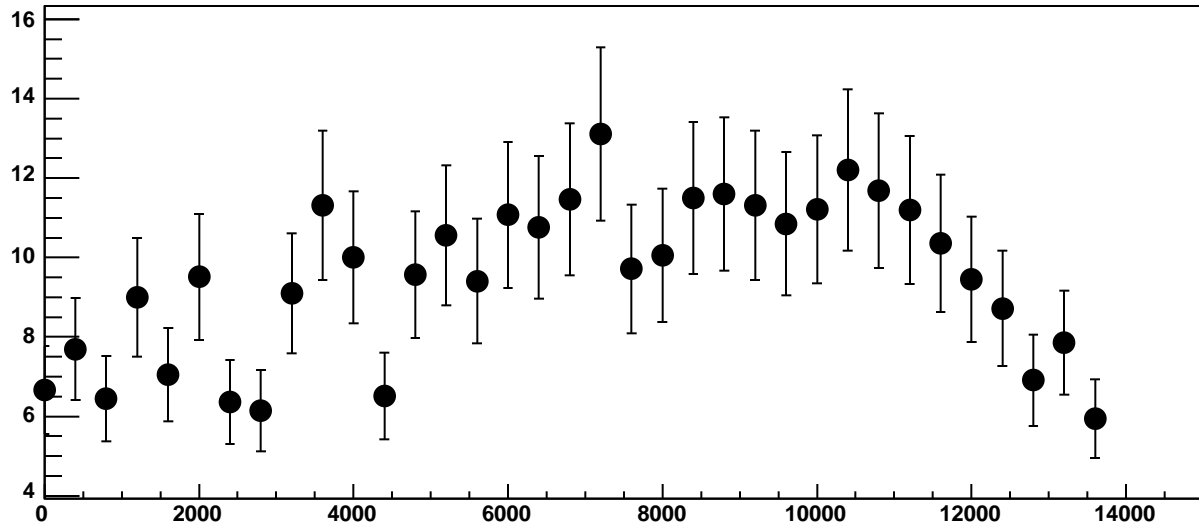


Chip 8, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

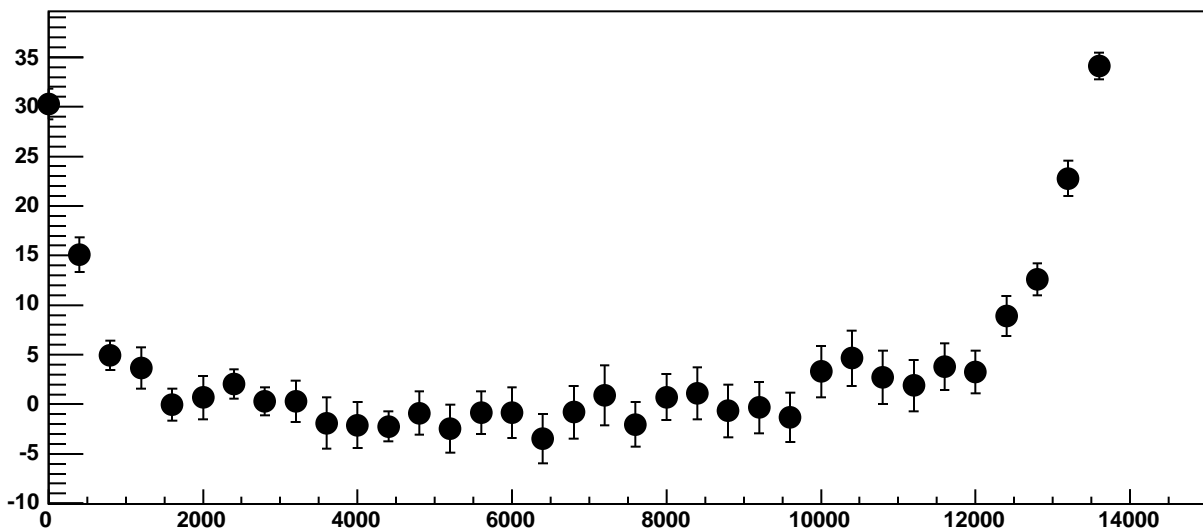


χ^2 / ndf 19.34 / 23
p0 189.8 ± 0.887
p1 0.04192 ± 0.0001522

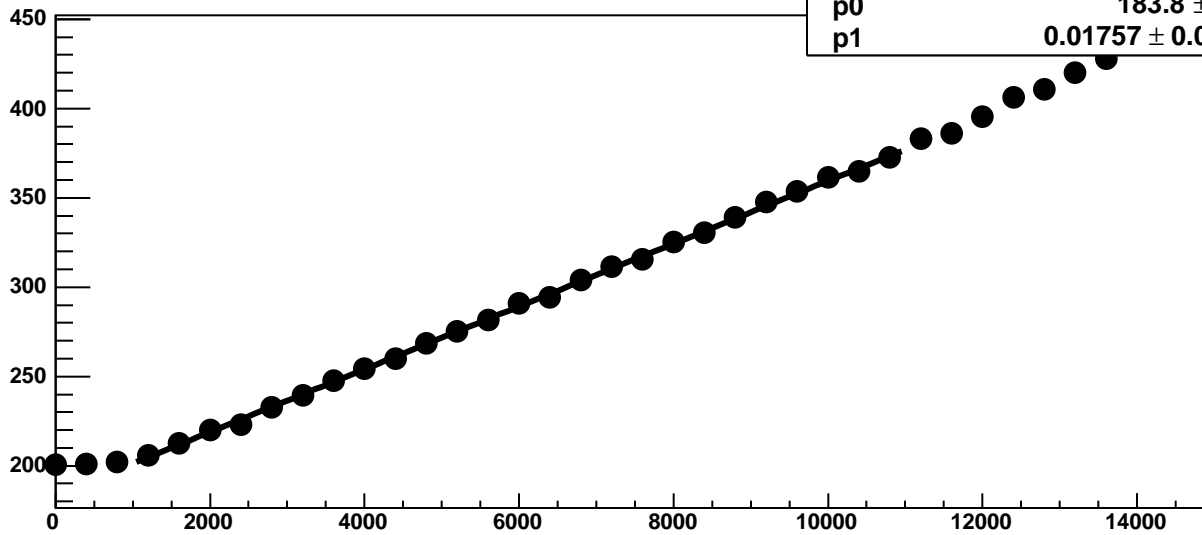
Chip 8, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

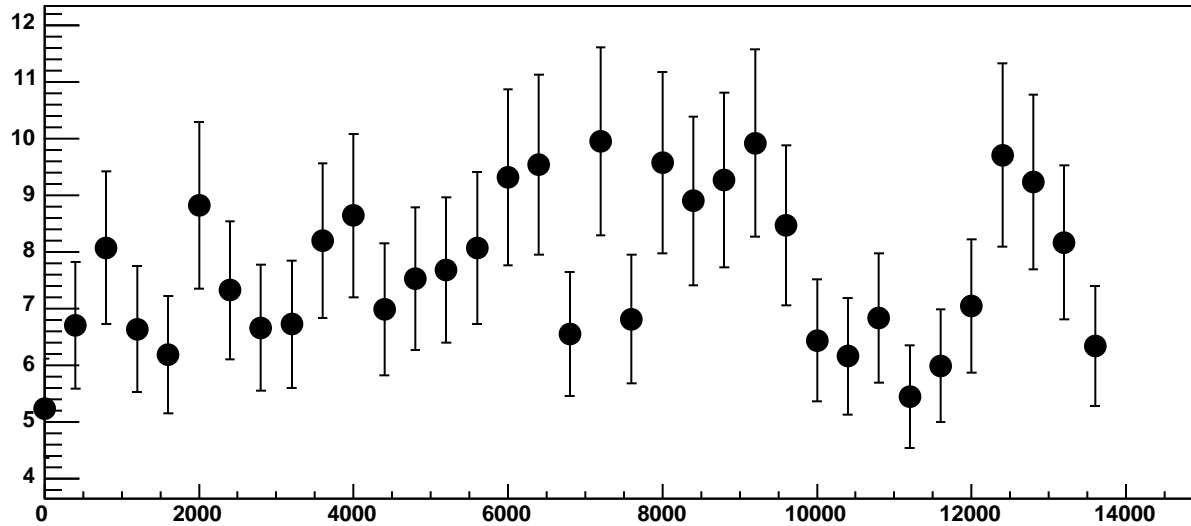


Chip 8, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

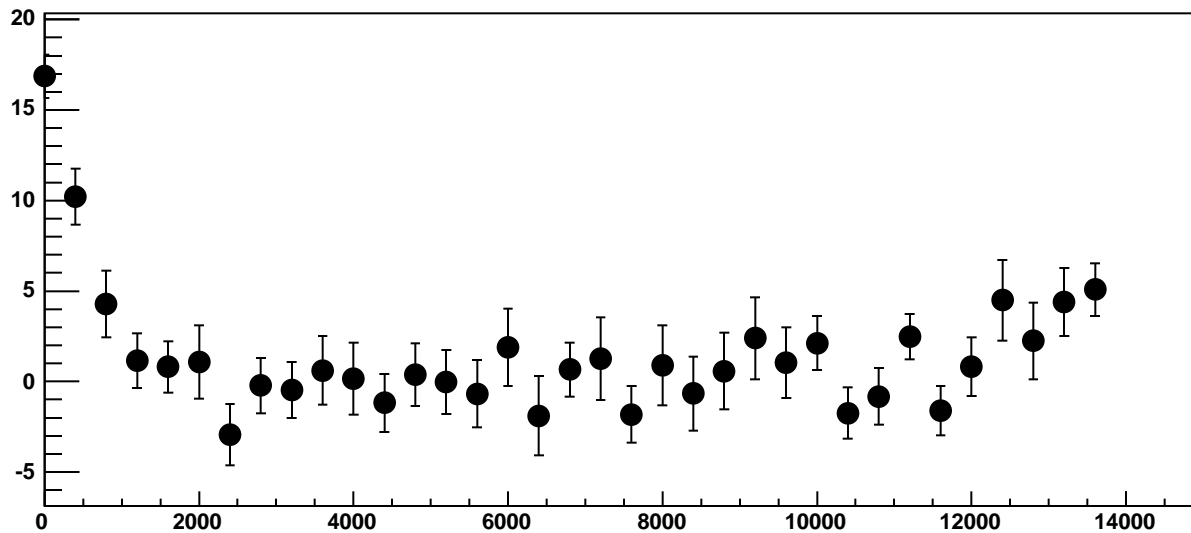


χ^2 / ndf 14.14 / 23
p0 183.8 ± 0.7526
p1 0.01757 ± 0.0001138

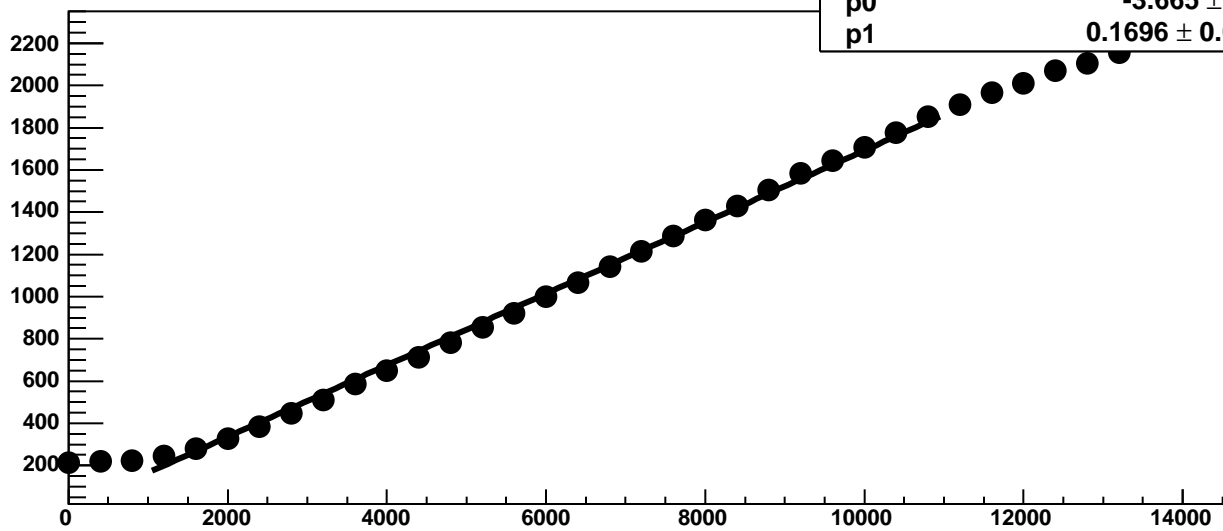
Chip 8, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



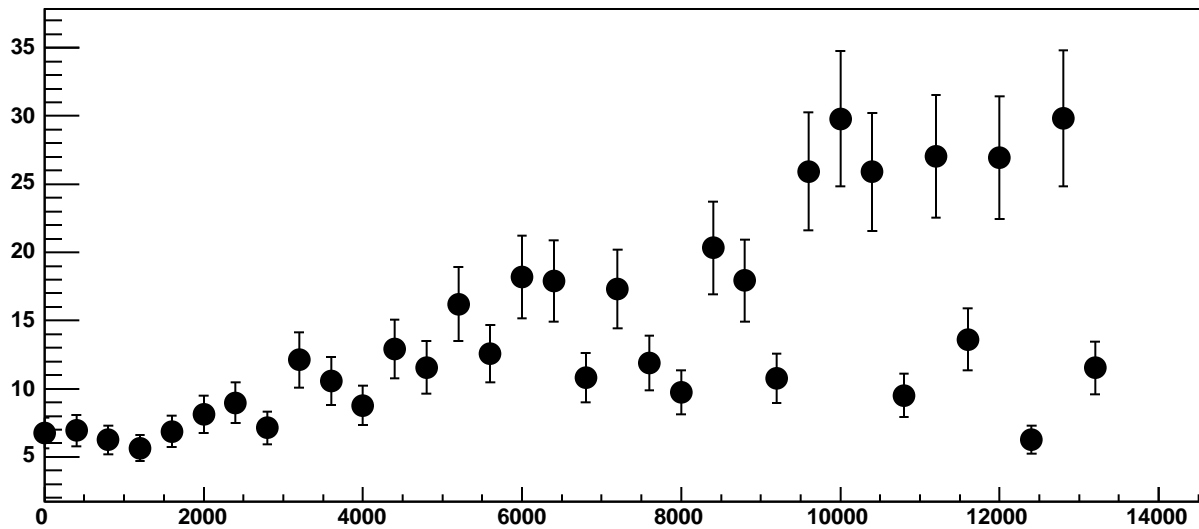
Chip 8, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



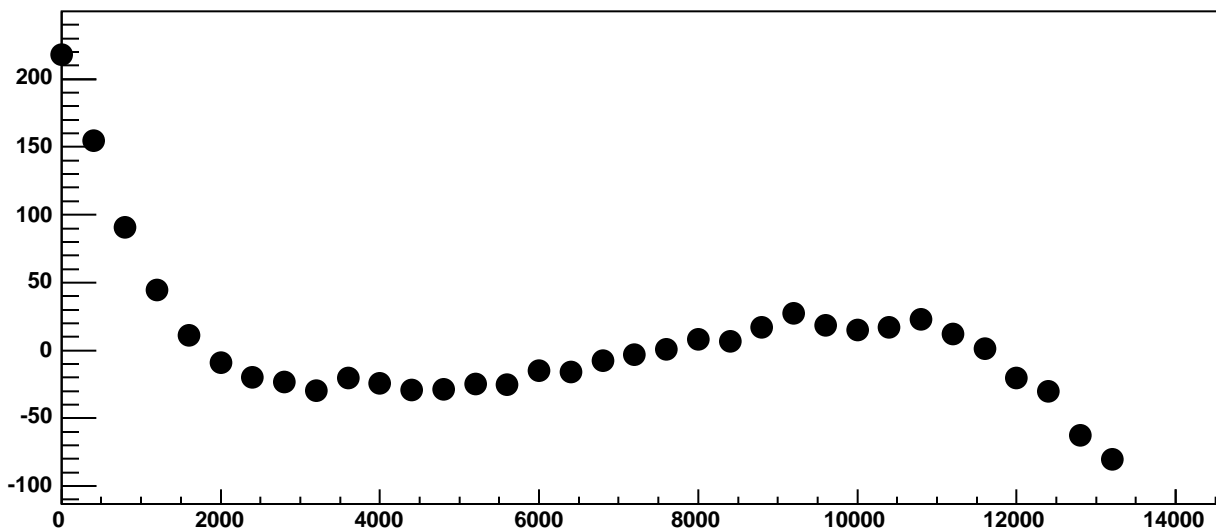
Chip 8, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



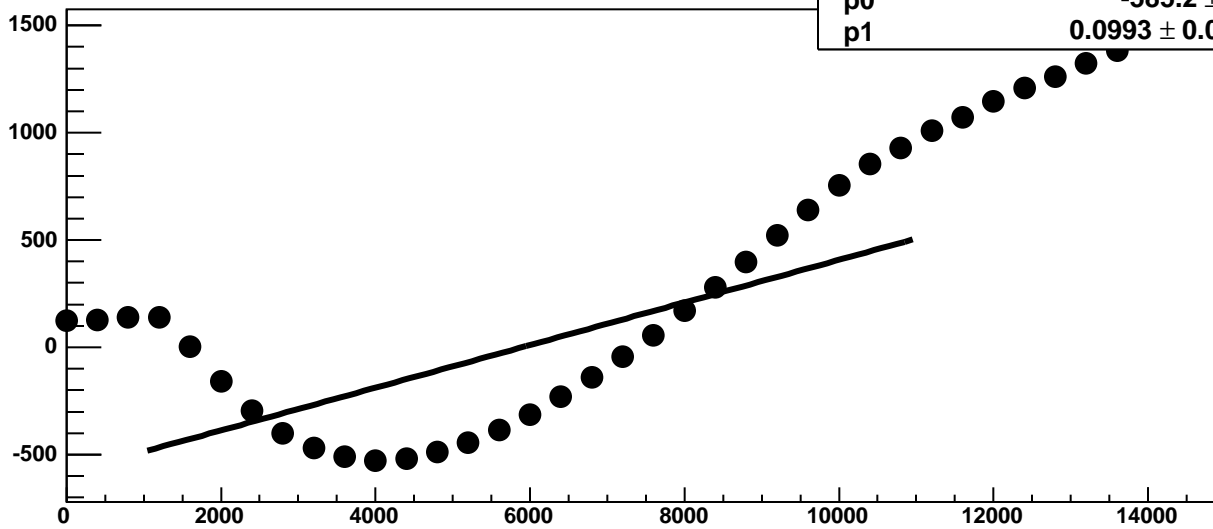
Chip 8, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

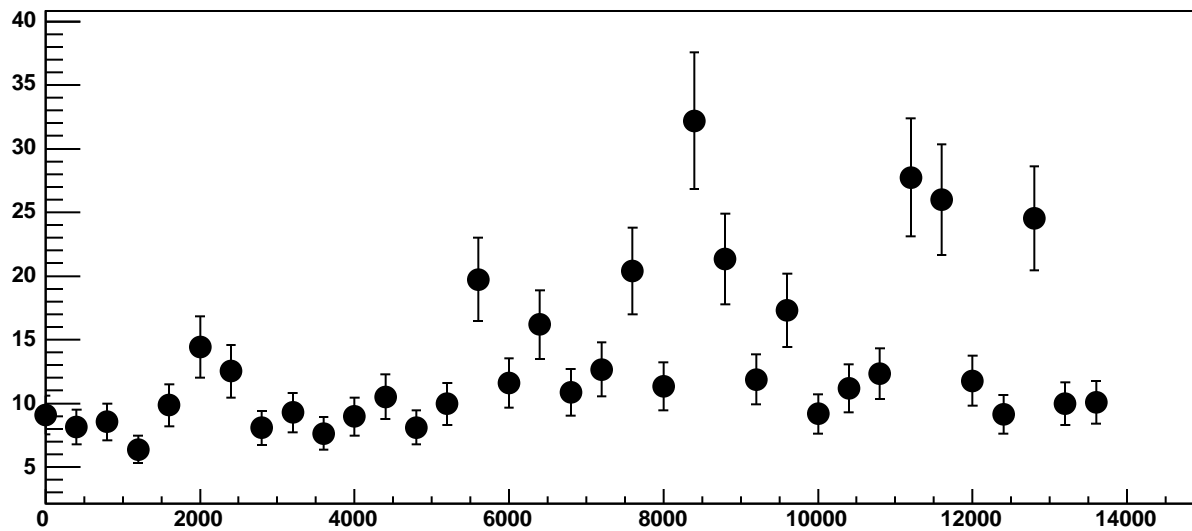


Chip 8, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

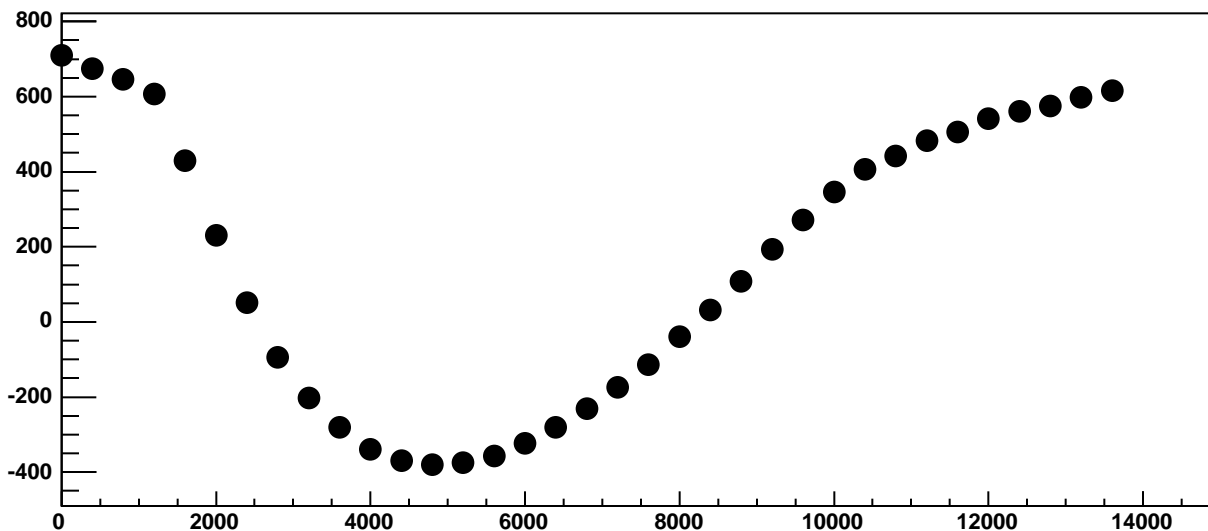


χ^2 / ndf 4.965e+05 / 23
p0 -585.2 ± 0.9819
p1 0.0993 ± 0.0001673

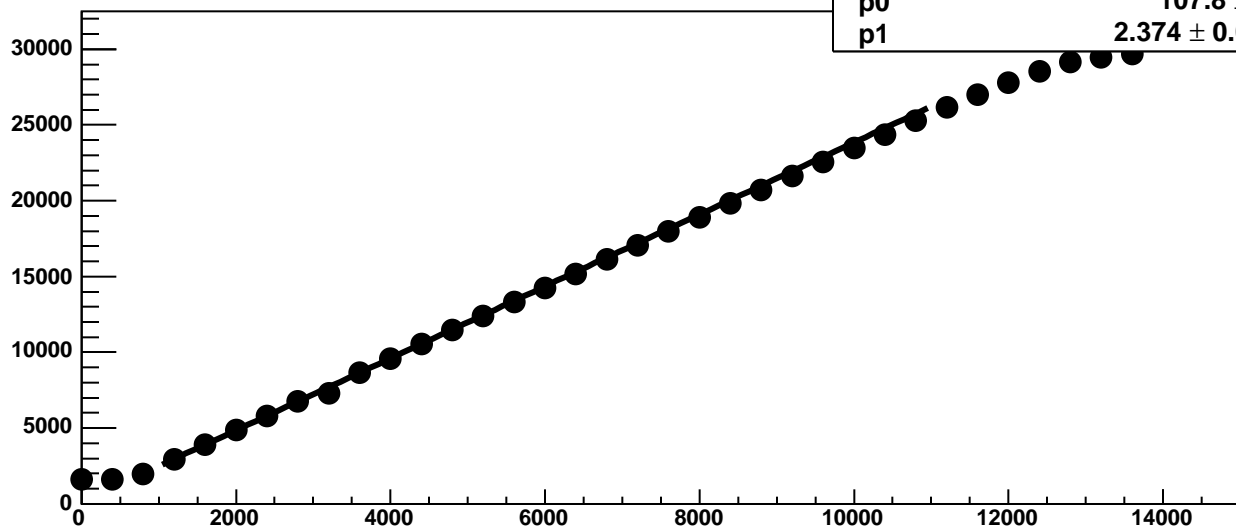
Chip 8, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



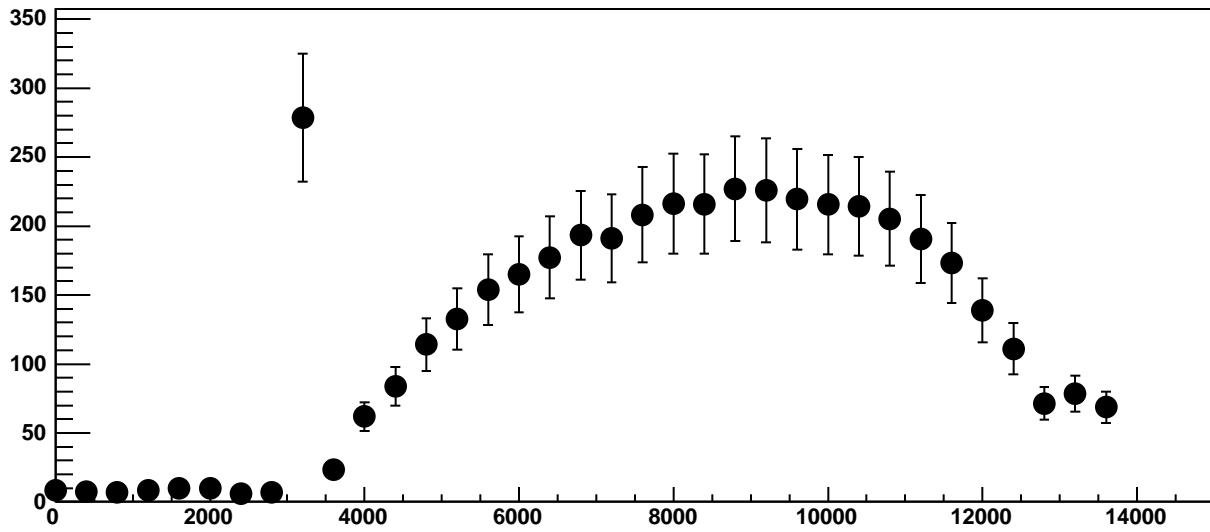
Chip 8, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



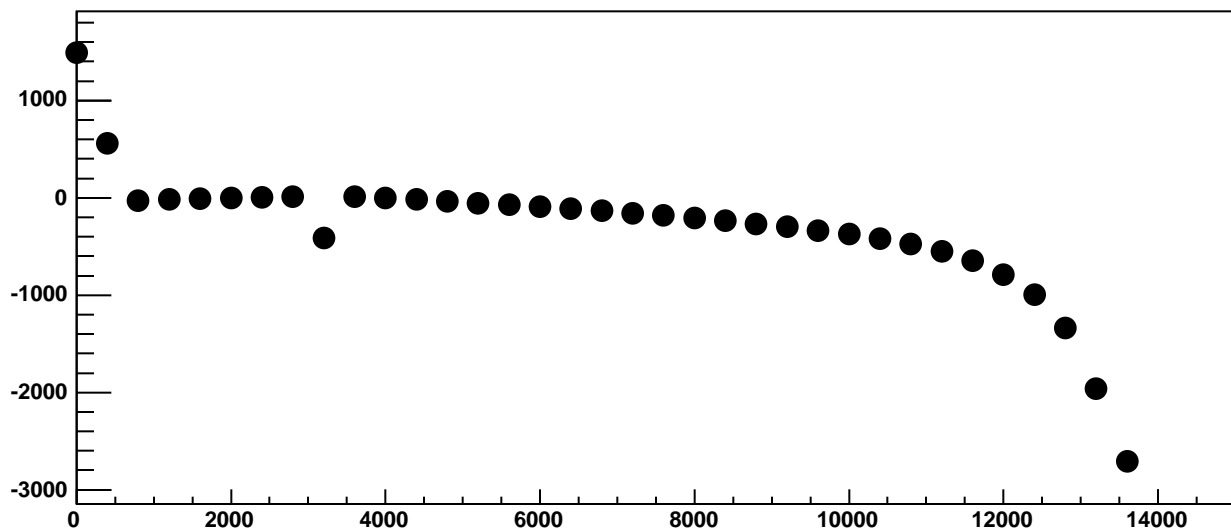
Chip 8, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC



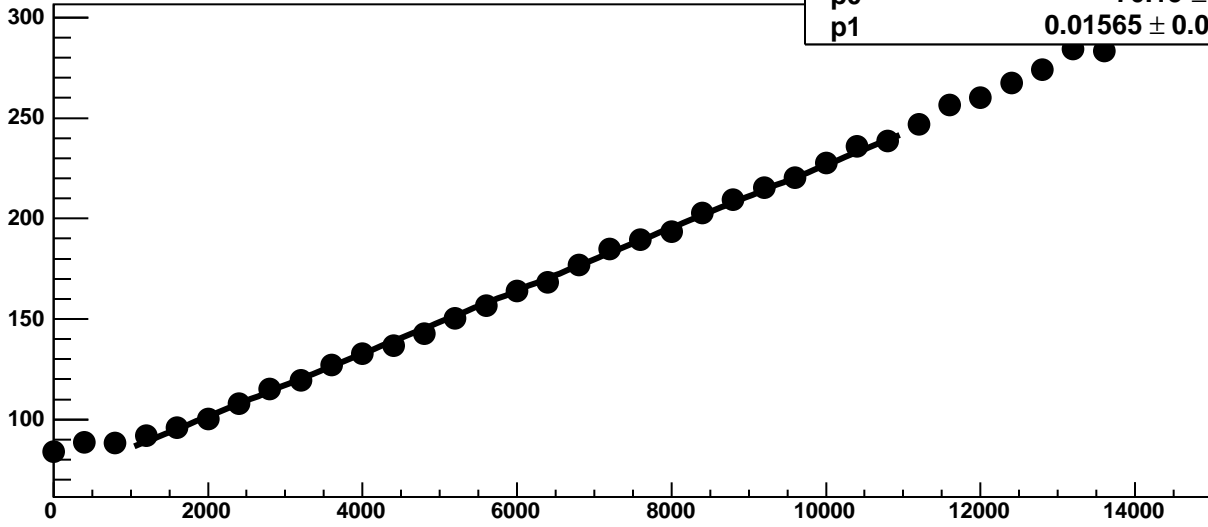
Chip 8, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC

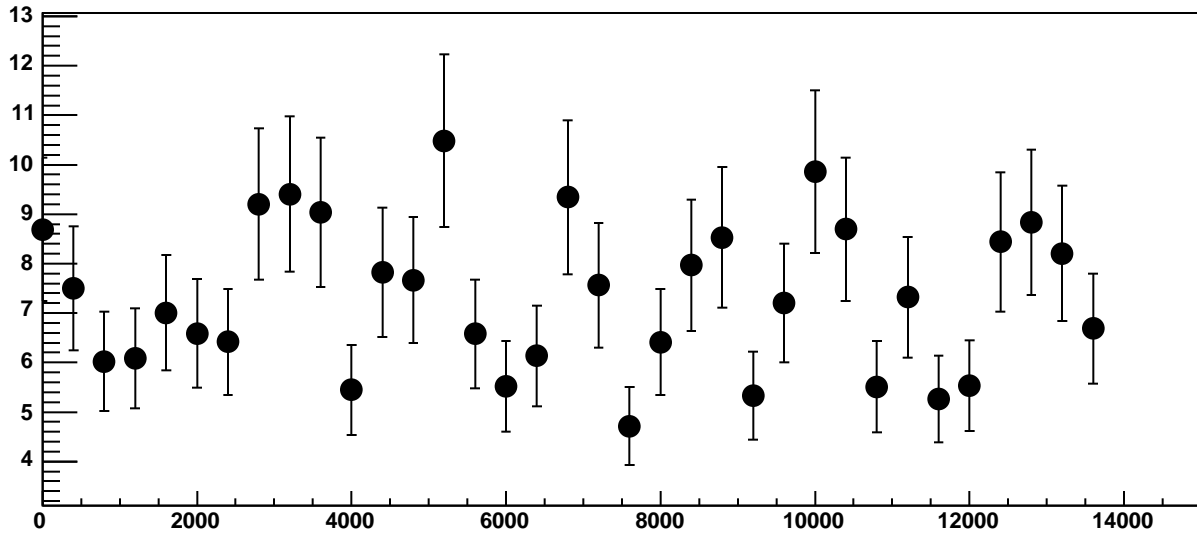


Chip 8, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

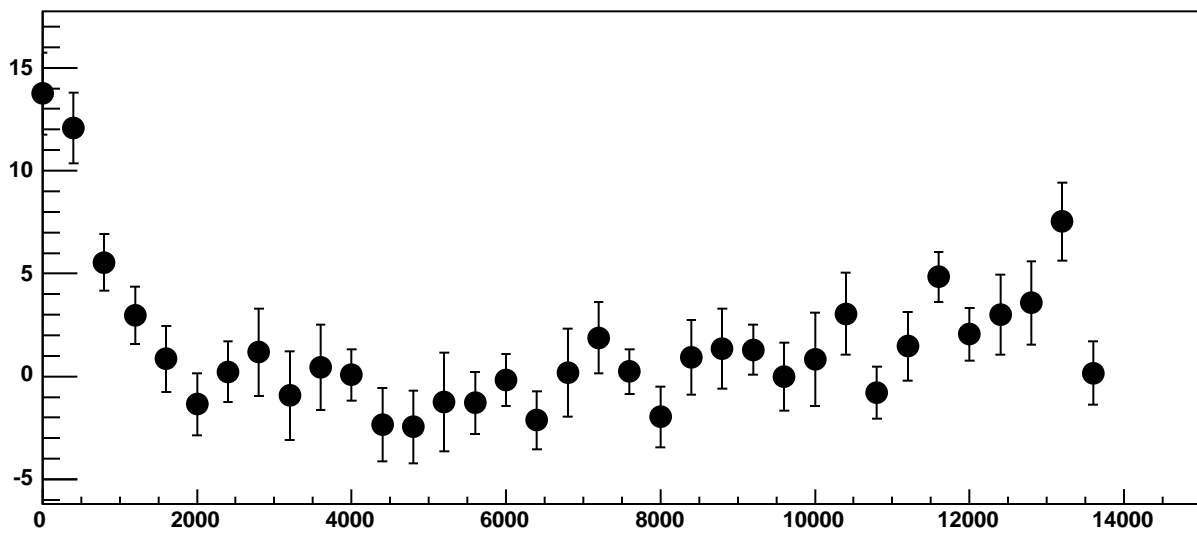


χ^2 / ndf 20.84 / 23
p0 70.15 ± 0.7358
p1 0.01565 ± 0.0001091

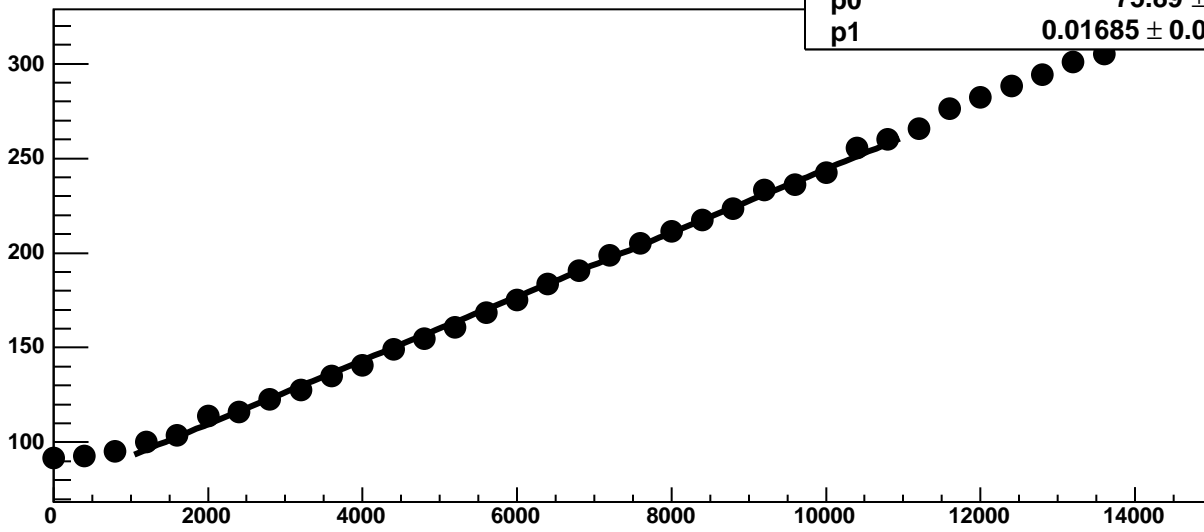
Chip 8, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

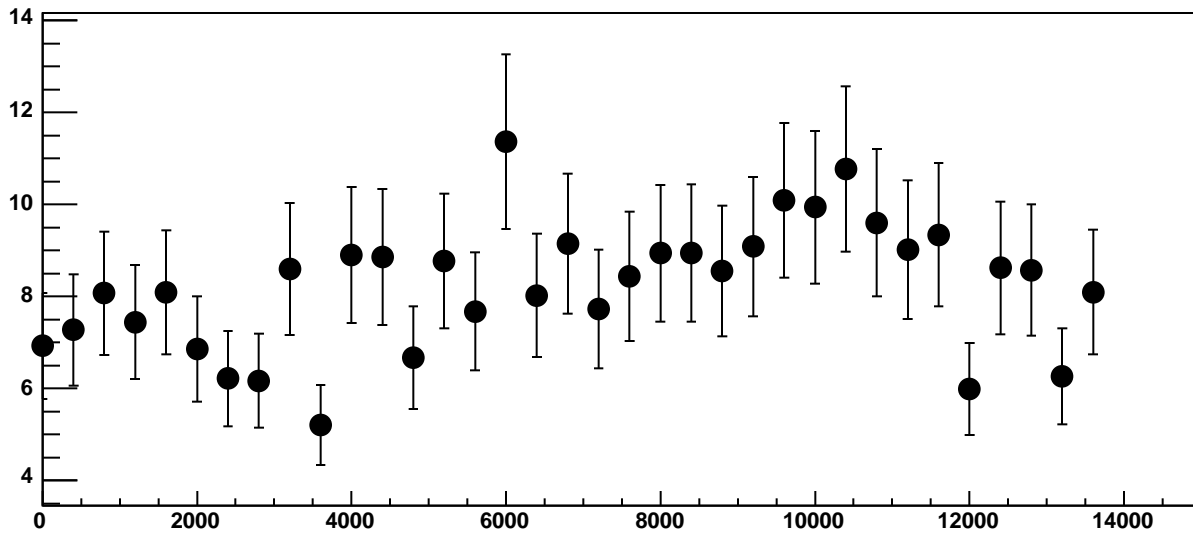


Chip 8, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

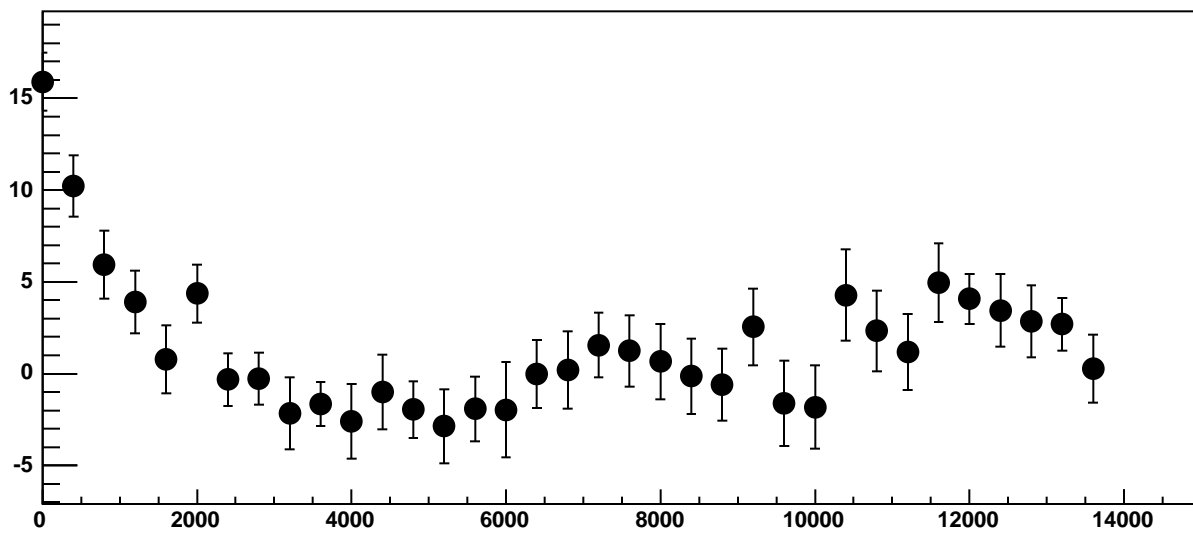


χ^2 / ndf 31.65 / 23
p0 75.89 ± 0.7876
p1 0.01685 ± 0.0001317

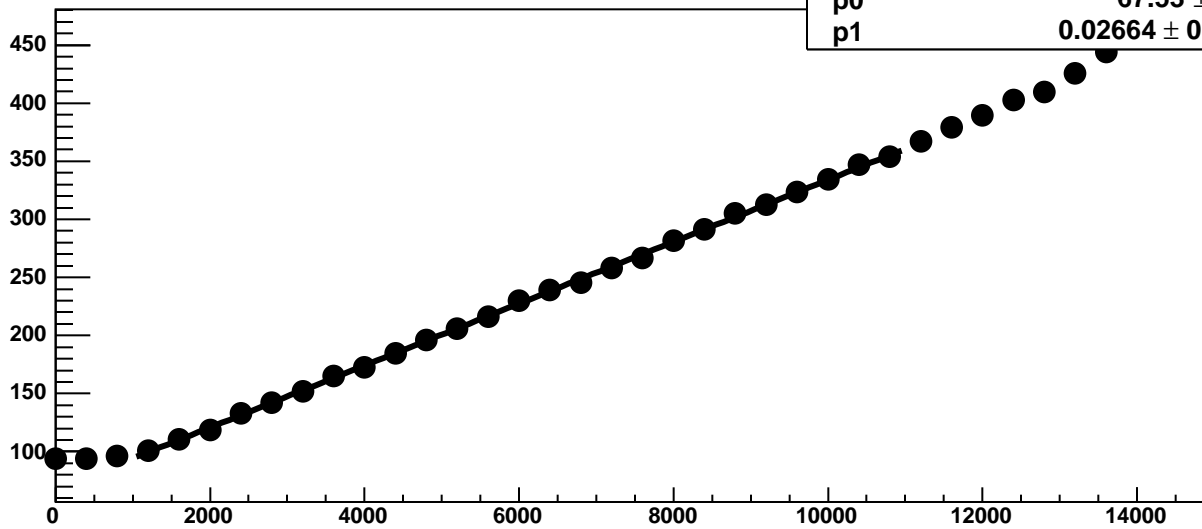
Chip 8, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

14.13 / 23

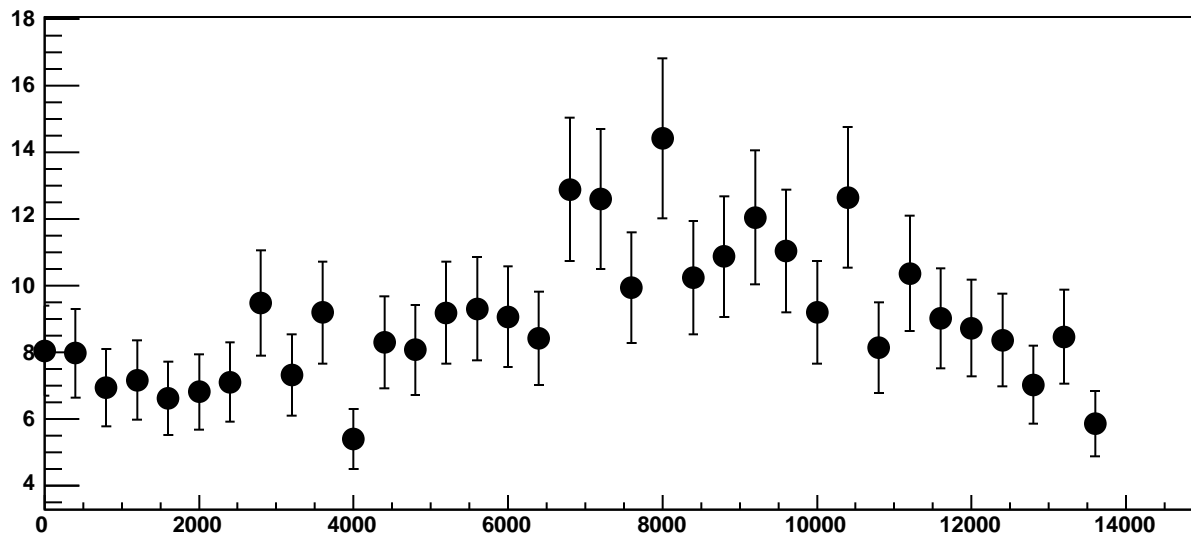
p0

67.53 ± 0.8073

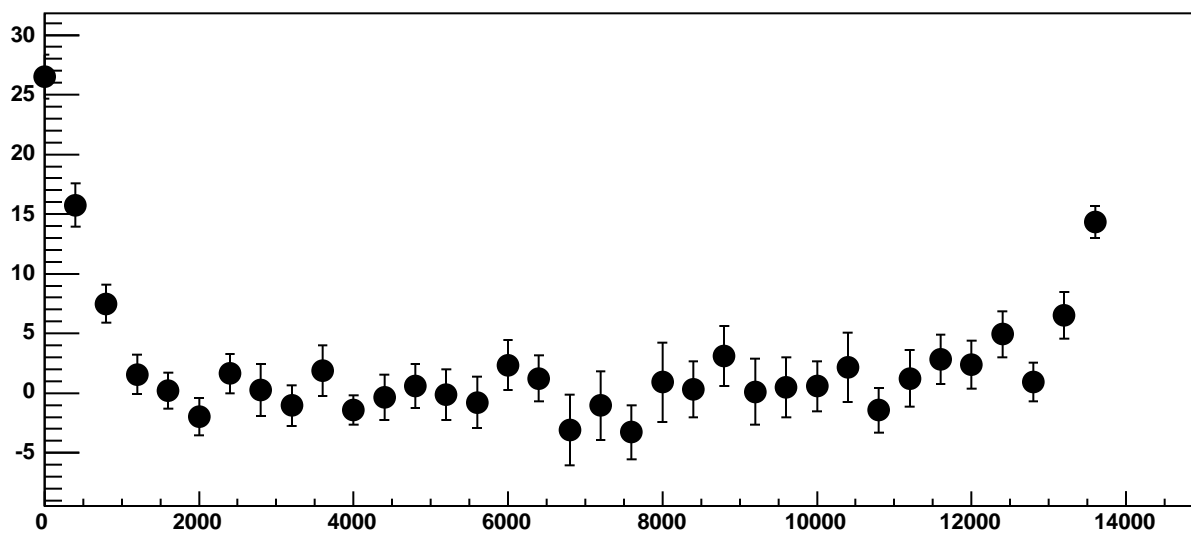
p1

0.02664 ± 0.000138

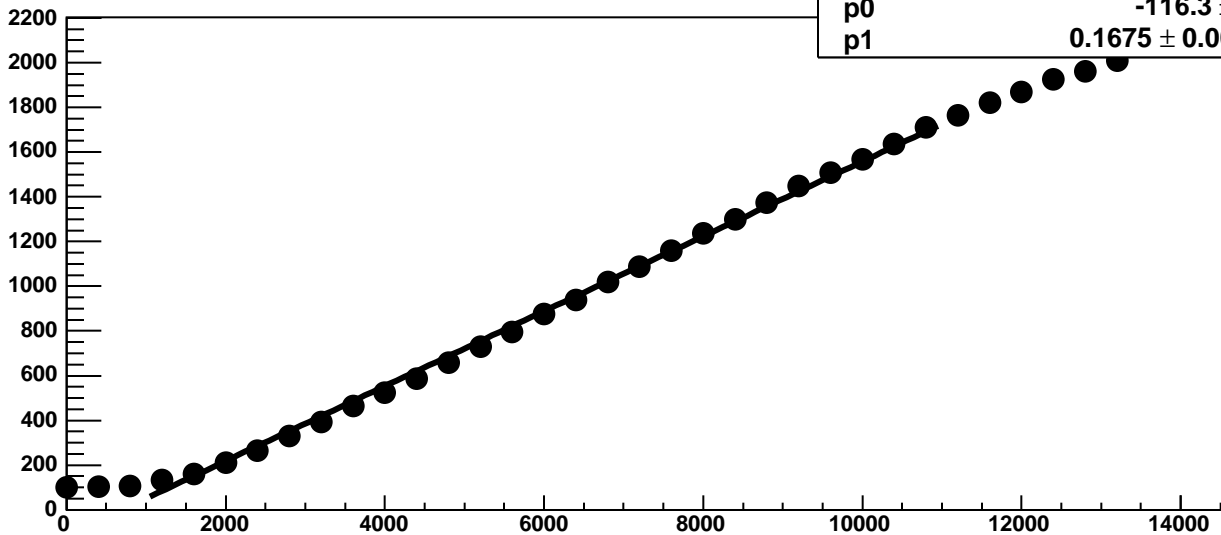
Chip 8, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC

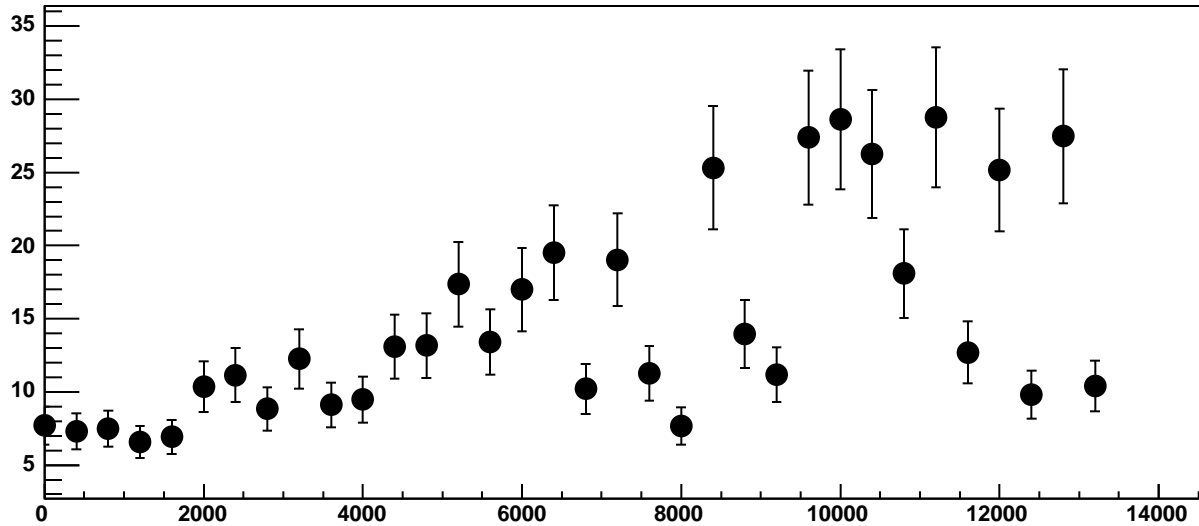


Chip 8, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

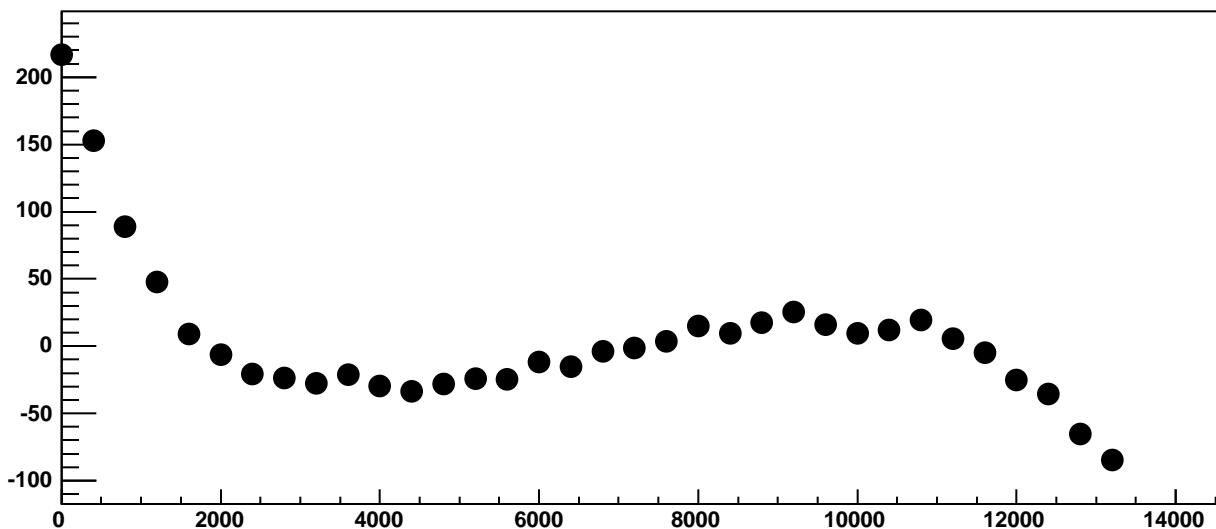


χ^2 / ndf 2197 / 23
p0 -116.3 ± 1.008
p1 0.1675 ± 0.0001874

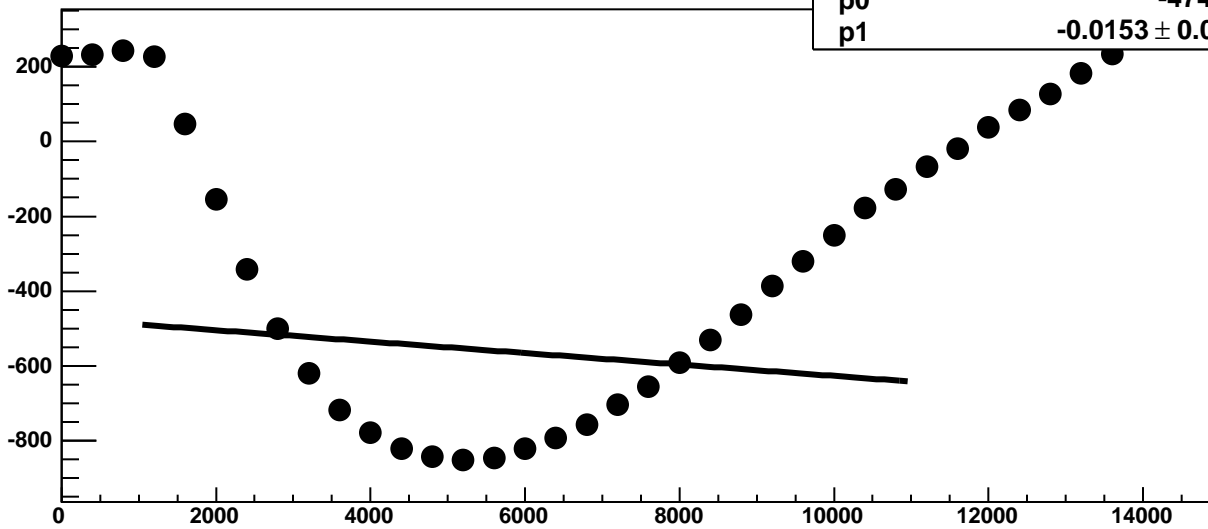
Chip 8, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



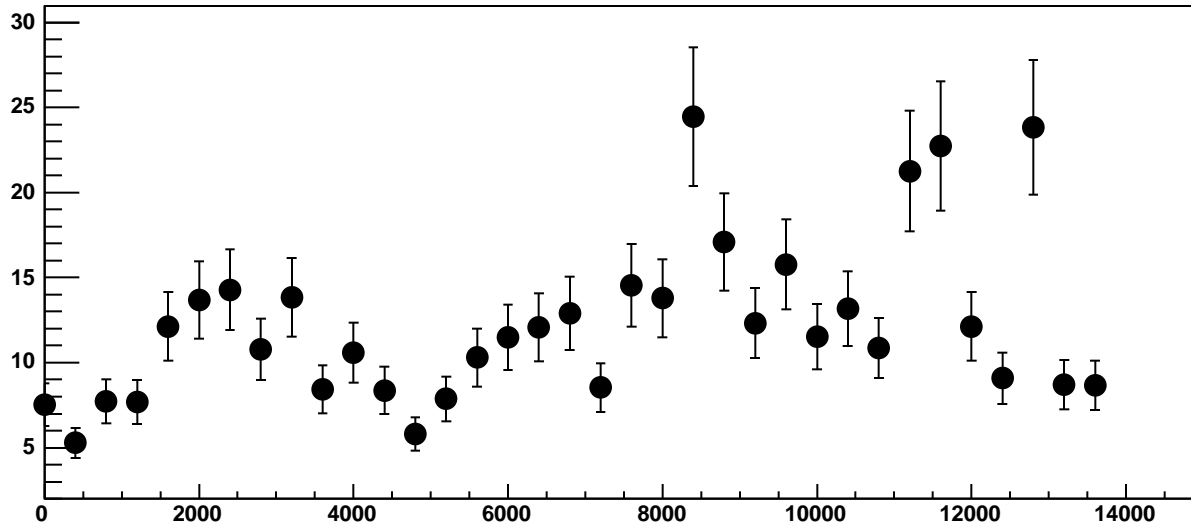
Chip 8, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



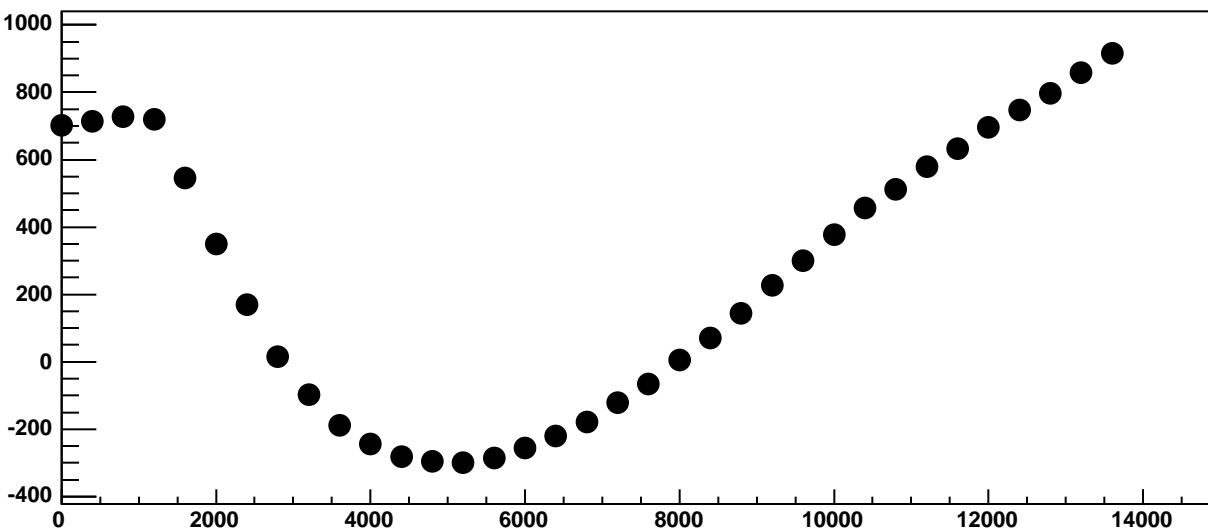
Chip 8, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



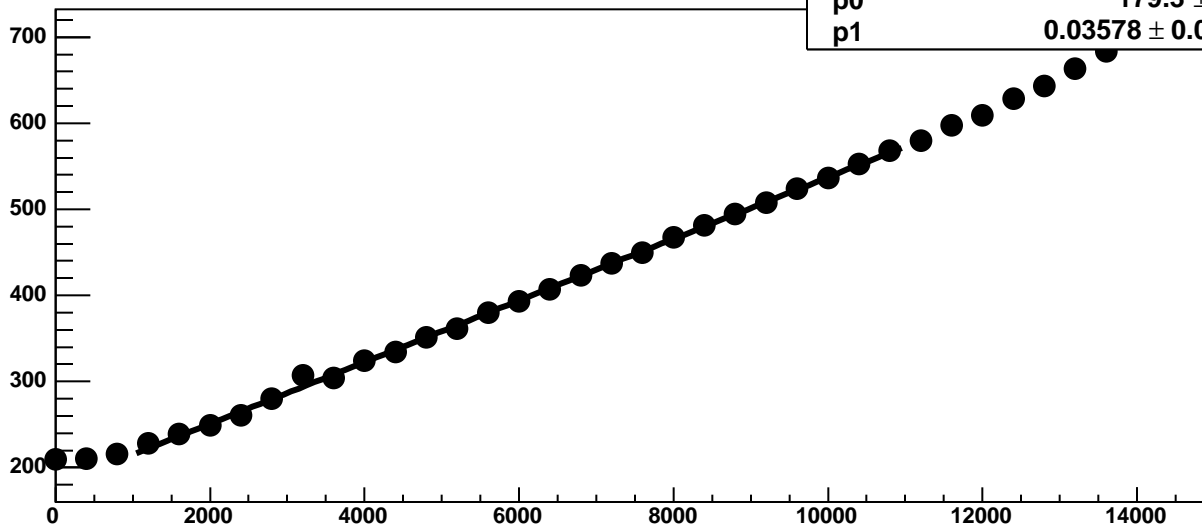
Chip 8, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

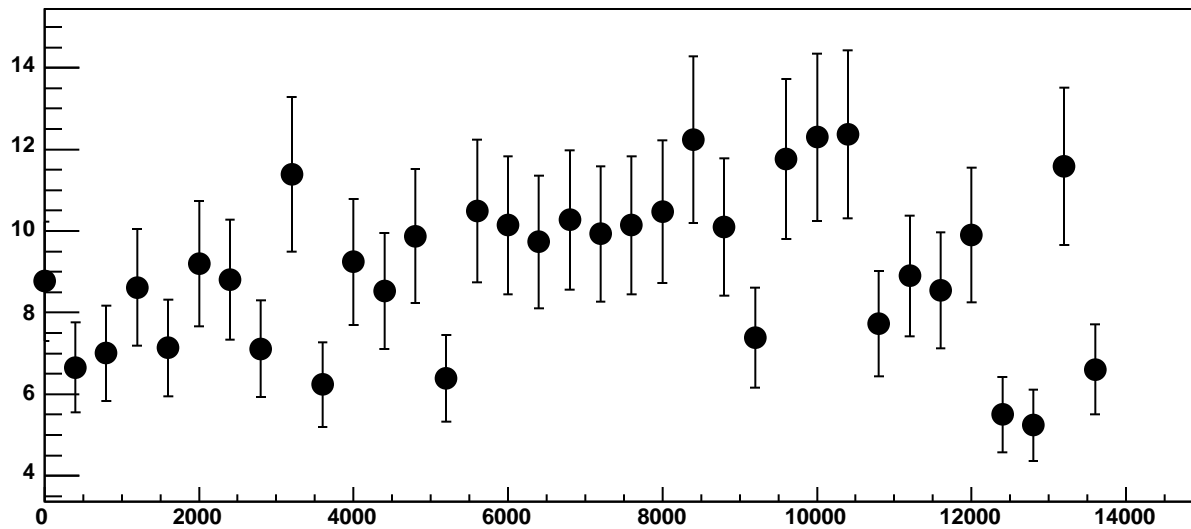


Chip 8, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

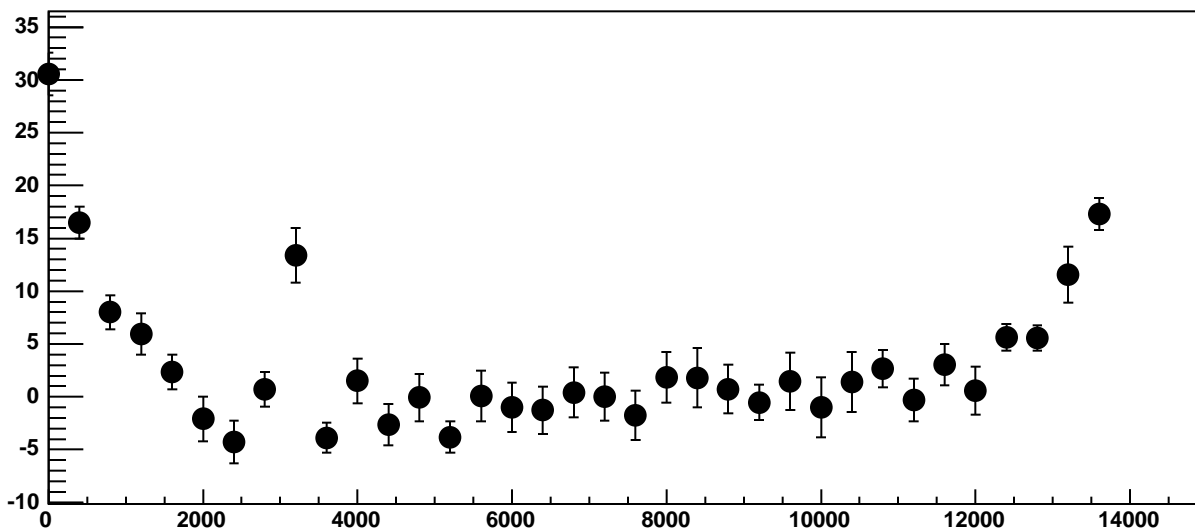


χ^2 / ndf 64.5 / 23
p0 179.3 ± 0.8864
p1 0.03578 ± 0.0001424

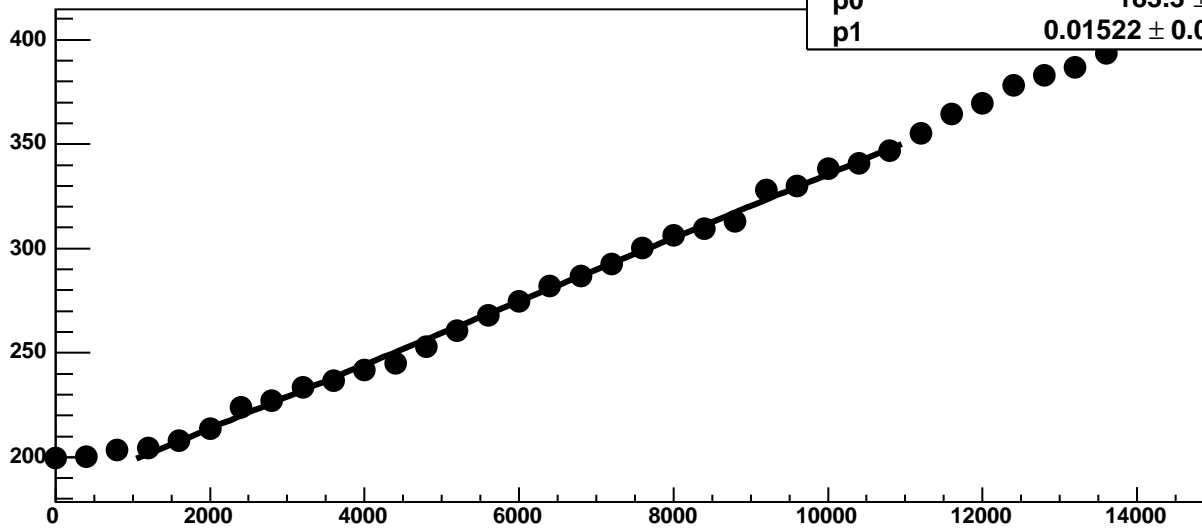
Chip 8, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC

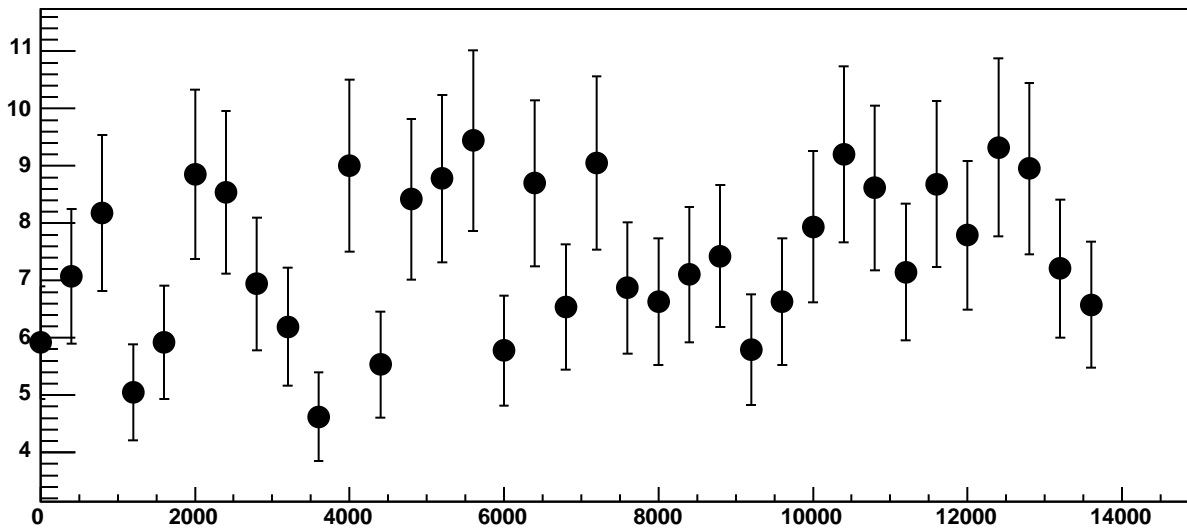


Chip 8, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

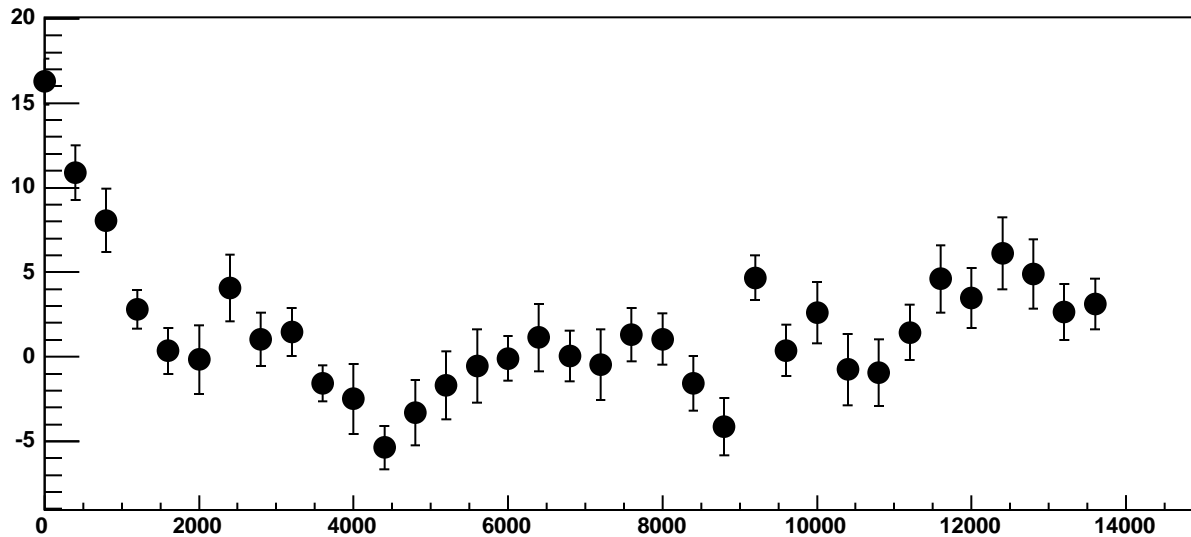


χ^2 / ndf 60.15 / 23
p0 183.3 ± 0.6886
p1 0.01522 ± 0.0001089

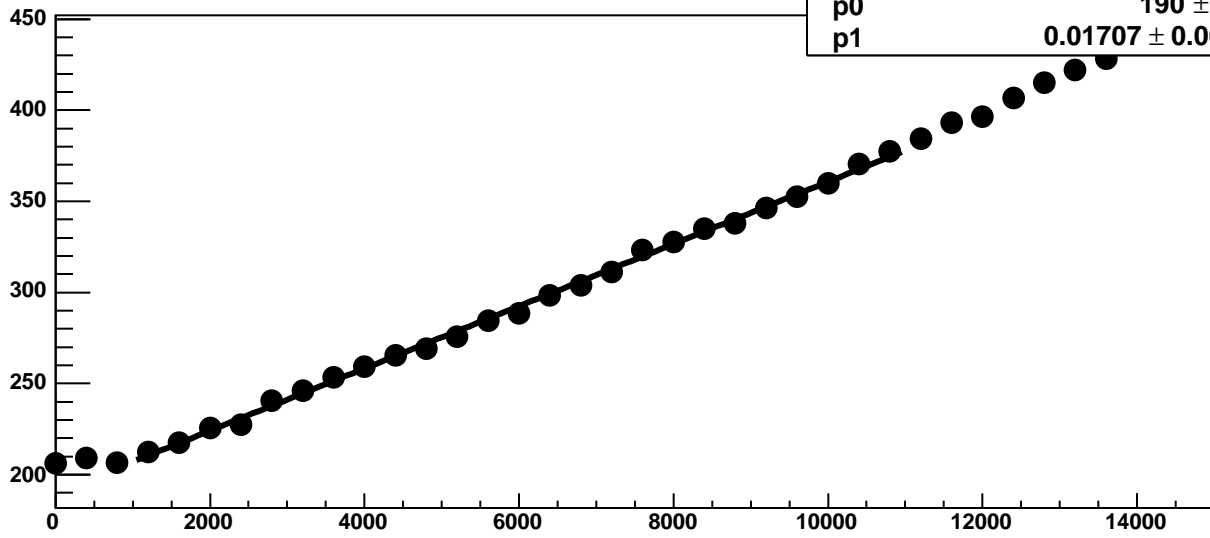
Chip 8, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

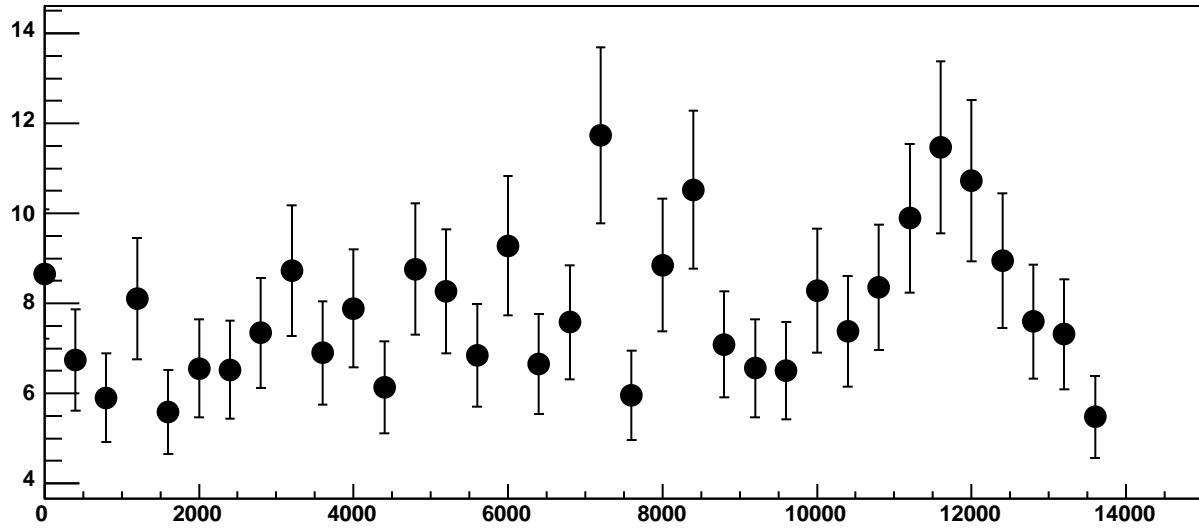


Chip 8, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

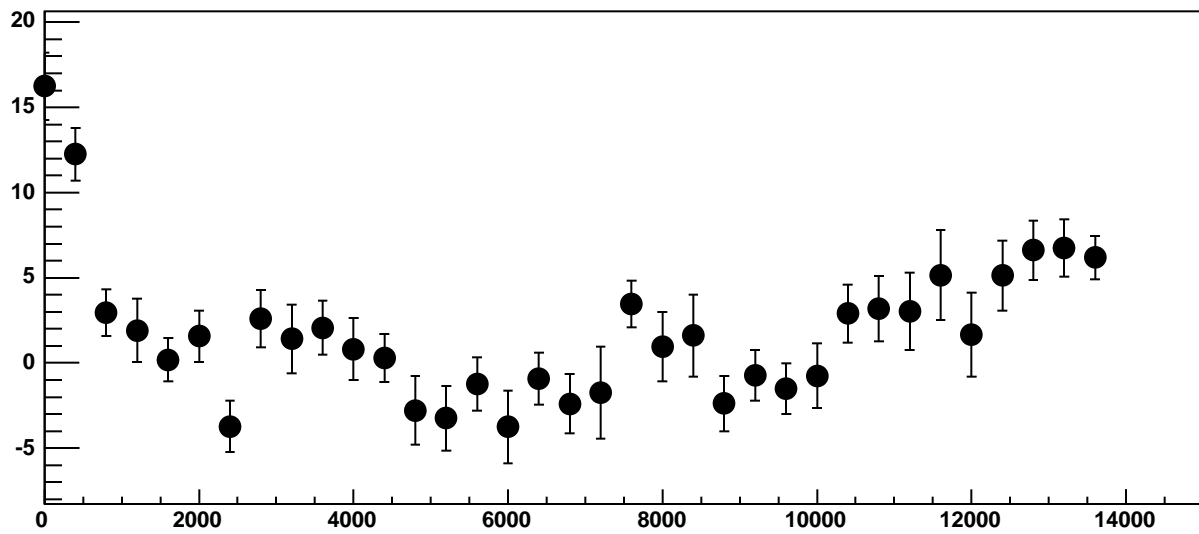


χ^2 / ndf 40.83 / 23
p0 190 ± 0.7424
p1 0.01707 ± 0.0001144

Chip 8, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

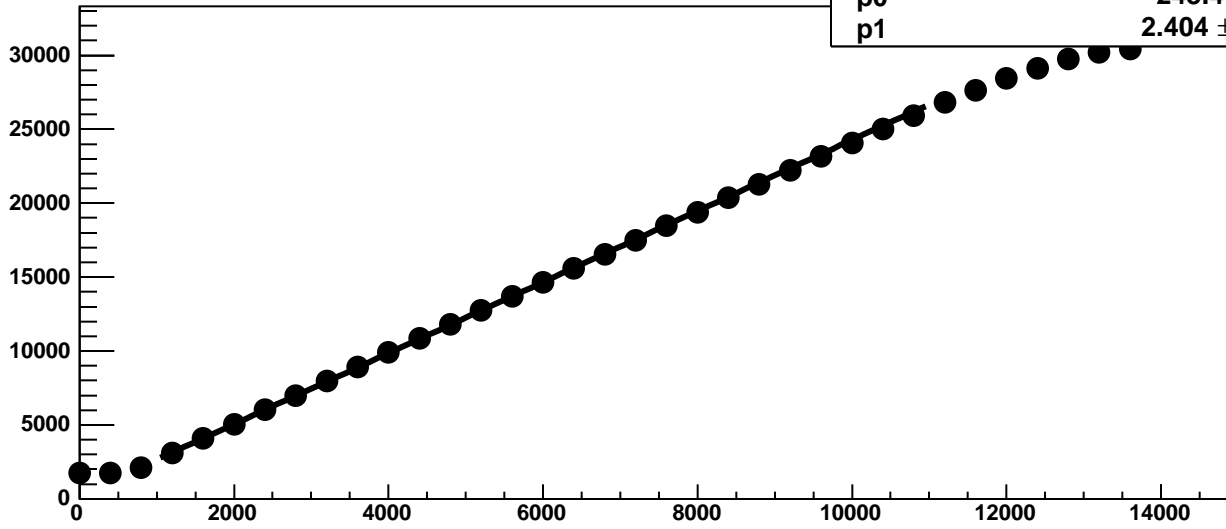


Chip 8, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

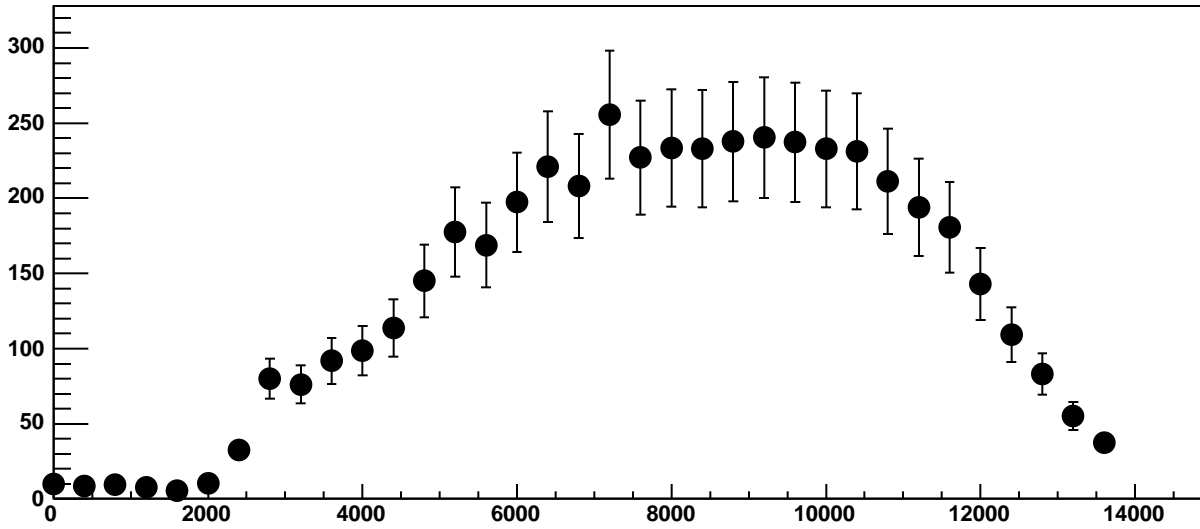


Chip 8, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC

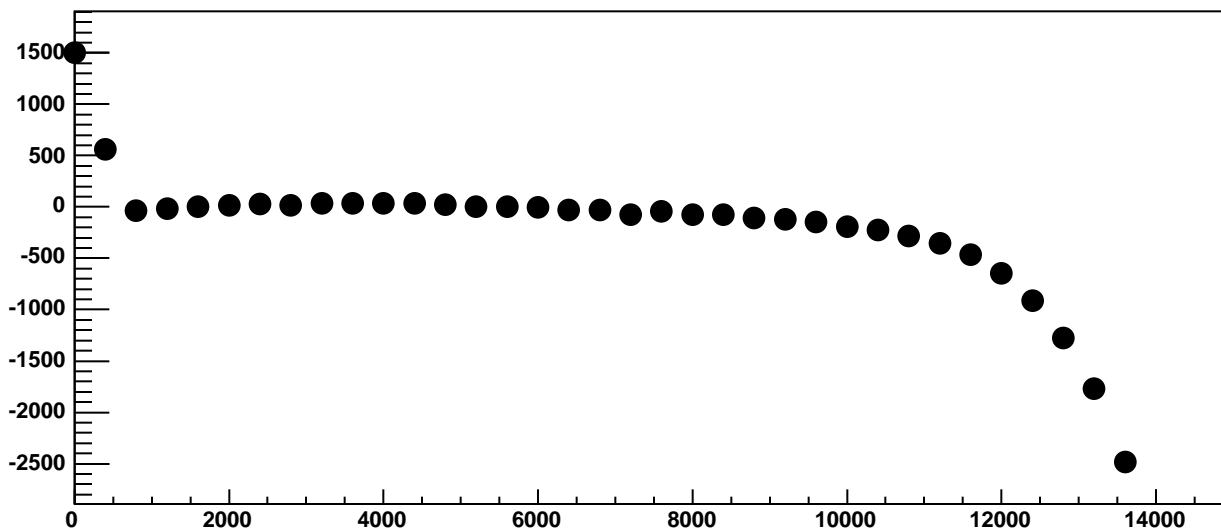
χ^2 / ndf 217 / 23
p0 243.4 ± 2.765
p1 2.404 ± 0.0016



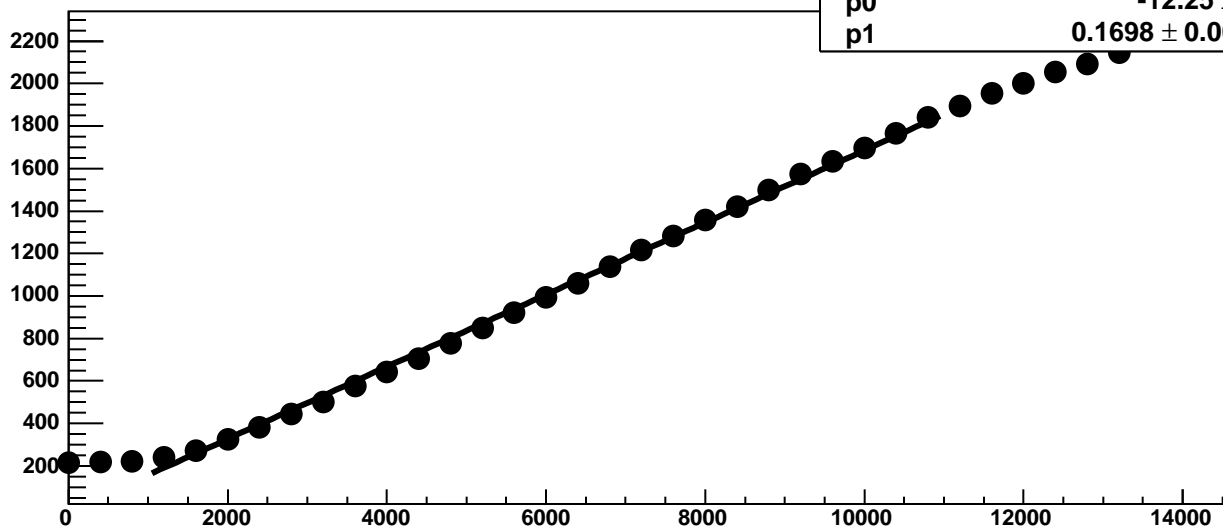
Chip 8, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



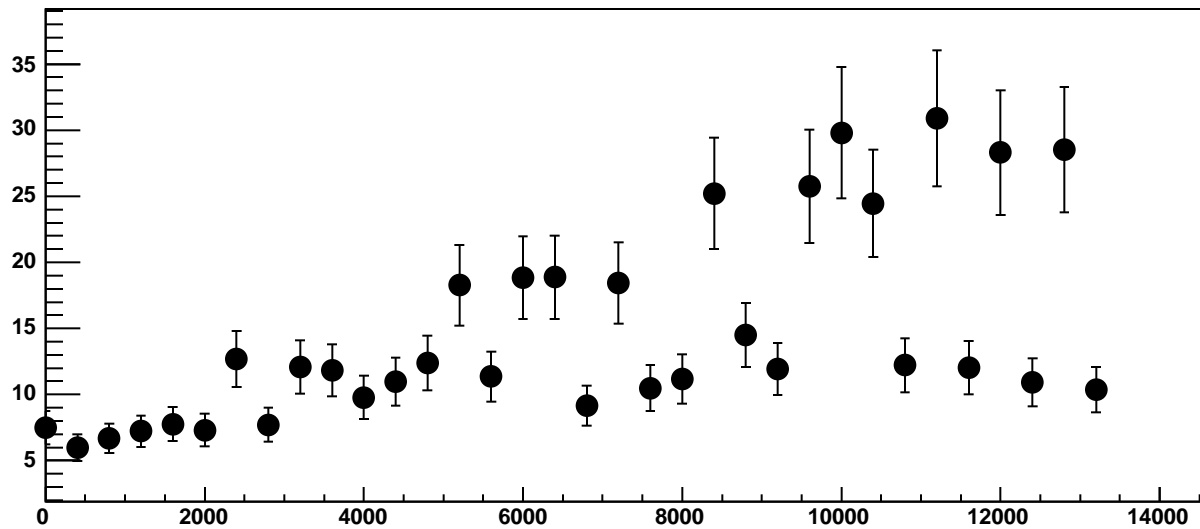
Chip 8, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC



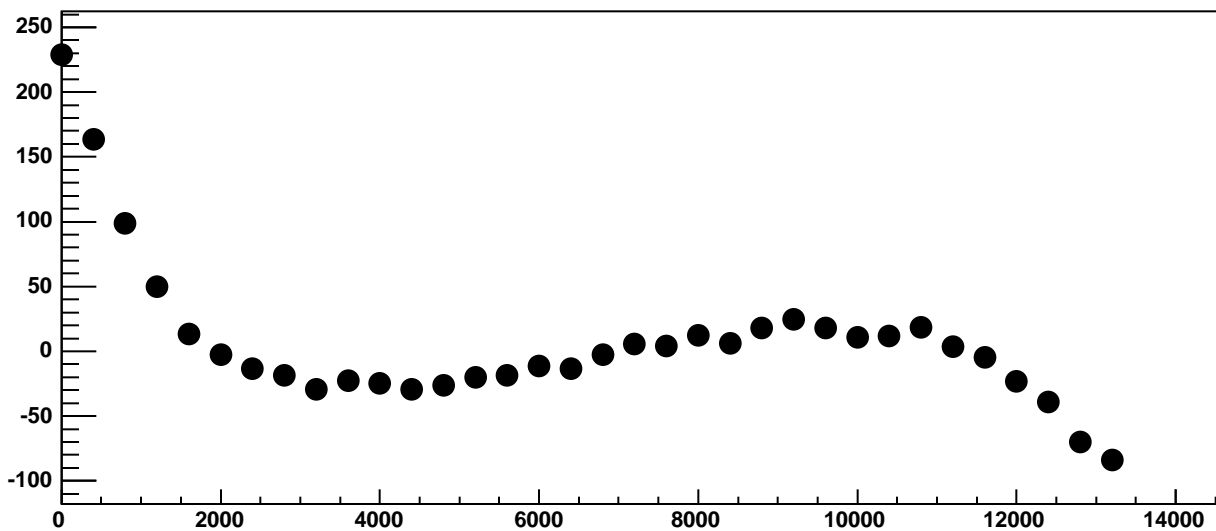
Chip 8, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



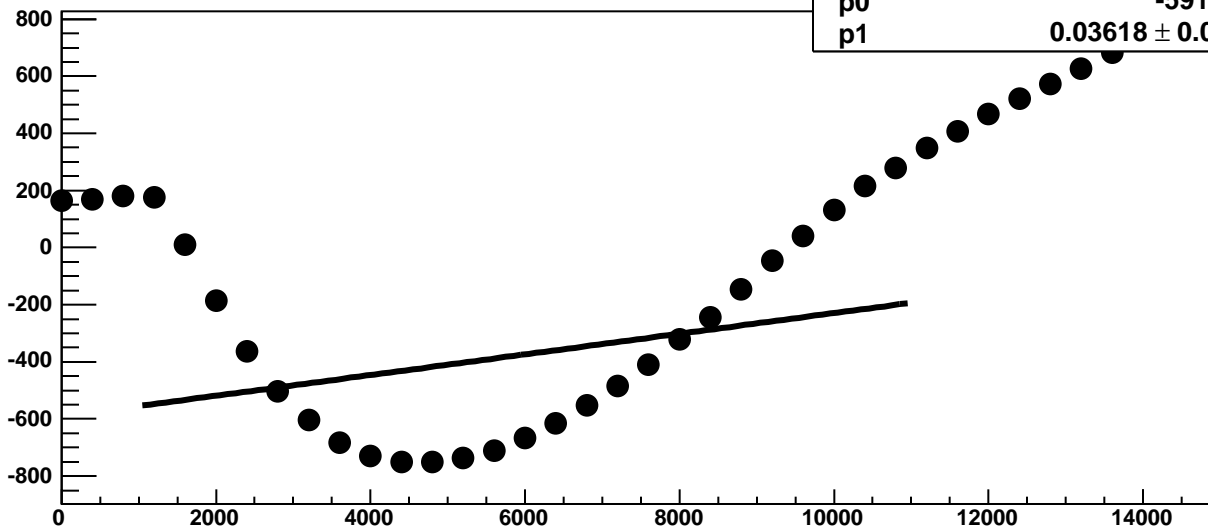
Chip 8, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

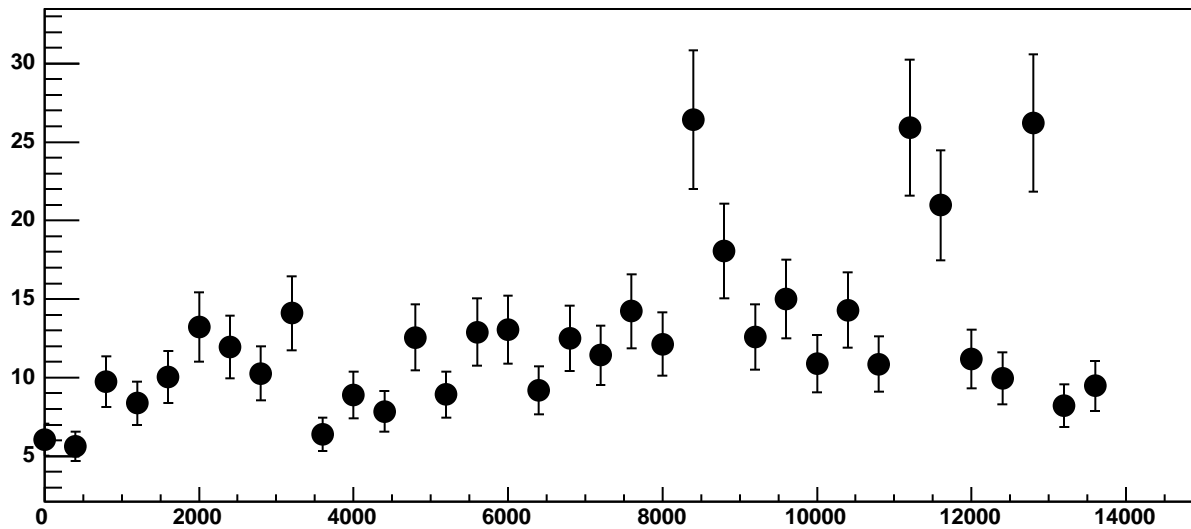


Chip 8, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

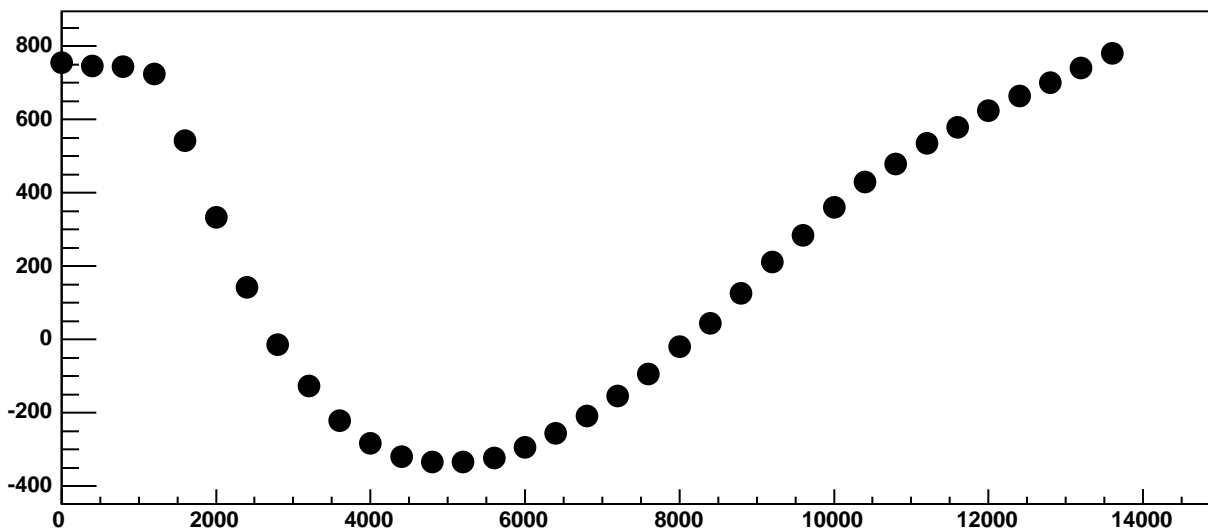


χ^2 / ndf 4.639e+05 / 23
p0 -591 ± 1.074
p1 0.03618 ± 0.0001795

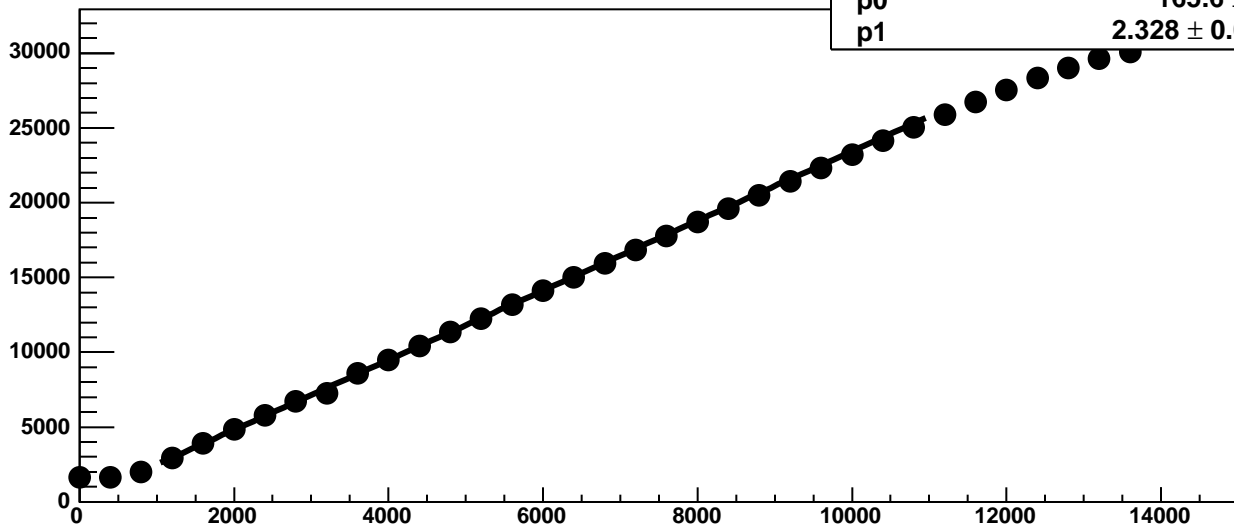
Chip 8, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

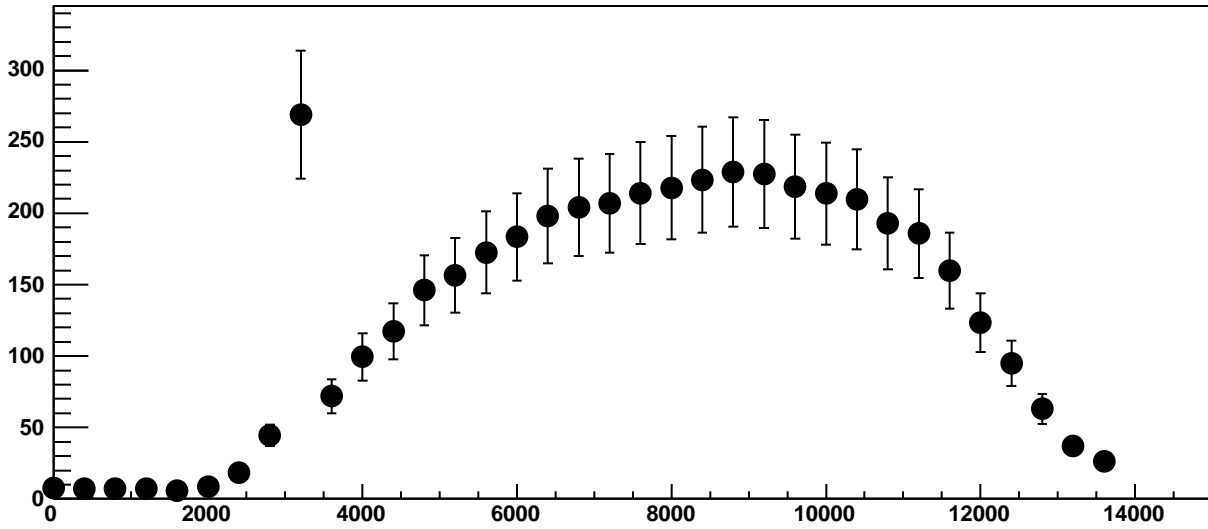


Chip 8, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC

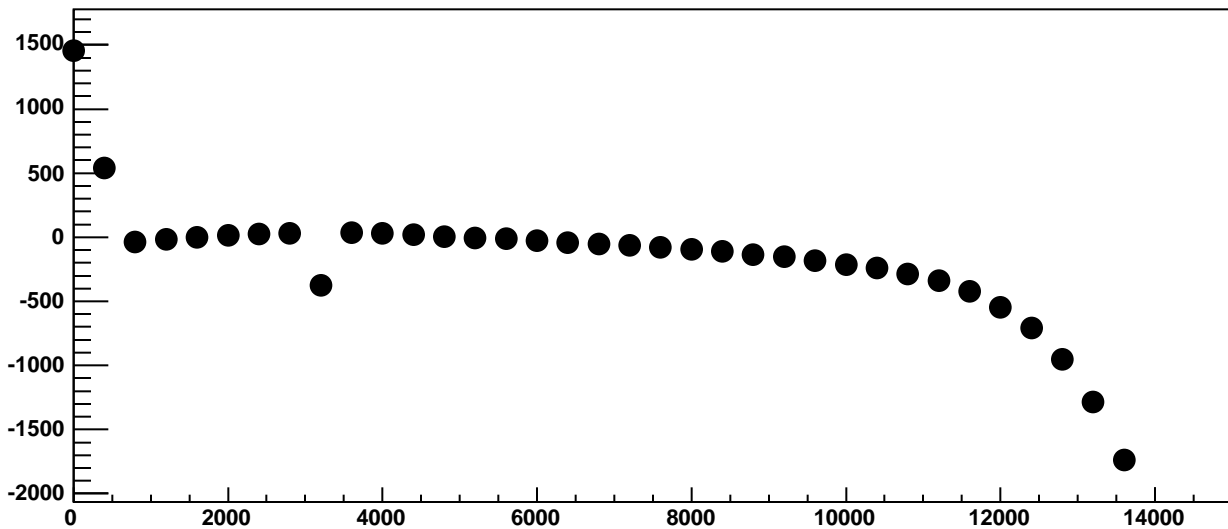


χ^2 / ndf	371.6 / 23
p0	165.6 ± 2.577
p1	2.328 ± 0.001449

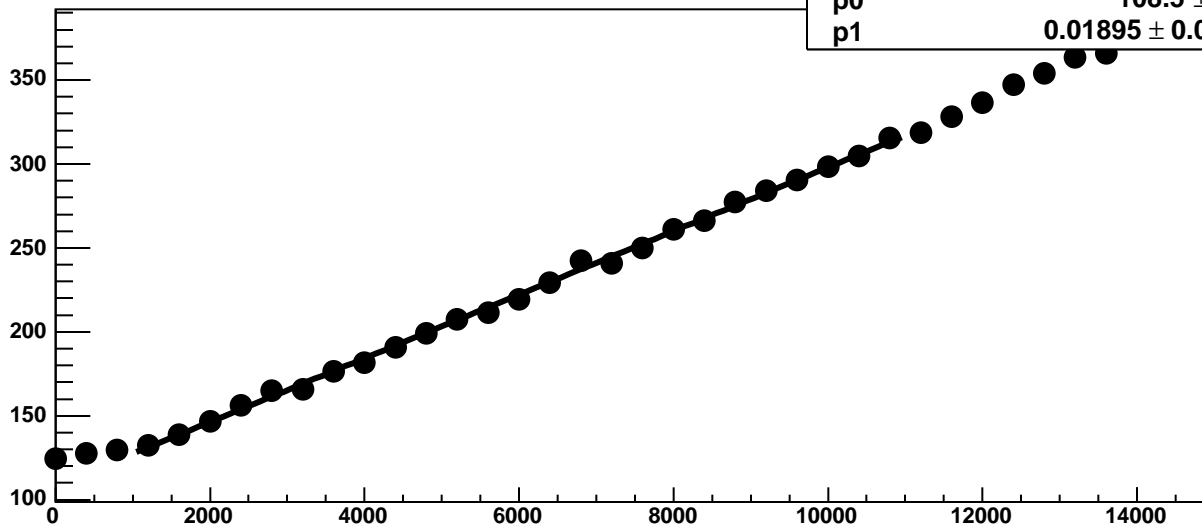
Chip 8, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC

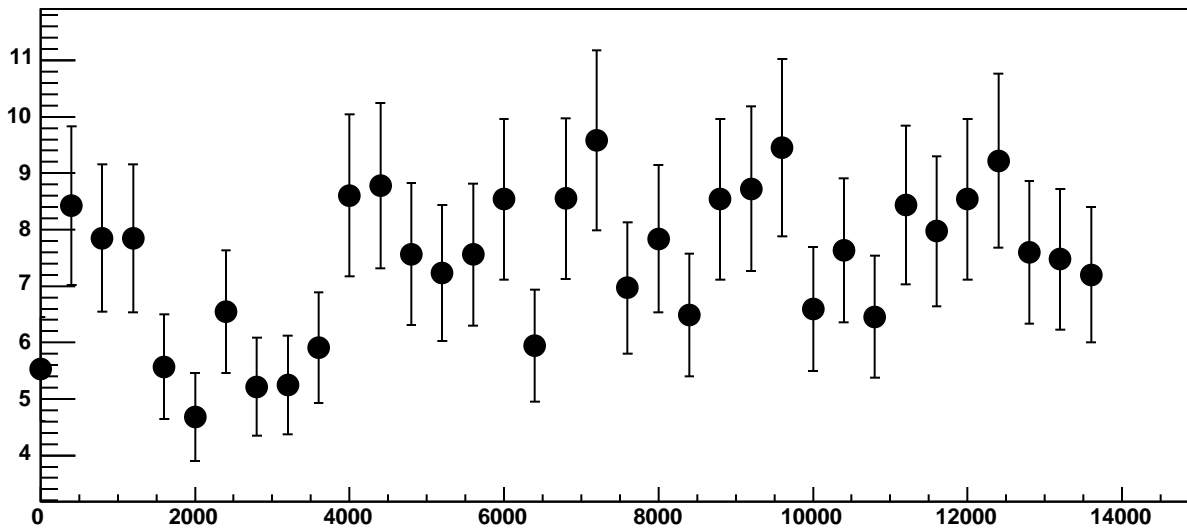


Chip 8, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

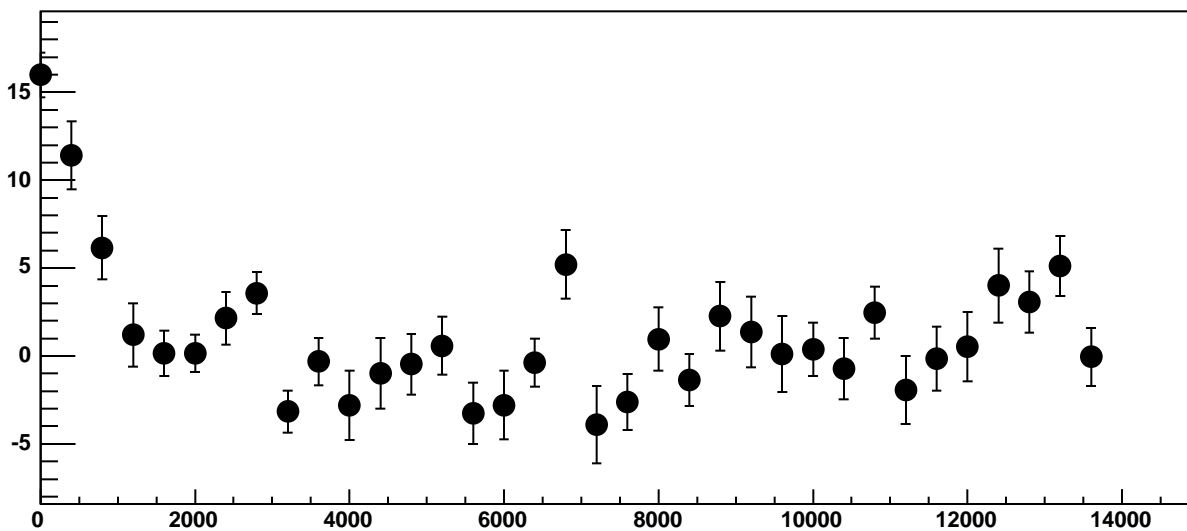


χ^2 / ndf 45.37 / 23
p0 108.5 ± 0.6534
p1 0.01895 ± 0.0001055

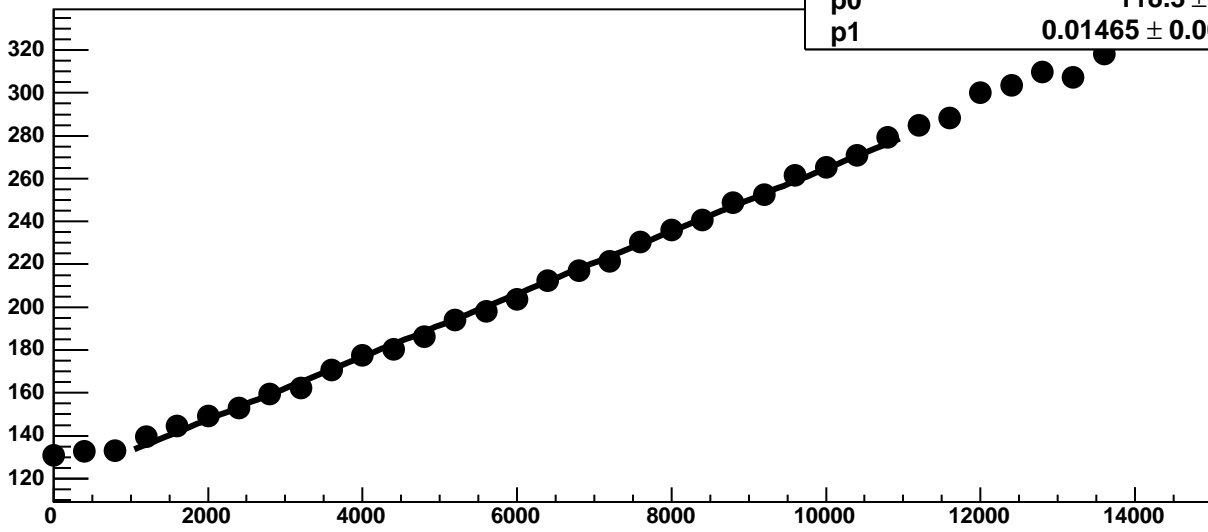
Chip 8, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

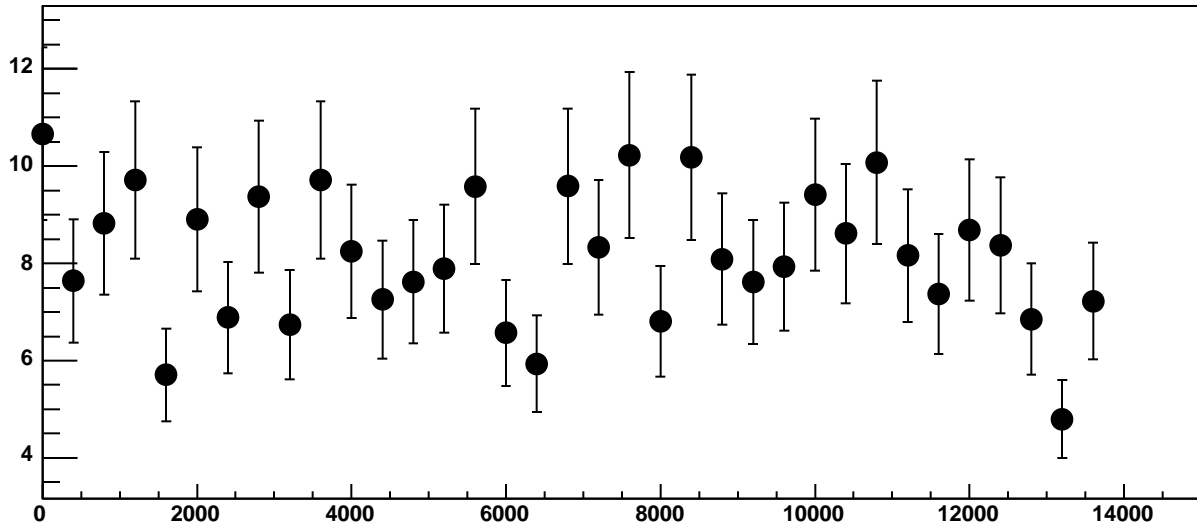


Chip 8, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

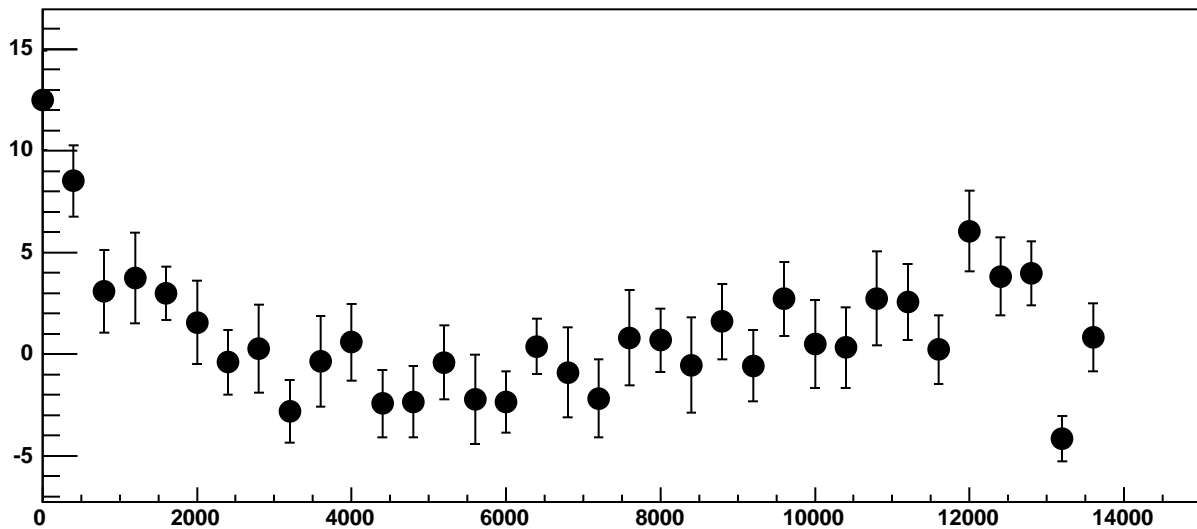


χ^2 / ndf 26.03 / 23
p0 118.3 ± 0.8254
p1 0.01465 ± 0.0001293

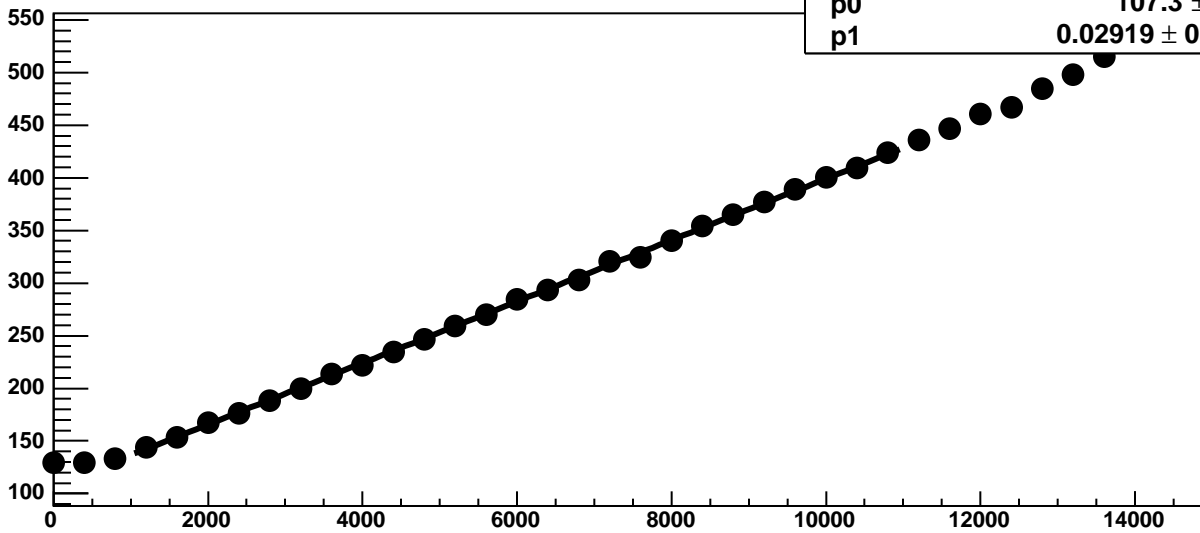
Chip 8, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

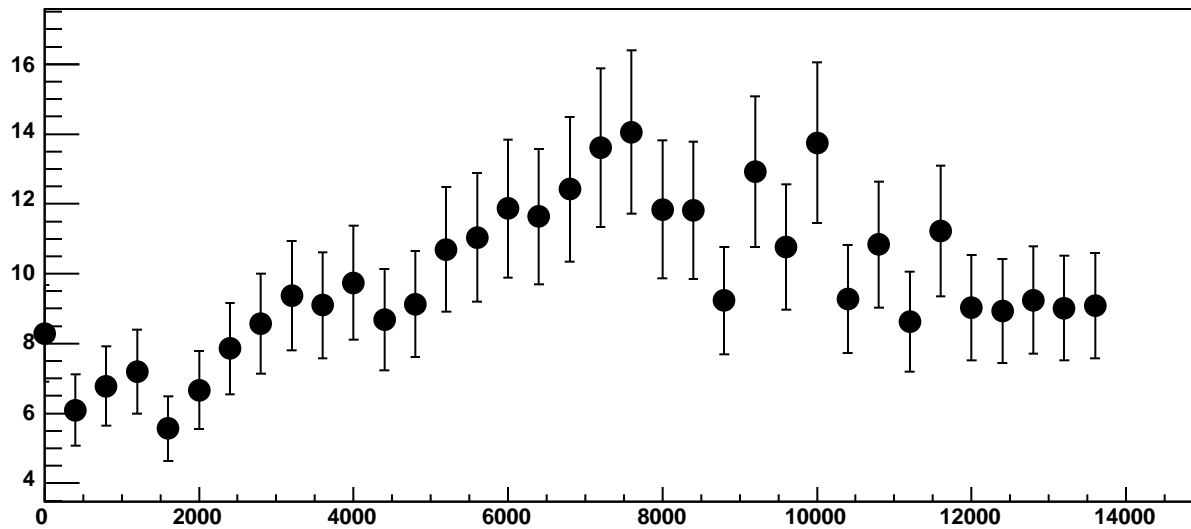


Chip 8, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

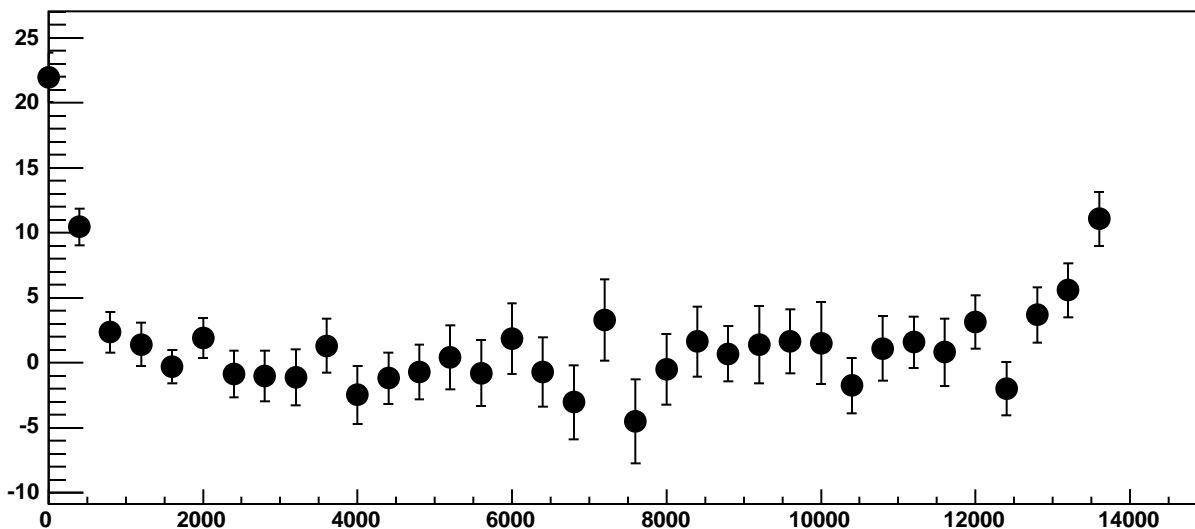


χ^2 / ndf 12.33 / 23
p0 107.3 ± 0.8259
p1 0.02919 ± 0.000143

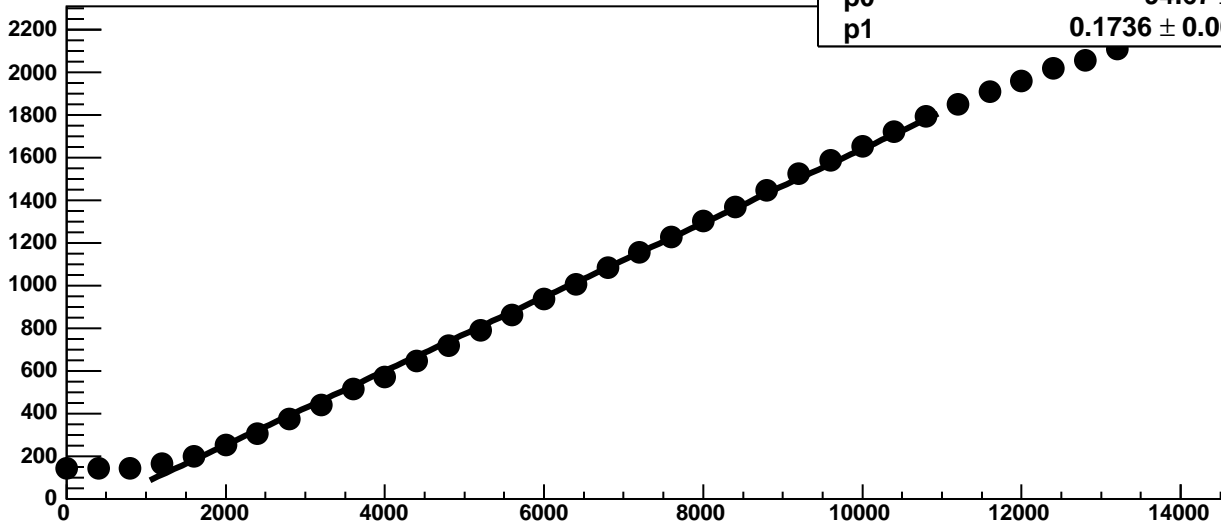
Chip 8, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC

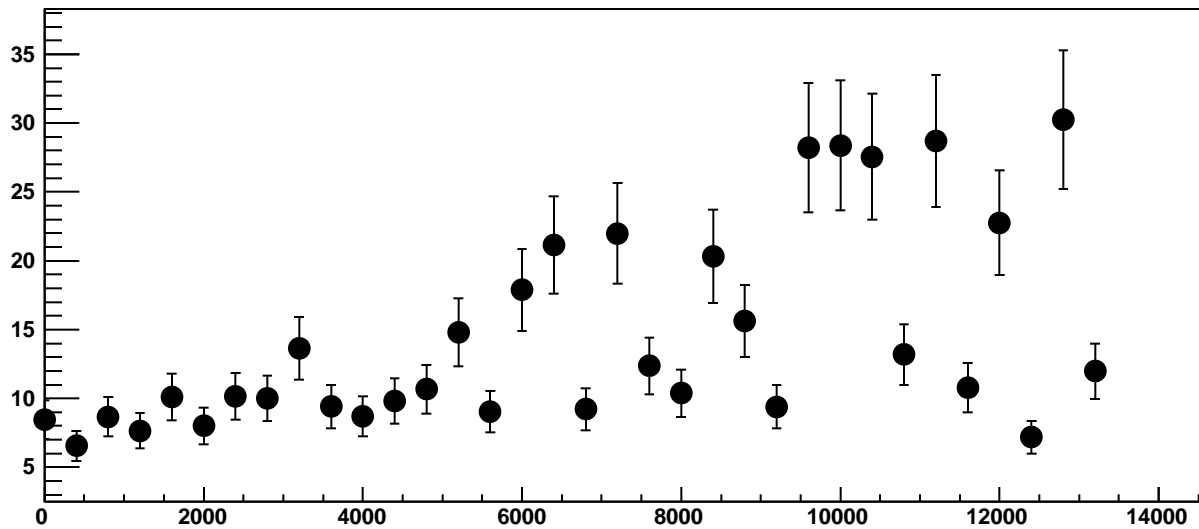


Chip 8, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

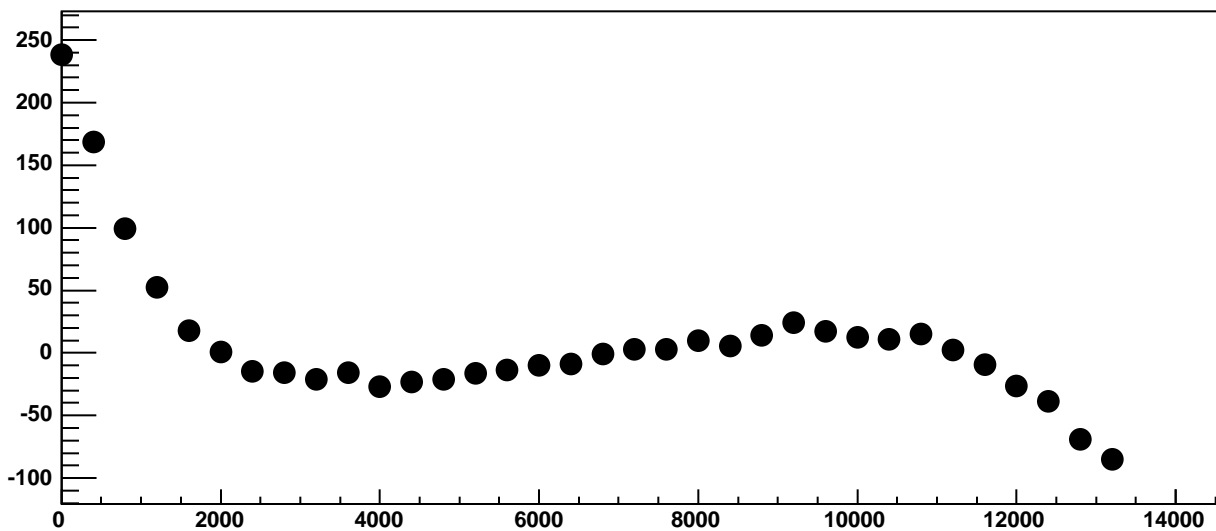


χ^2 / ndf 1768 / 23
p0 -94.67 ± 1.067
p1 0.1736 ± 0.0001918

Chip 8, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC

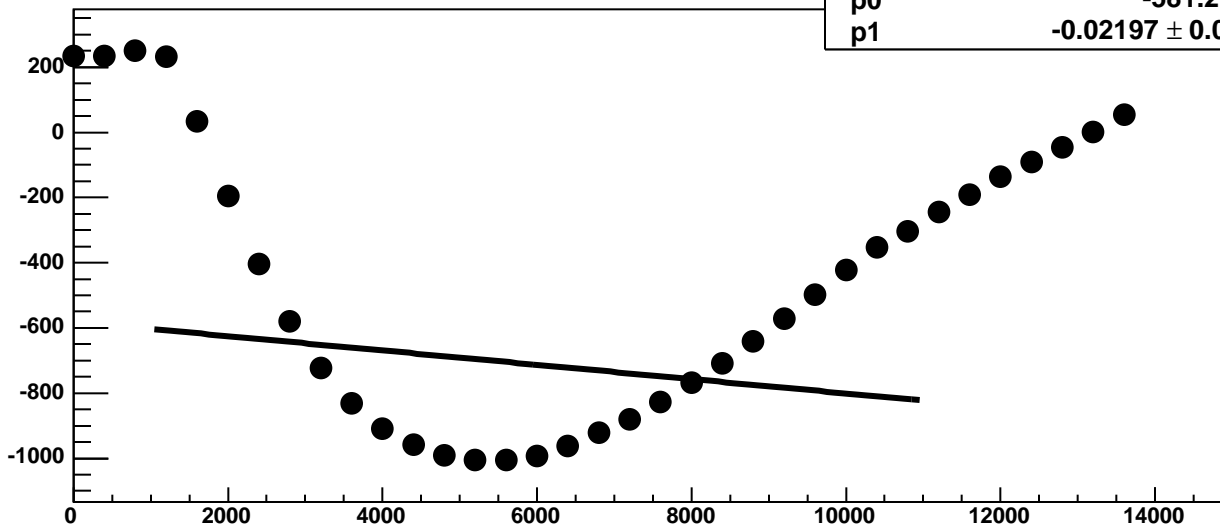


Chip 8, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

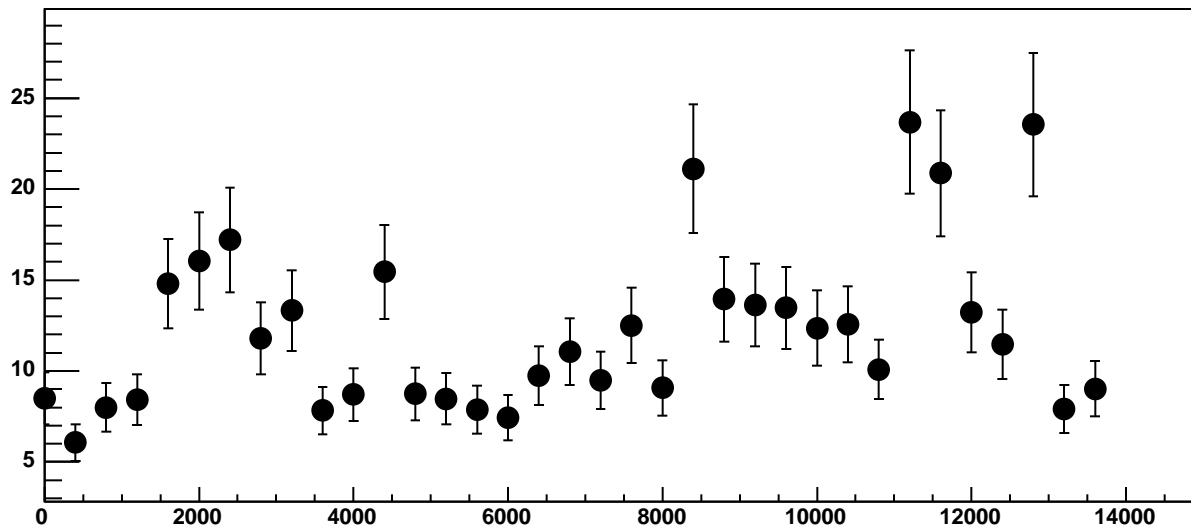


Chip 8, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

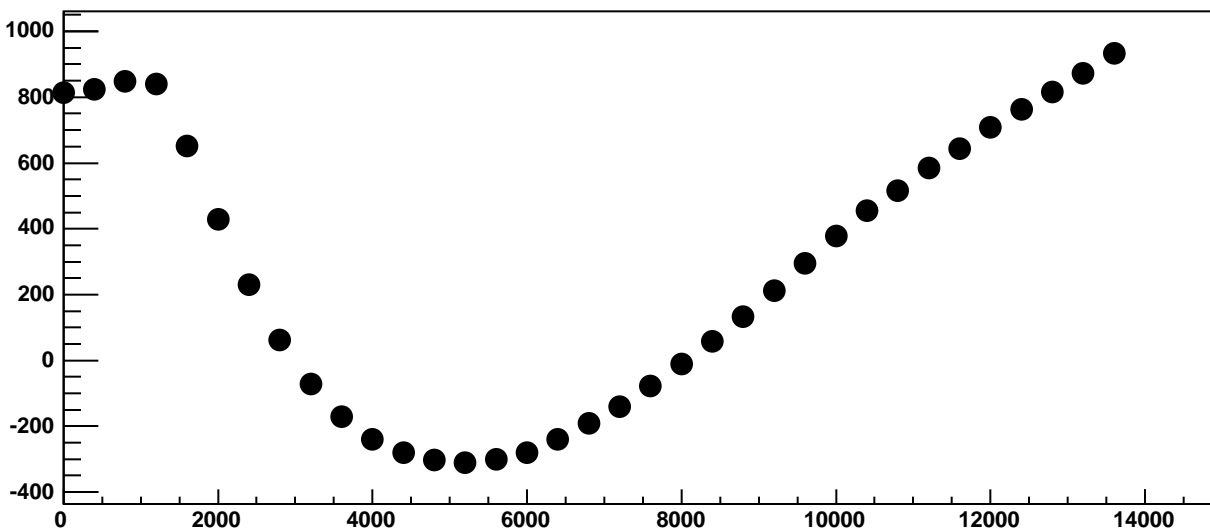
χ^2 / ndf 5.088e+05 / 23
p0 -581.2 ± 1.177
p1 -0.02197 ± 0.0001842



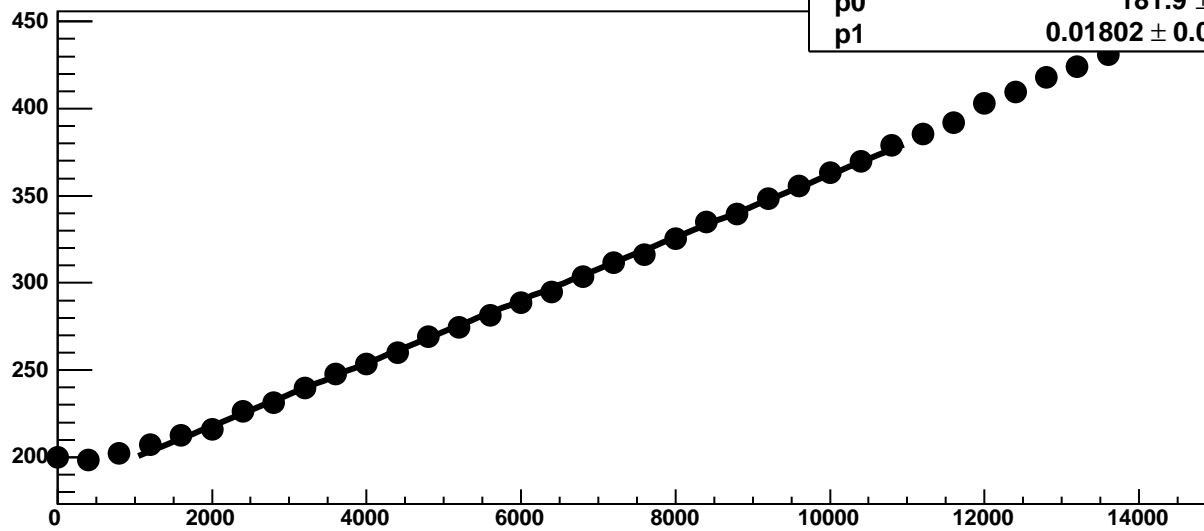
Chip 8, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

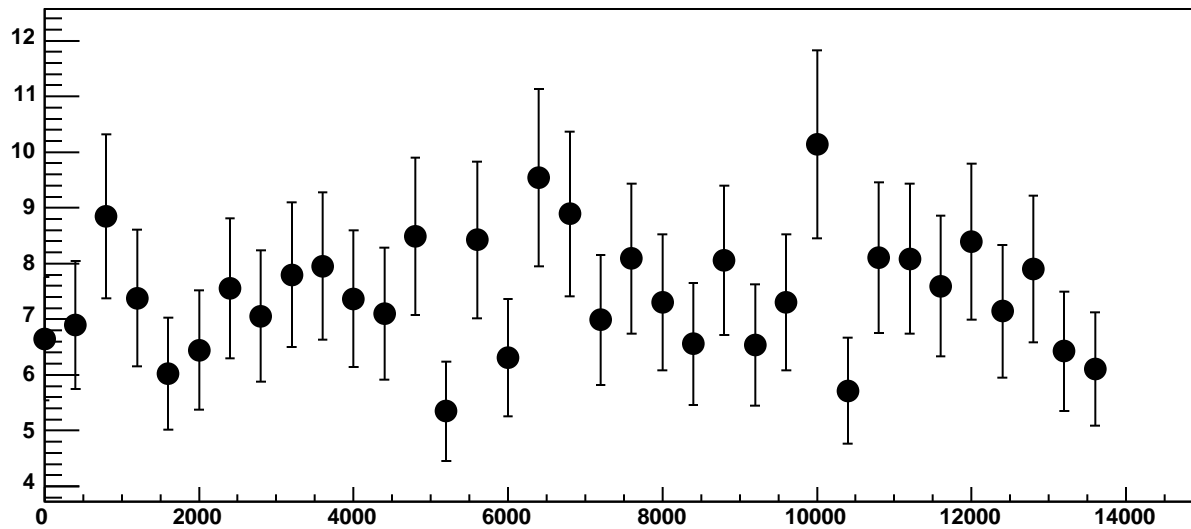


Chip 8, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

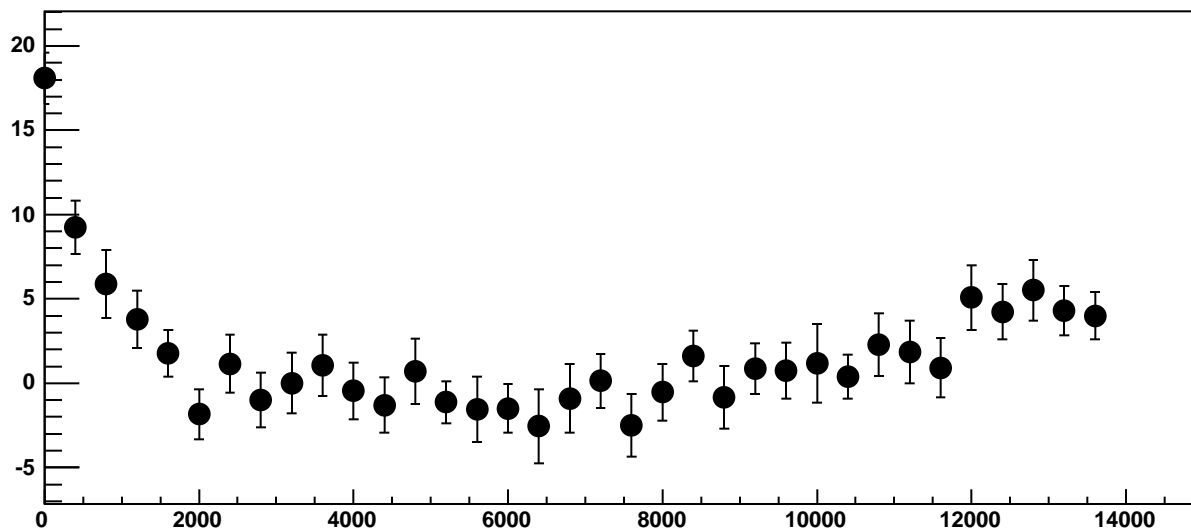


χ^2 / ndf 19.97 / 23
p0 181.9 ± 0.7426
p1 0.01802 ± 0.0001126

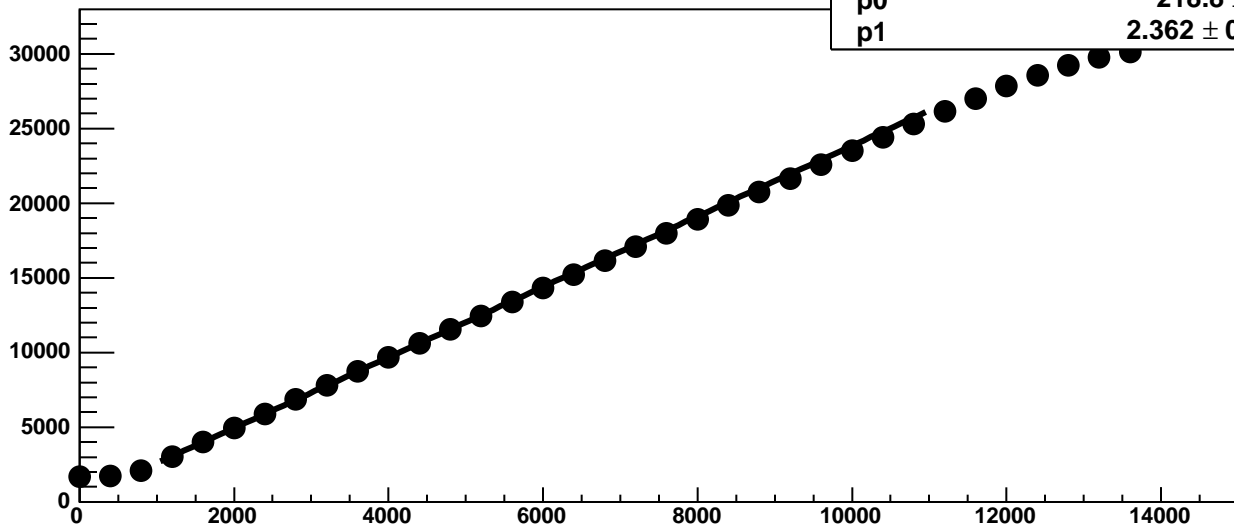
Chip 8, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

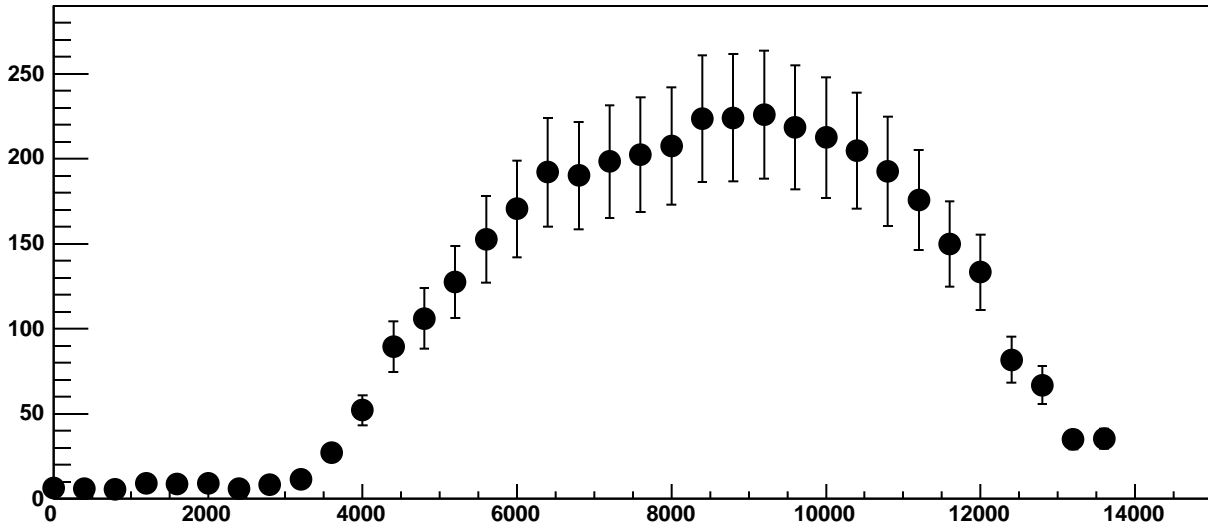


Chip 8, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC

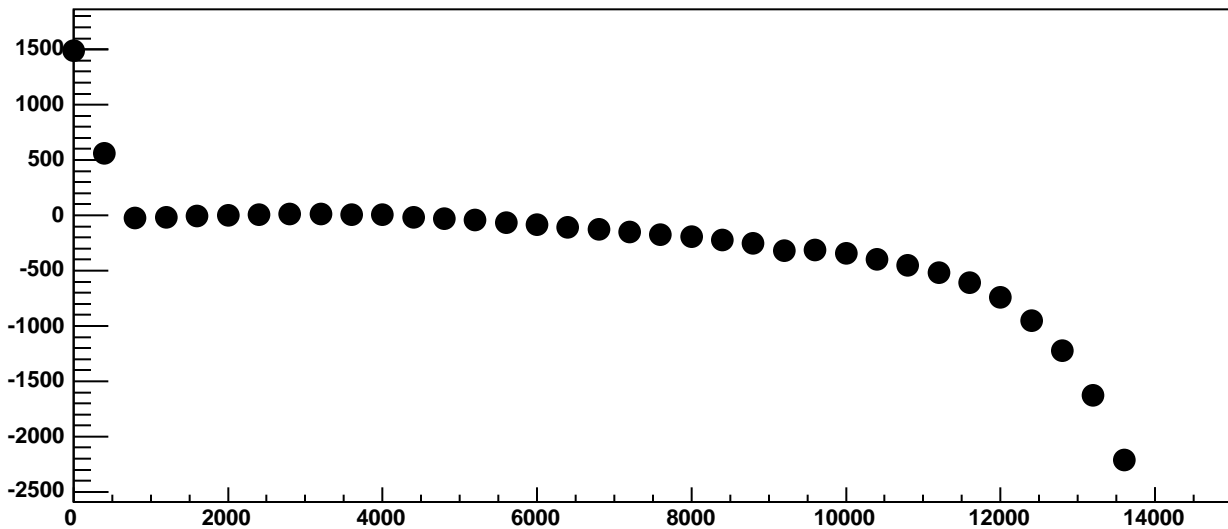


χ^2 / ndf 586.2 / 23
p0 218.8 ± 2.467
p1 2.362 ± 0.00104

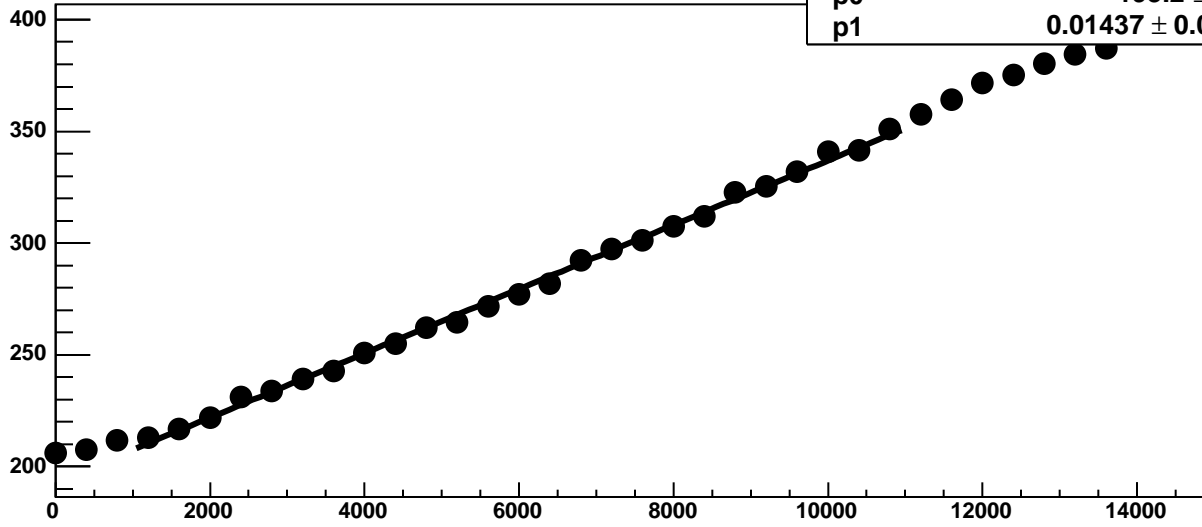
Chip 8, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC

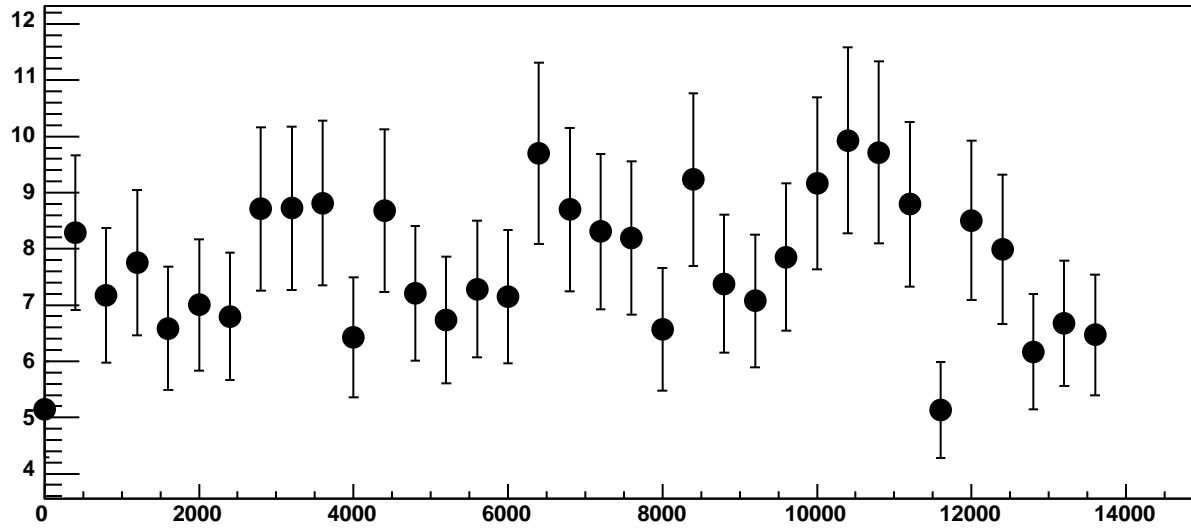


Chip 8, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

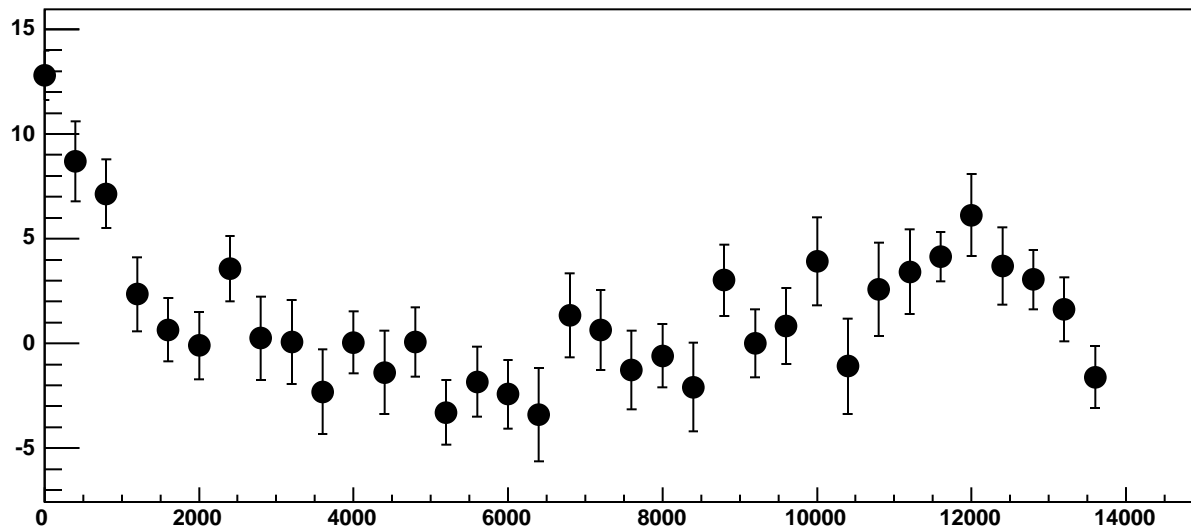


χ^2 / ndf 29.84 / 23
p0 193.2 ± 0.8035
p1 0.01437 ± 0.0001261

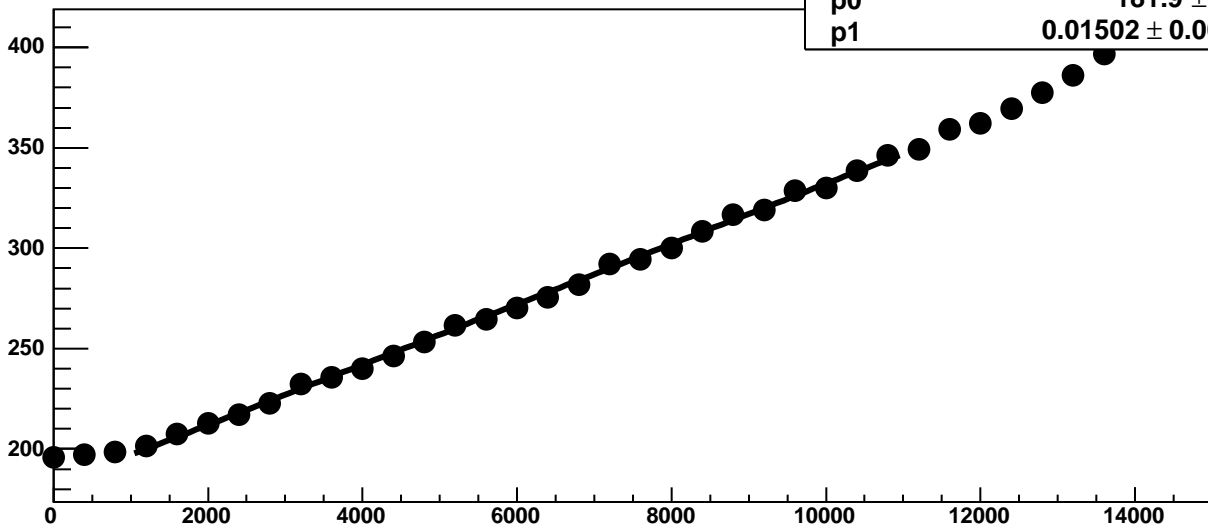
Chip 8, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

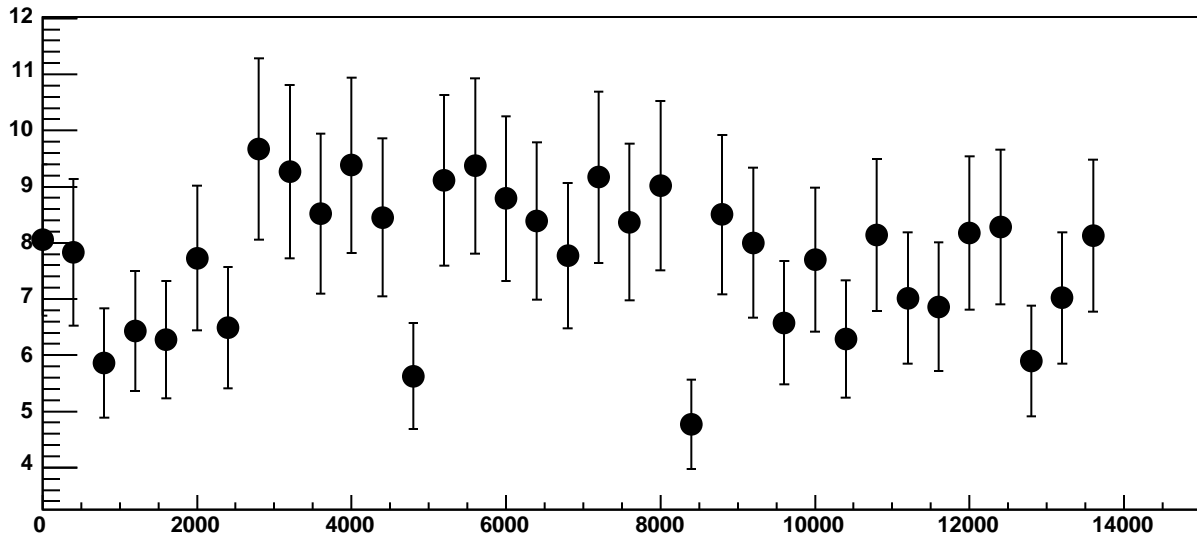


Chip 8, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

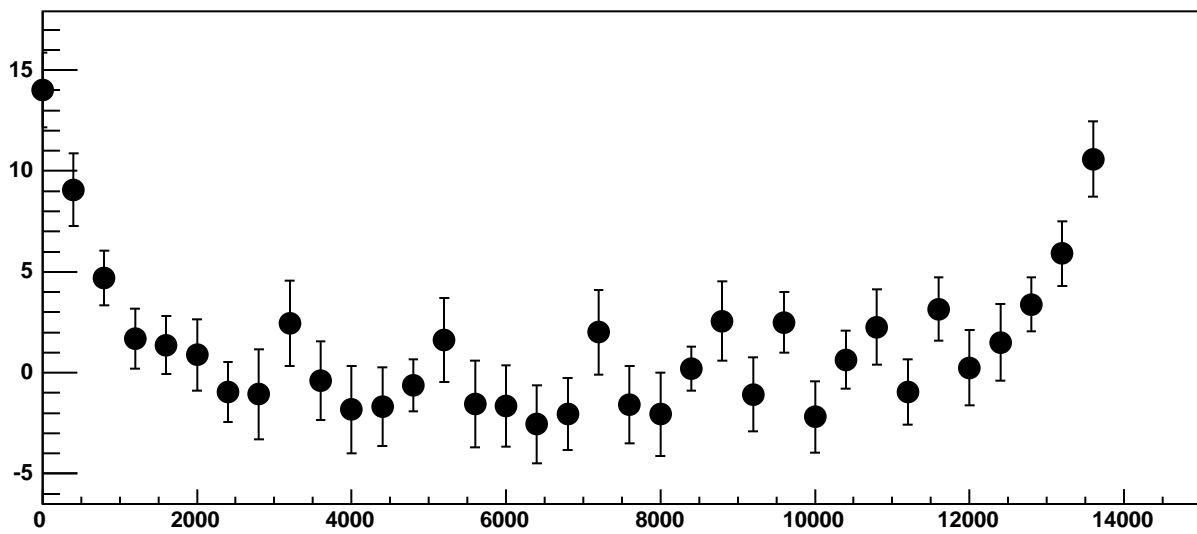


χ^2 / ndf 21.68 / 23
p0 181.9 ± 0.7708
p1 0.01502 ± 0.0001135

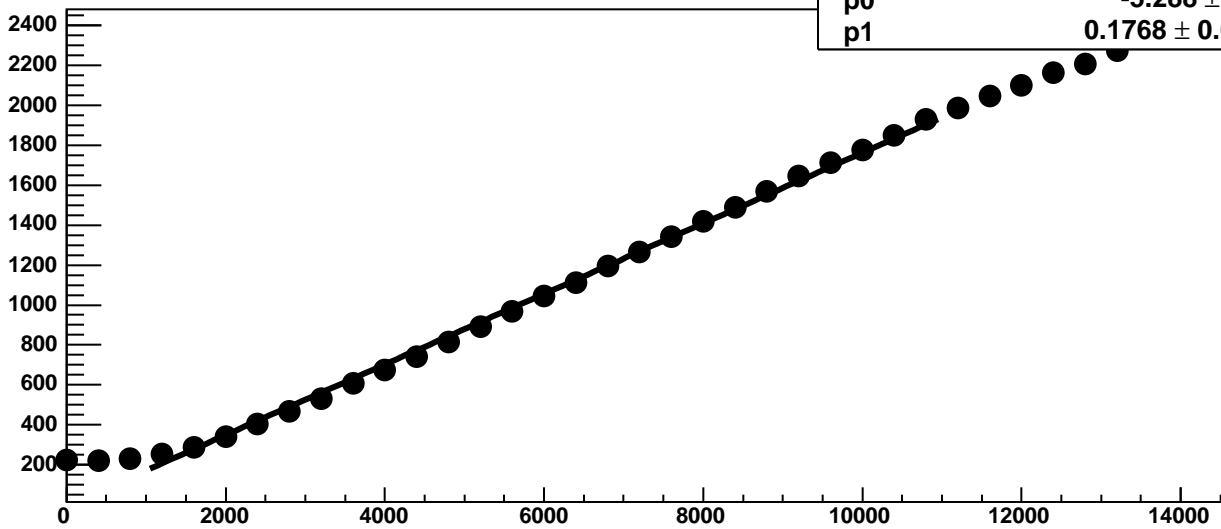
Chip 8, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

2525 / 23

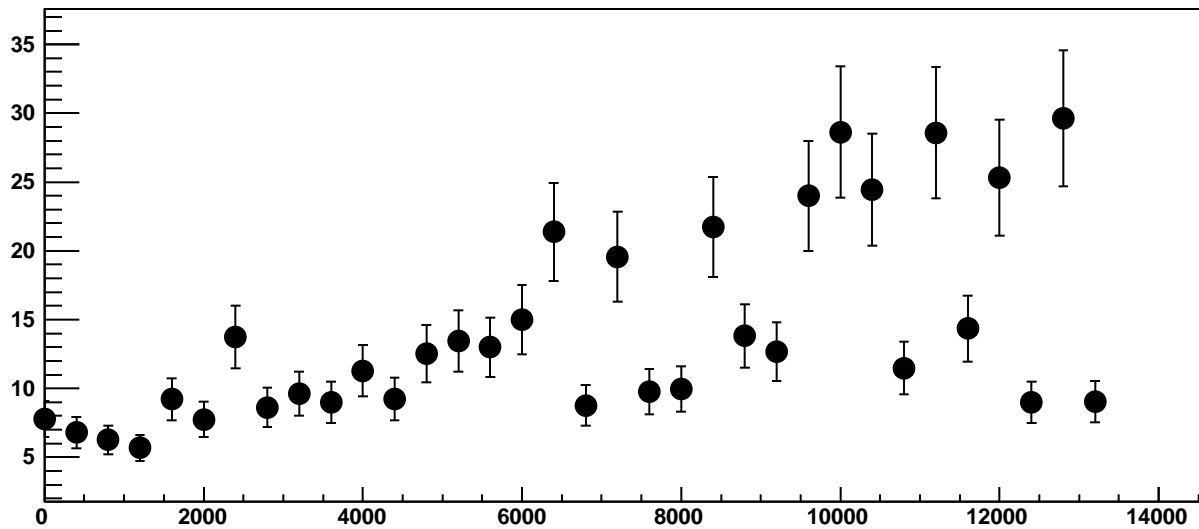
p0

-5.288 ± 0.9563

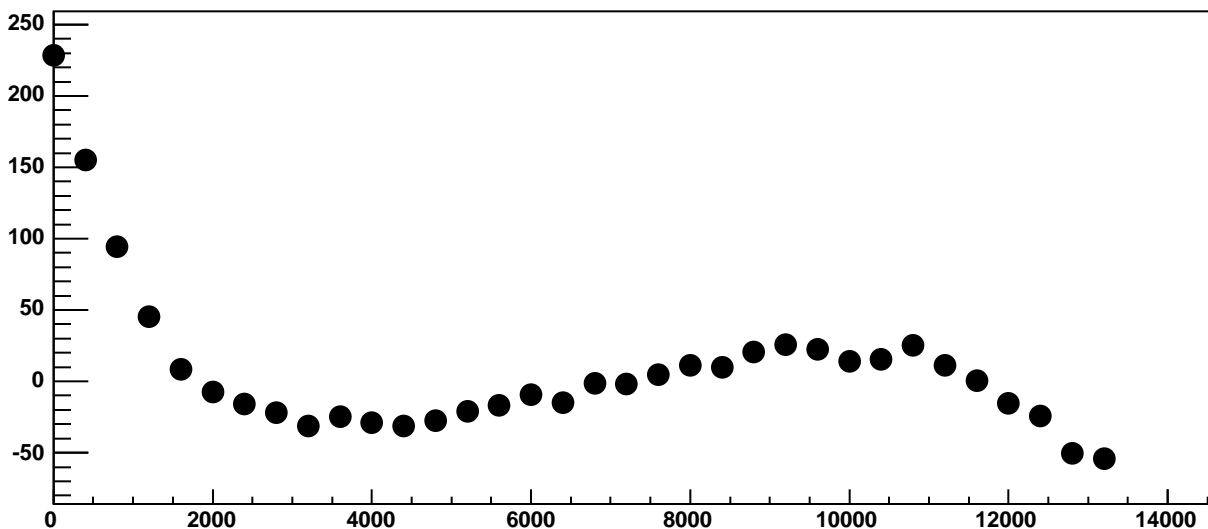
p1

0.1768 ± 0.000177

Chip 8, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC

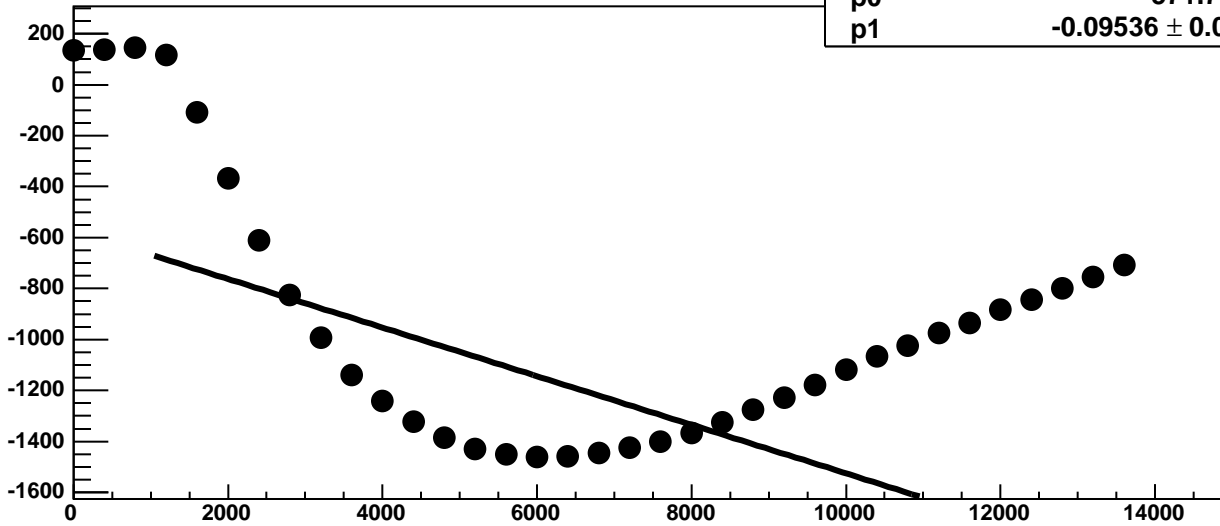


Chip 8, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC

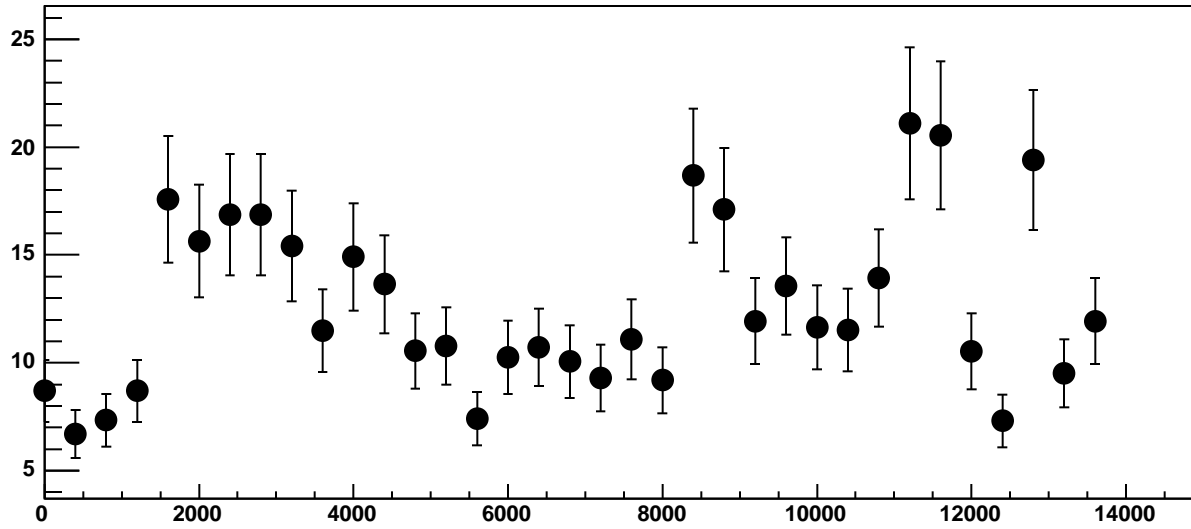


Chip 8, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

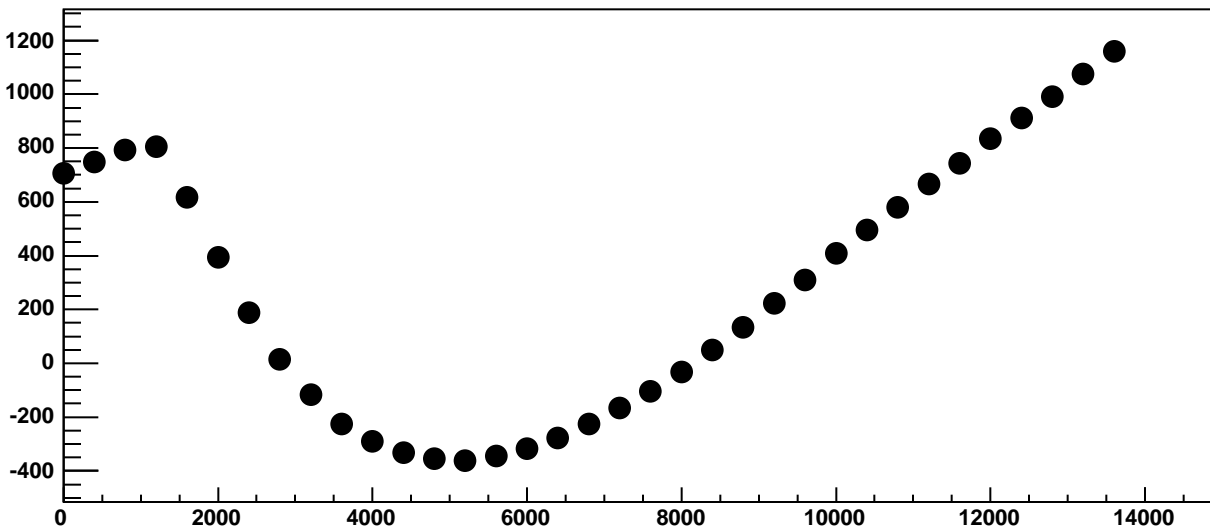
χ^2 / ndf 4.708e+05 / 23
p0 -571.7 ± 1.335
p1 -0.09536 ± 0.0002005



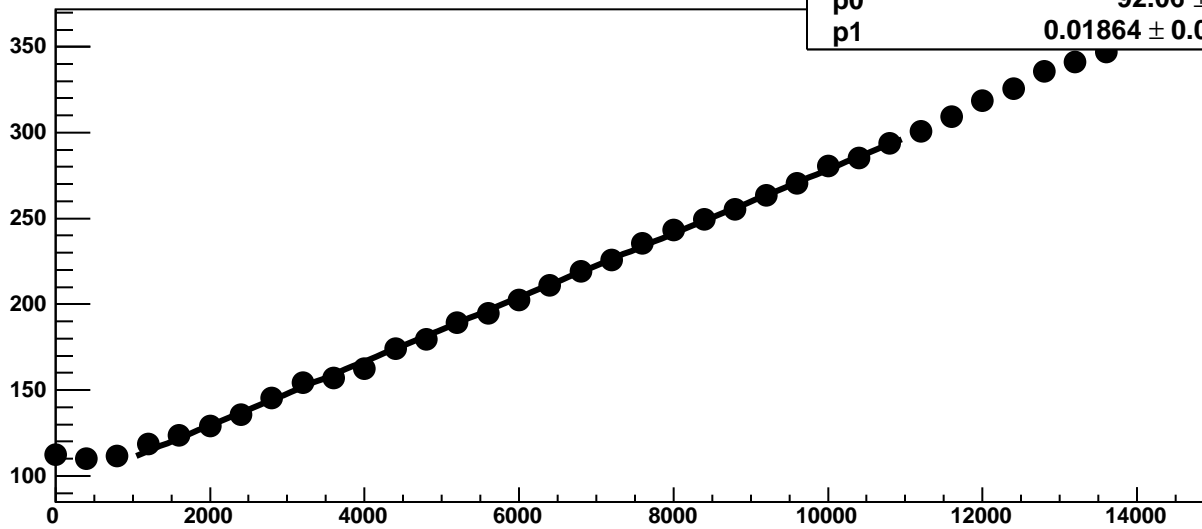
Chip 8, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

22.86 / 23

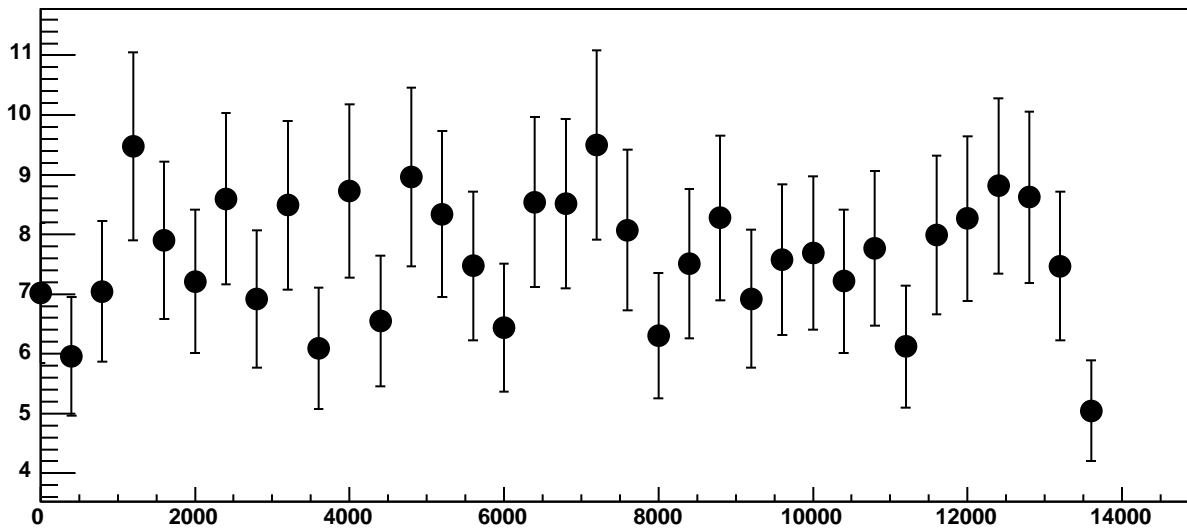
p0

92.06 ± 0.8218

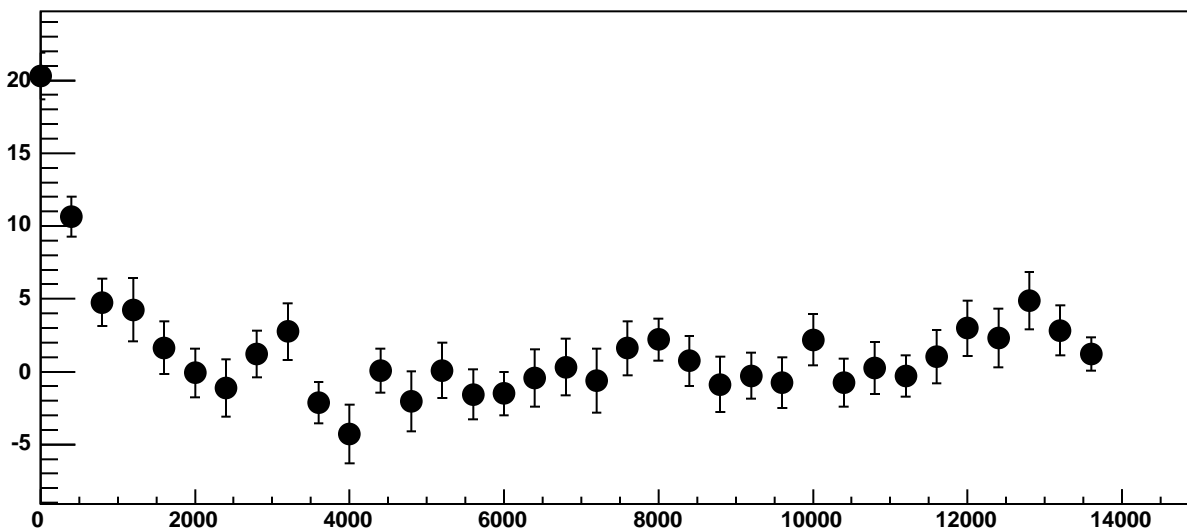
p1

0.01864 ± 0.0001224

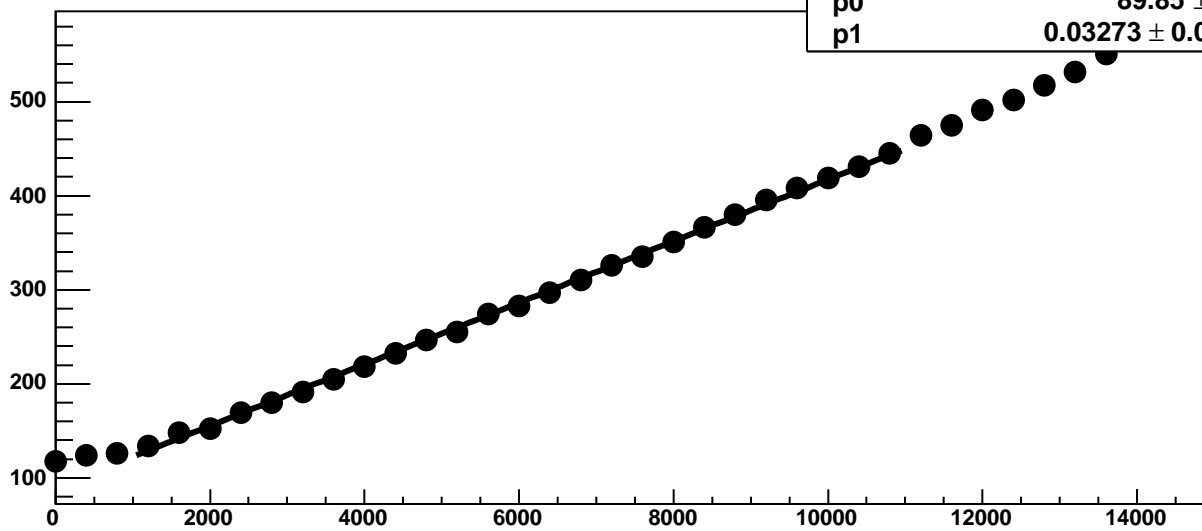
Chip 8, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

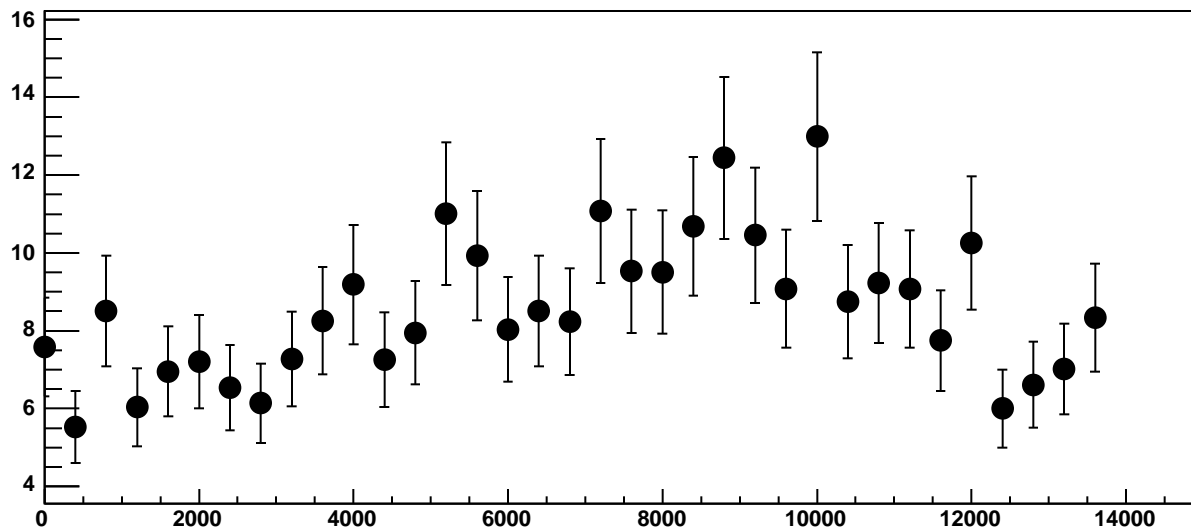


Chip 8, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC

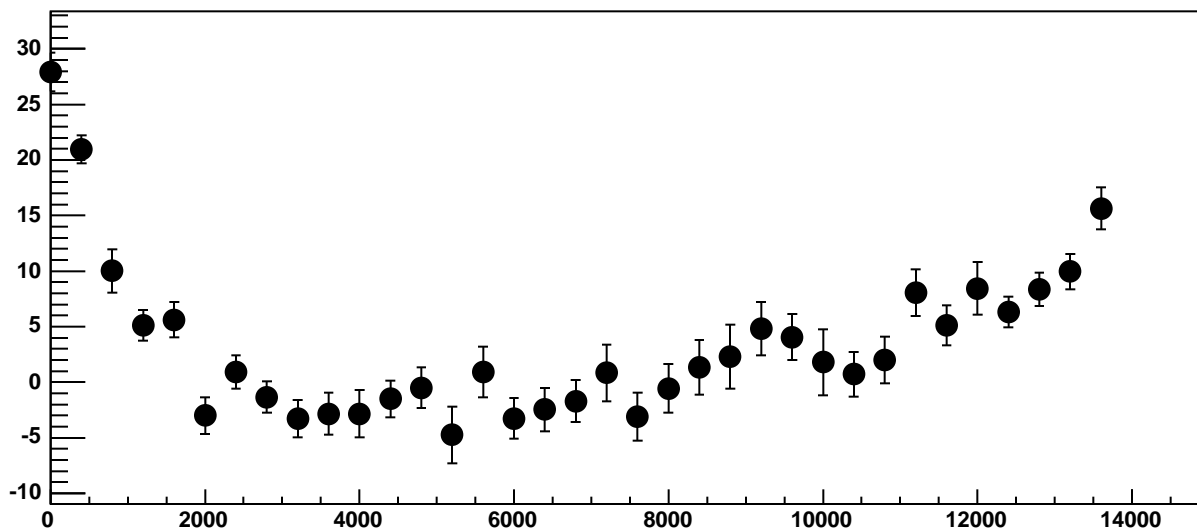


χ^2 / ndf 61.16 / 23
p0 89.85 ± 0.7716
p1 0.03273 ± 0.0001315

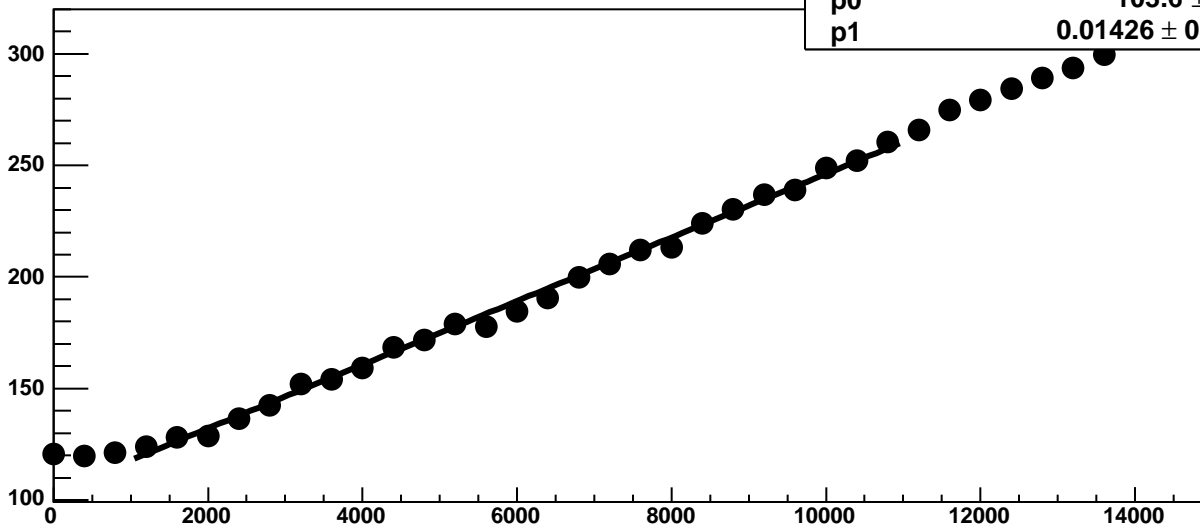
Chip 8, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

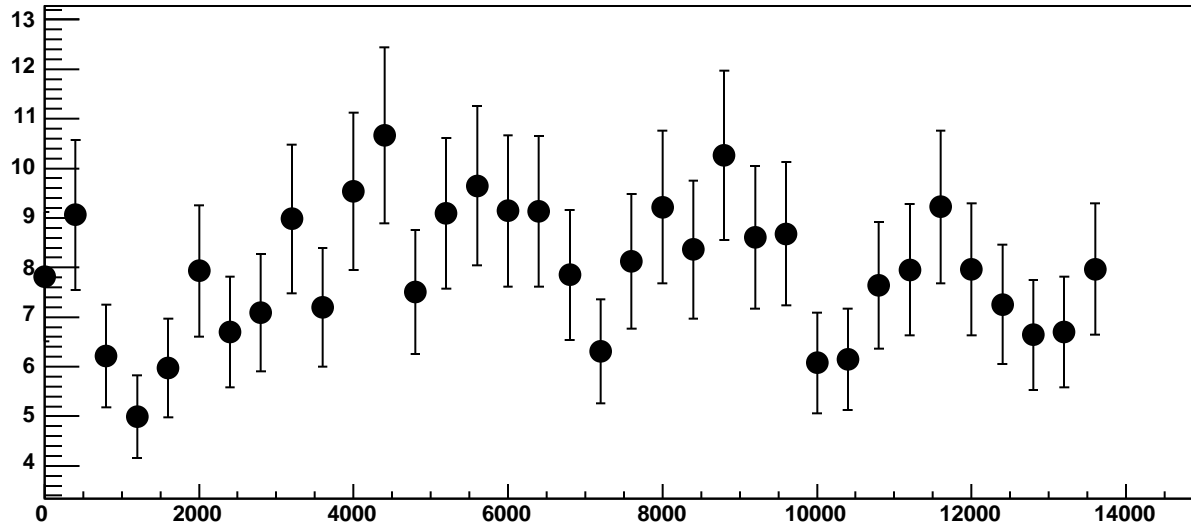


Chip 8, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

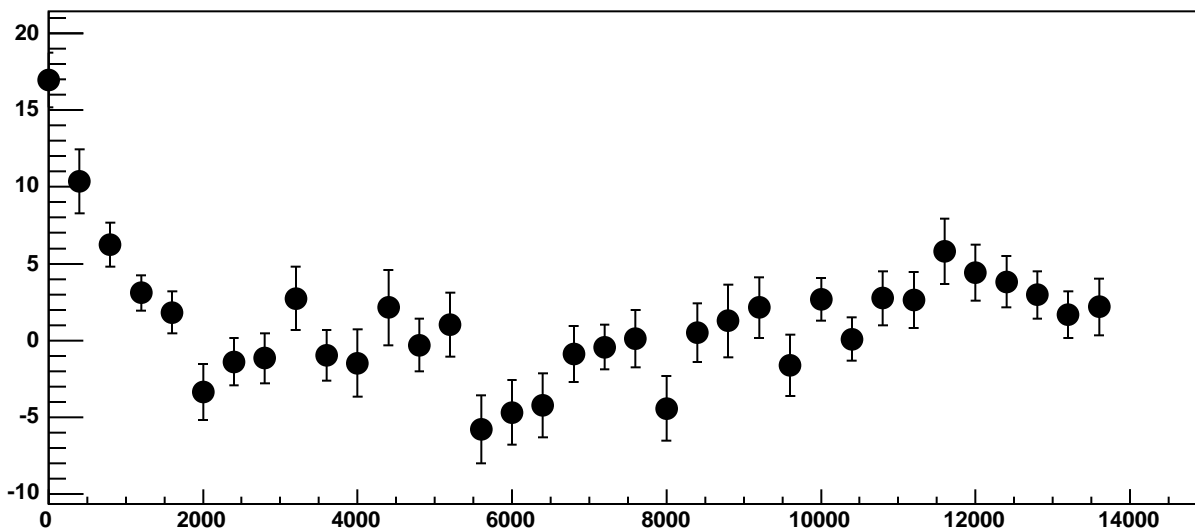


χ^2 / ndf 46.31 / 23
p0 103.6 ± 0.7144
p1 0.01426 ± 0.000109

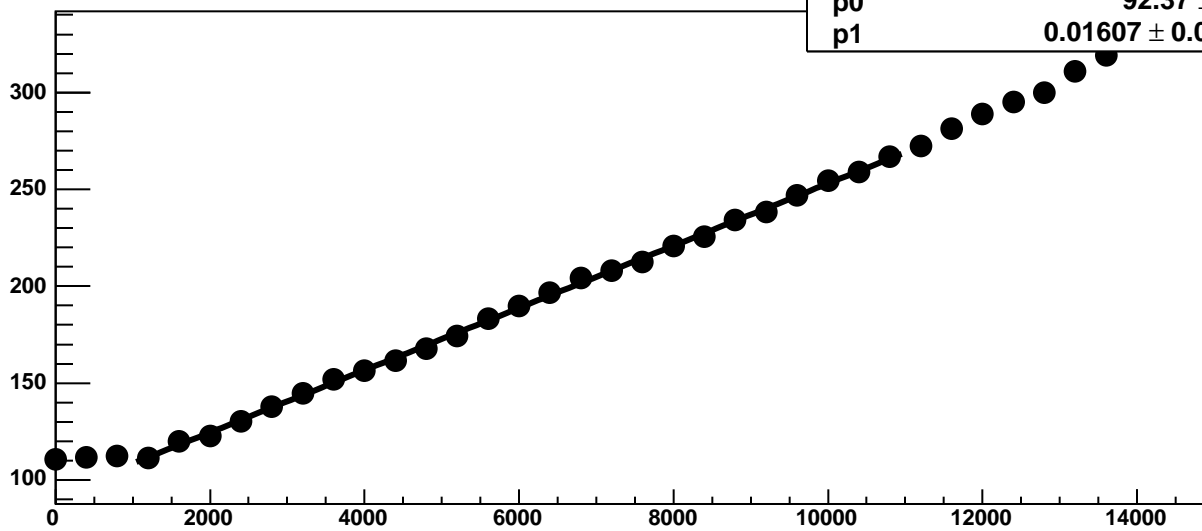
Chip 8, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

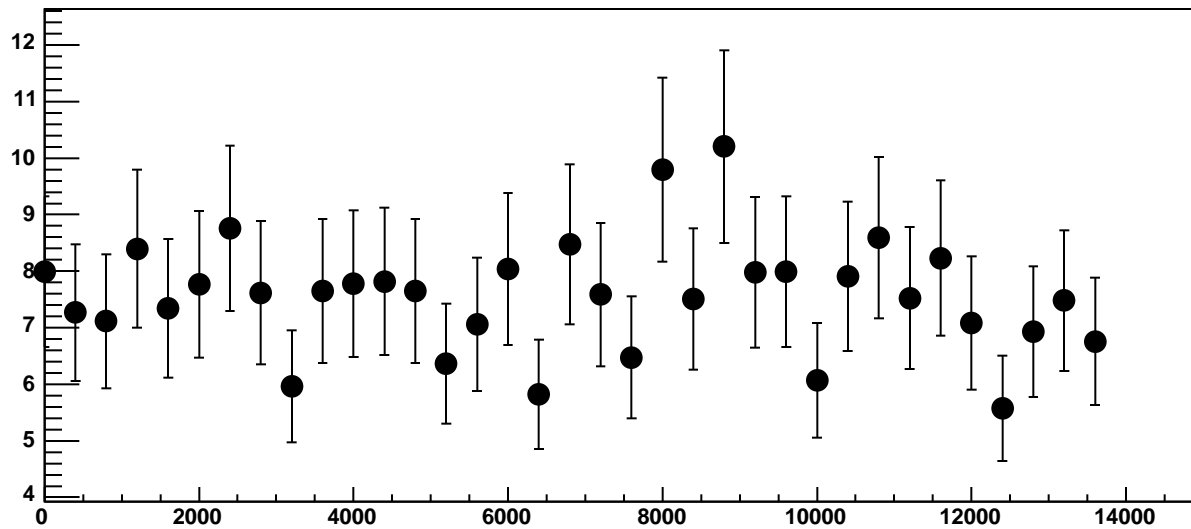


Chip 8, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

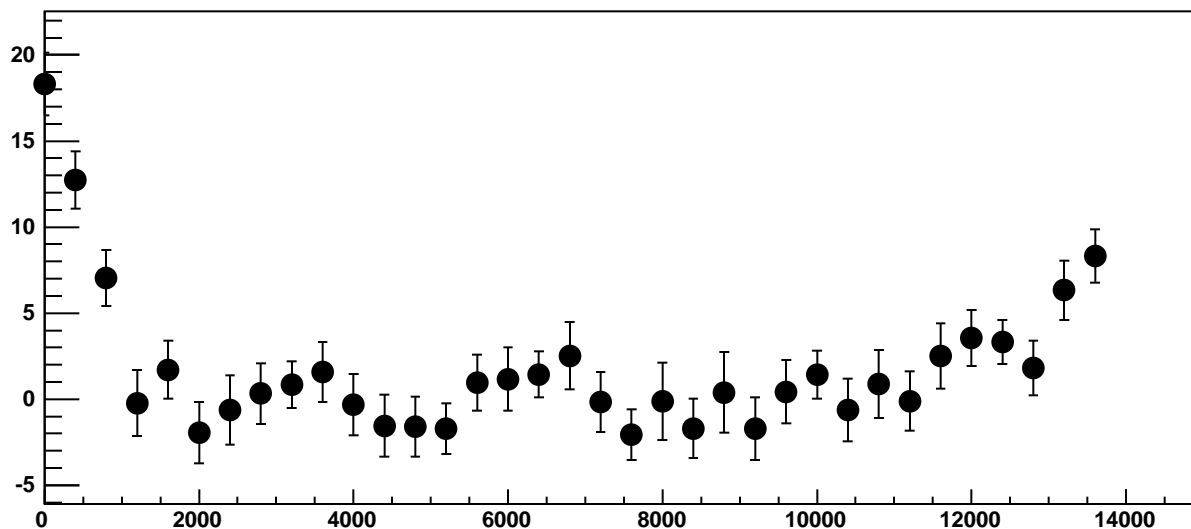


χ^2 / ndf 15.36 / 23
p0 92.37 ± 0.8061
p1 0.01607 ± 0.0001226

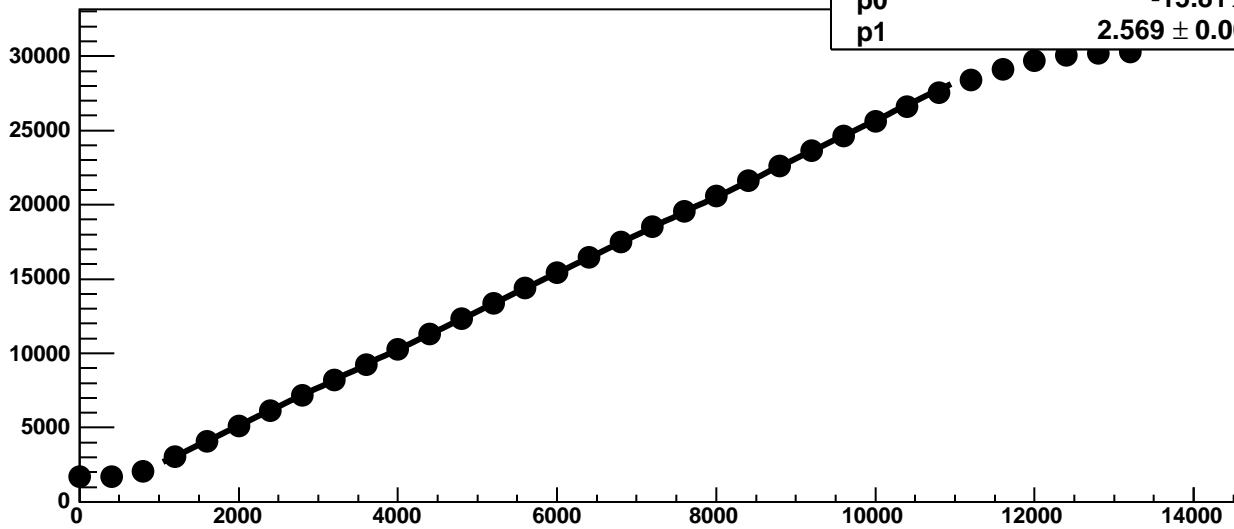
Chip 8, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

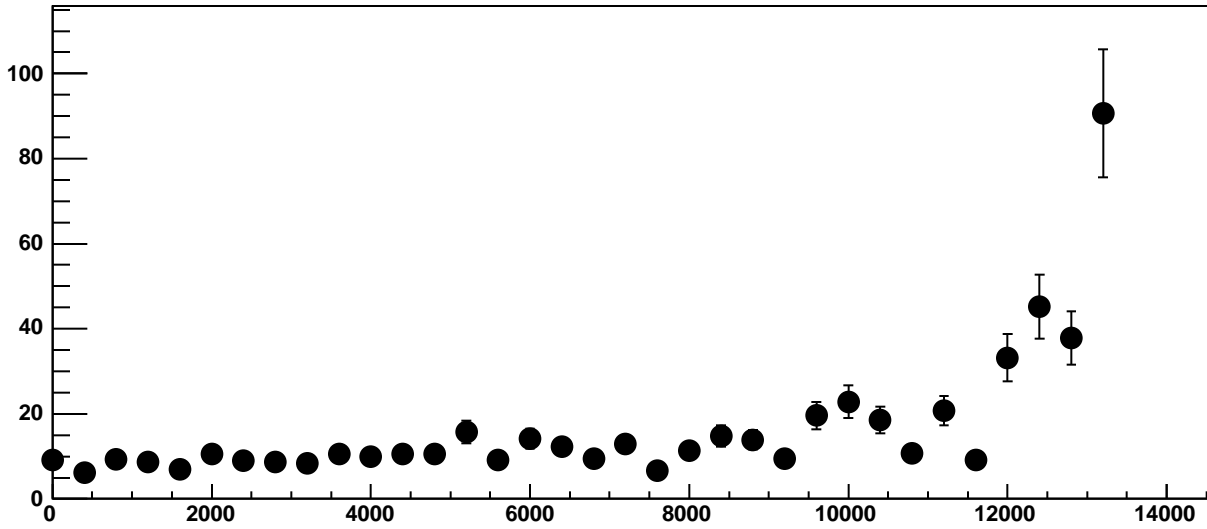


Chip 8, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC

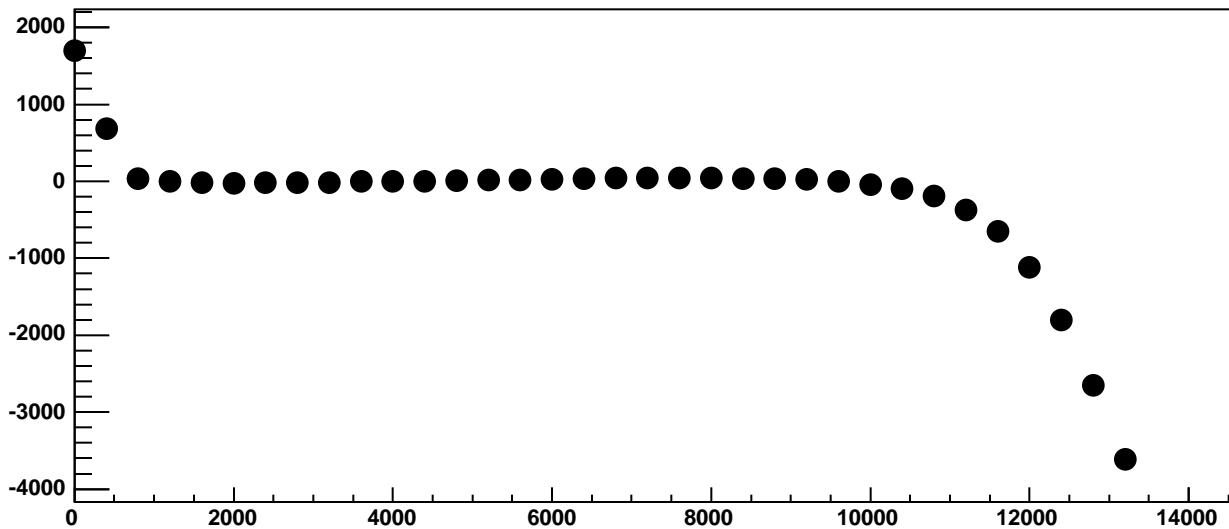


χ^2 / ndf 9255 / 23
p0 -15.81 ± 1.002
p1 2.569 ± 0.0001695

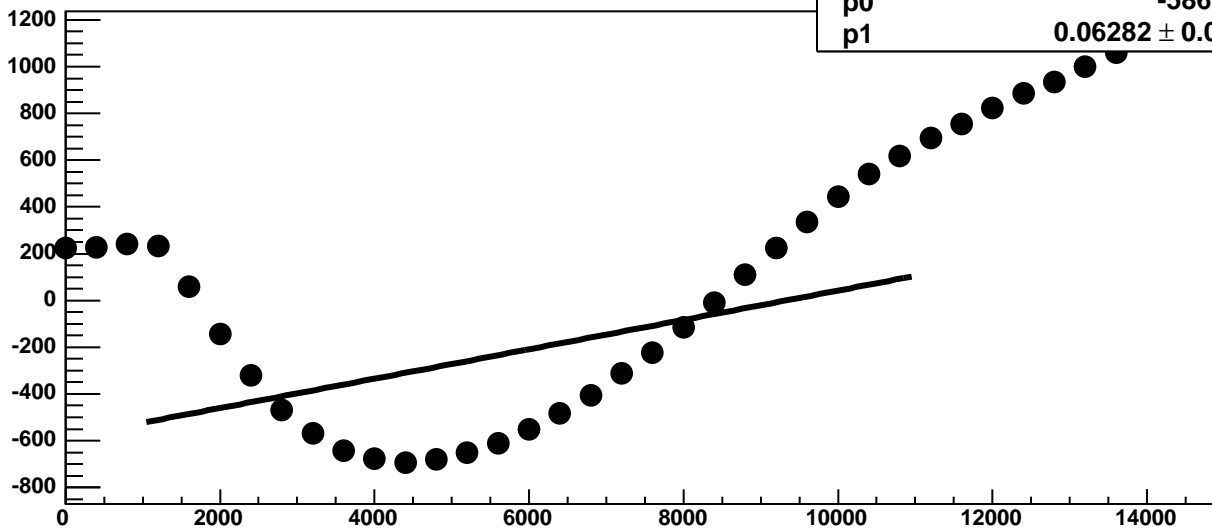
Chip 8, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC

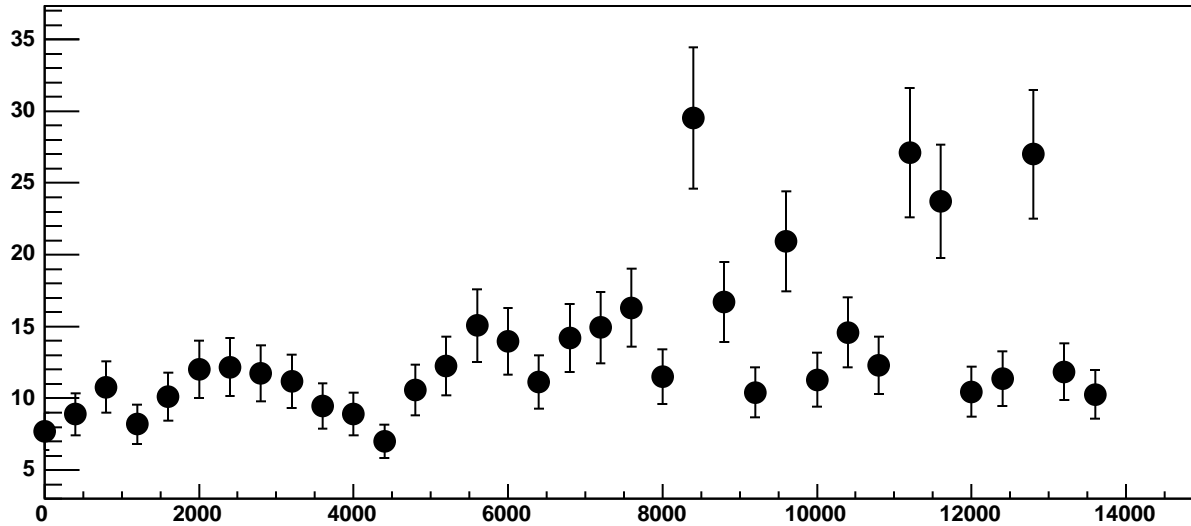


Chip 8, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

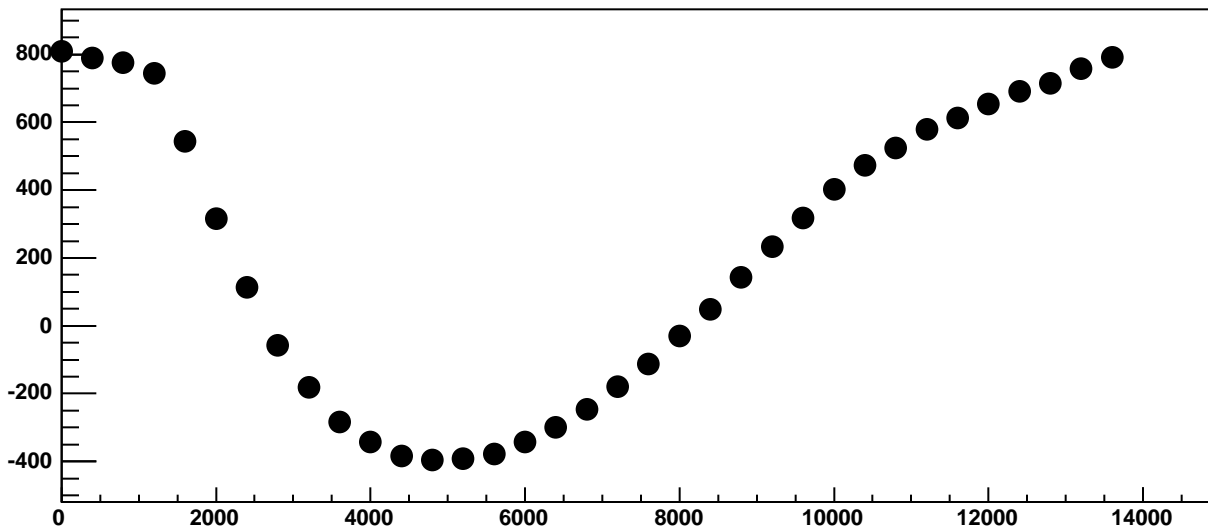


χ^2 / ndf 5.222e+05 / 23
p0 -586 ± 1.106
p1 0.06282 ± 0.0001857

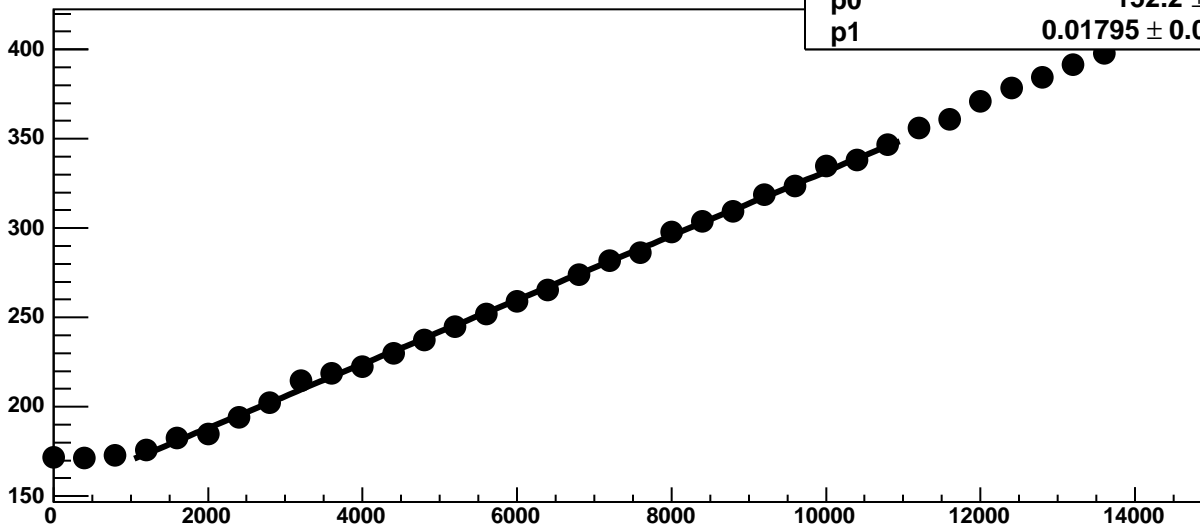
Chip 8, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

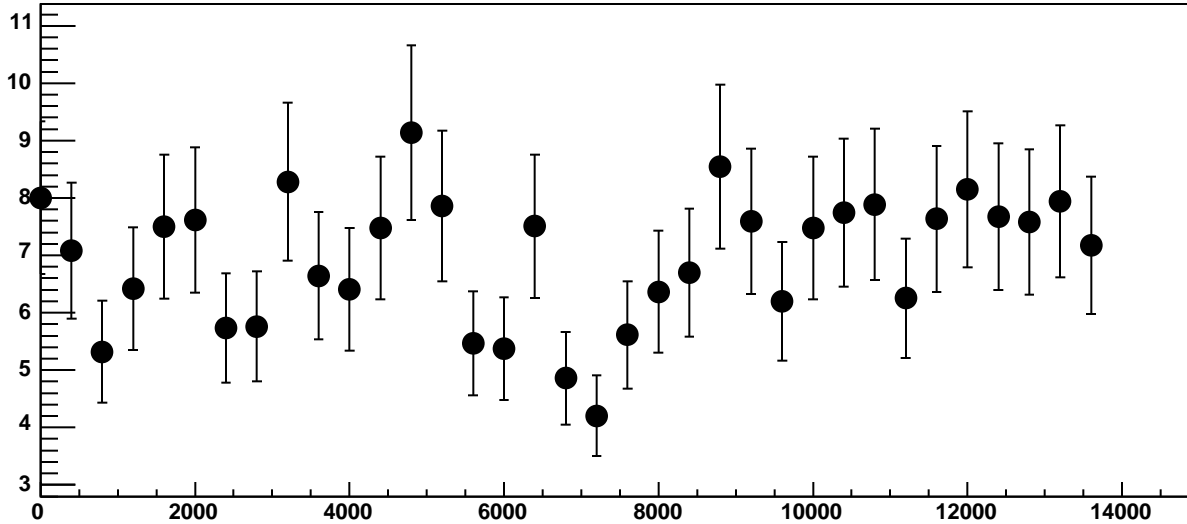


Chip 8, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

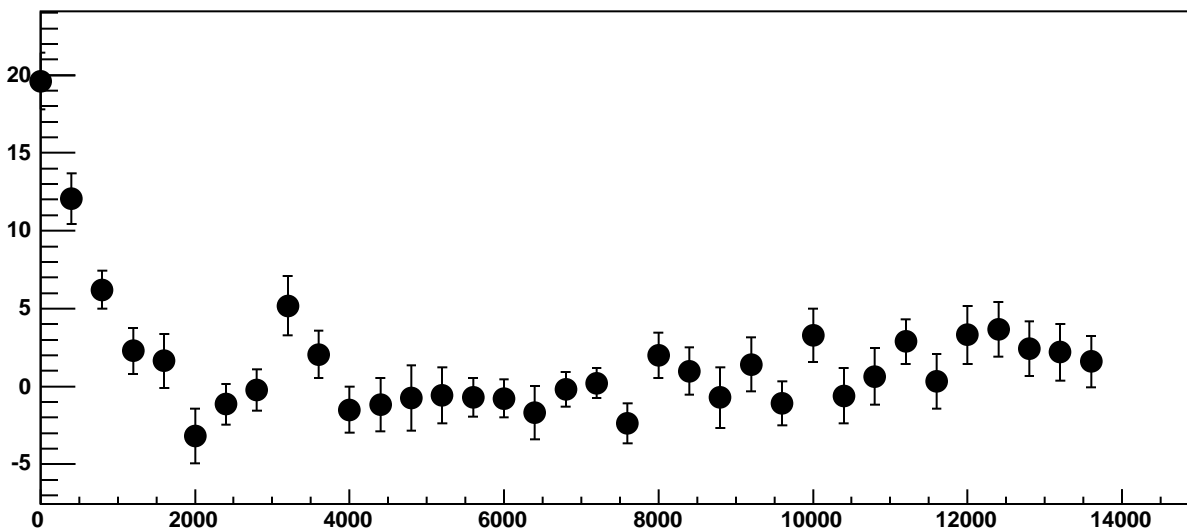


χ^2 / ndf 31.08 / 23
p0 152.2 ± 0.7236
p1 0.01795 ± 0.0001103

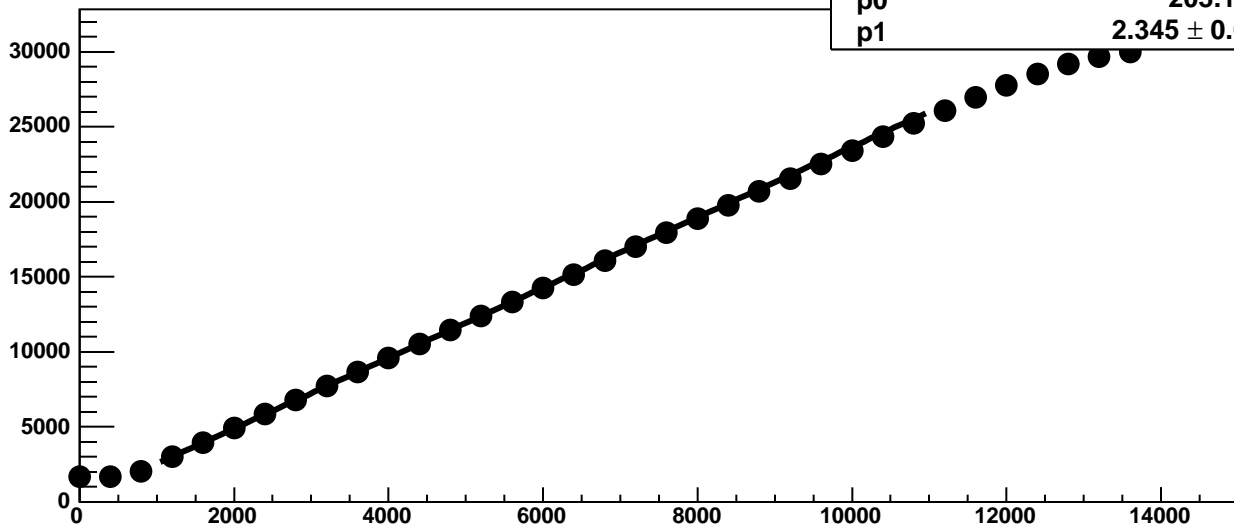
Chip 8, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

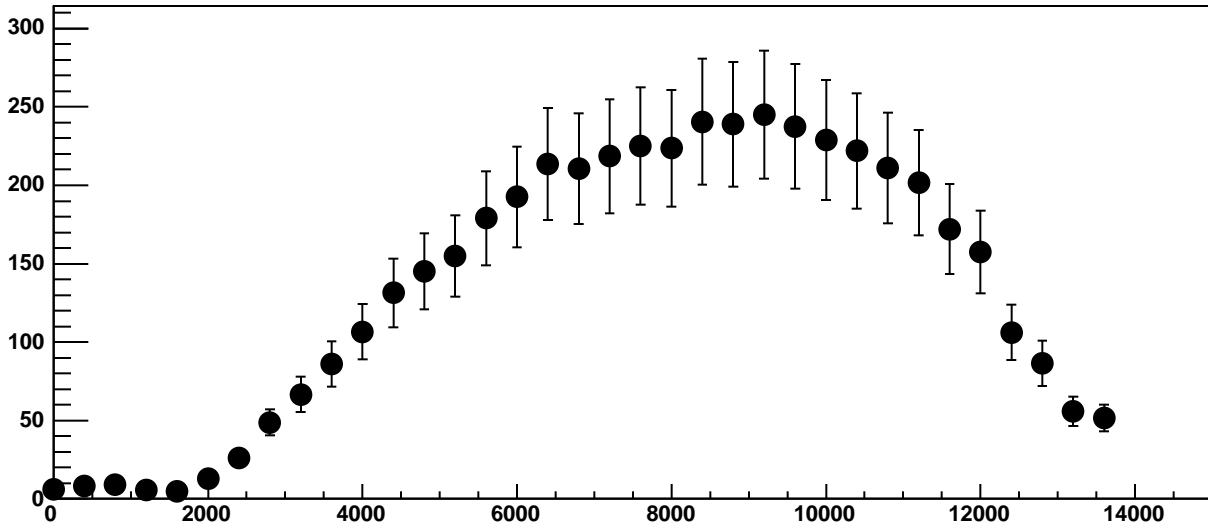


Chip 8, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC

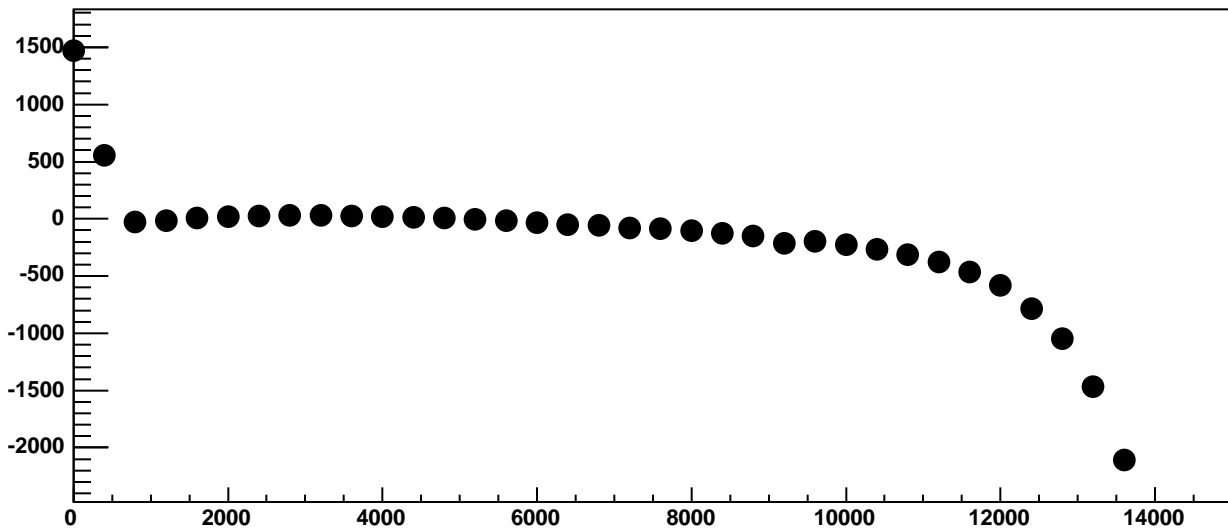


χ^2 / ndf 379.4 / 23
p0 203.1 ± 2.48
p1 2.345 ± 0.001515

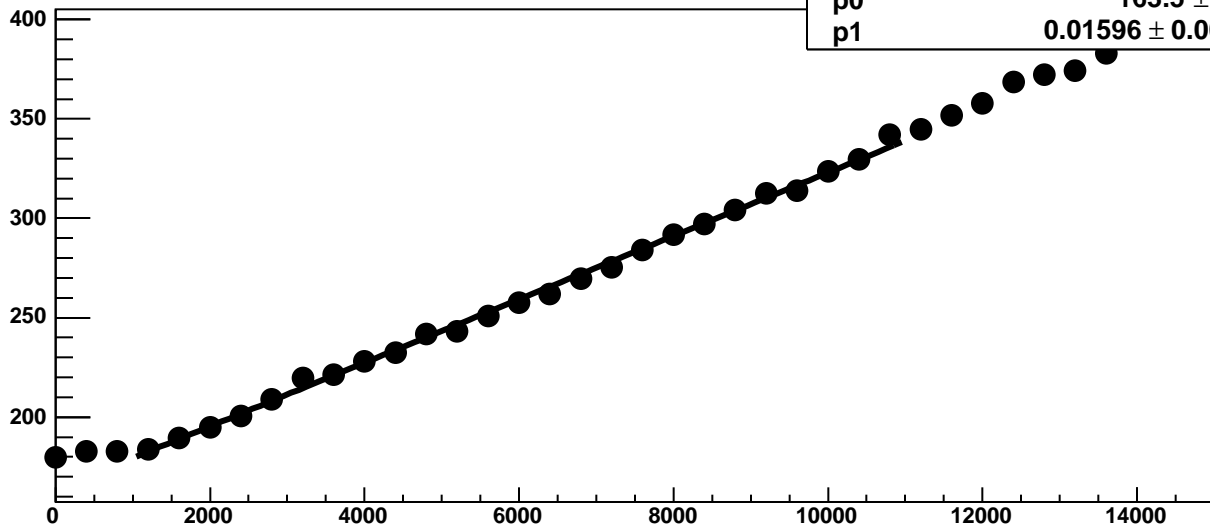
Chip 8, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



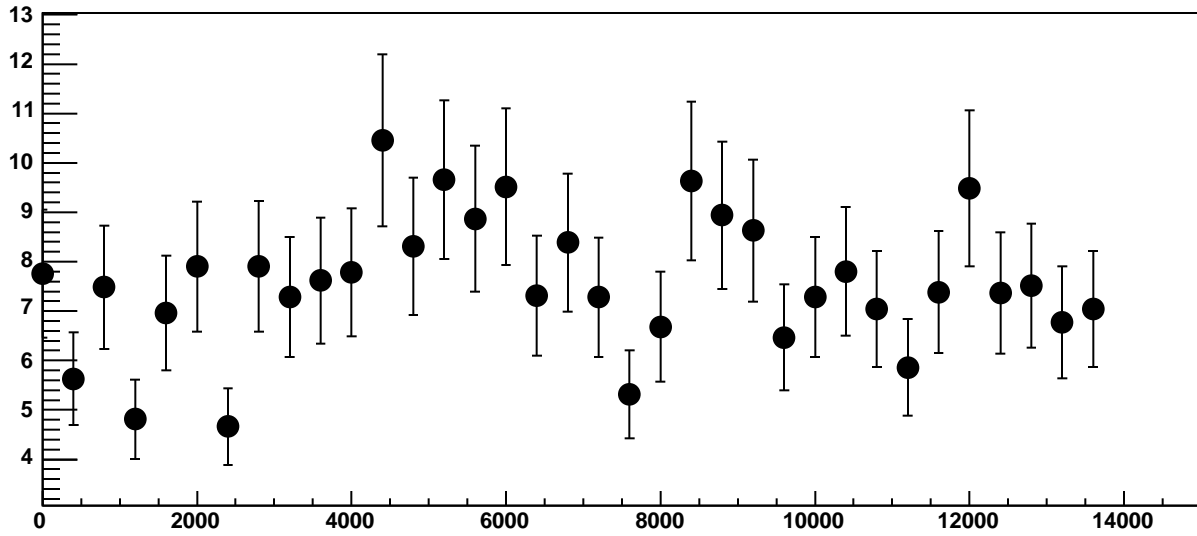
Chip 8, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC



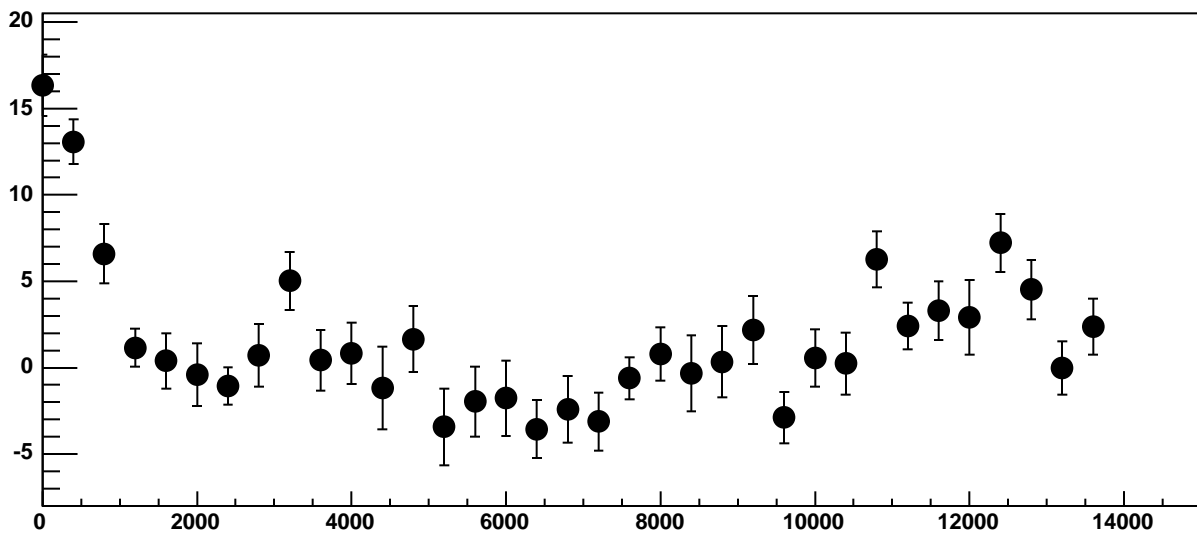
Chip 8, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



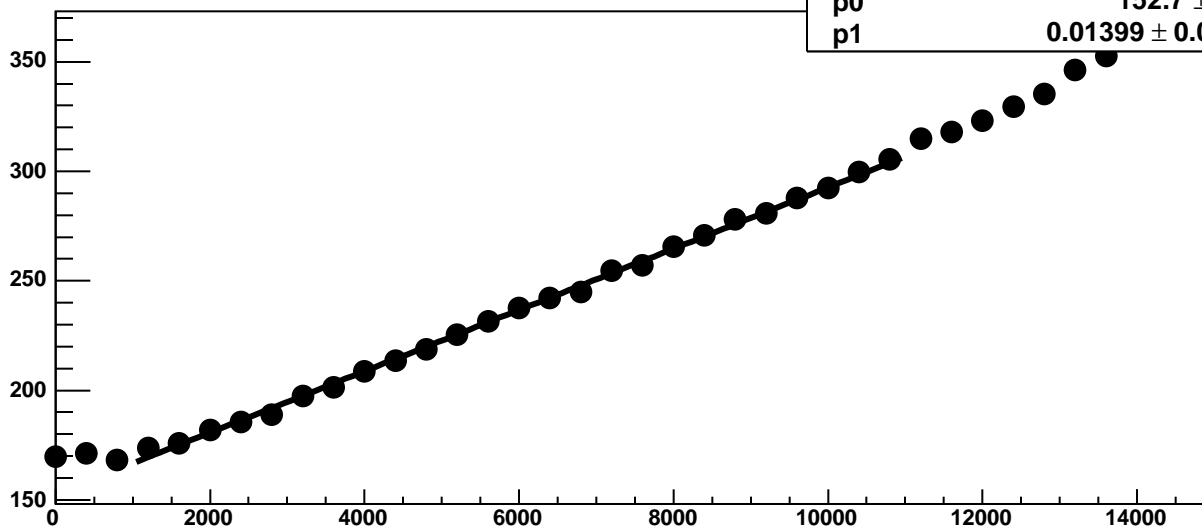
Chip 8, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

29.47 / 23

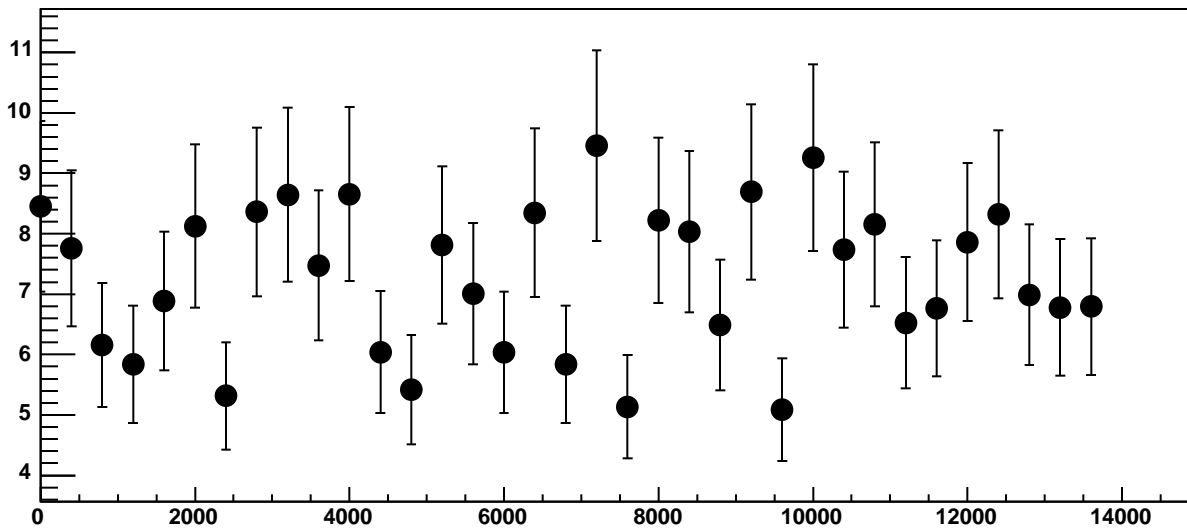
p0

152.7 ± 0.7234

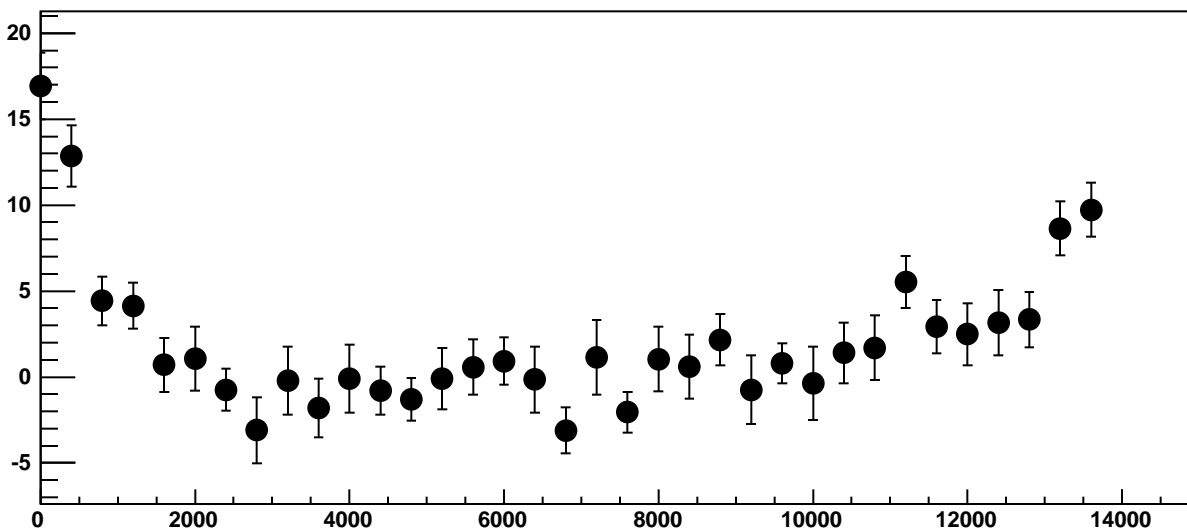
p1

0.01399 ± 0.0001109

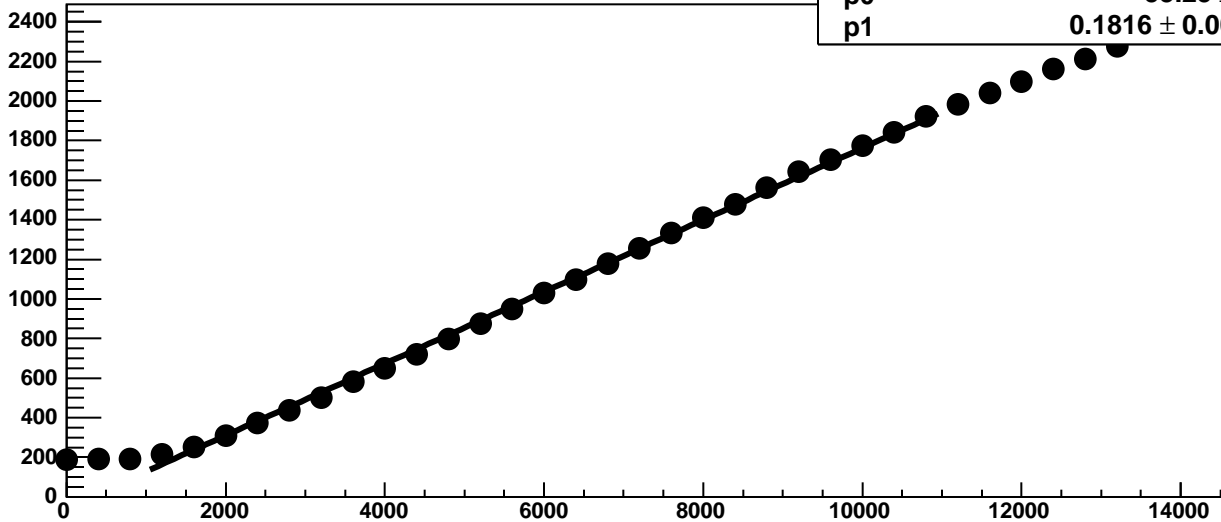
Chip 8, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

2081 / 23

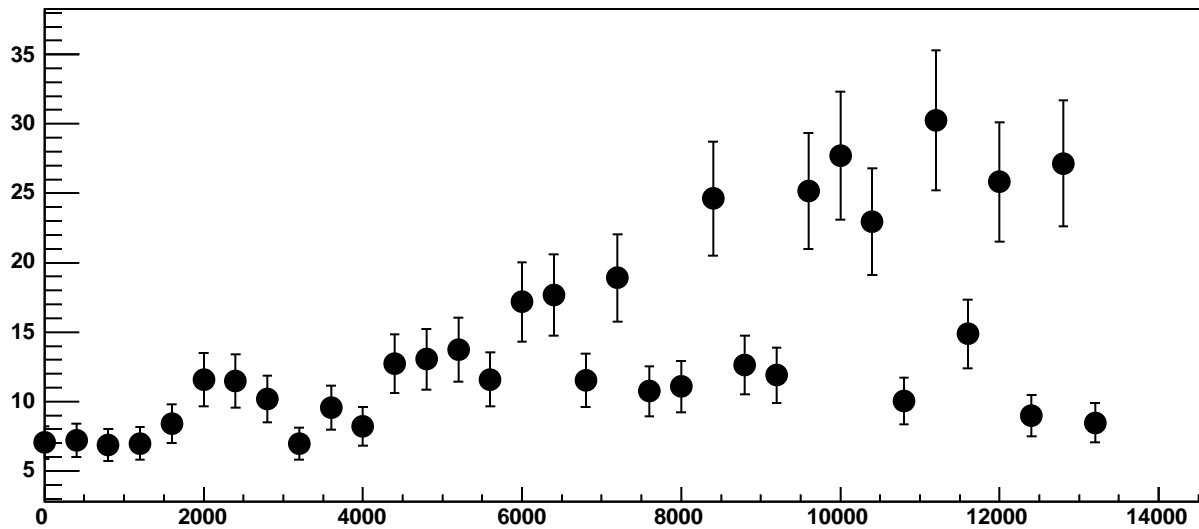
p0

-53.25 ± 1.015

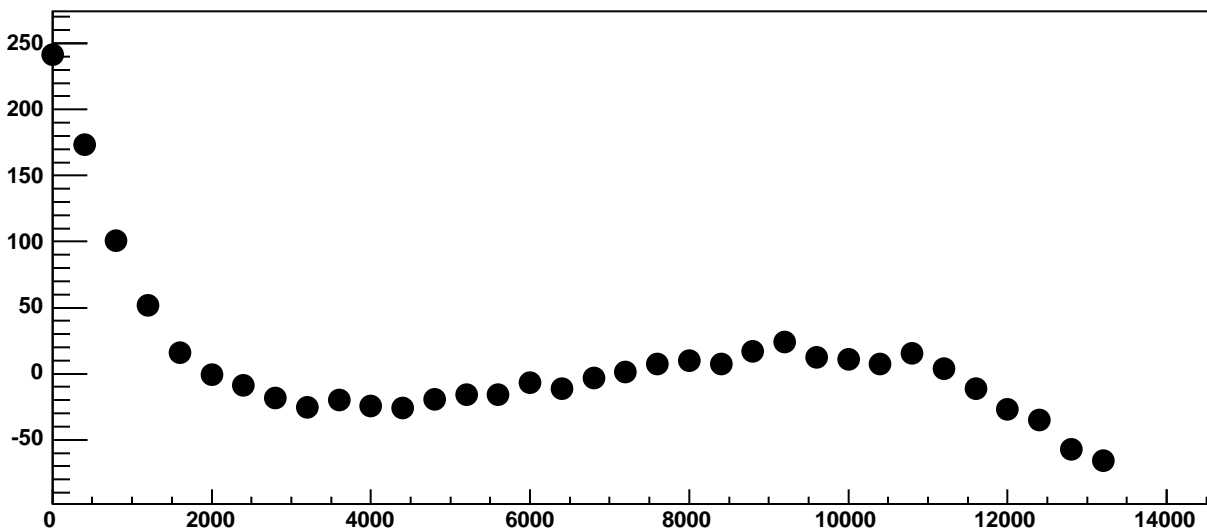
p1

0.1816 ± 0.0001818

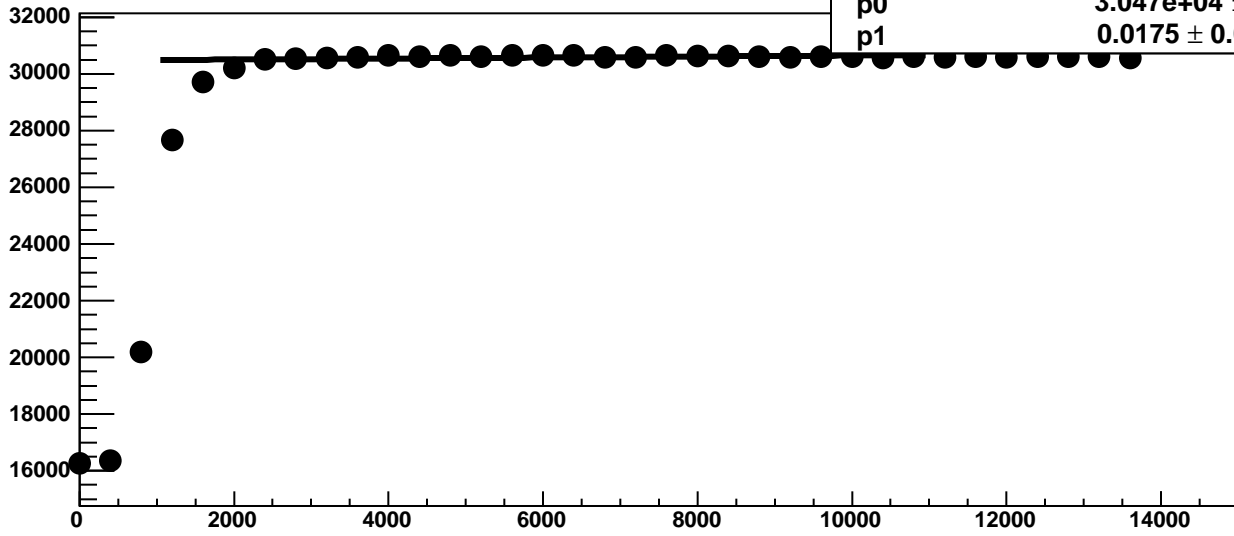
Chip 8, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



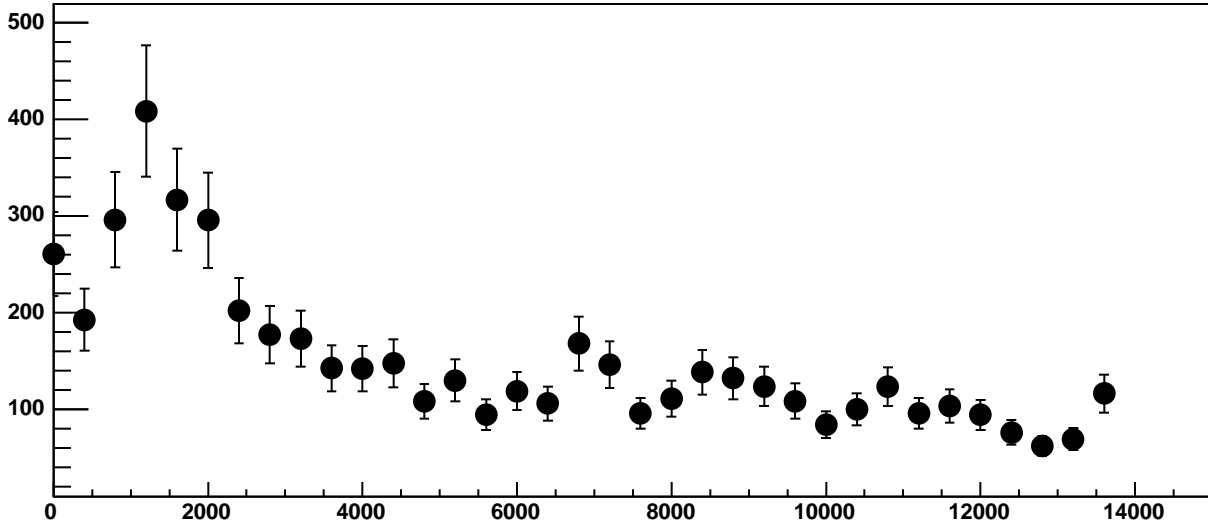
Chip 8, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



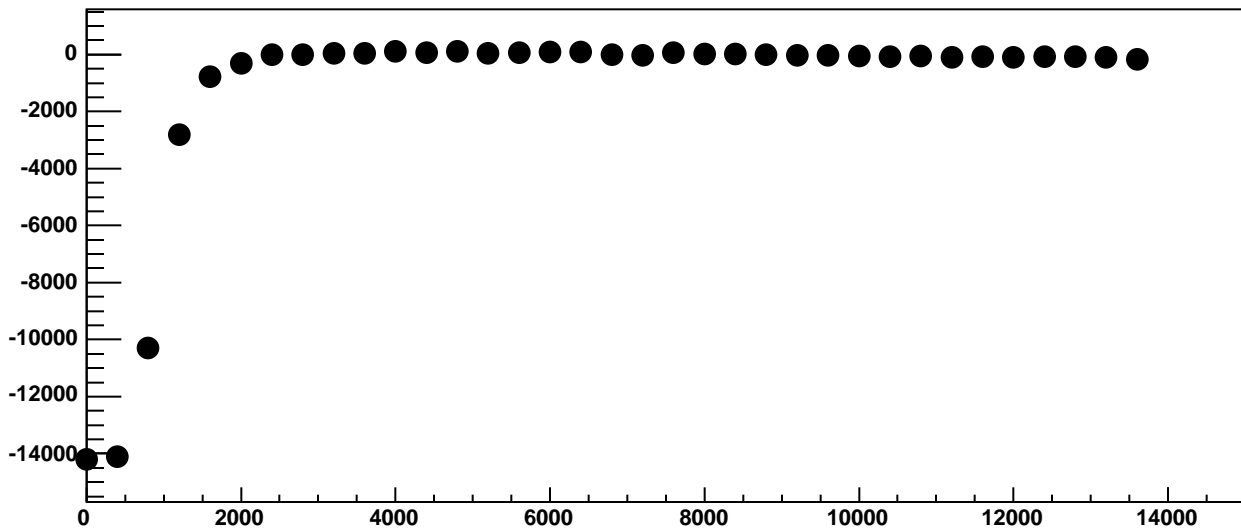
Chip 8, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



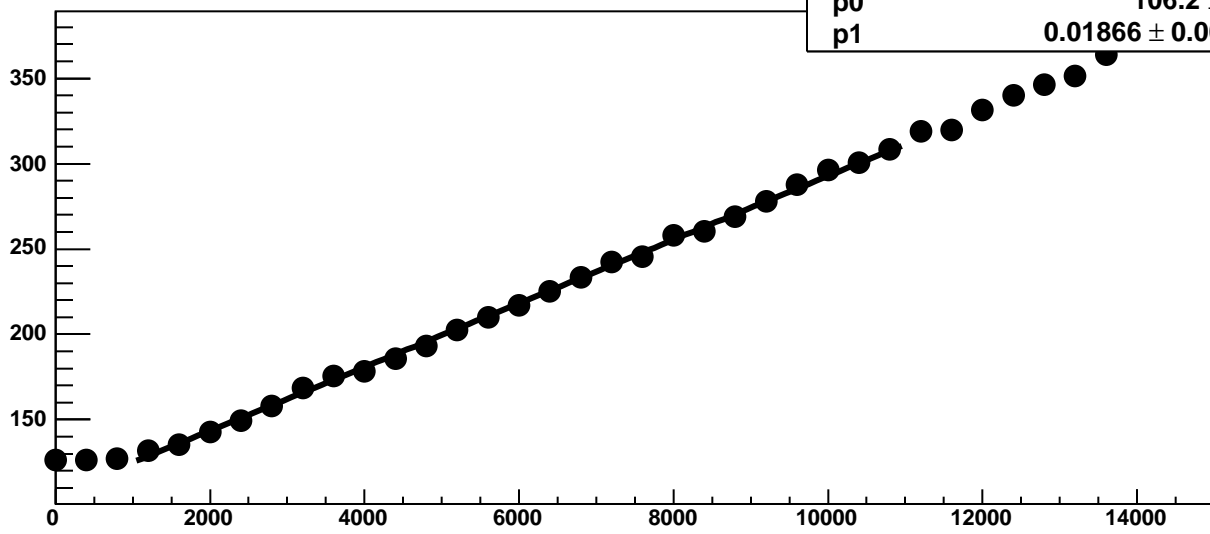
Chip 8, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC

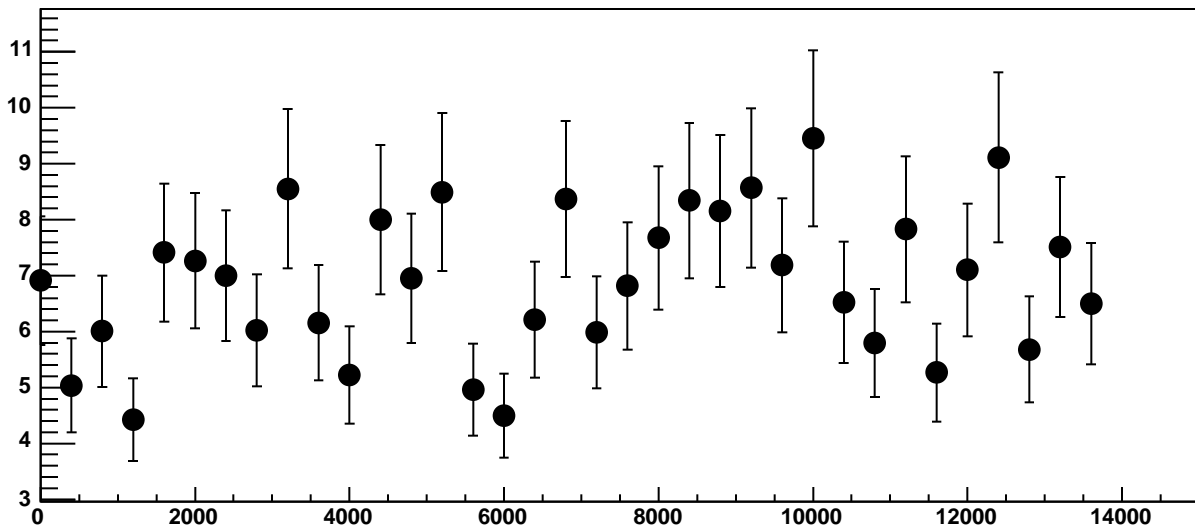


Chip 8, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

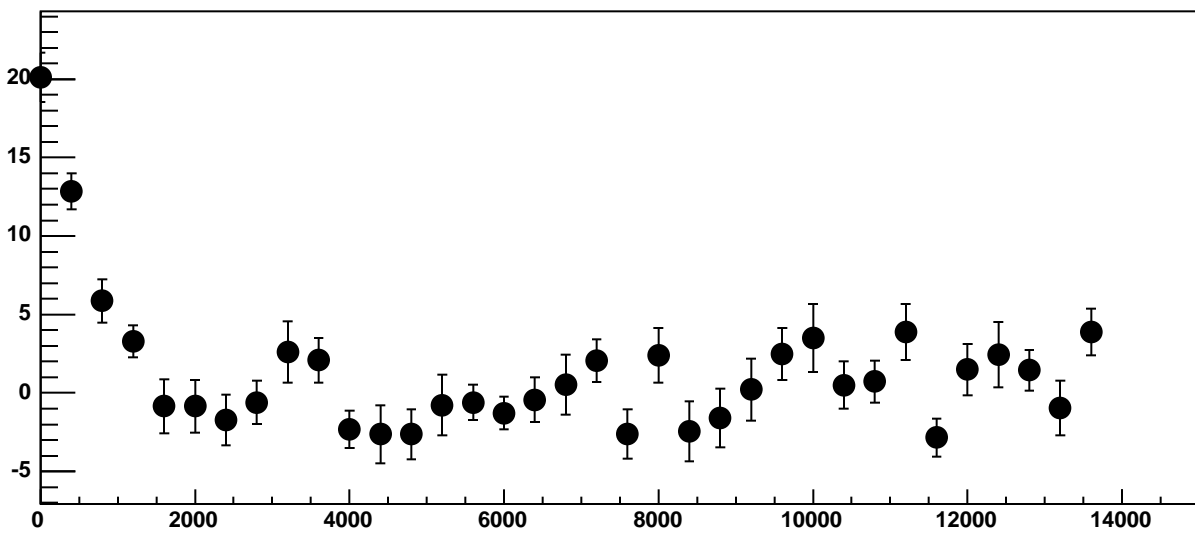


χ^2 / ndf 41.55 / 23
p0 106.2 ± 0.658
p1 0.01866 ± 0.0001038

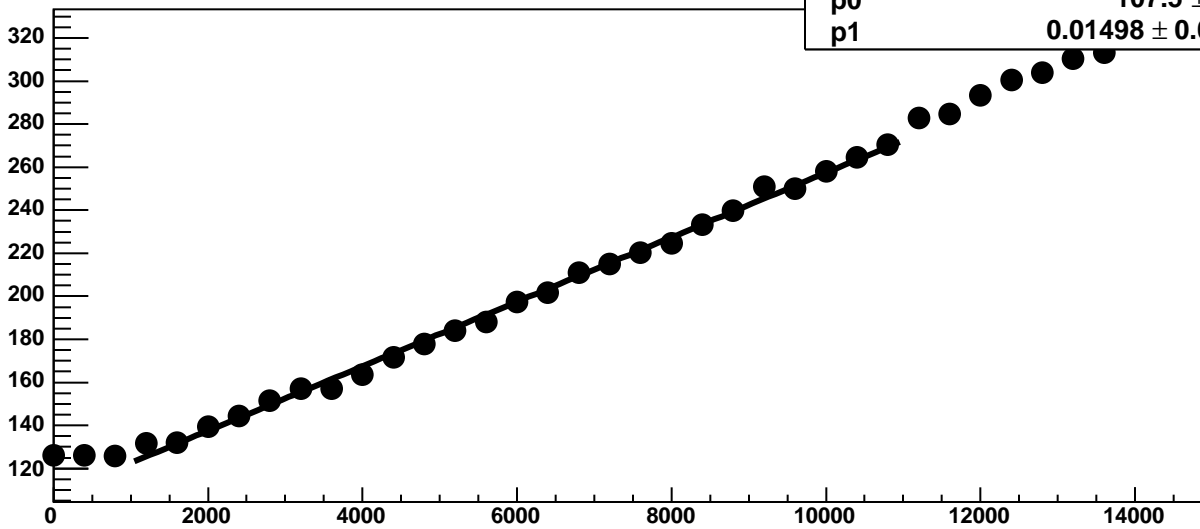
Chip 8, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

47.61 / 23

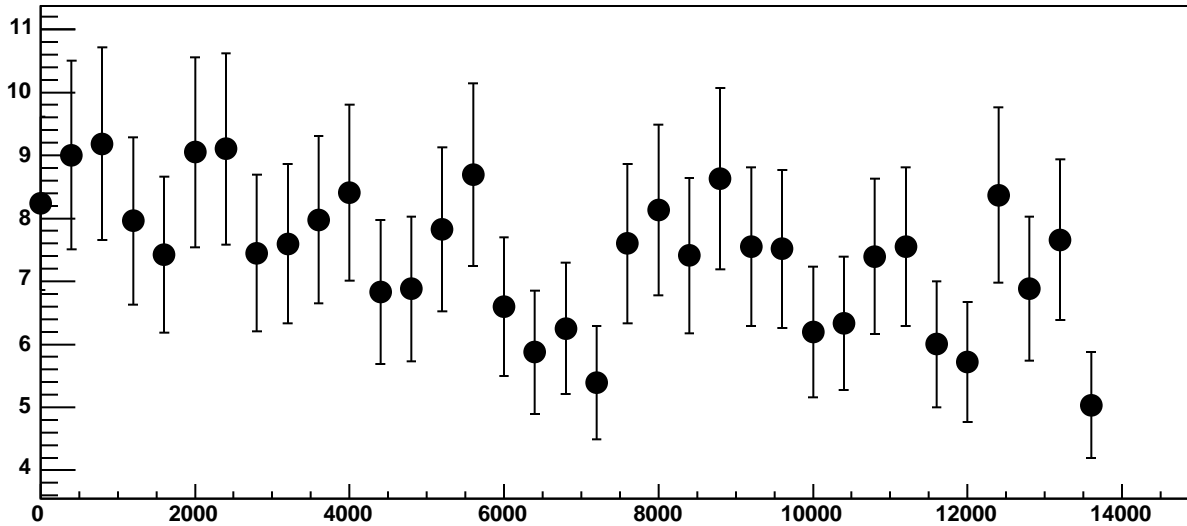
p0

107.5 ± 0.8194

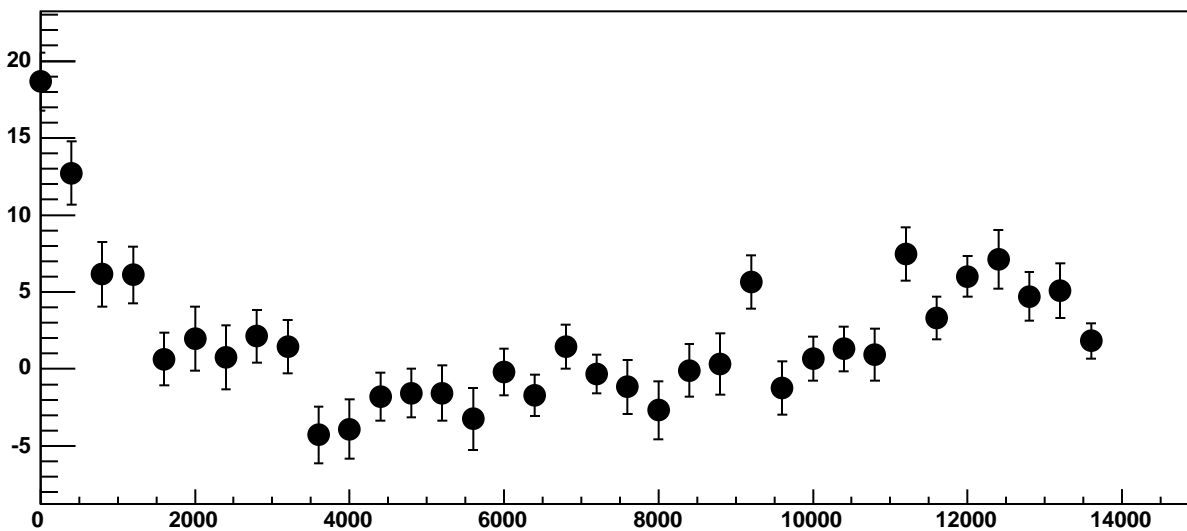
p1

0.01498 ± 0.0001191

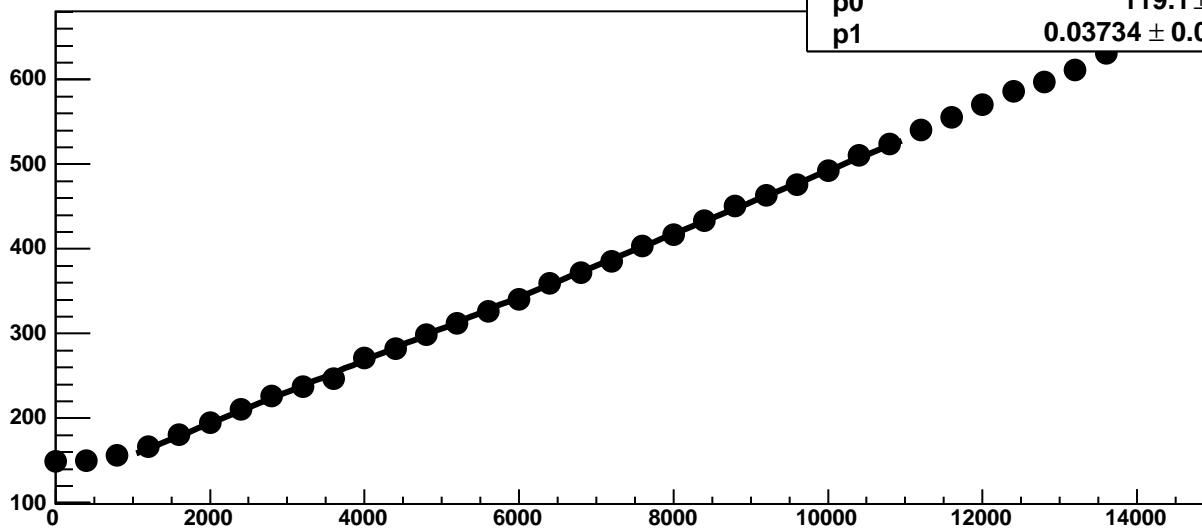
Chip 8, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

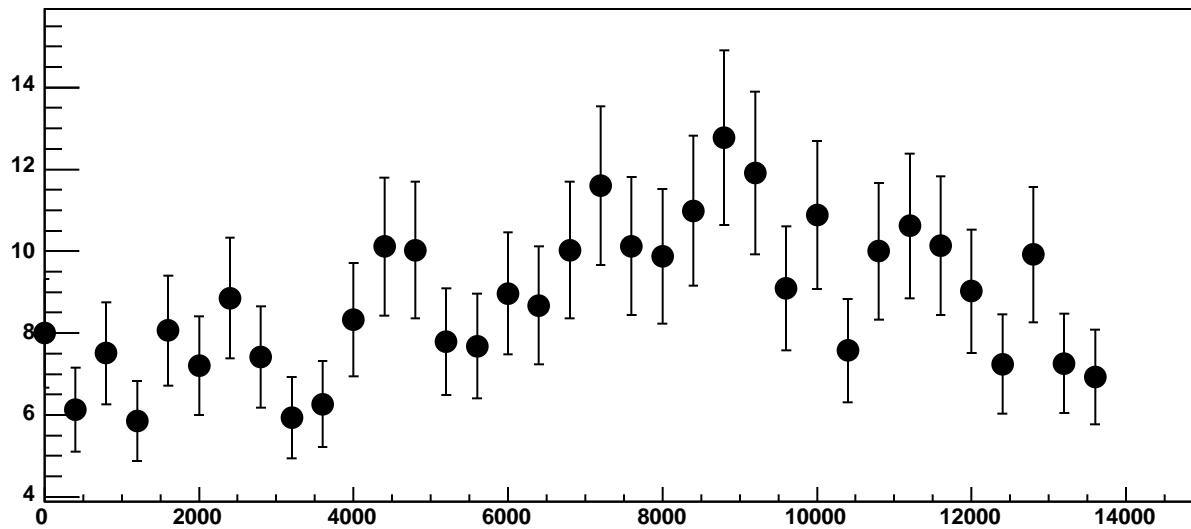


Chip 8, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

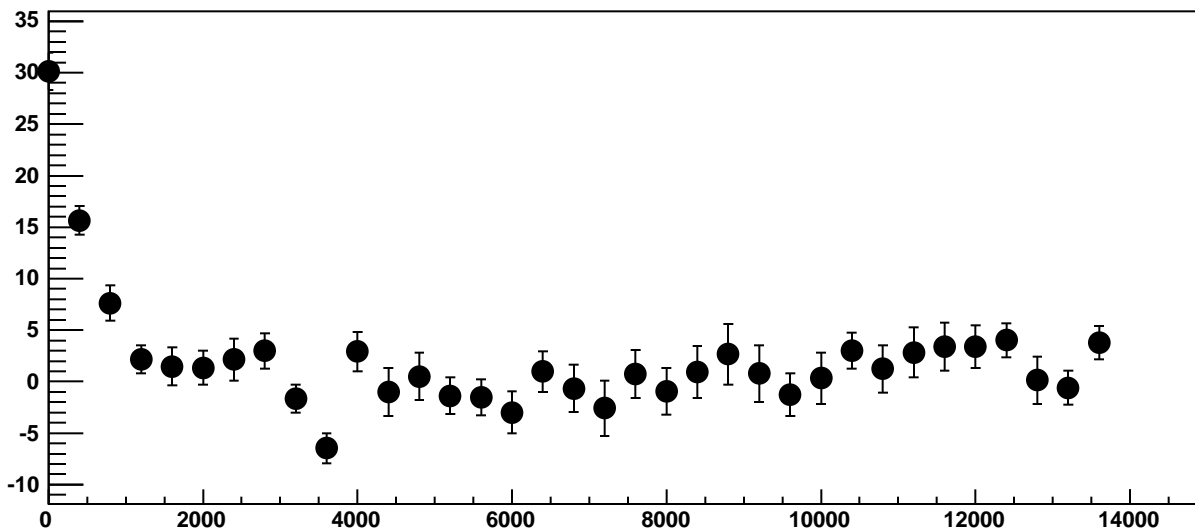


χ^2 / ndf 42.01 / 23
p0 119.1 ± 0.7898
p1 0.03734 ± 0.0001327

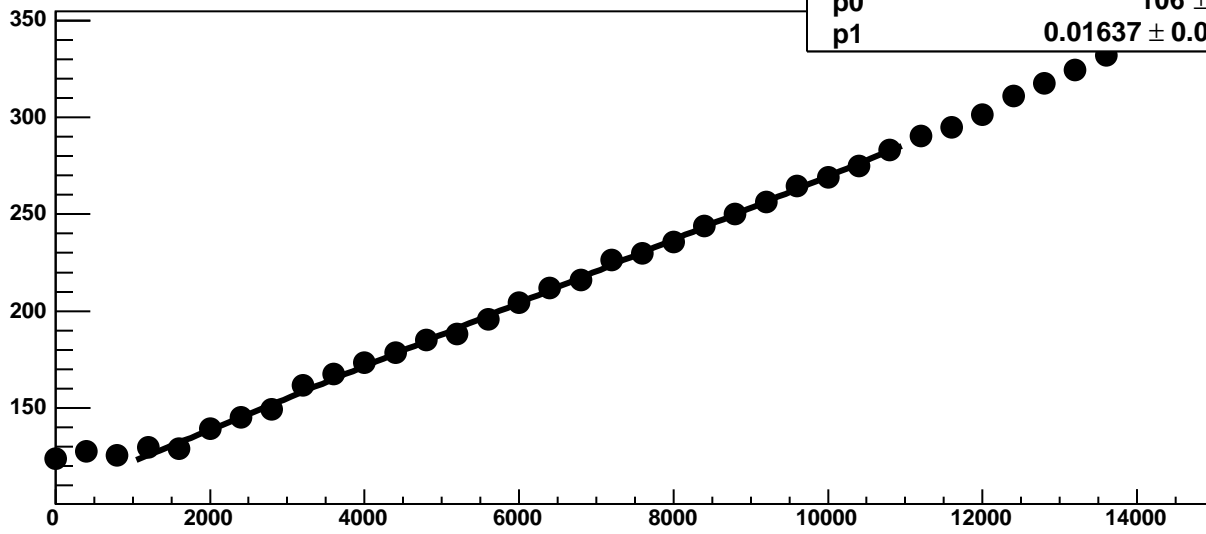
Chip 8, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

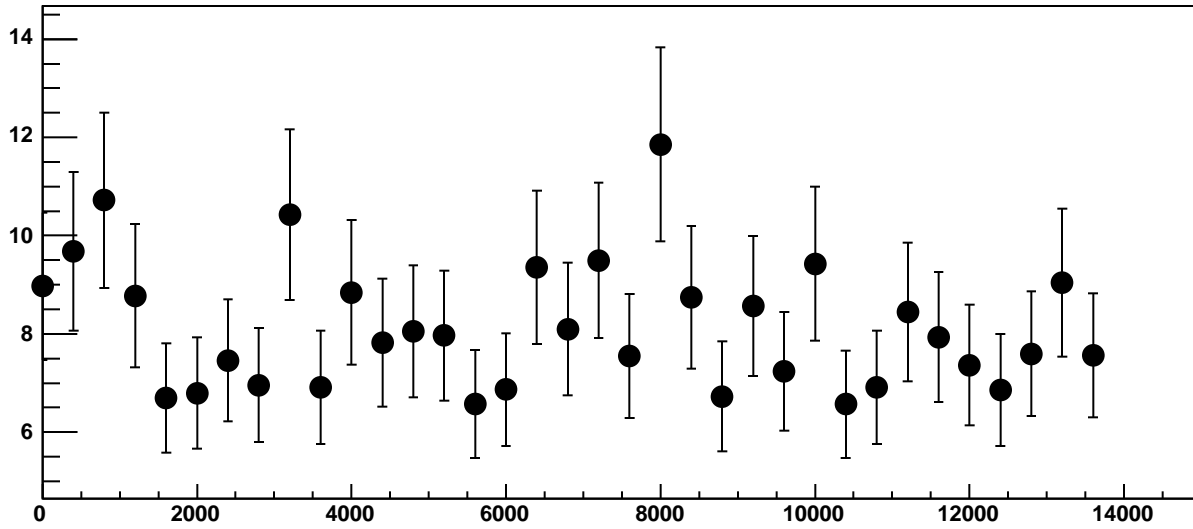


Chip 8, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

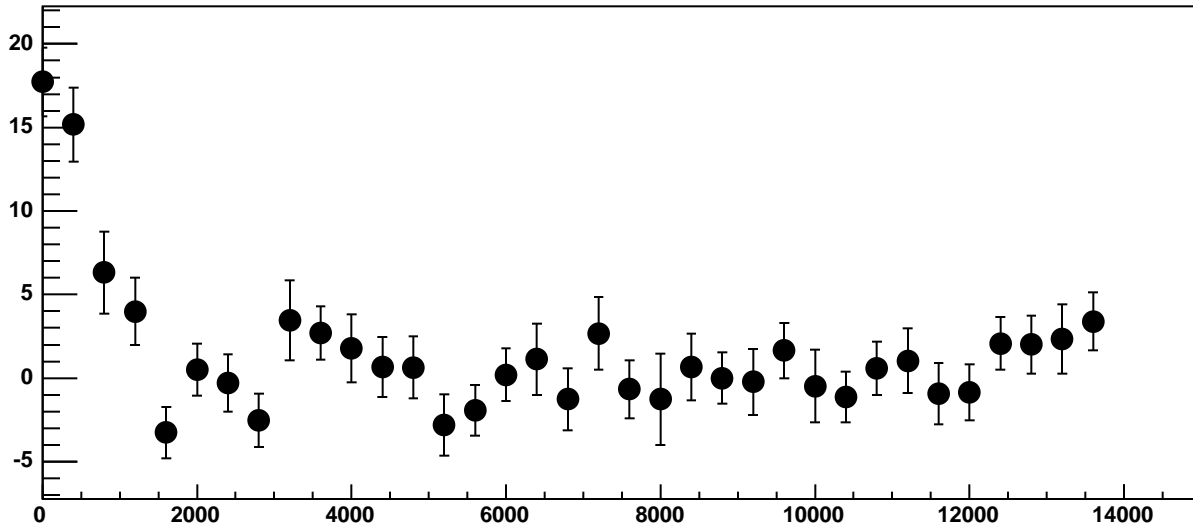


χ^2 / ndf 25.51 / 23
p0 106 ± 0.7946
p1 0.01637 ± 0.0001194

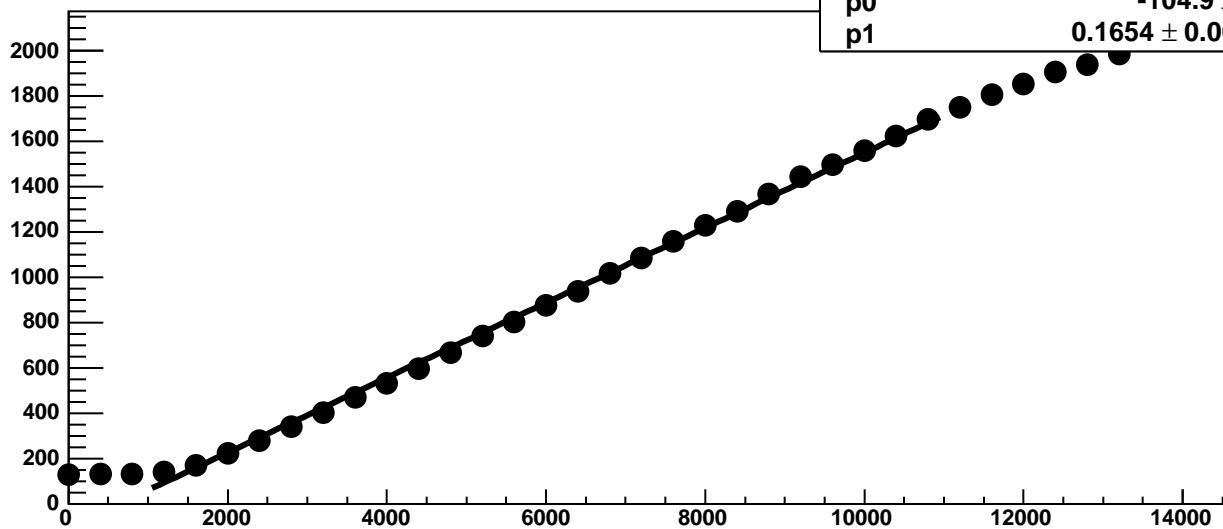
Chip 8, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



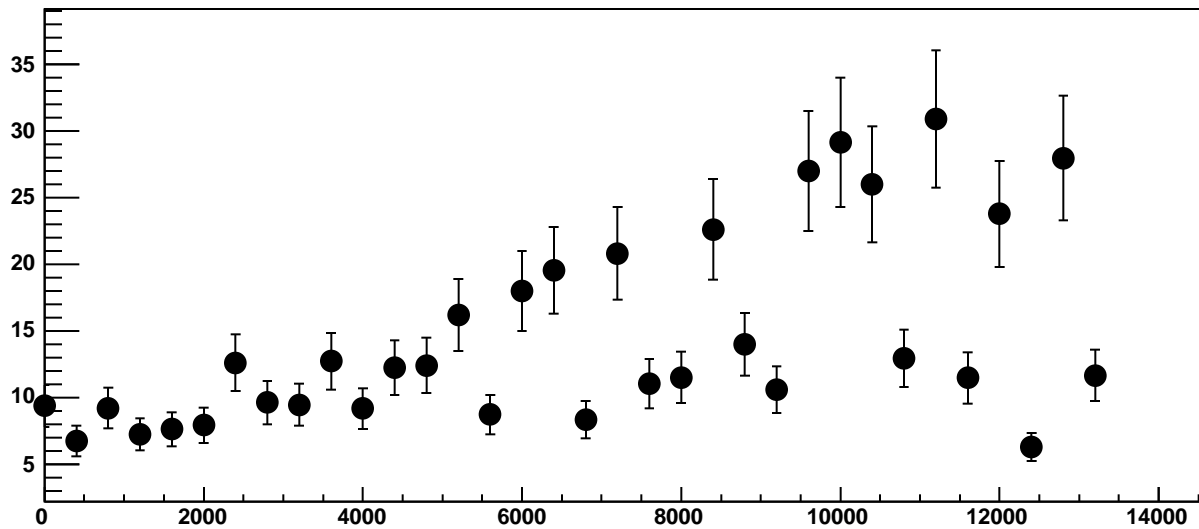
Chip 8, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



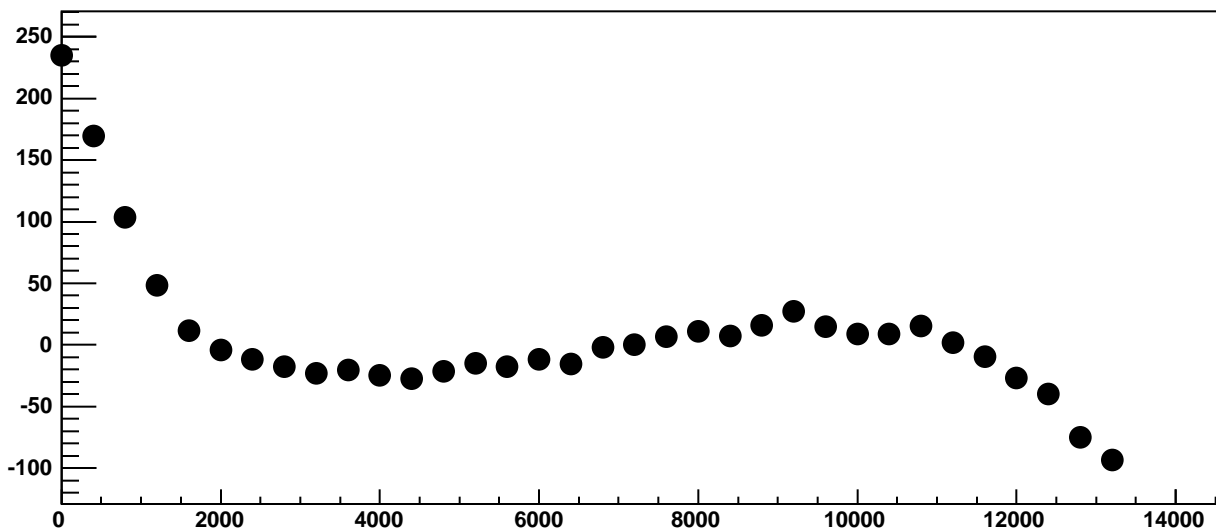
Chip 8, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



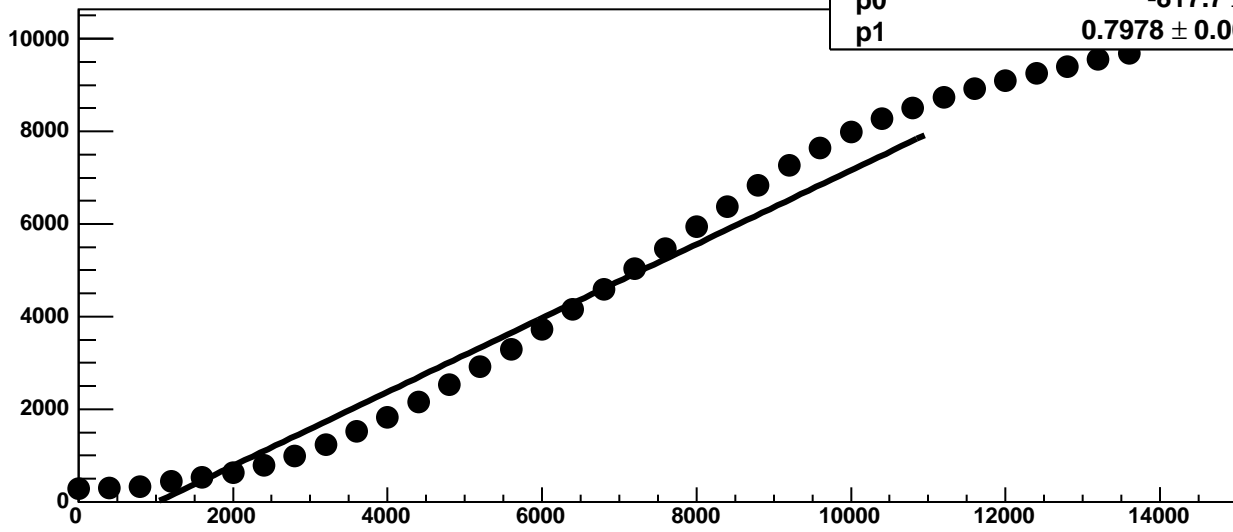
Chip 8, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



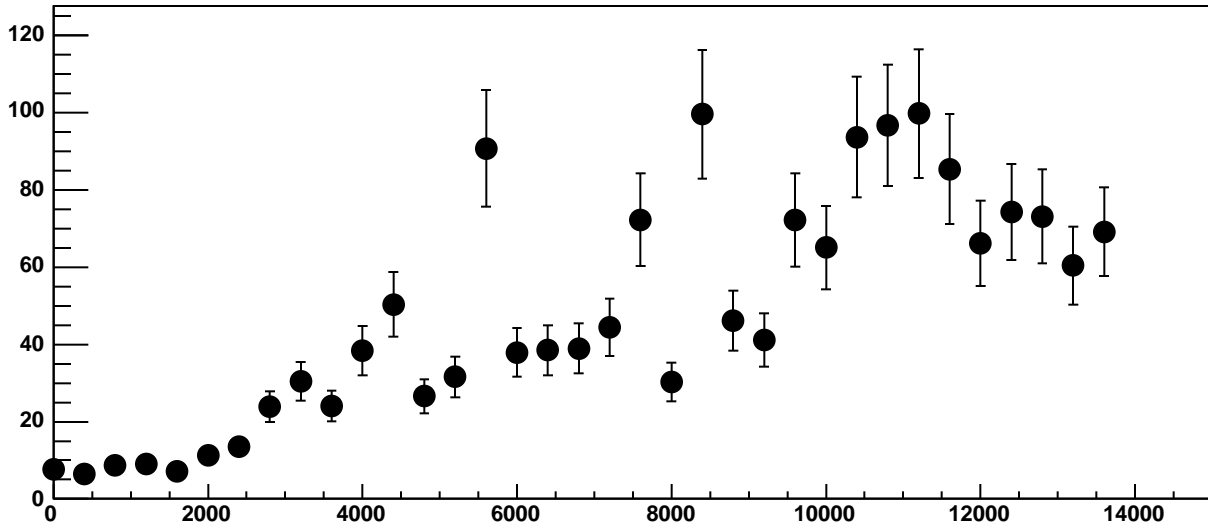
Chip 8, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



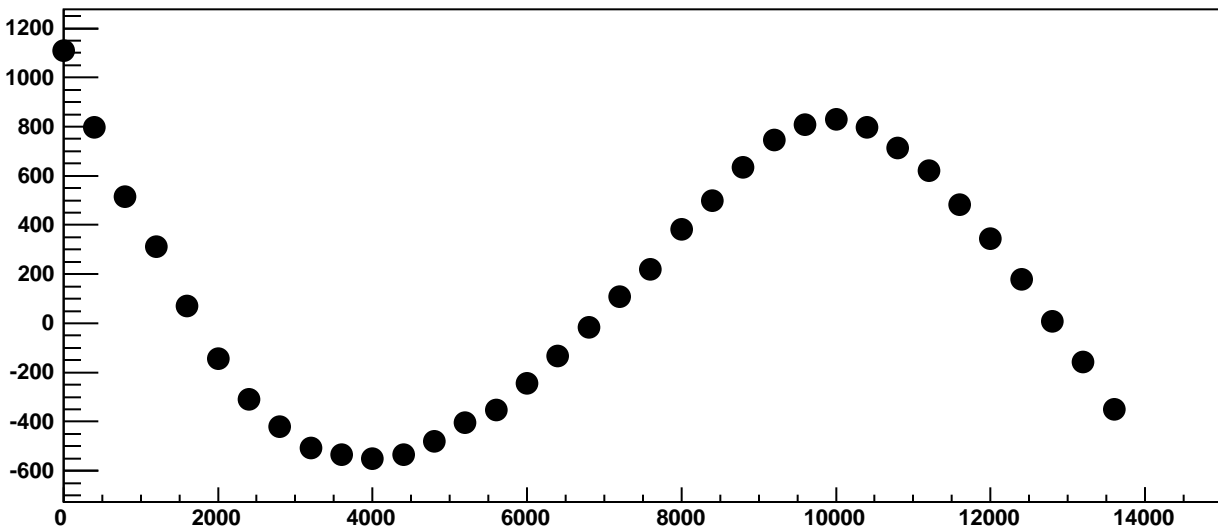
Chip 8, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC



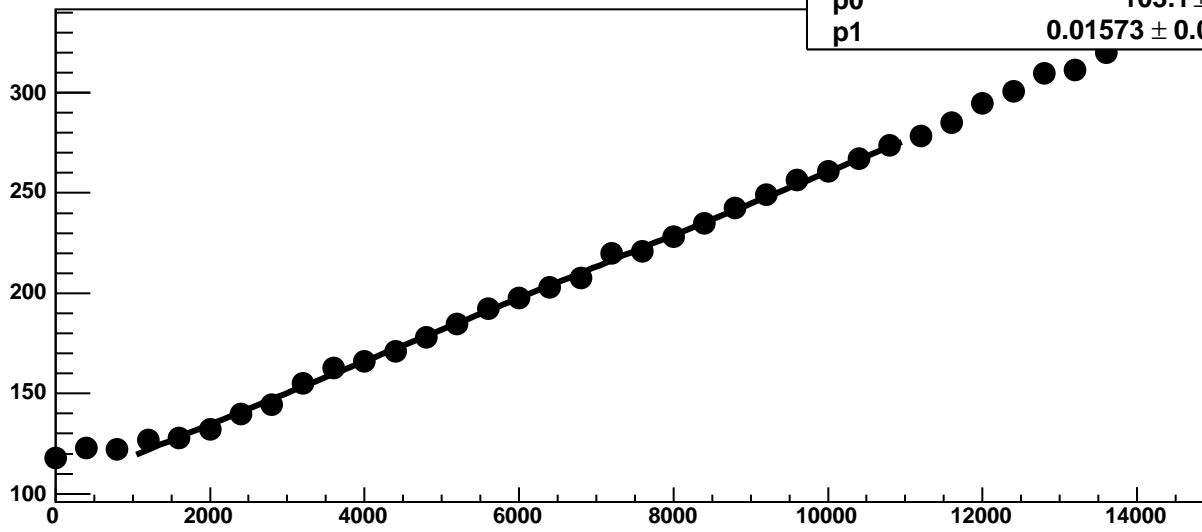
Chip 8, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC

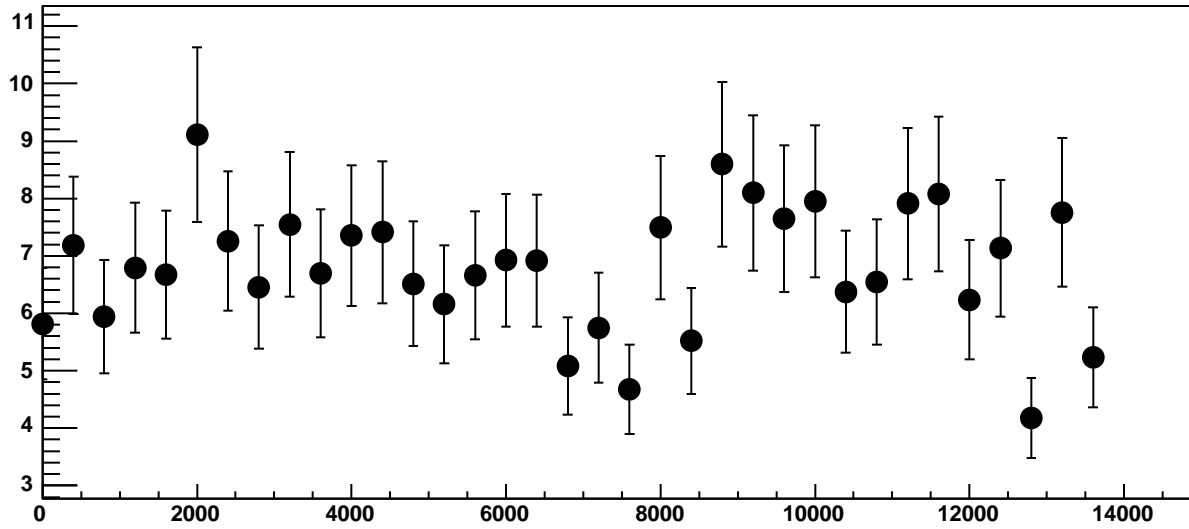


Chip 8, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

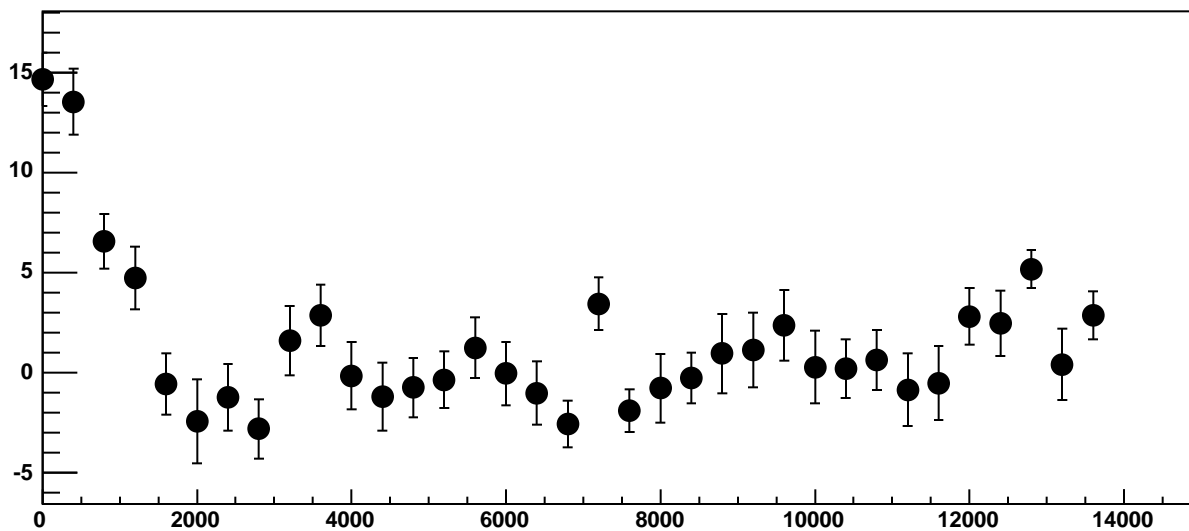


χ^2 / ndf 38.88 / 23
p0 103.1 ± 0.7469
p1 0.01573 ± 0.0001113

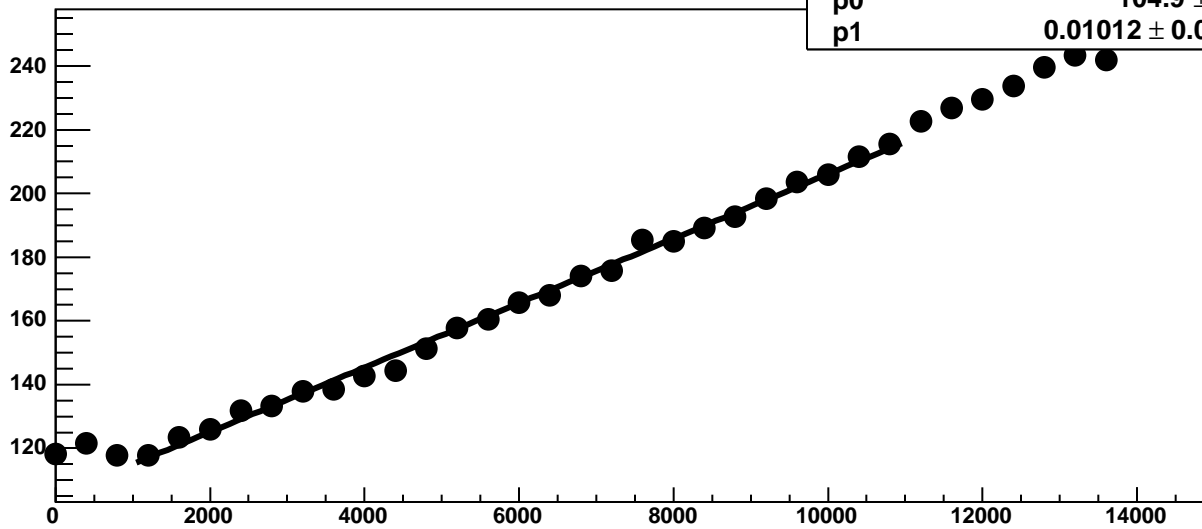
Chip 8, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

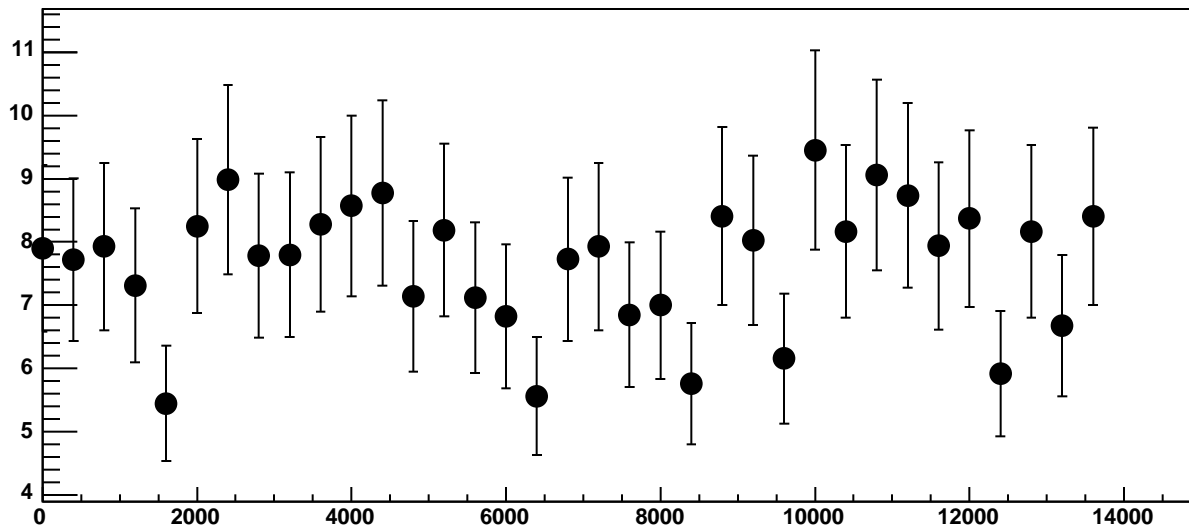


Chip 8, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

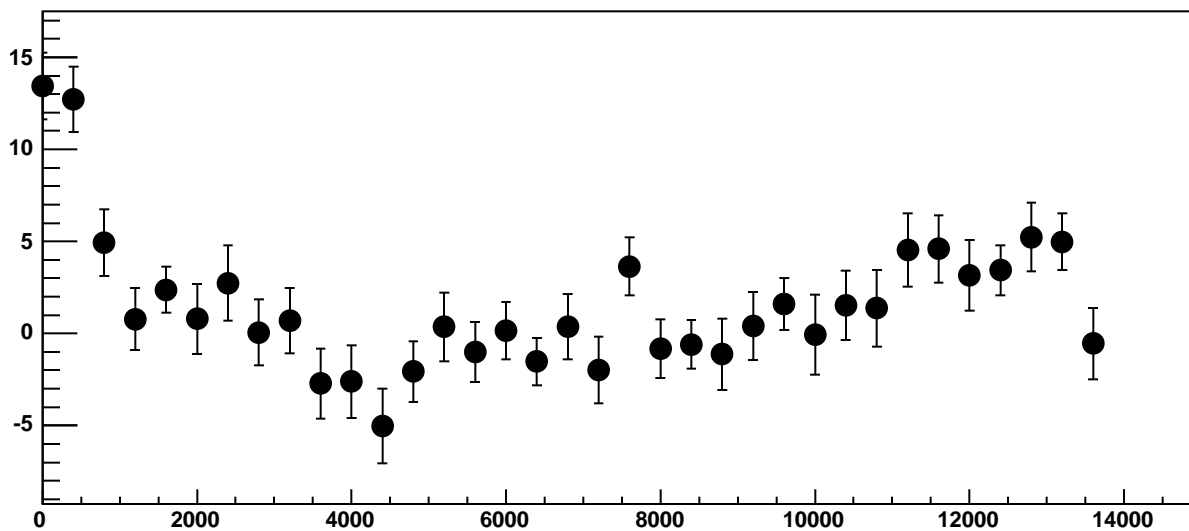


χ^2 / ndf 29.27 / 23
p0 104.9 ± 0.7868
p1 0.01012 ± 0.0001193

Chip 8, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

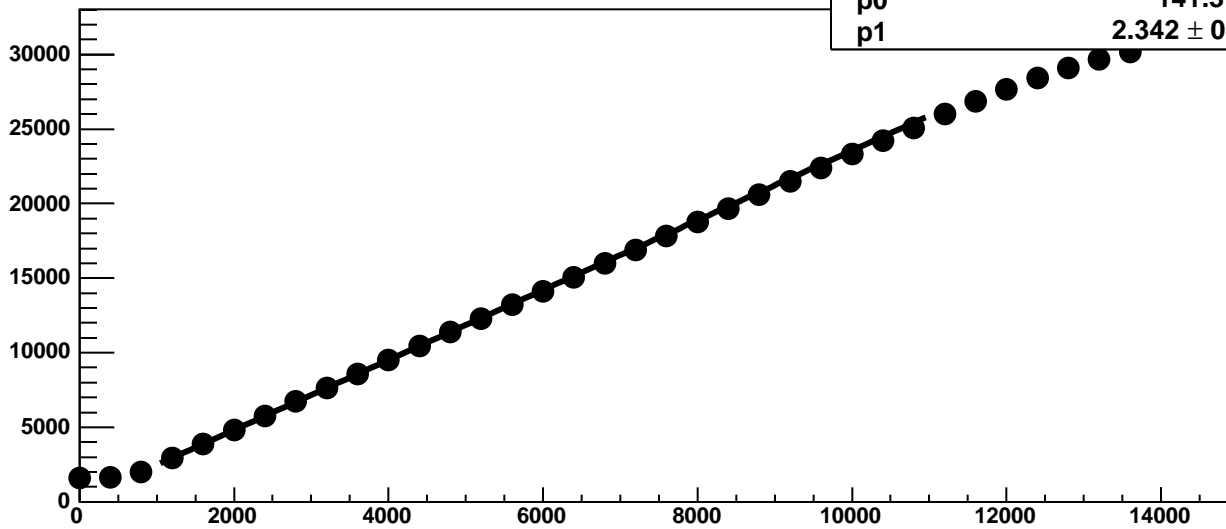


Chip 8, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

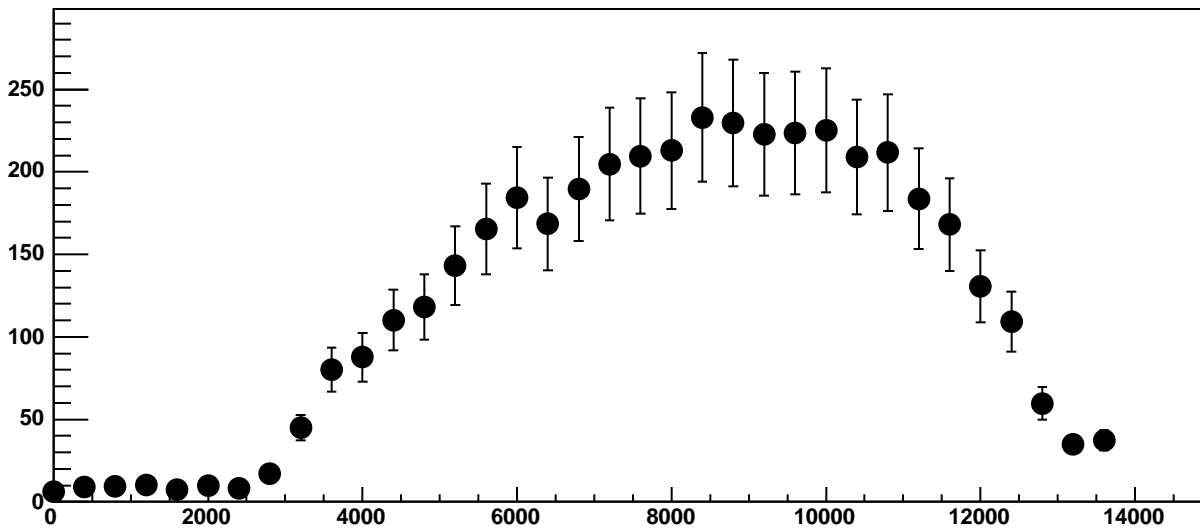


Chip 8, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC

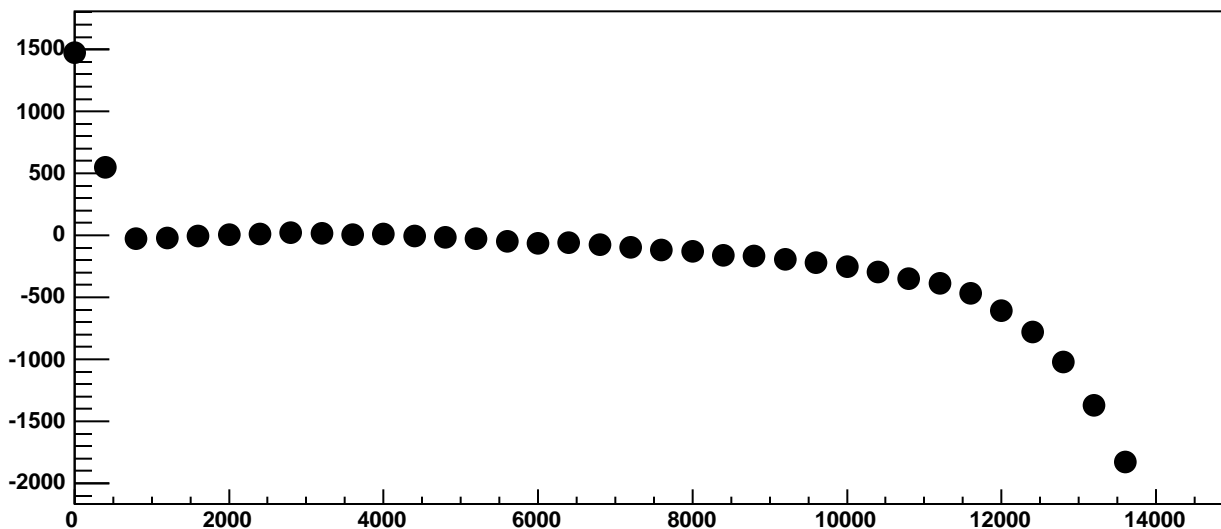
χ^2 / ndf 373.5 / 23
p0 141.3 ± 2.795
p1 2.342 ± 0.001339



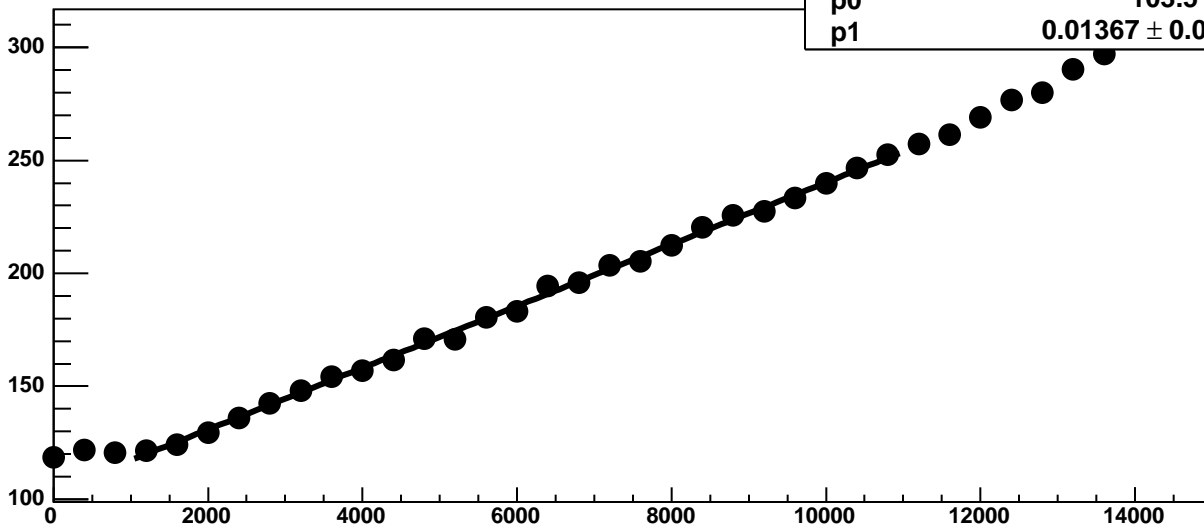
Chip 8, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC

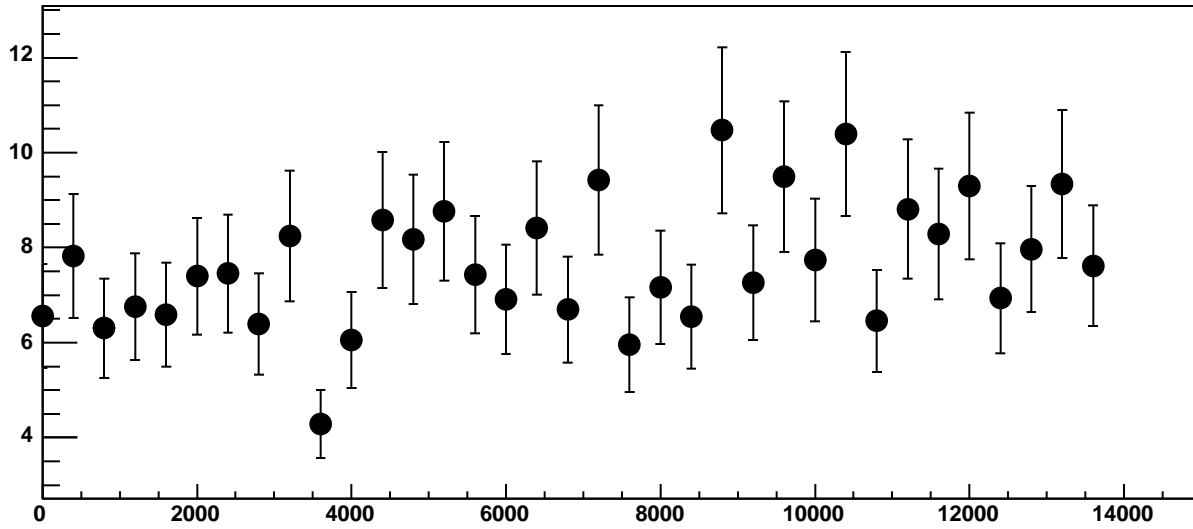


Chip 8, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

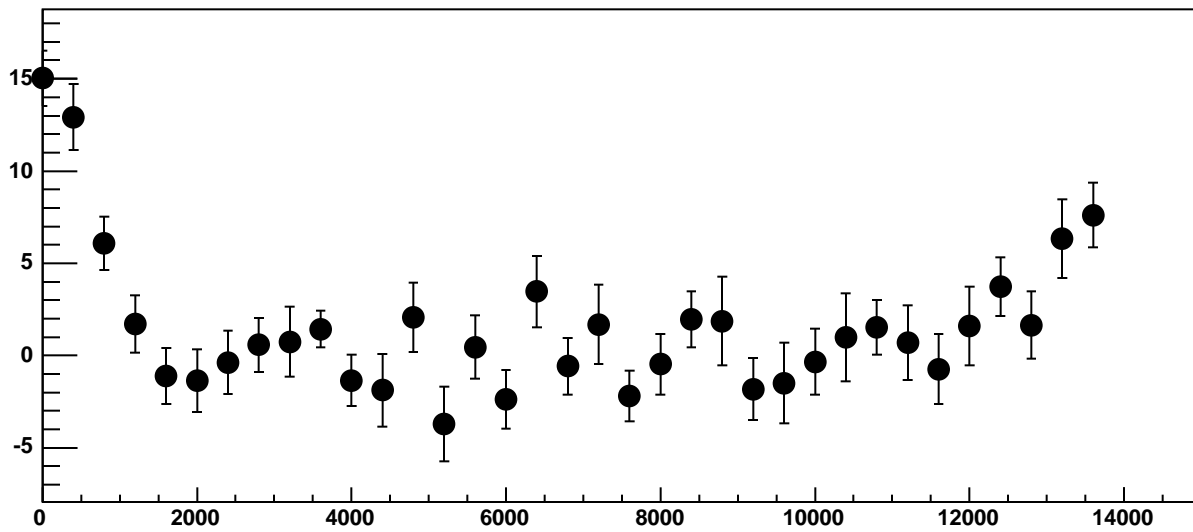


χ^2 / ndf 25.59 / 23
p0 103.5 ± 0.725
p1 0.01367 ± 0.0001148

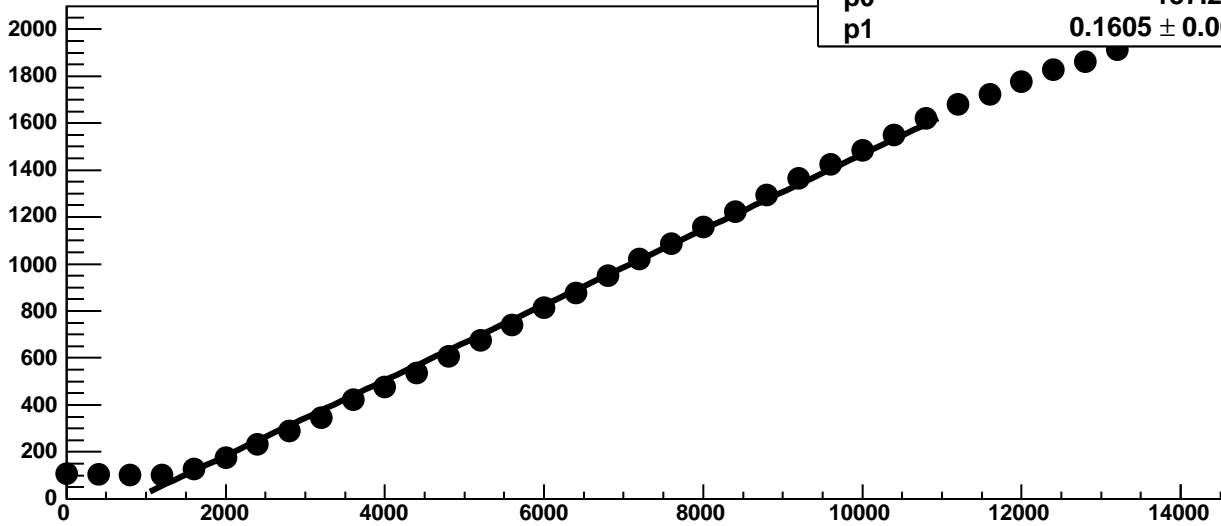
Chip 8, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1745 / 23

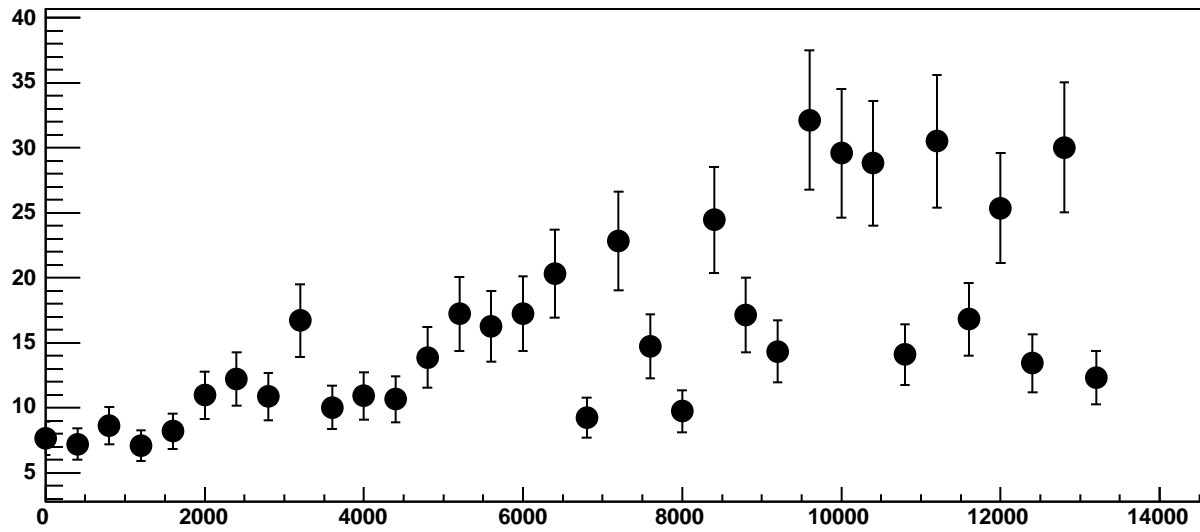
p0

-137.2 ± 1.12

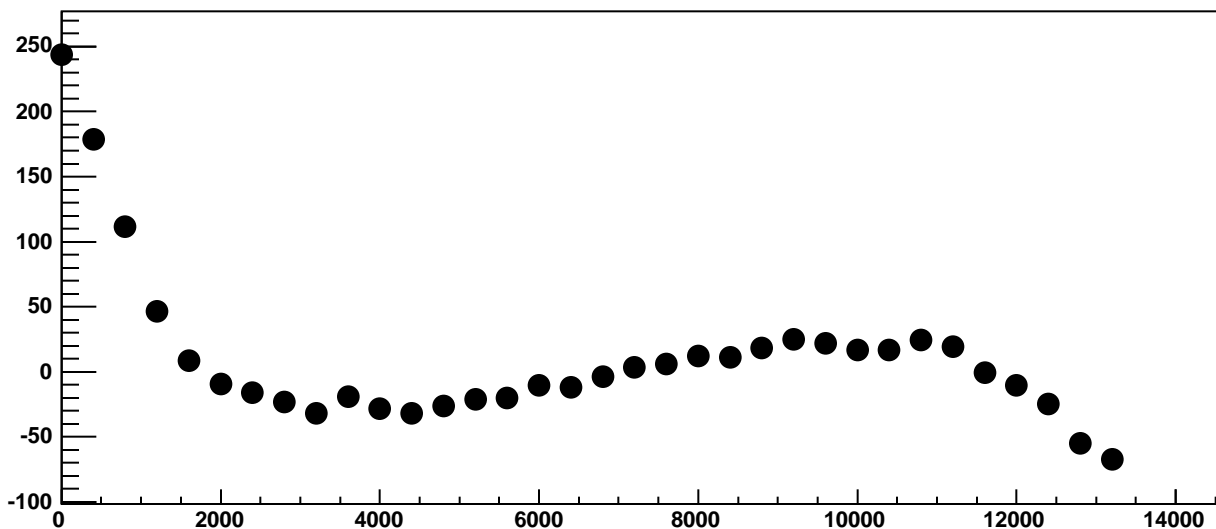
p1

0.1605 ± 0.0002068

Chip 8, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

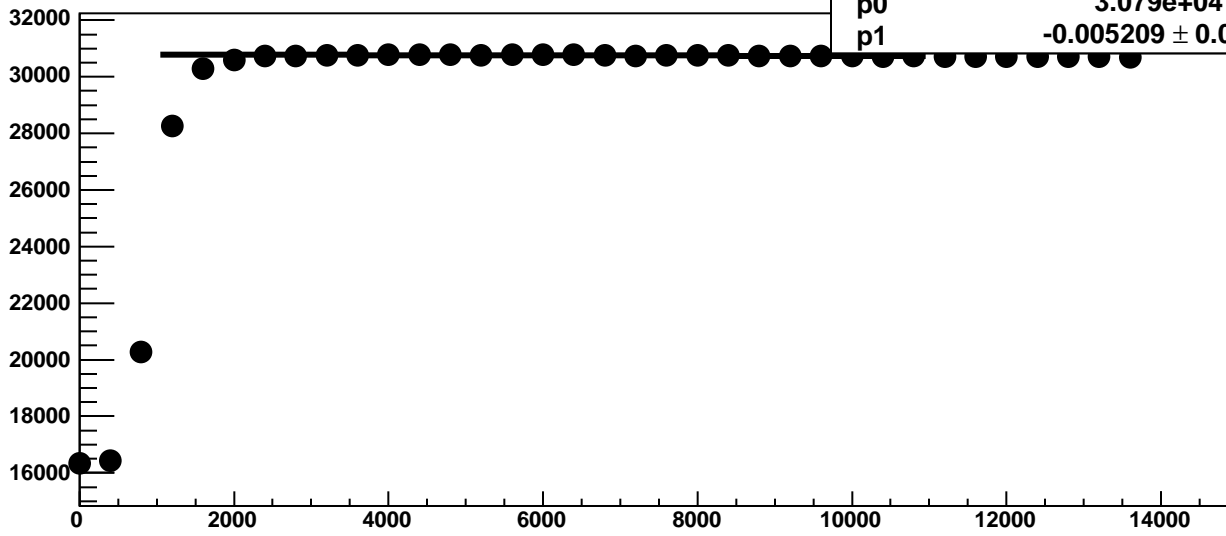
1019 / 23

p0

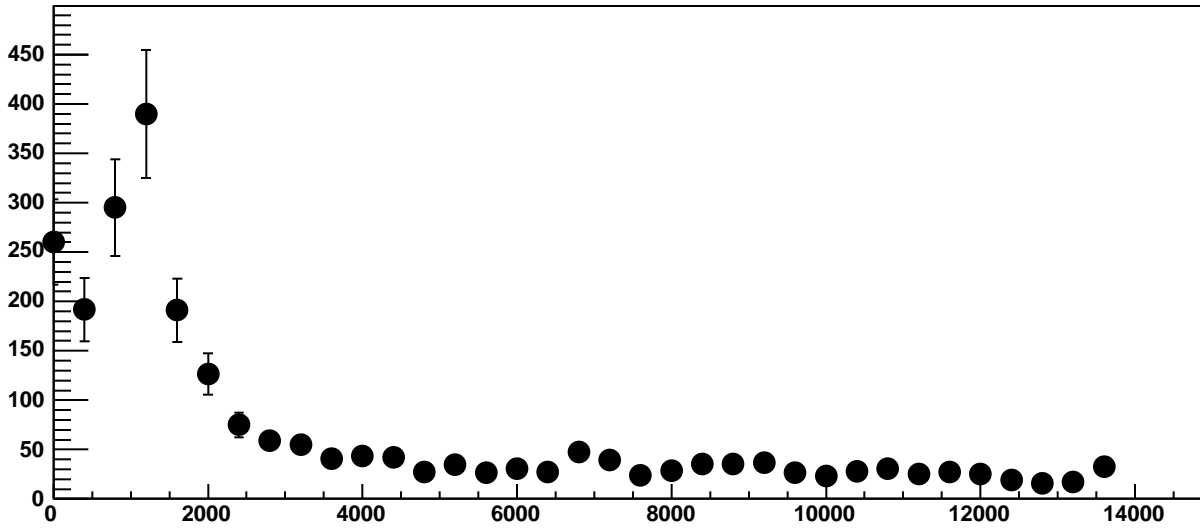
$3.079\text{e}+04 \pm 5.352$

p1

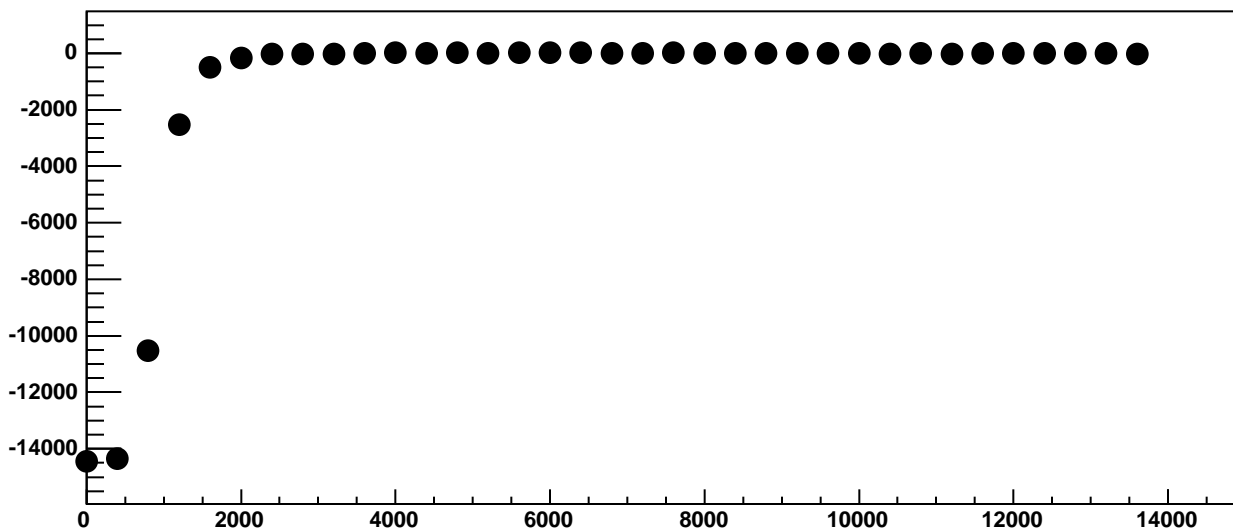
-0.005209 ± 0.0006952



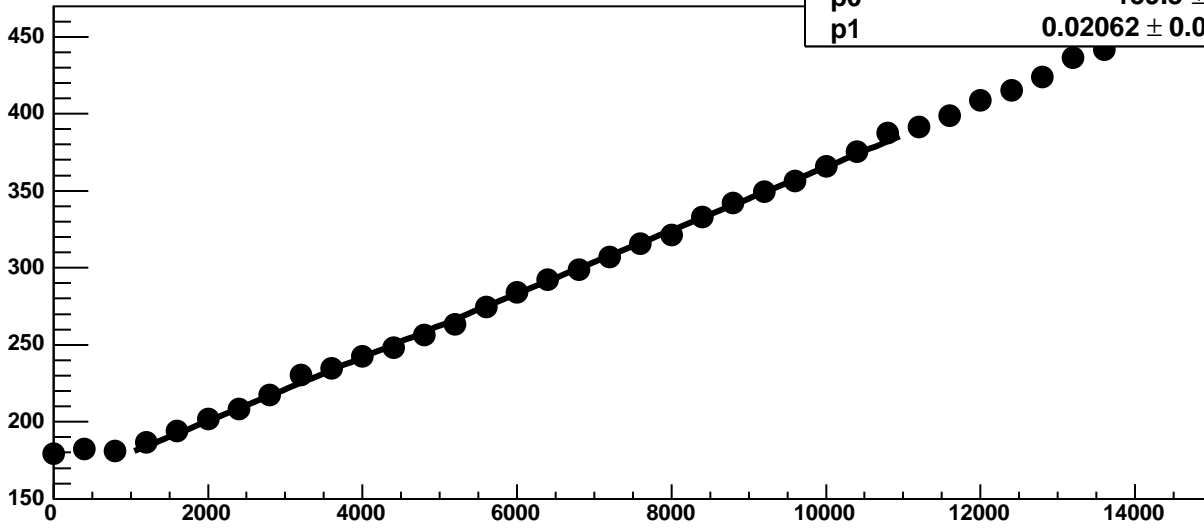
Chip 8, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC

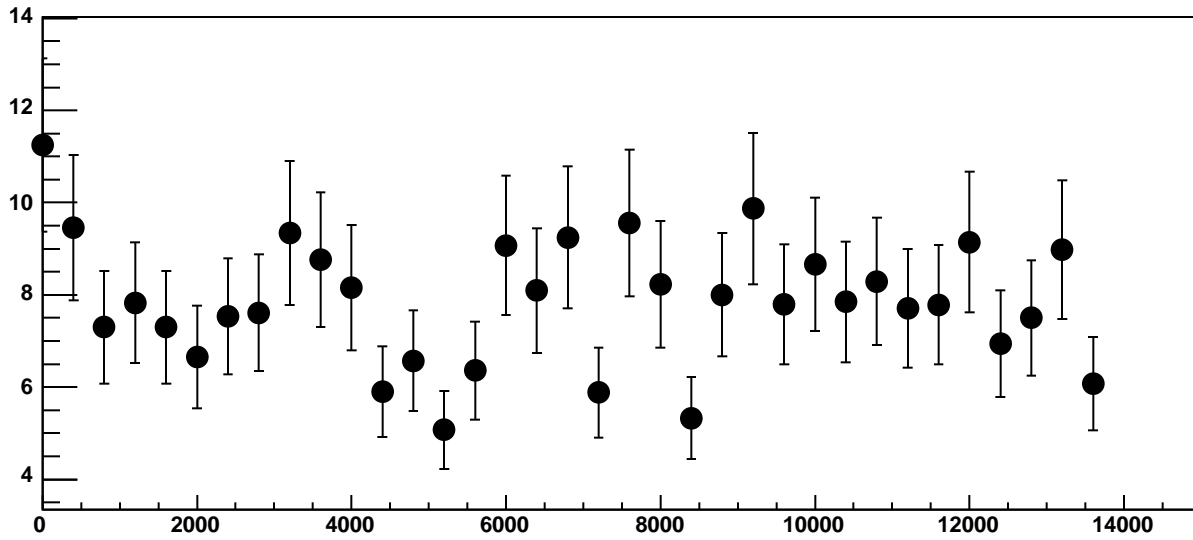


Chip 8, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

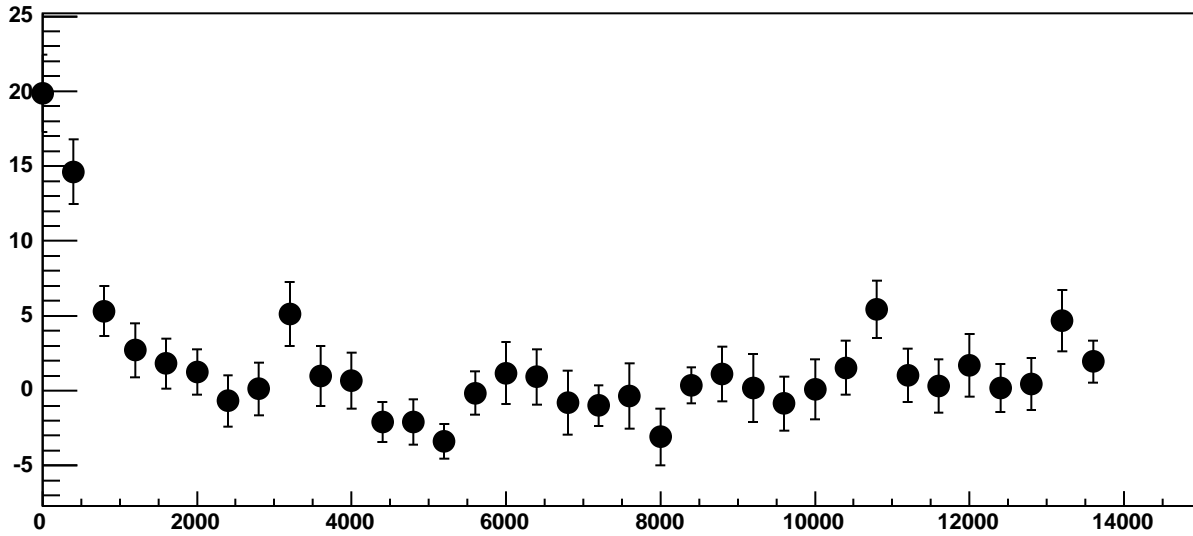


χ^2 / ndf 36.63 / 23
p0 159.3 ± 0.7933
p1 0.02062 ± 0.0001226

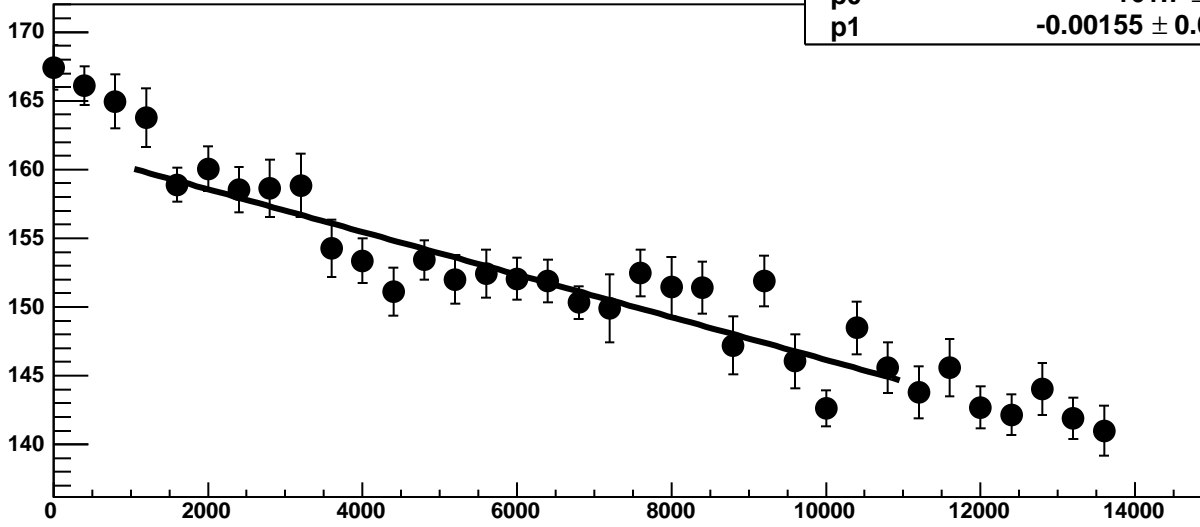
Chip 8, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

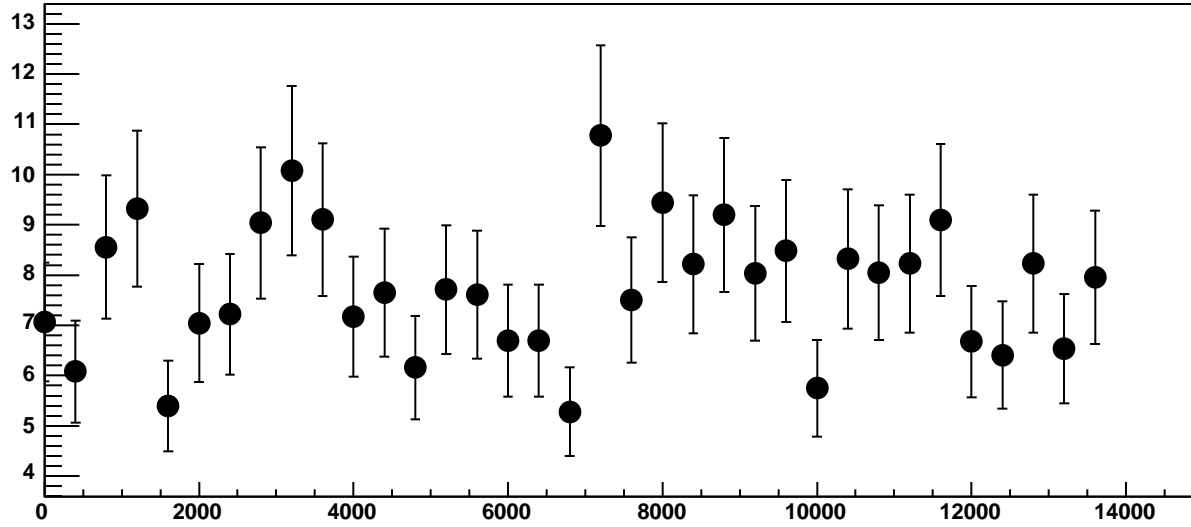


Chip 8, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

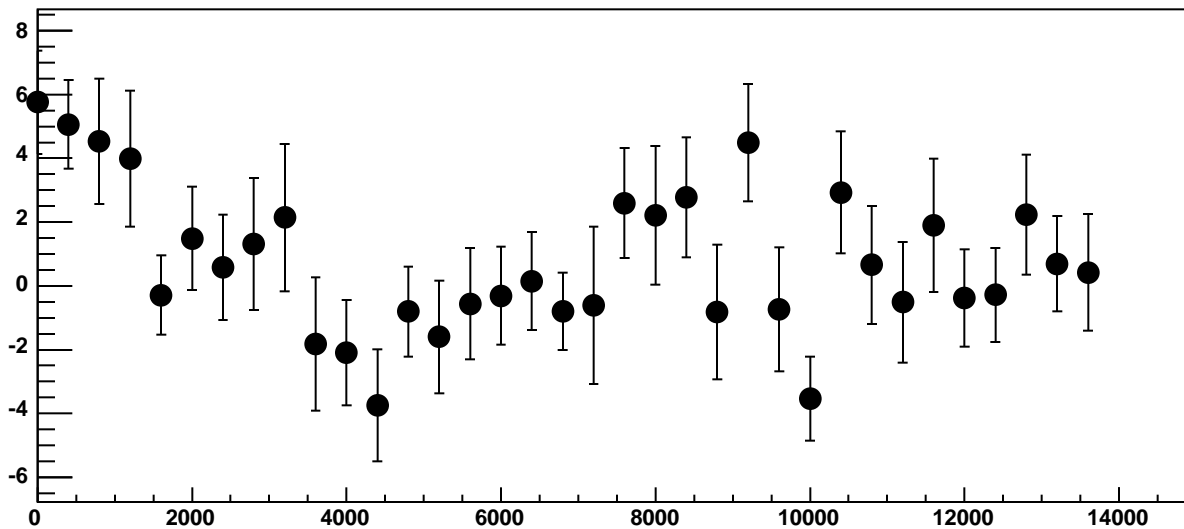


χ^2 / ndf 35.83 / 23
p0 161.7 ± 0.7818
p1 -0.00155 ± 0.0001191

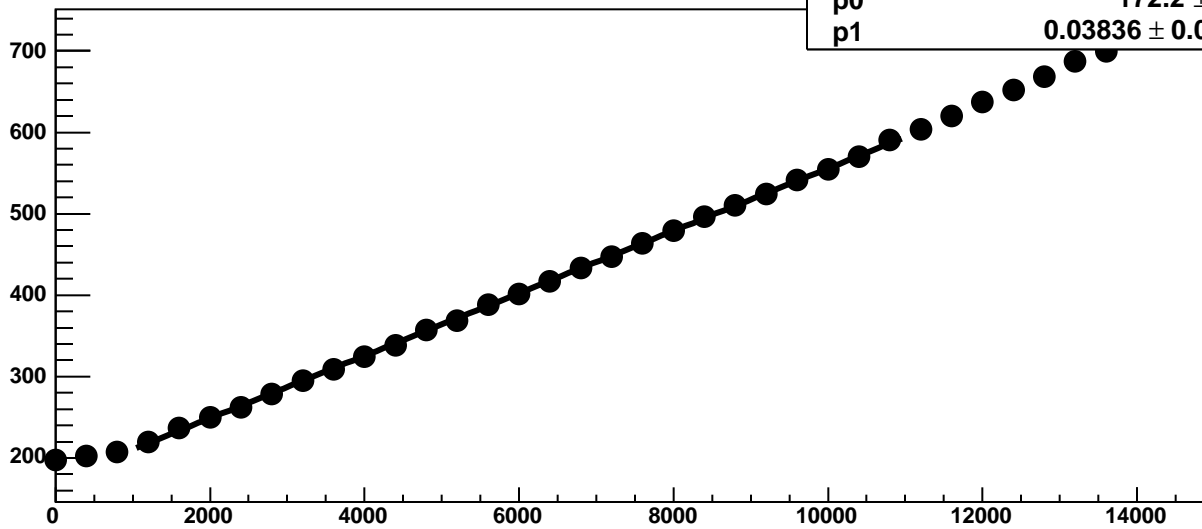
Chip 8, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

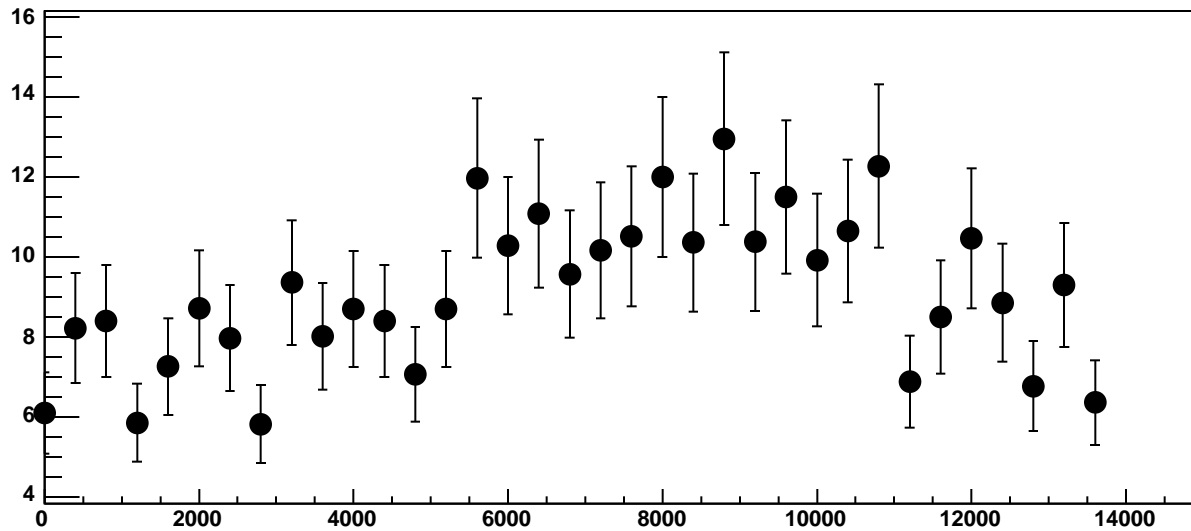


Chip 8, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

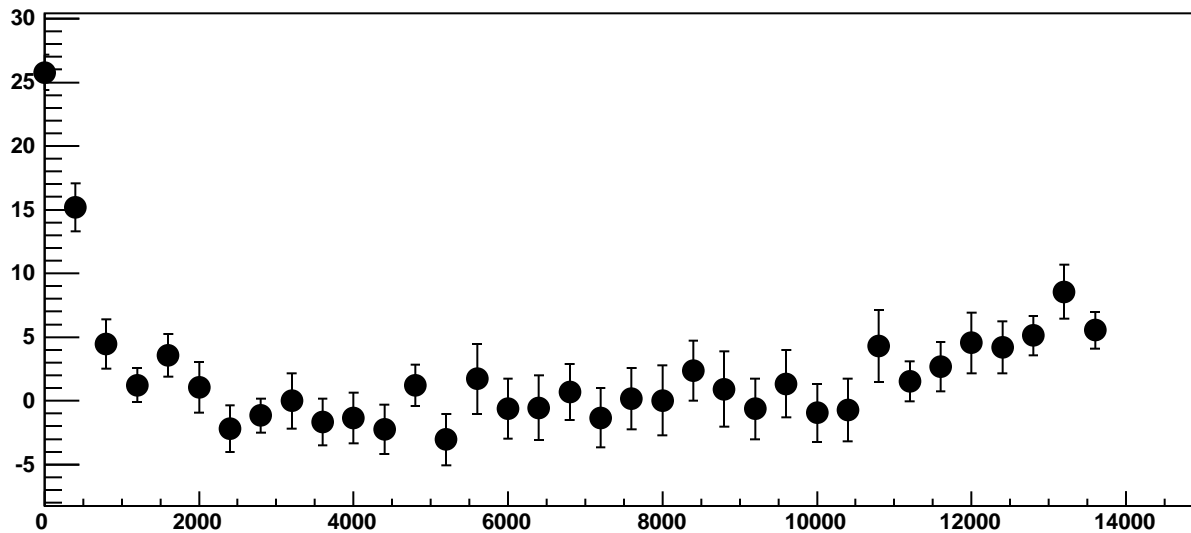


χ^2 / ndf 18.31 / 23
p0 172.2 ± 0.8193
p1 0.03836 ± 0.0001428

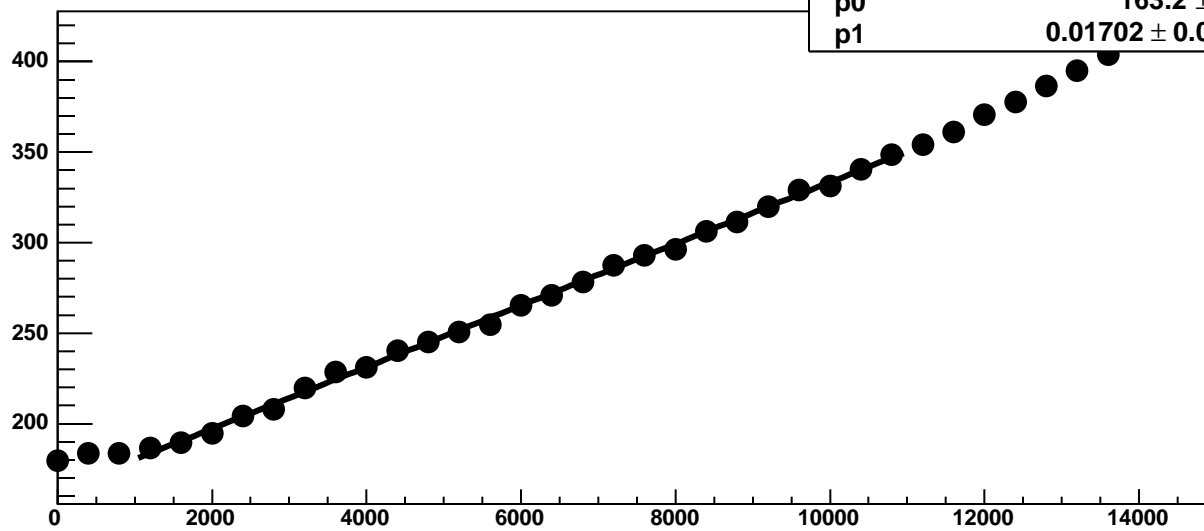
Chip 8, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

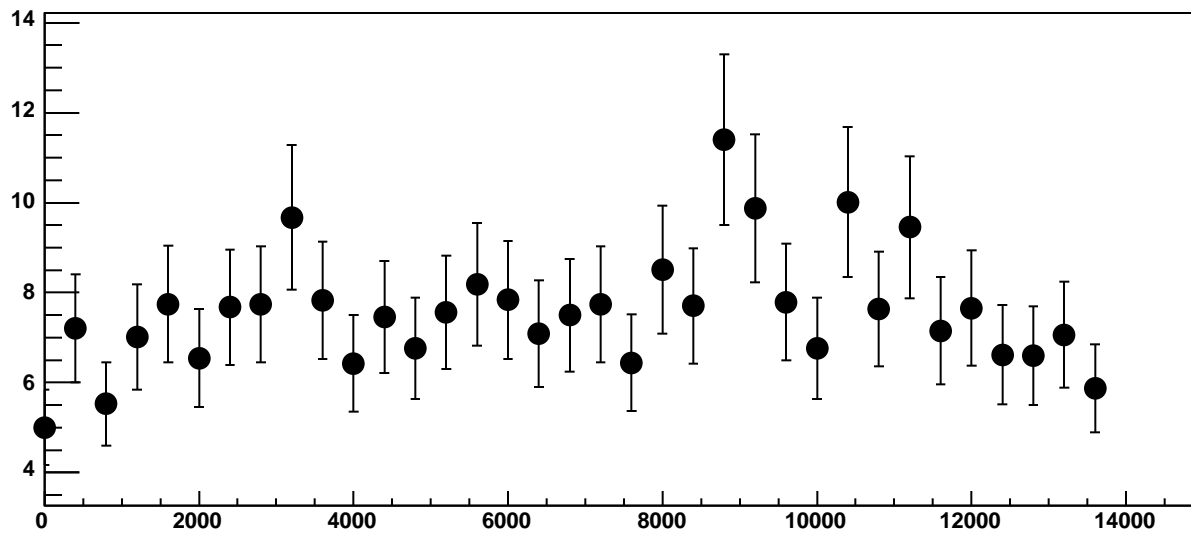


Chip 8, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

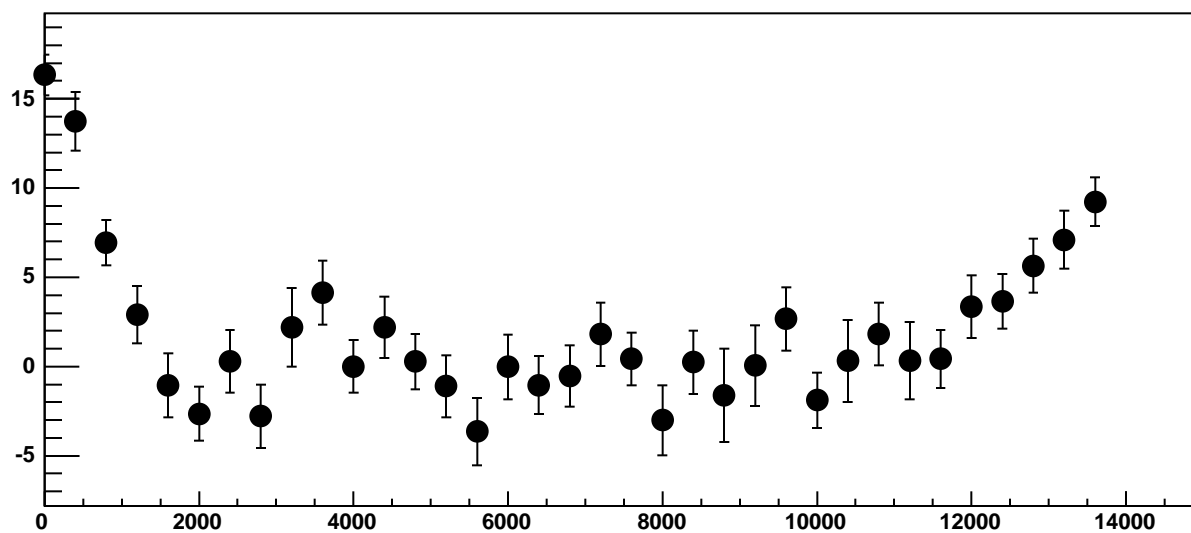


χ^2 / ndf 30.53 / 23
p0 163.2 ± 0.7936
p1 0.01702 ± 0.0001232

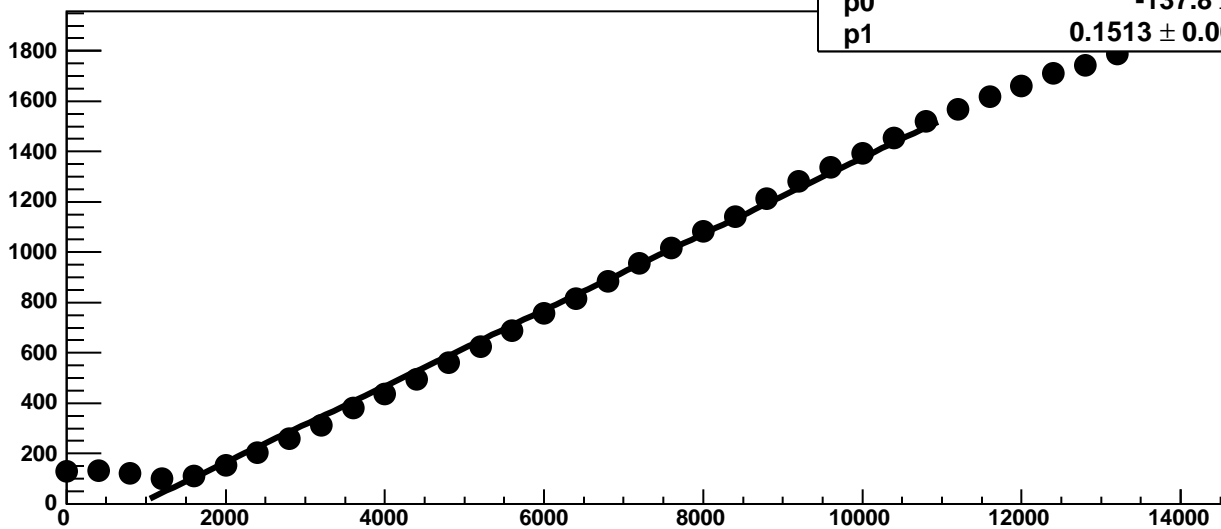
Chip 8, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

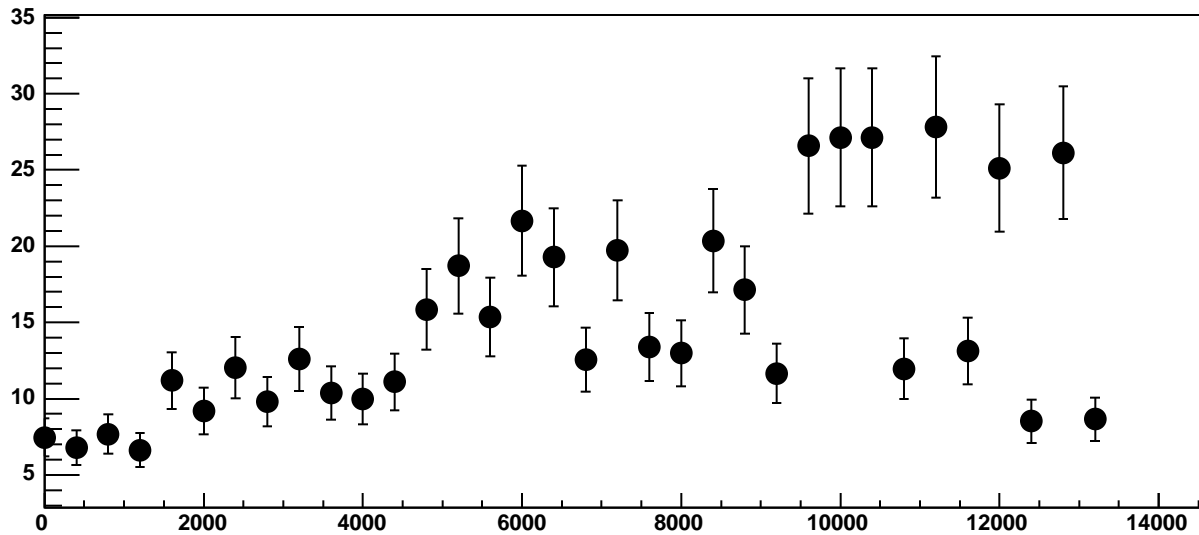


Chip 8, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

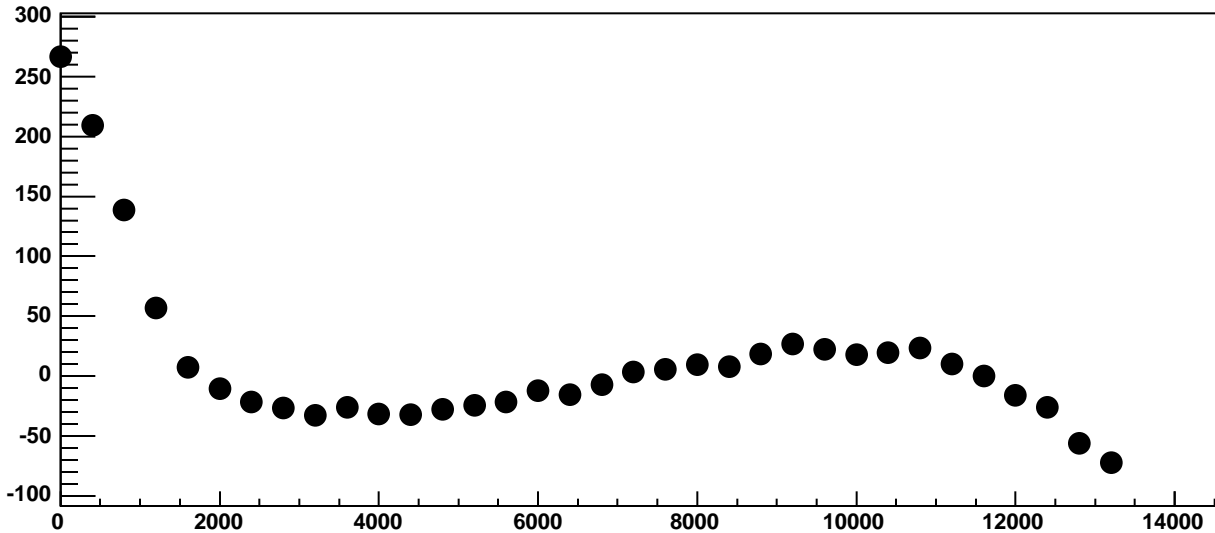


χ^2 / ndf 2632 / 23
p0 -137.8 ± 1.089
p1 0.1513 ± 0.0001987

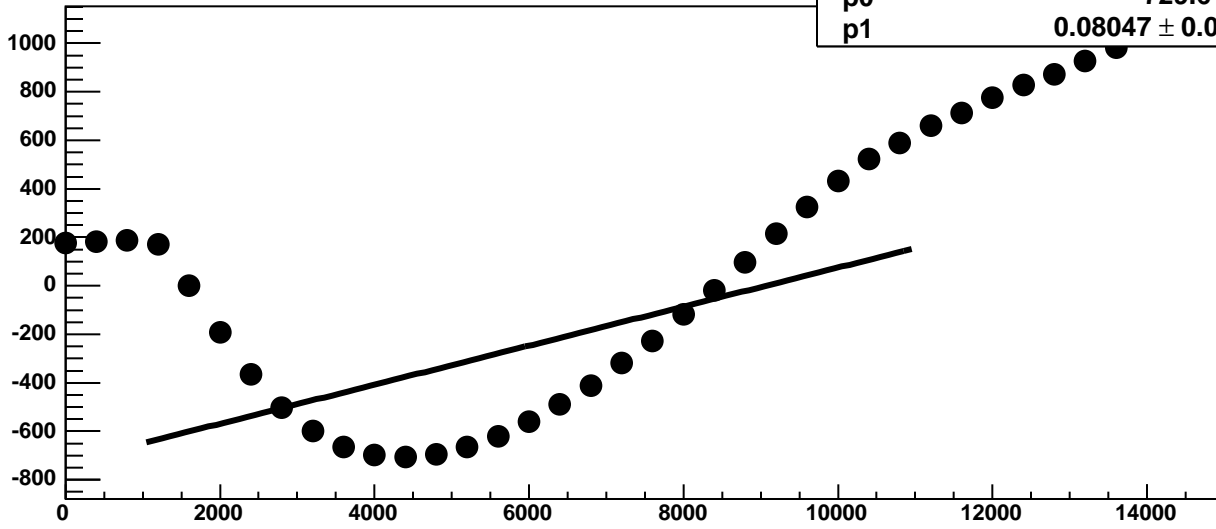
Chip 8, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



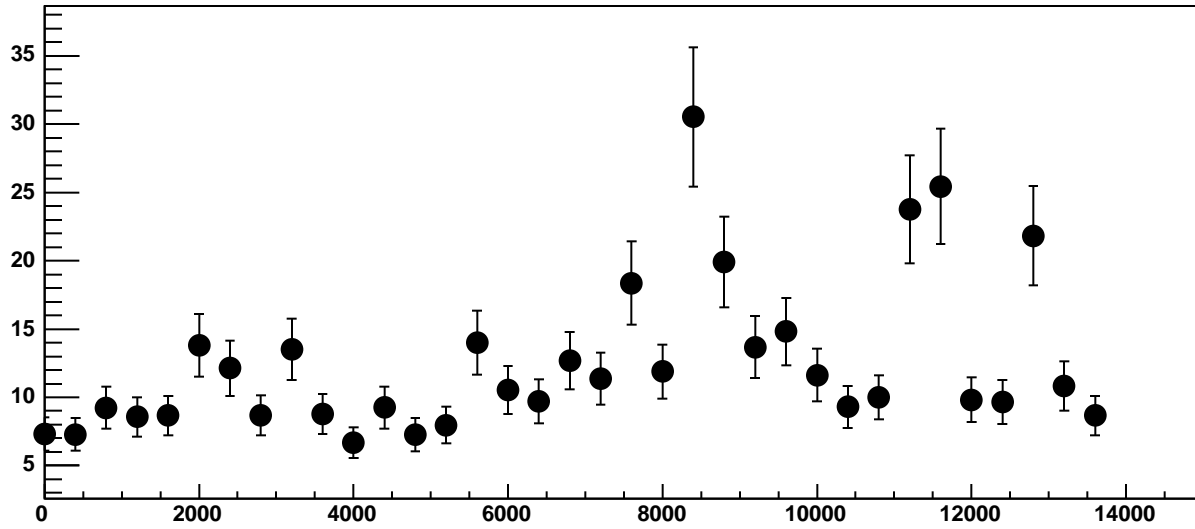
Chip 8, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



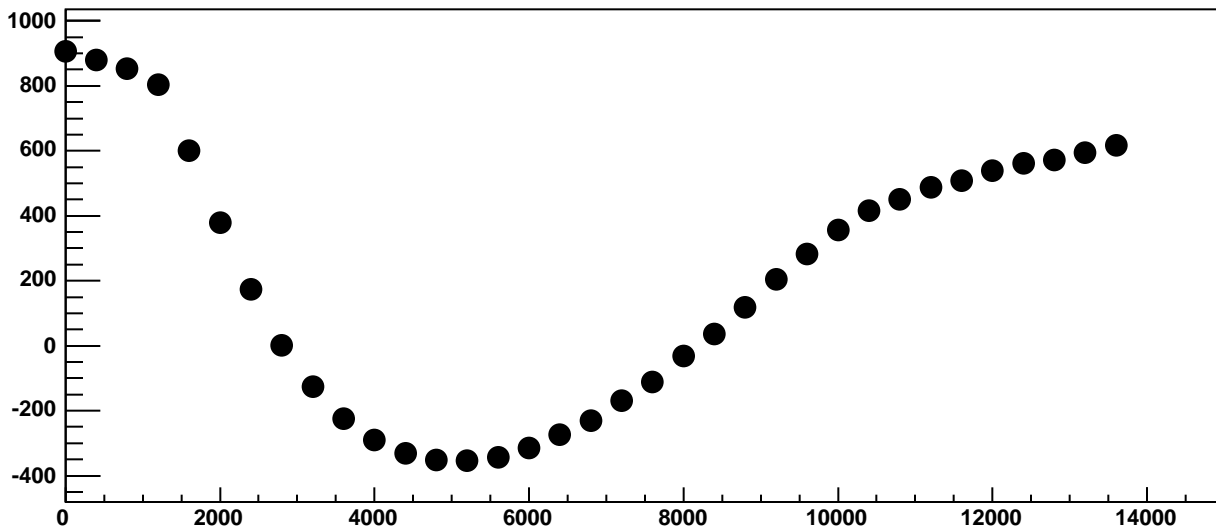
Chip 8, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



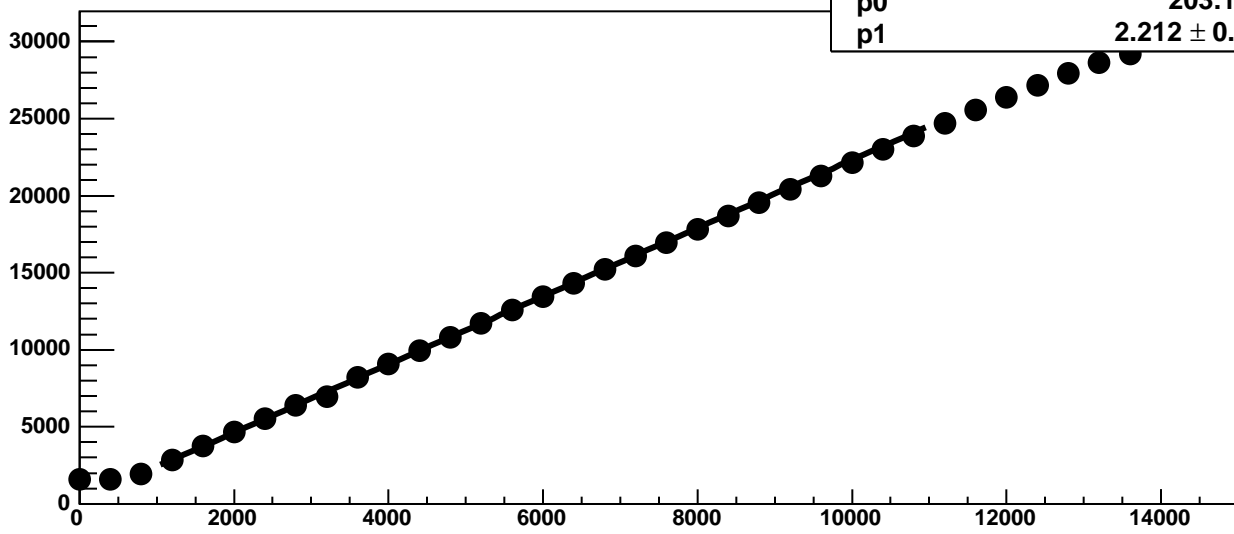
Chip 8, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



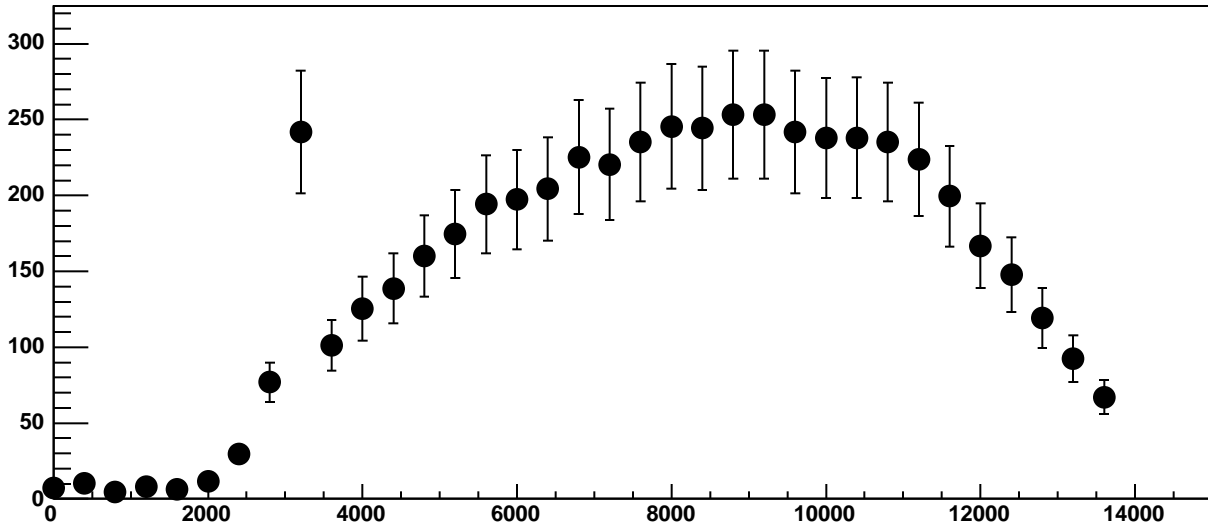
Chip 8, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



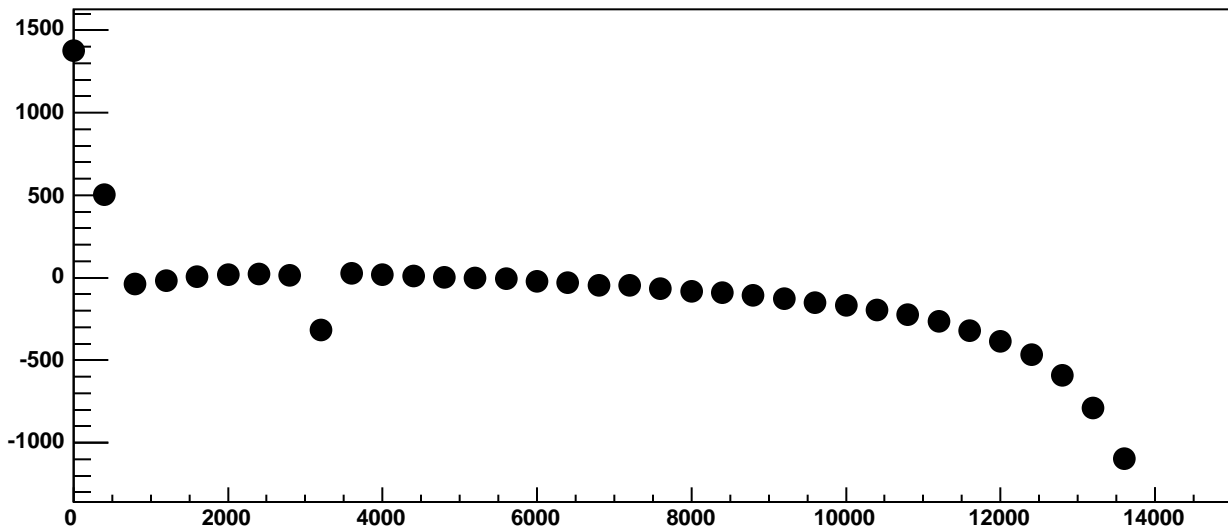
Chip 8, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC



Chip 8, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC

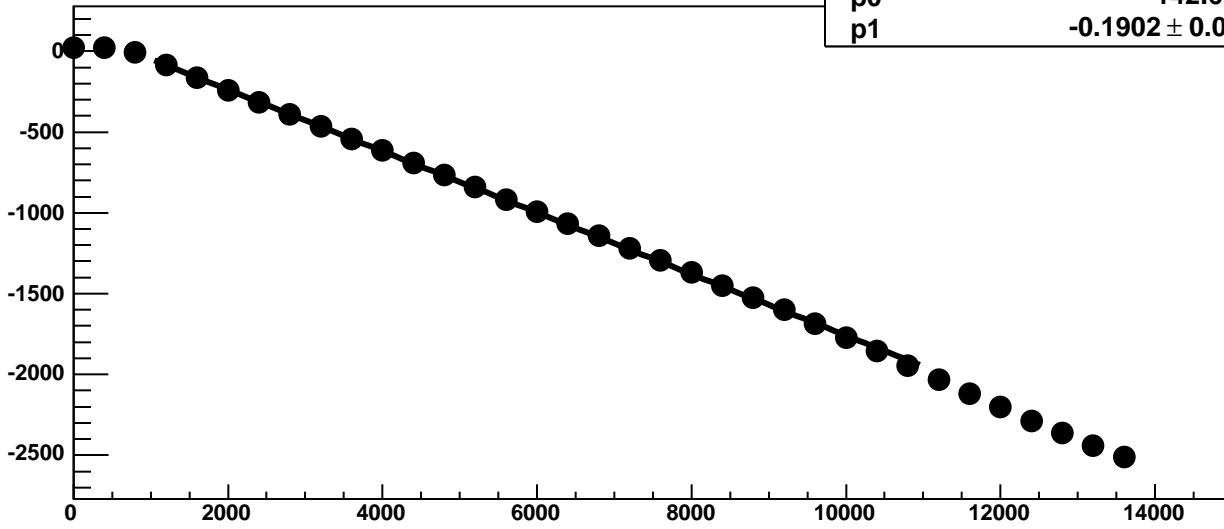


Chip 8, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC

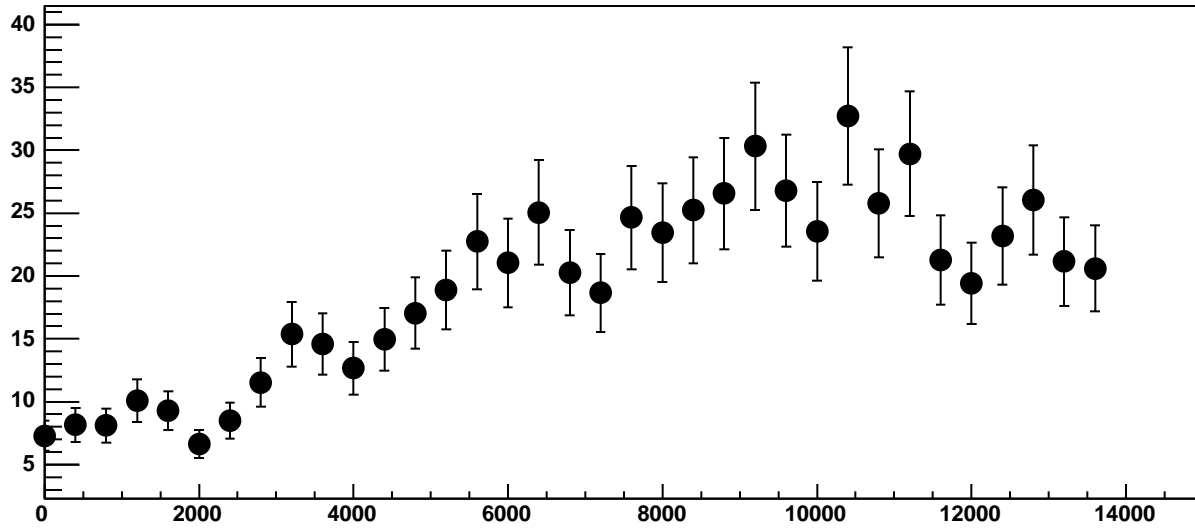


Chip 8, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

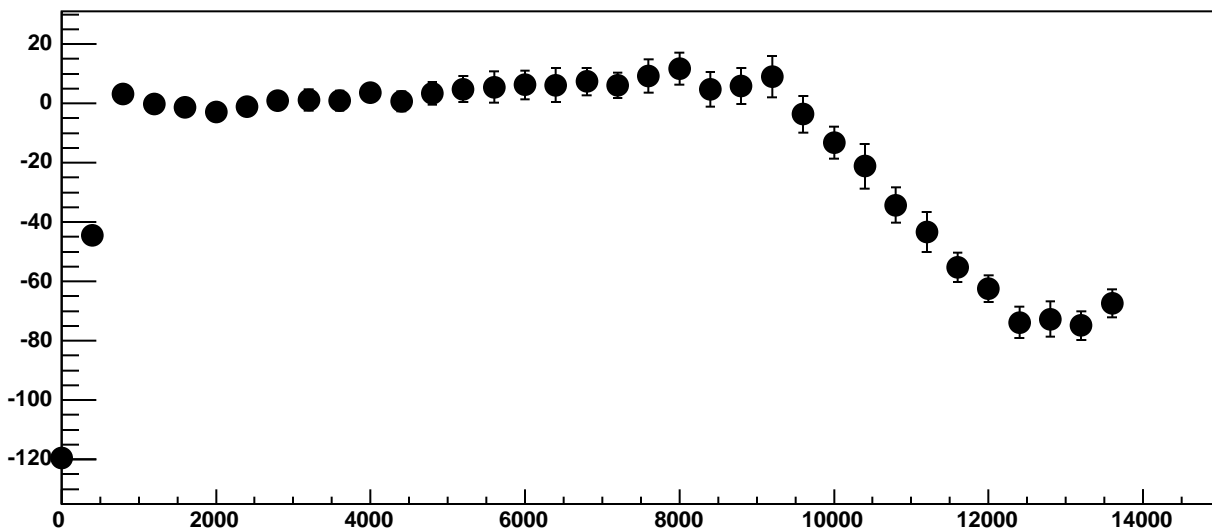
χ^2 / ndf 75.36 / 23
p0 142.6 ± 1.211
p1 -0.1902 ± 0.0002718



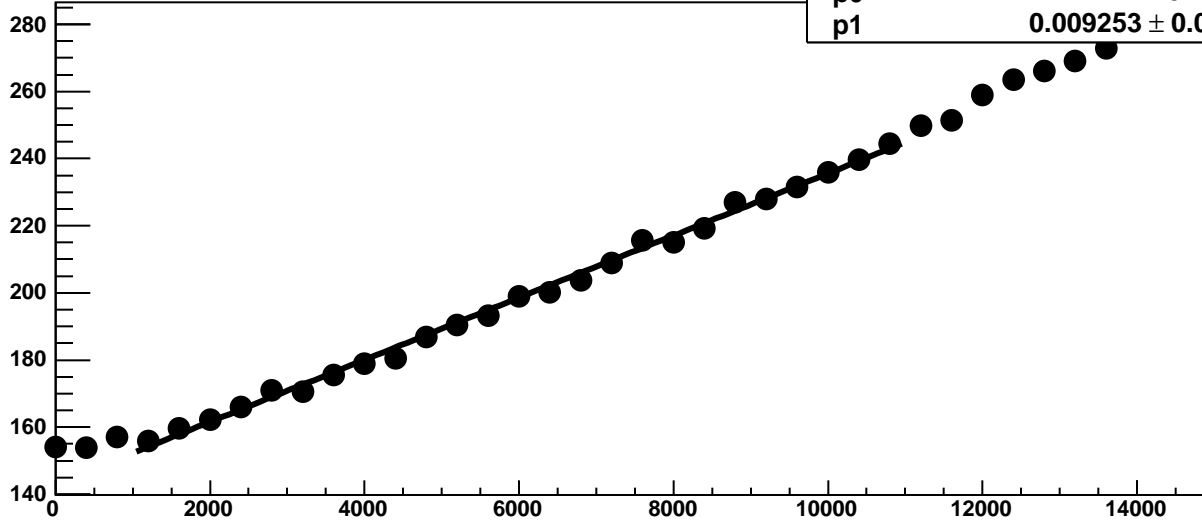
Chip 8, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

20.13 / 23

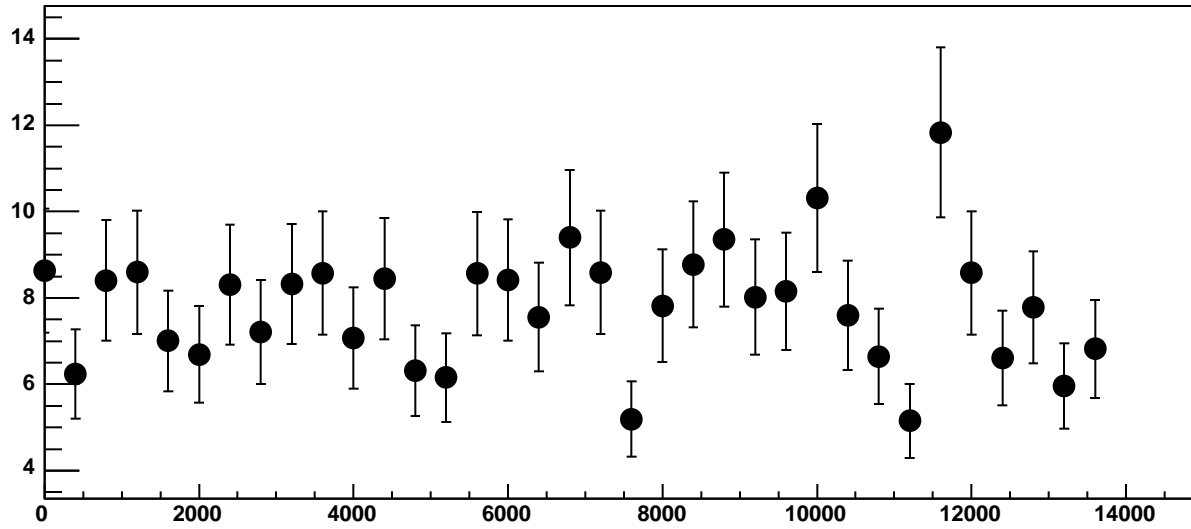
p0

143.1 ± 0.803

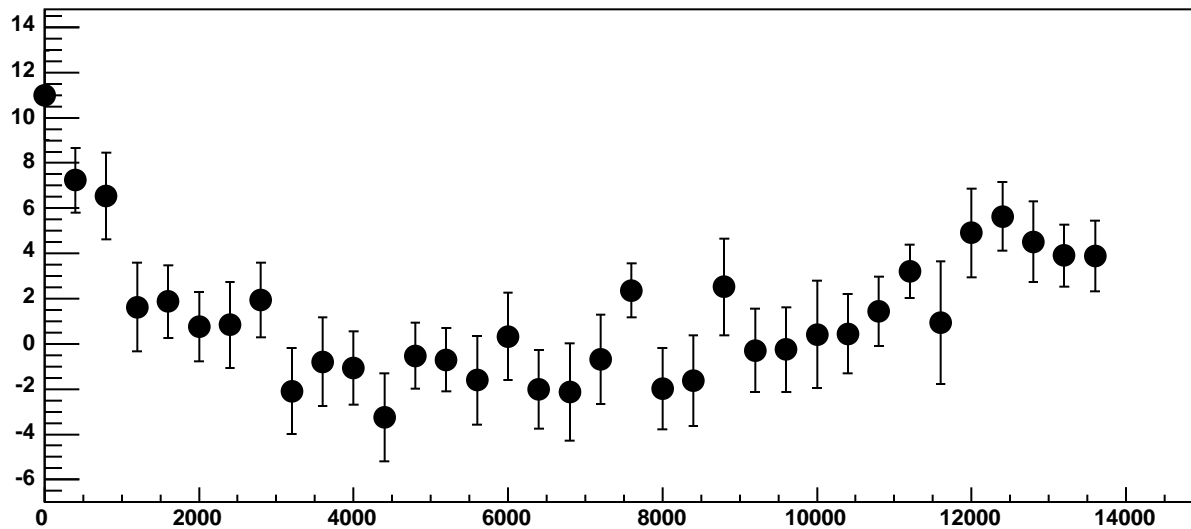
p1

0.009253 ± 0.0001222

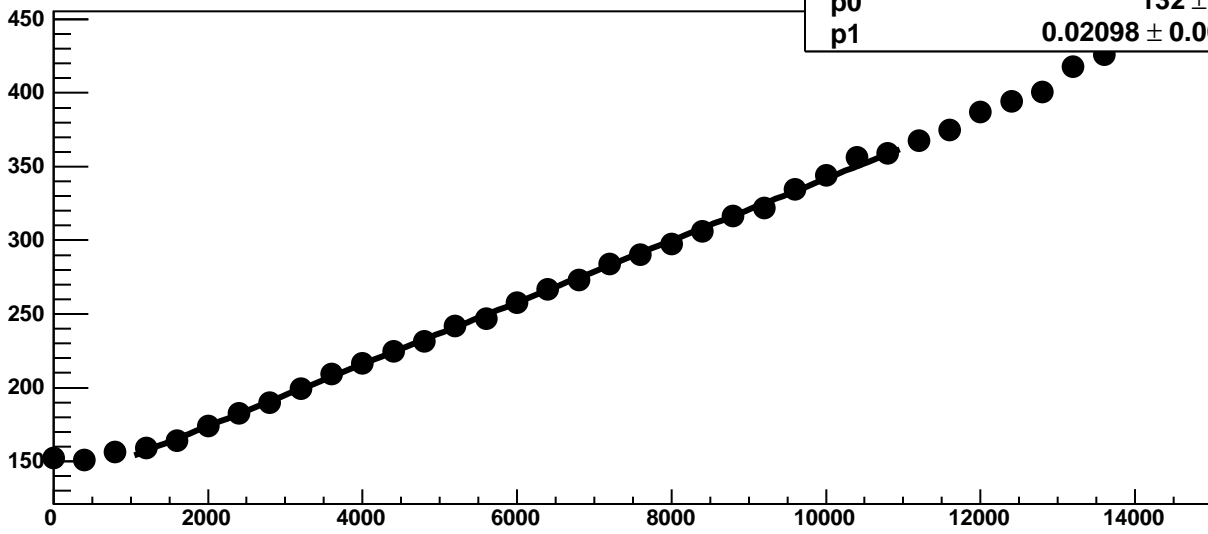
Chip 8, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

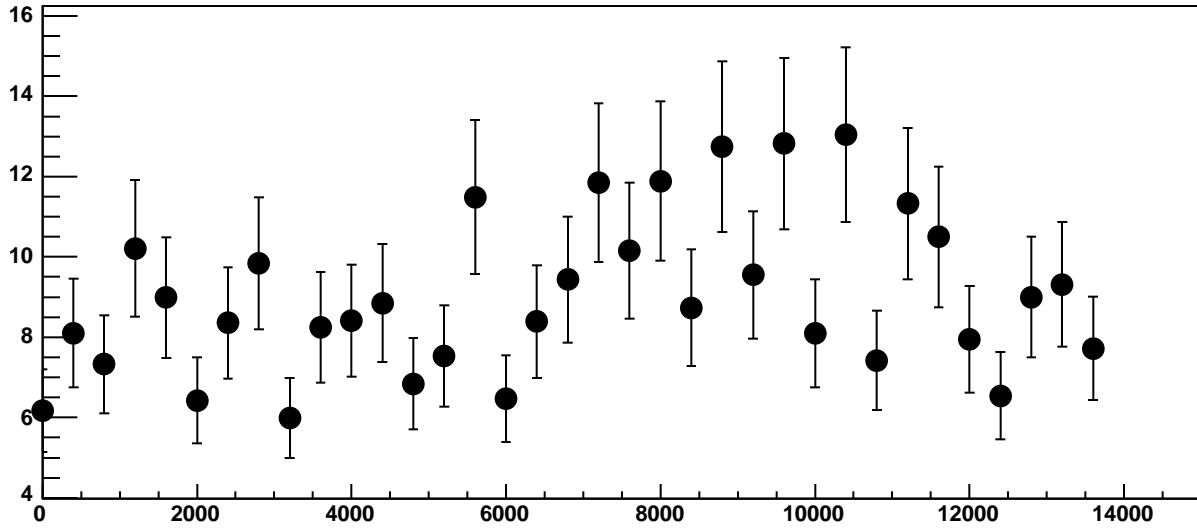


Chip 8, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC

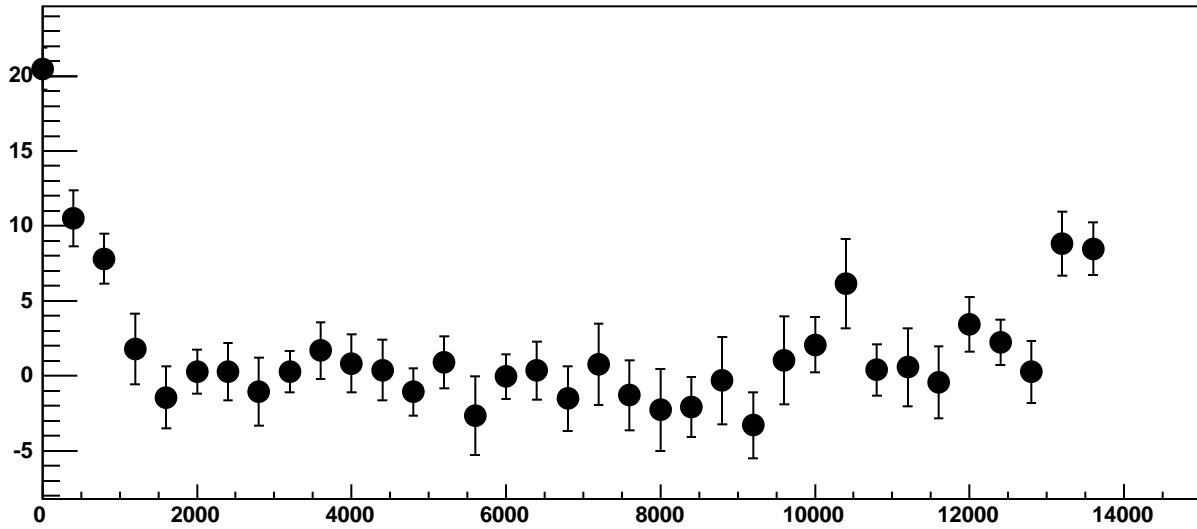


χ^2 / ndf 14.78 / 23
p0 132 ± 0.8732
p1 0.02098 ± 0.0001405

Chip 8, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

χ^2 / ndf

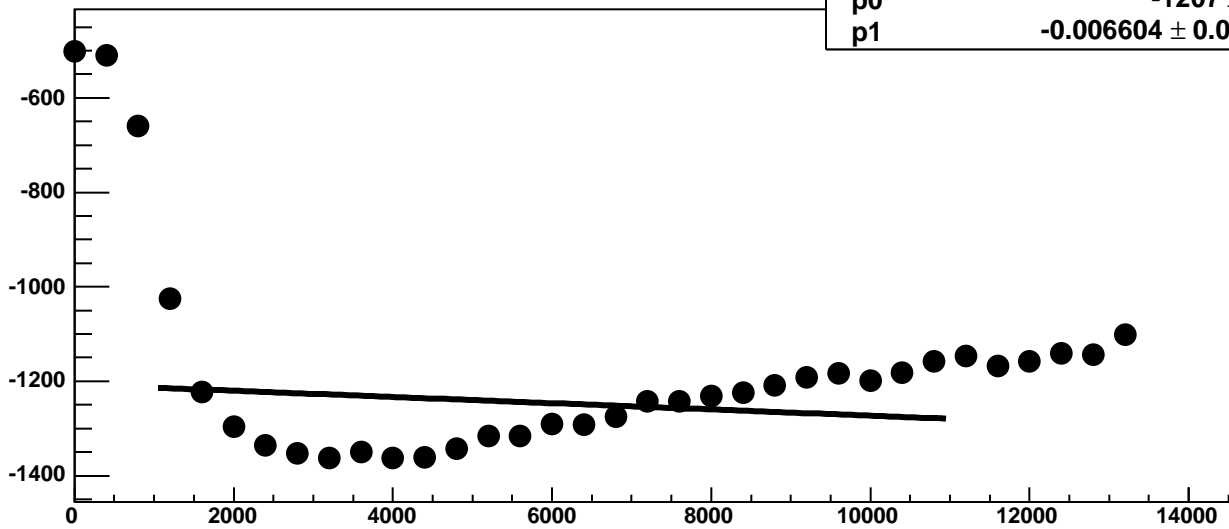
8678 / 23

p0

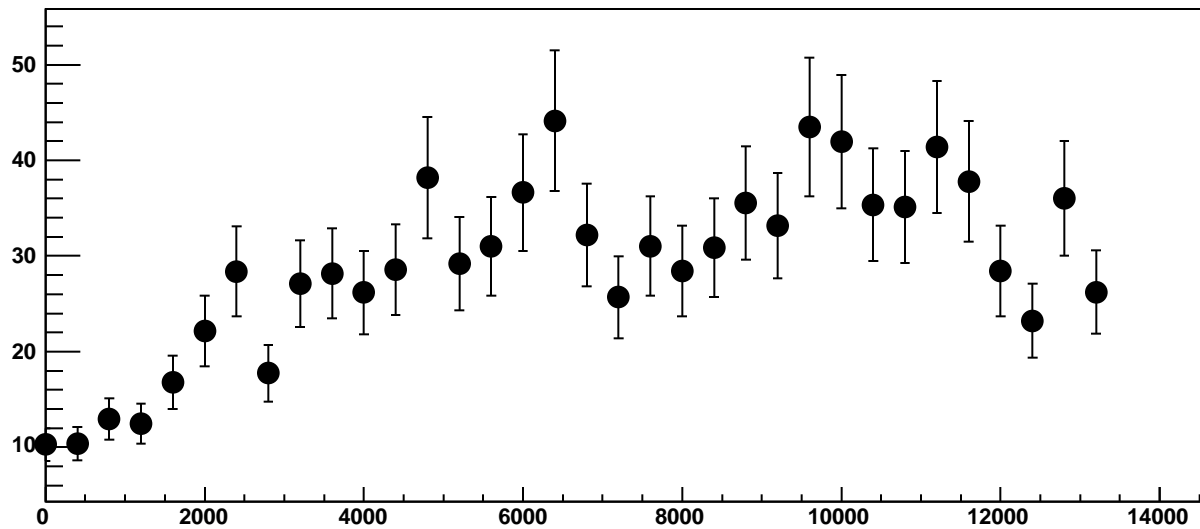
-1207 ± 2.164

p1

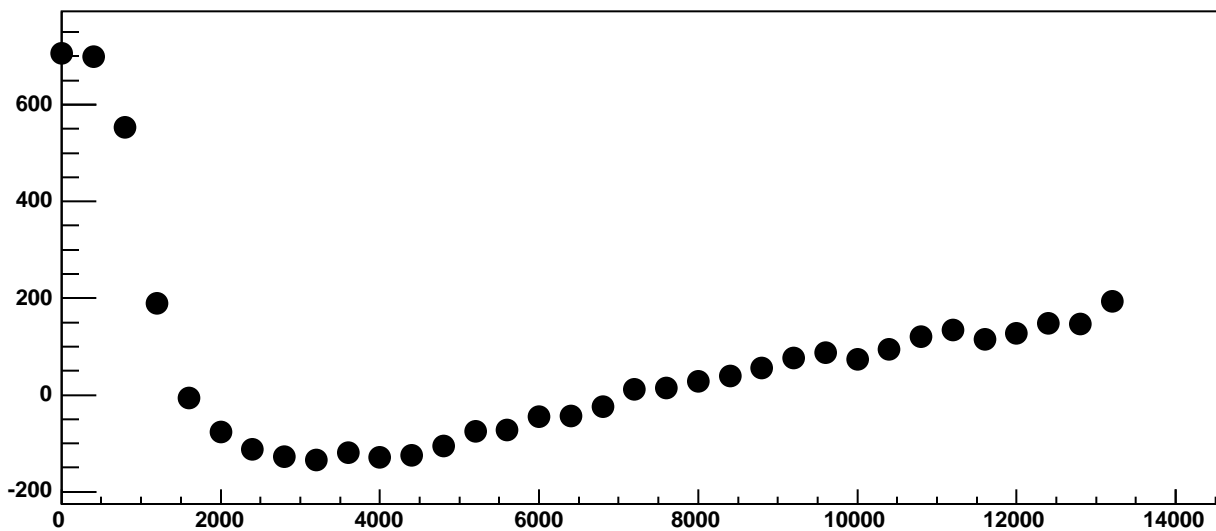
-0.006604 ± 0.0004061



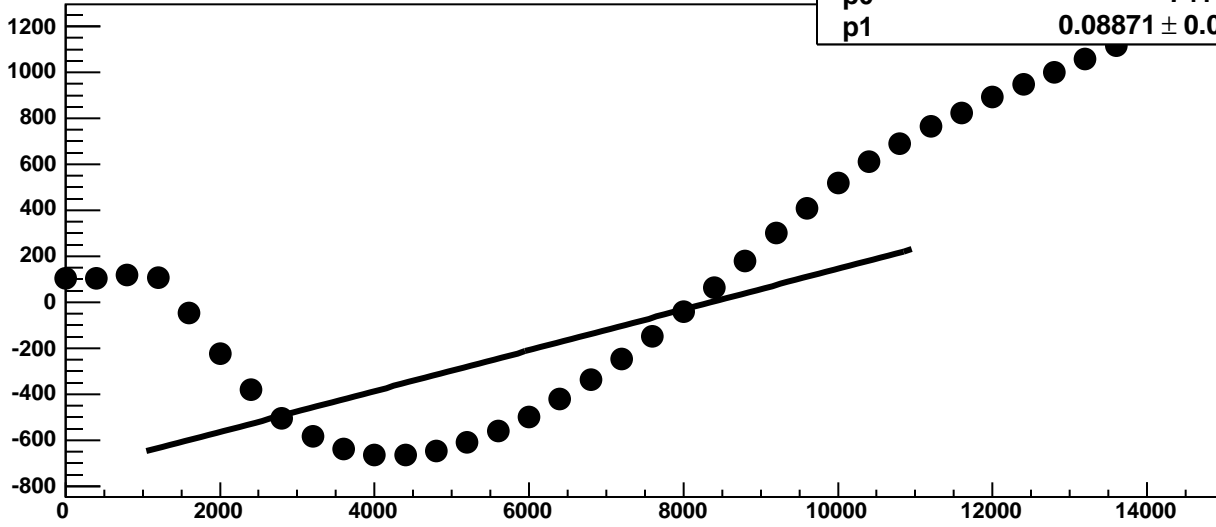
Chip 8, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

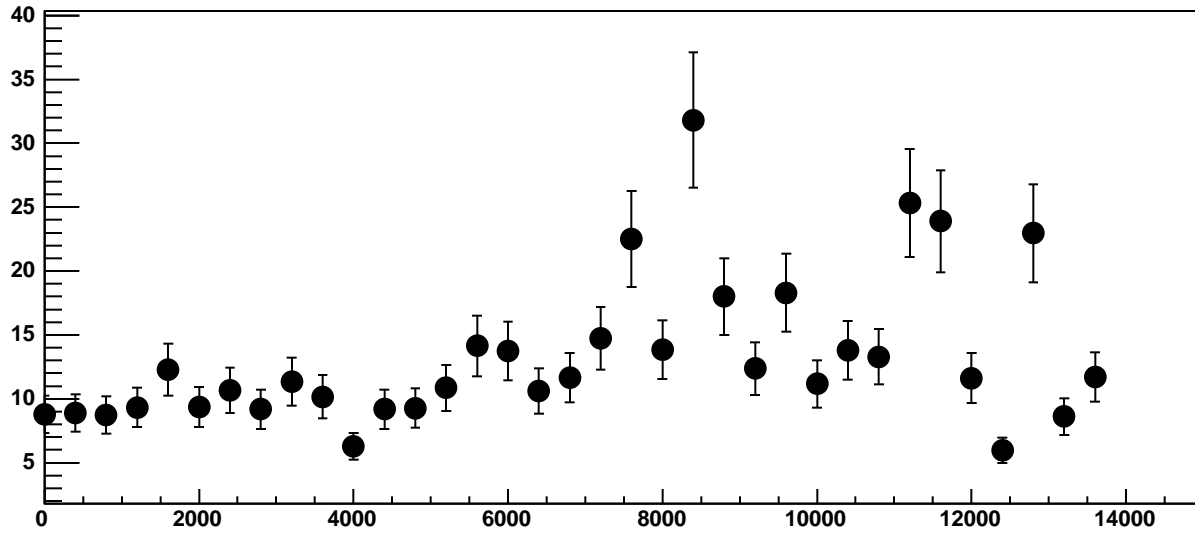


Chip 8, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

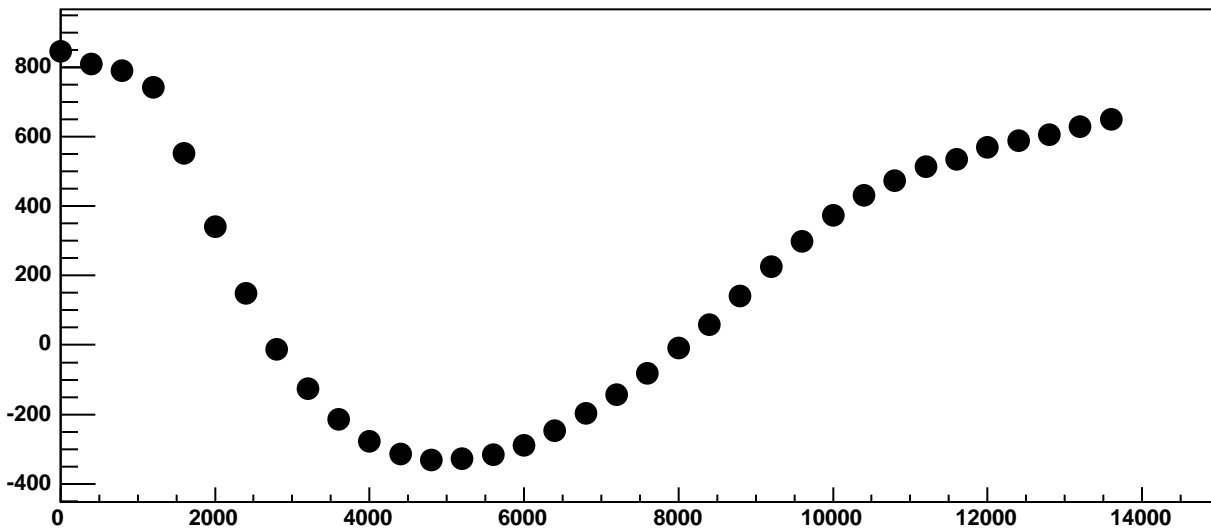


χ^2 / ndf 4.137e+05 / 23
p0 -741 ± 1.094
p1 0.08871 ± 0.0001901

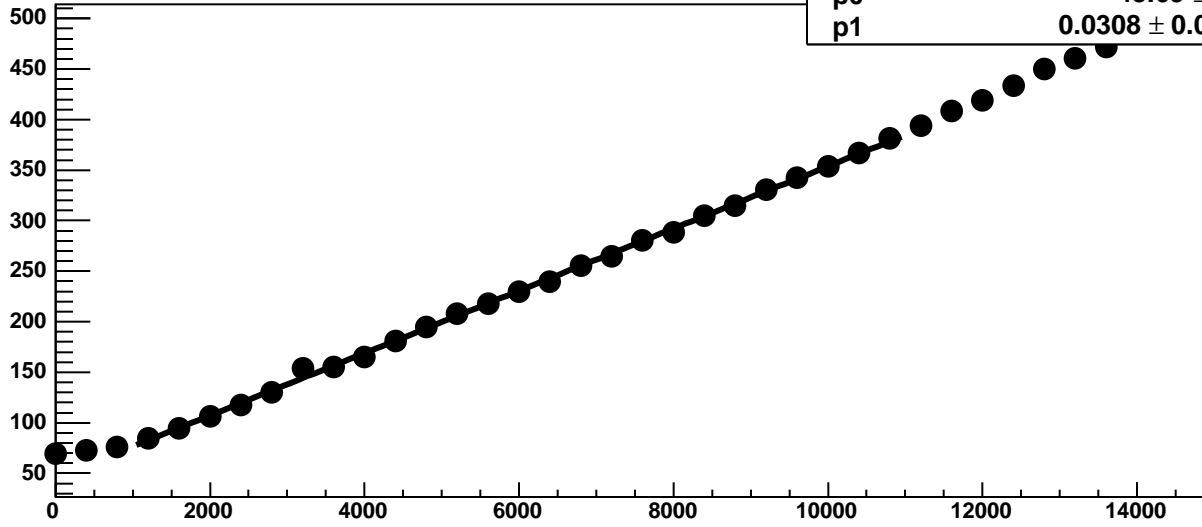
Chip 8, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

38.54 / 23

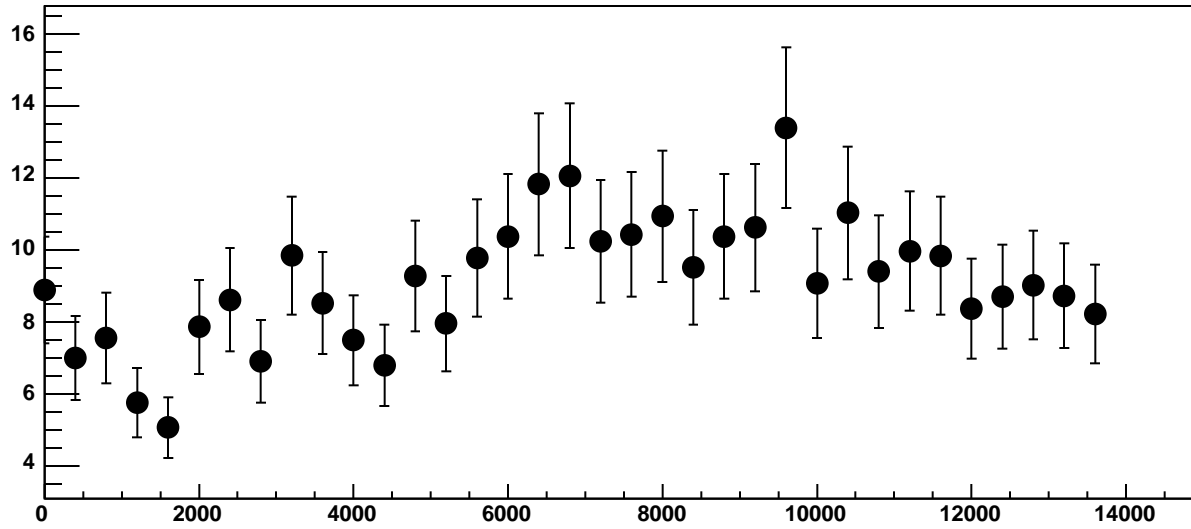
p0

45.69 ± 0.7607

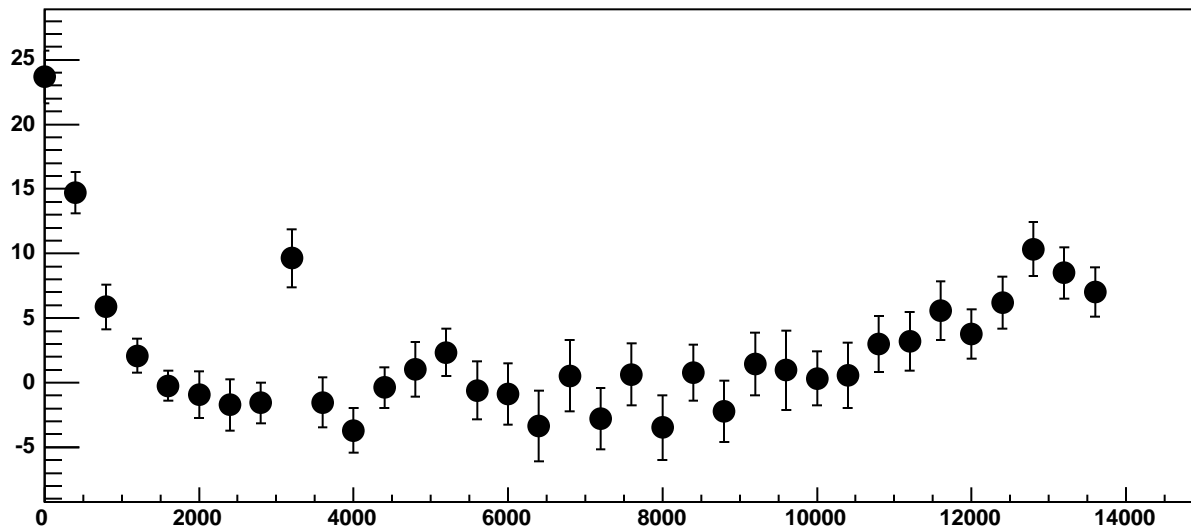
p1

0.0308 ± 0.0001324

Chip 8, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC

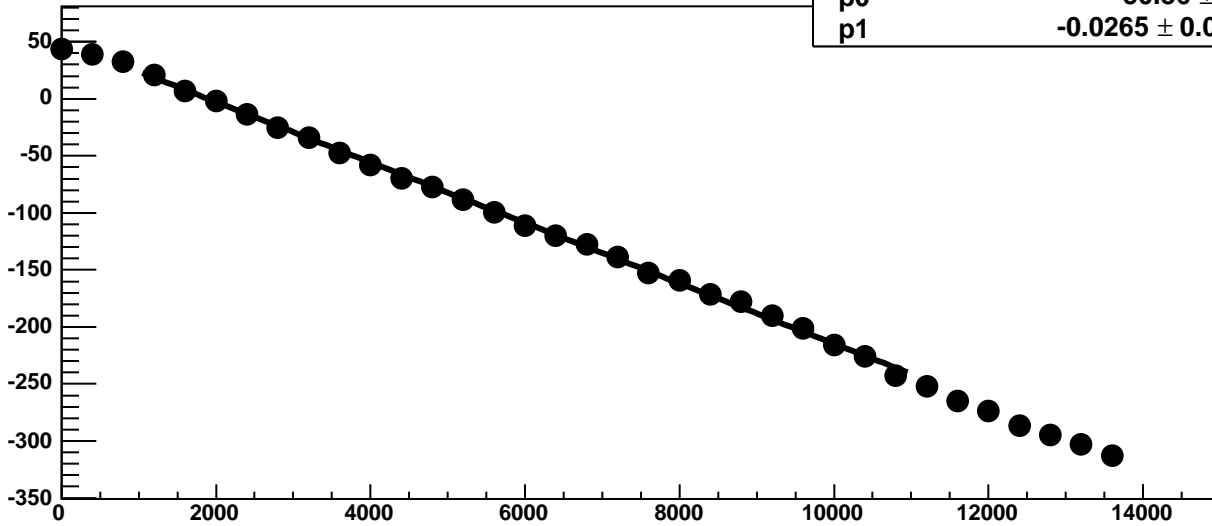


Chip 8, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC

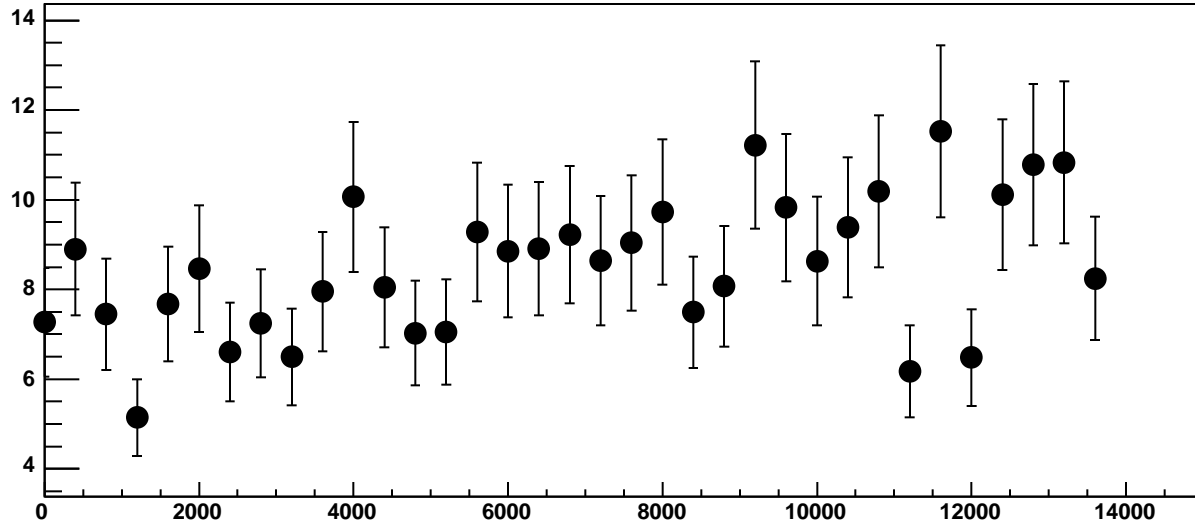


Chip 8, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

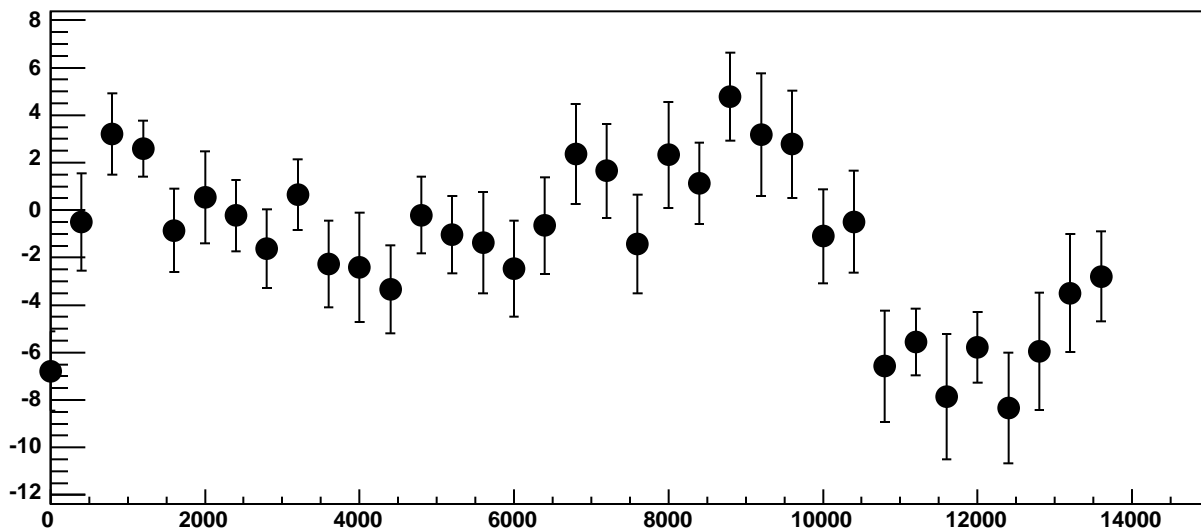
χ^2 / ndf 36.63 / 23
p0 50.36 ± 0.7608
p1 -0.0265 ± 0.0001261



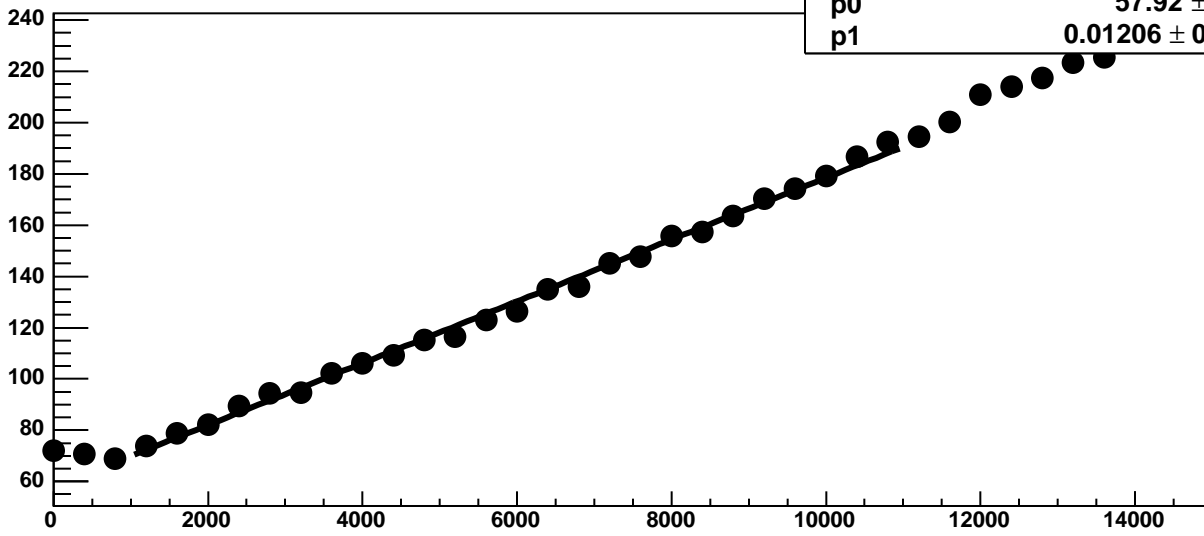
Chip 8, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

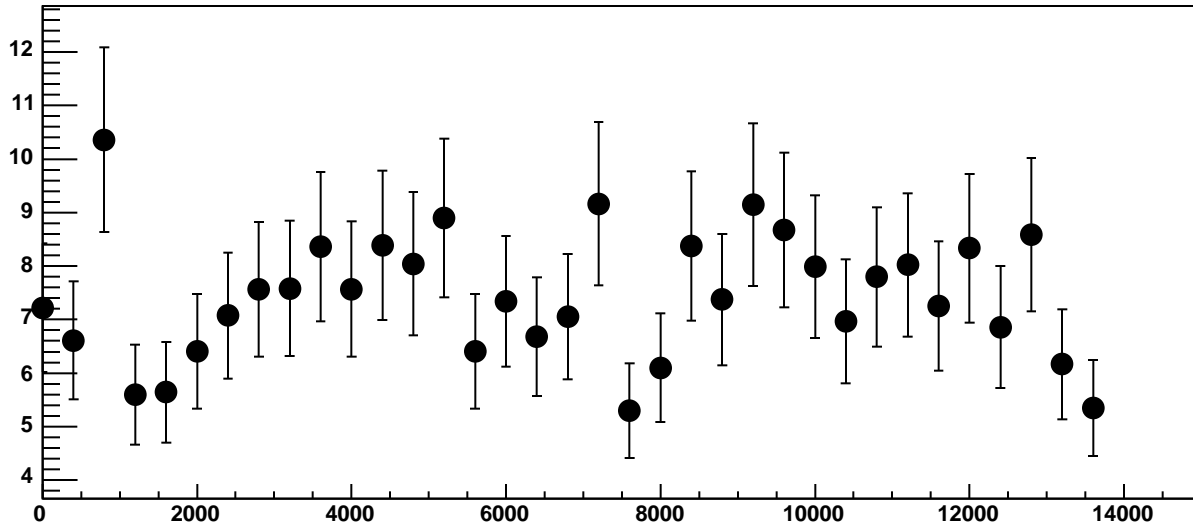


Chip 8, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

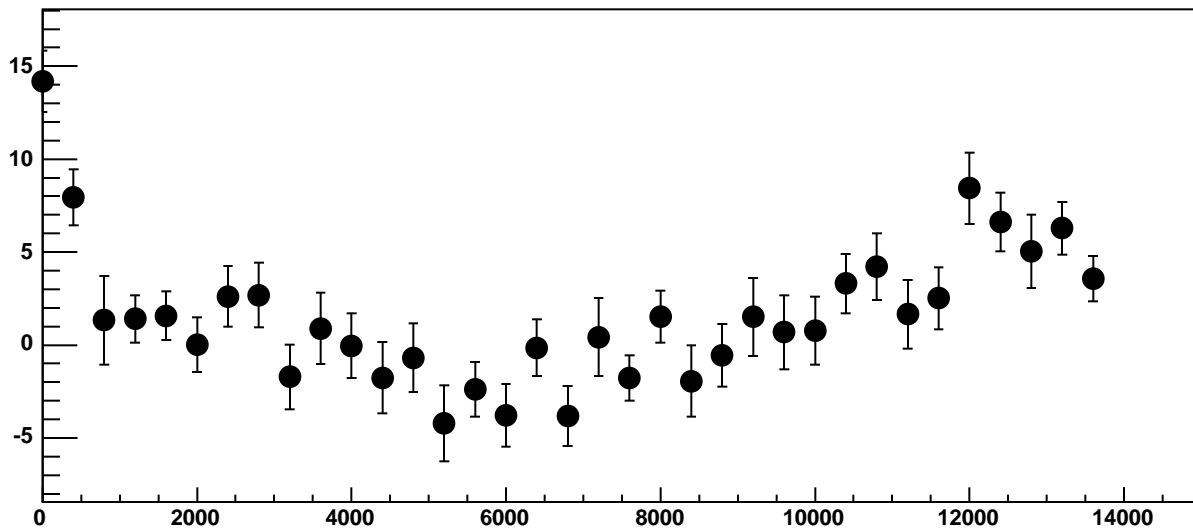


χ^2 / ndf 42.46 / 23
p0 57.92 ± 0.7139
p1 0.01206 ± 0.000111

Chip 8, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

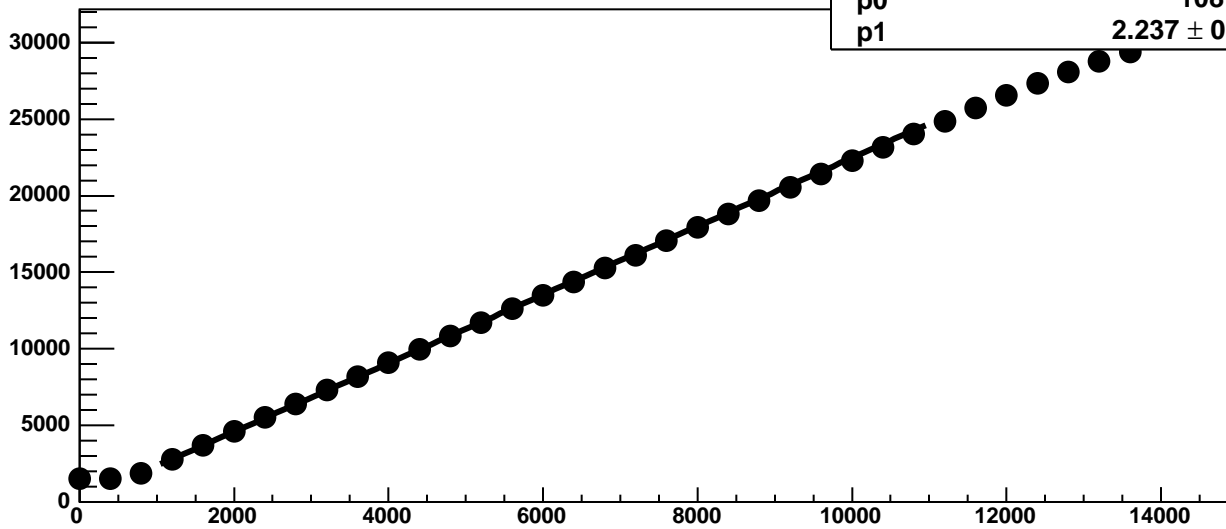


Chip 8, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

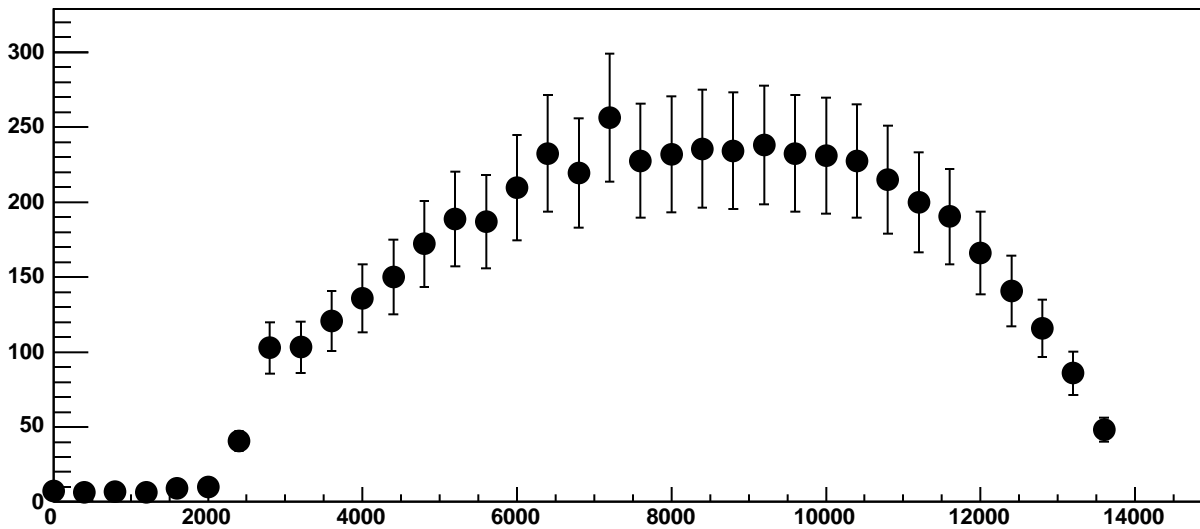


Chip 8, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

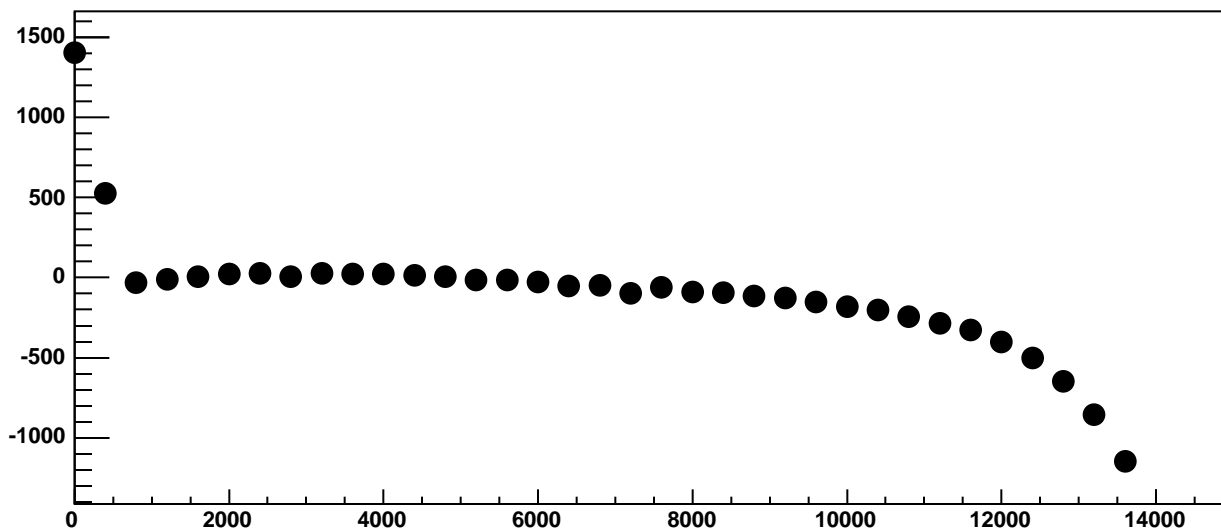
χ^2 / ndf 242.9 / 23
p0 108 ± 2.713
p1 2.237 ± 0.001615



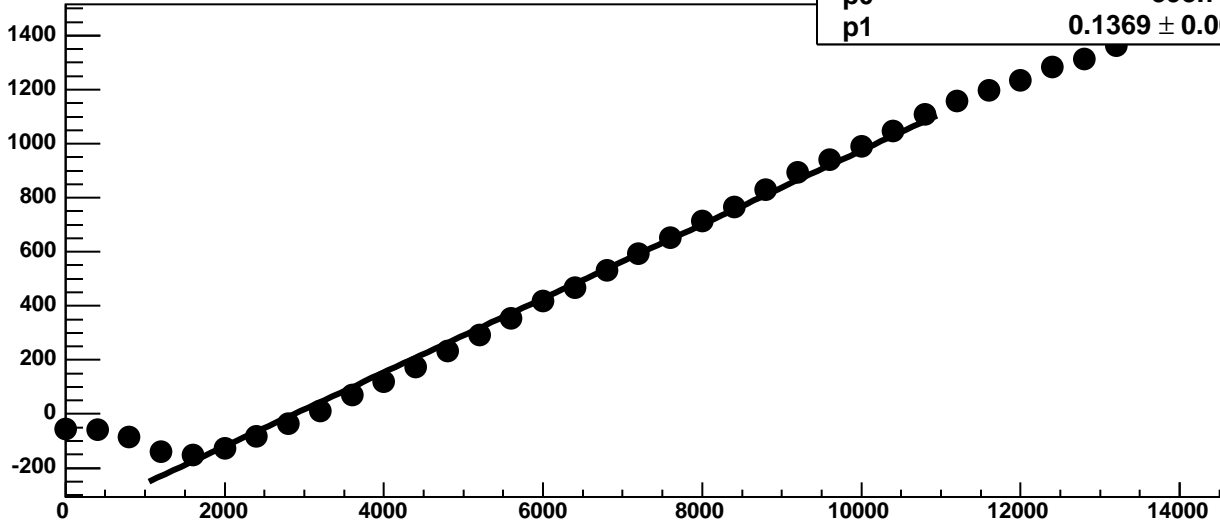
Chip 8, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC



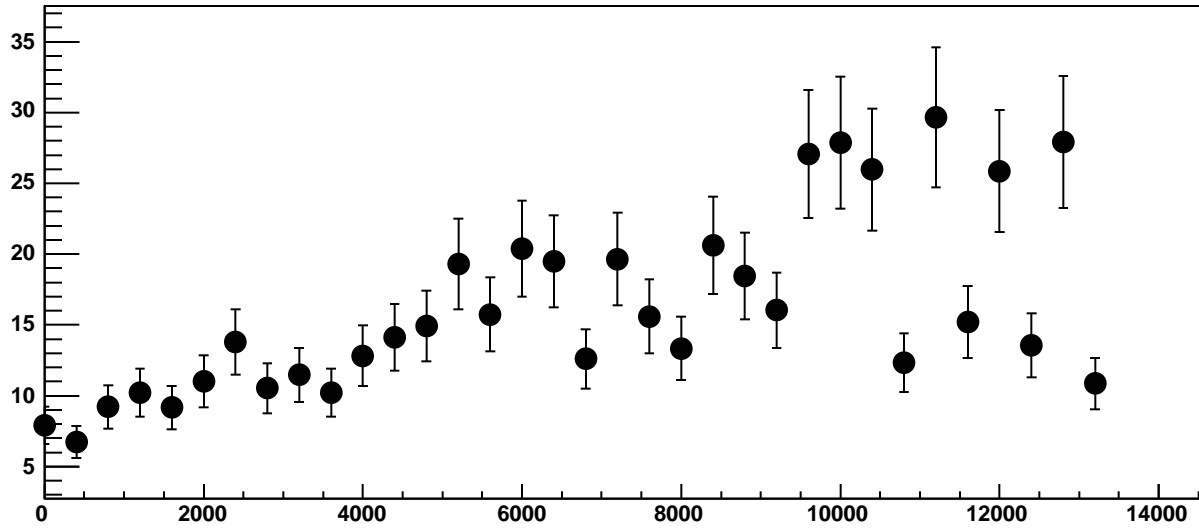
Chip 8, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC



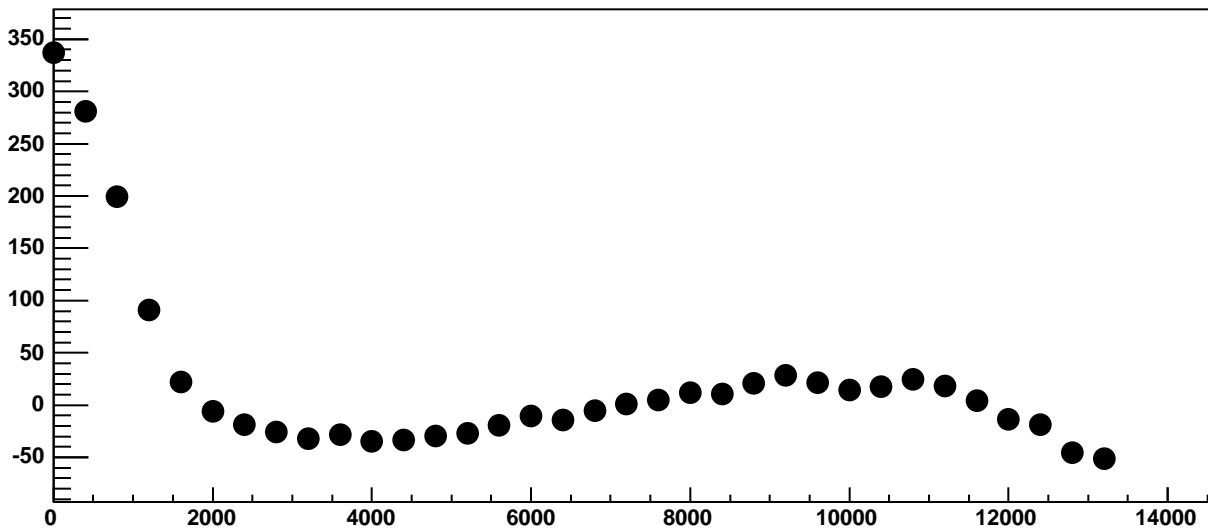
Chip 8, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



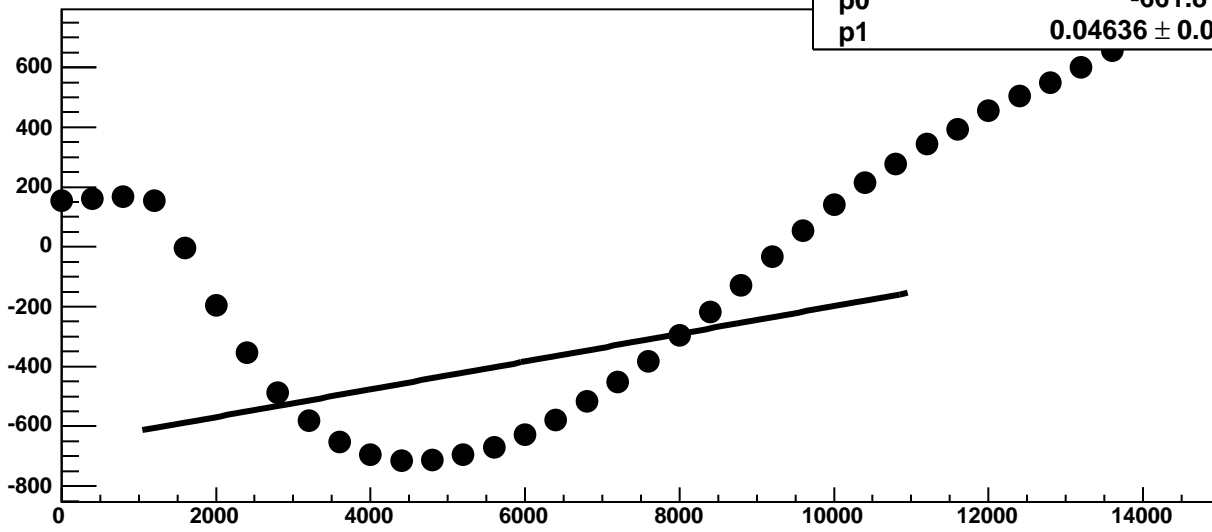
Chip 8, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



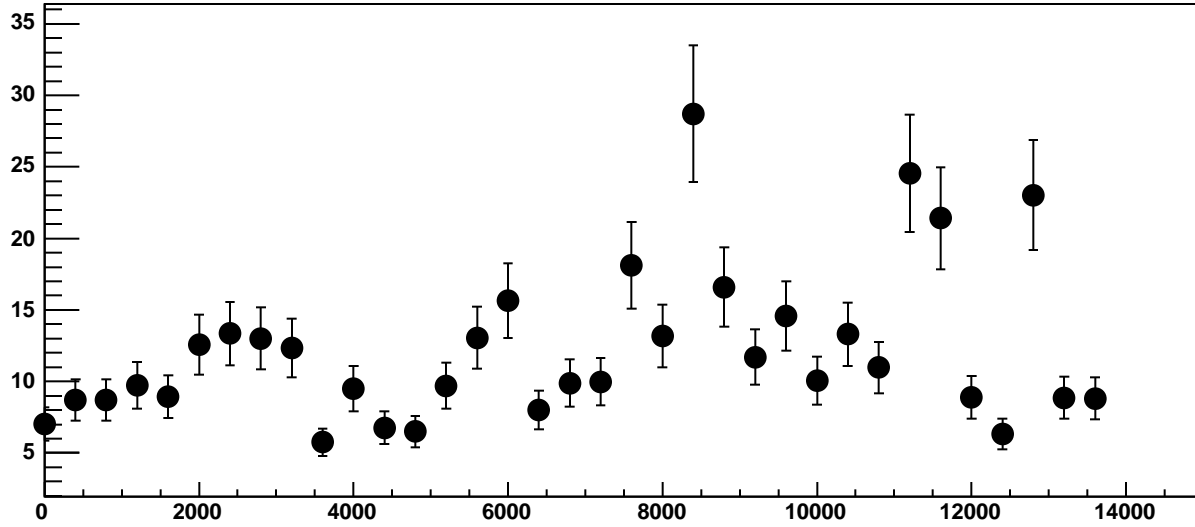
Chip 8, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



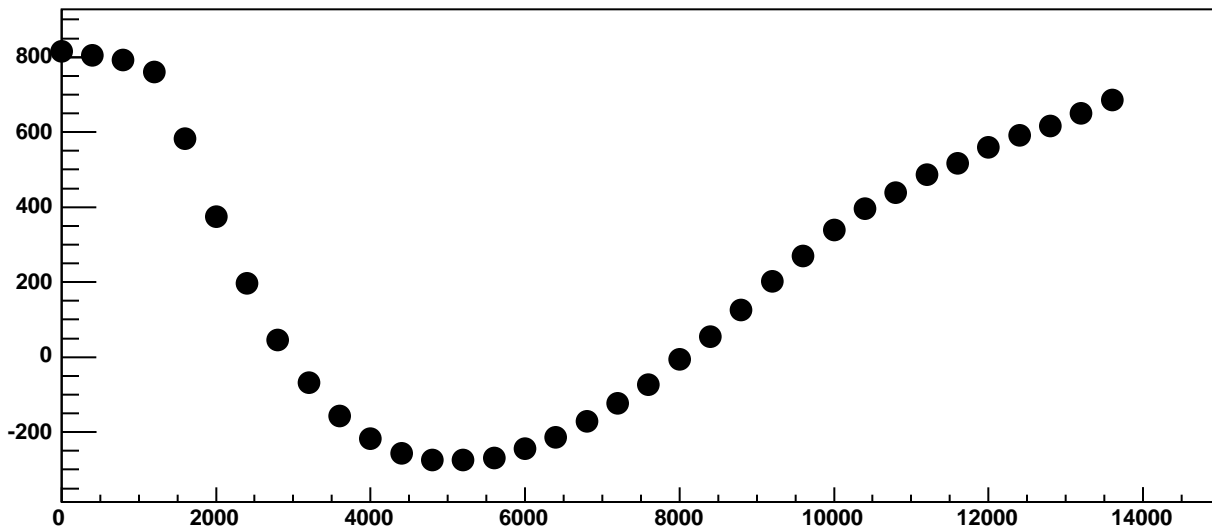
Chip 8, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



Chip 8, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

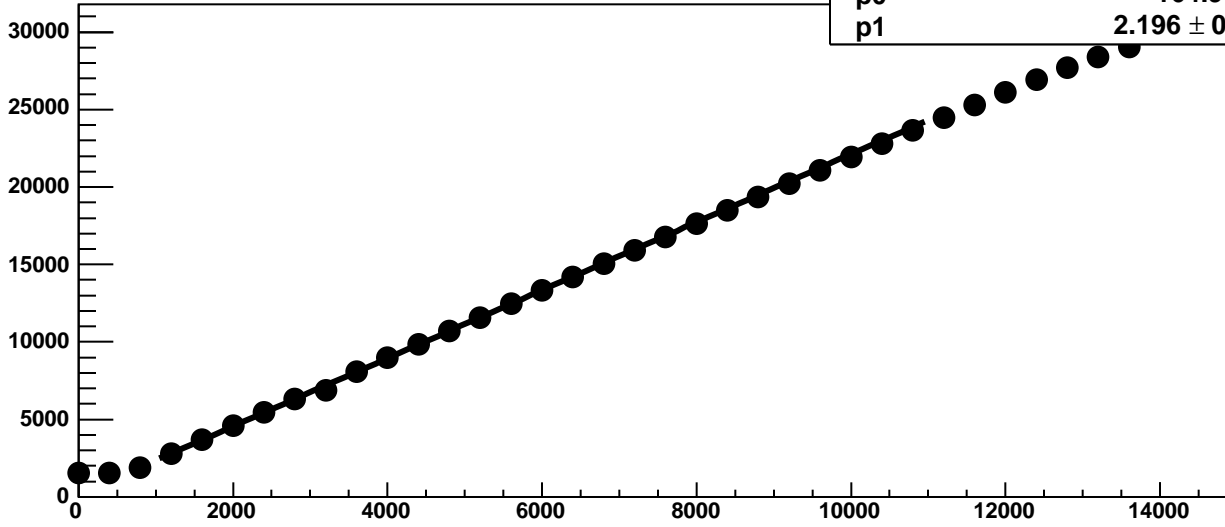


Chip 8, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

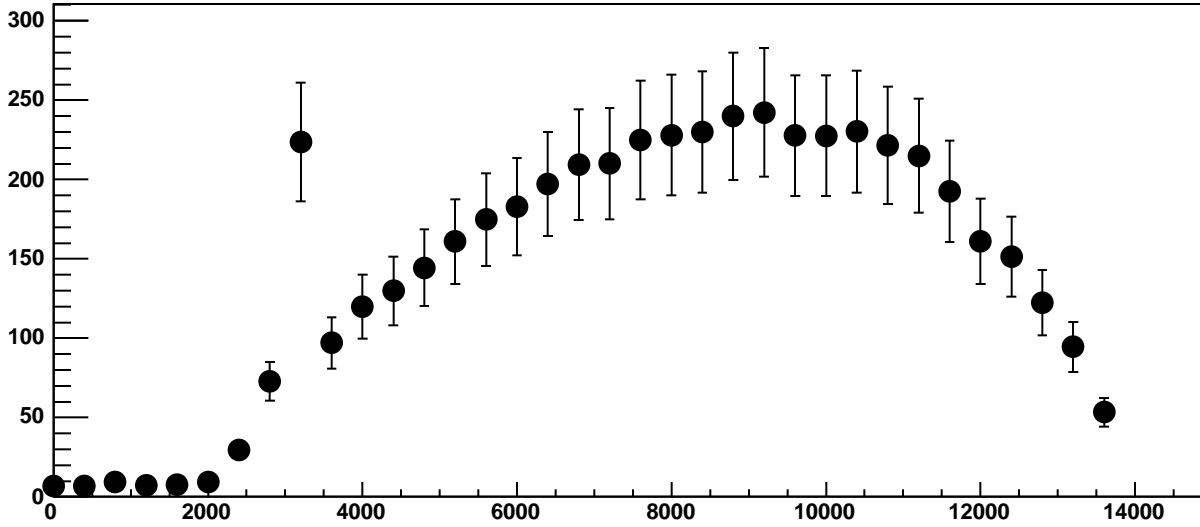


Chip 8, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC

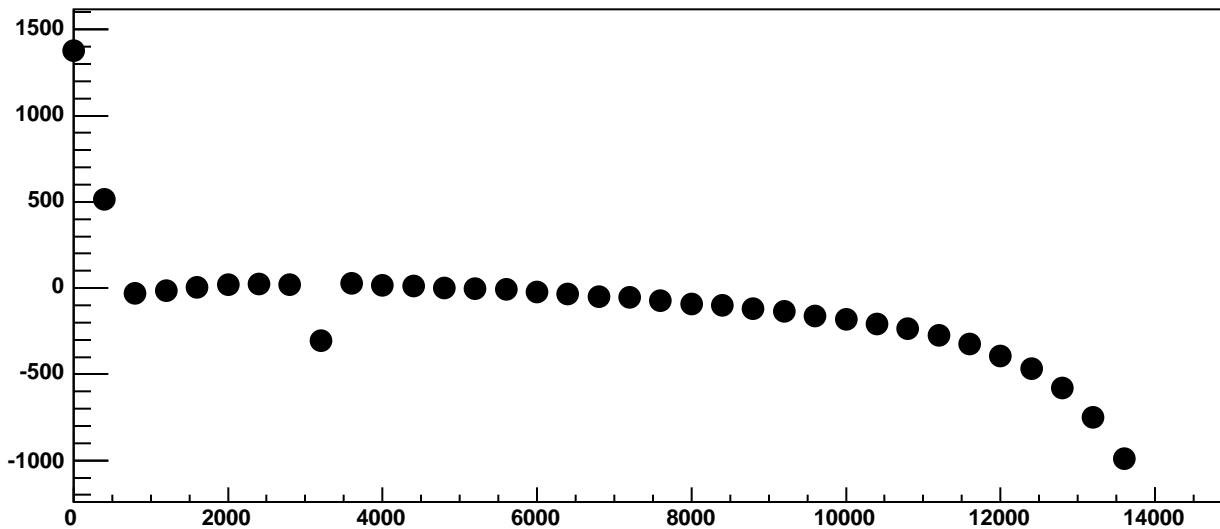
χ^2 / ndf 289.2 / 23
p0 164.9 ± 2.772
p1 2.196 ± 0.001581



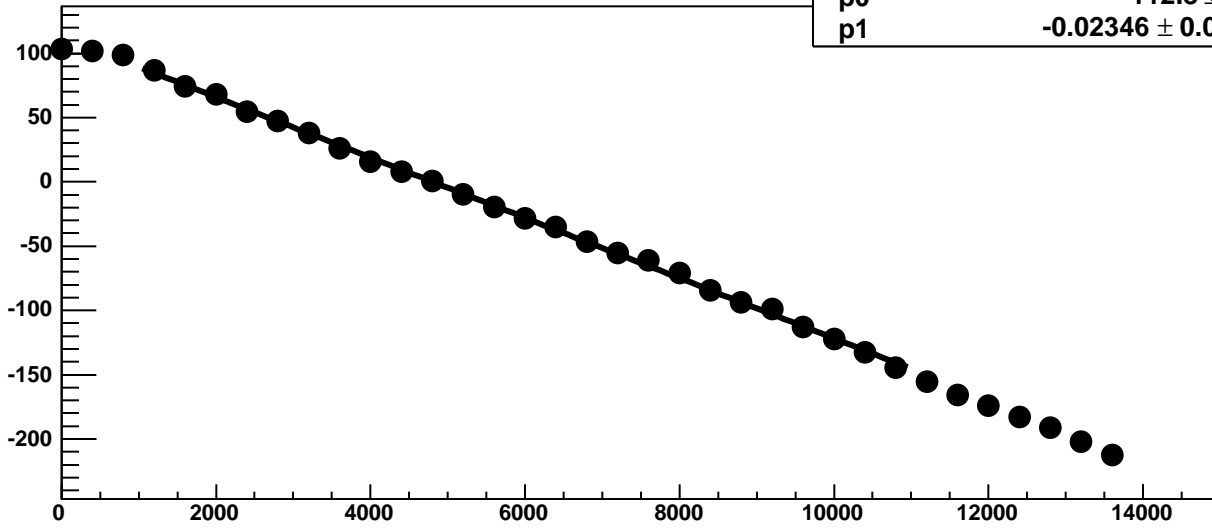
Chip 8, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC

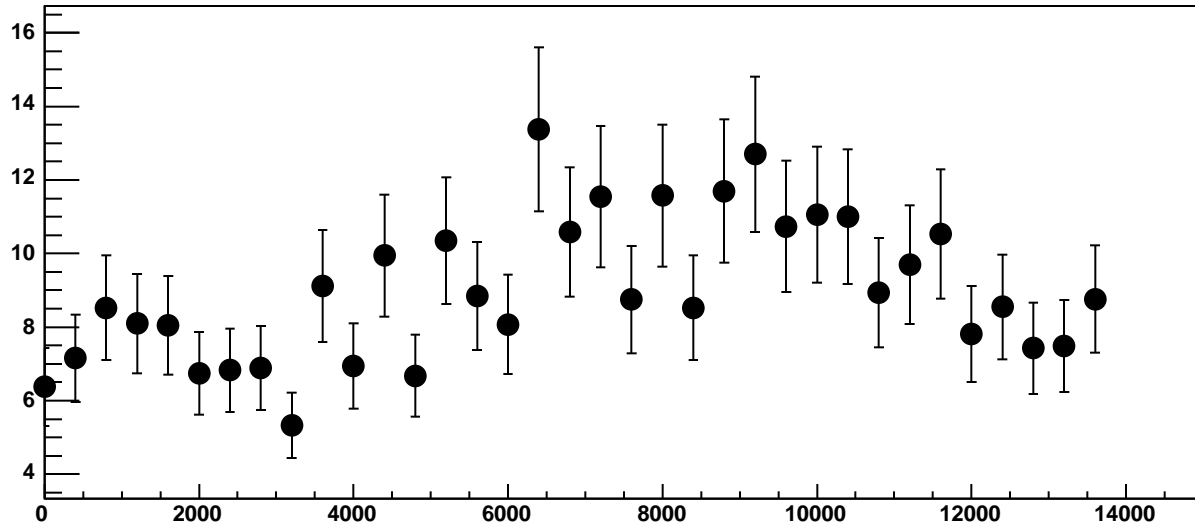


Chip 8, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

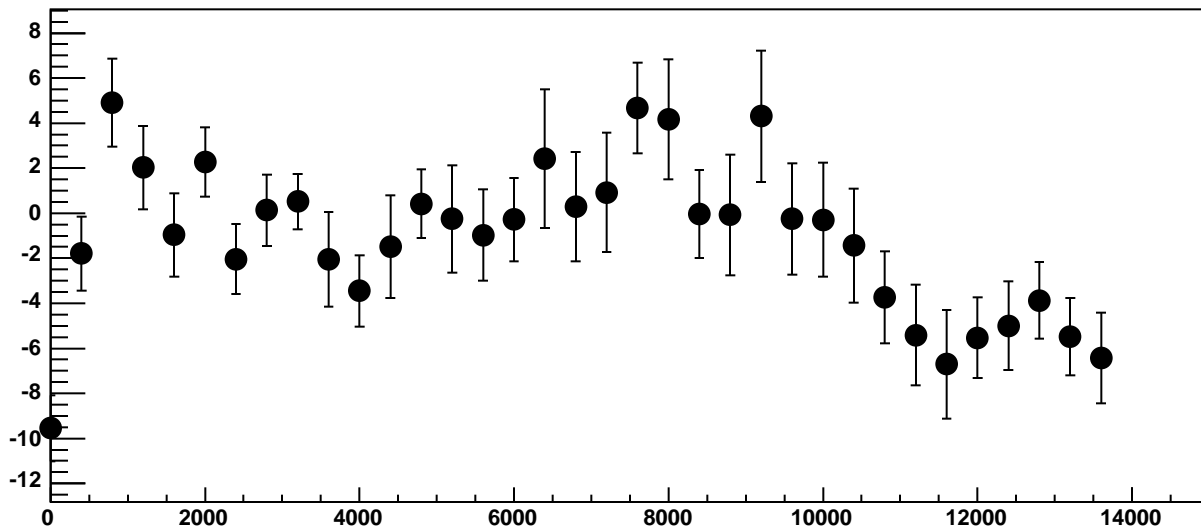


χ^2 / ndf 26.41 / 23
p0 112.8 ± 0.8141
p1 -0.02346 ± 0.0001401

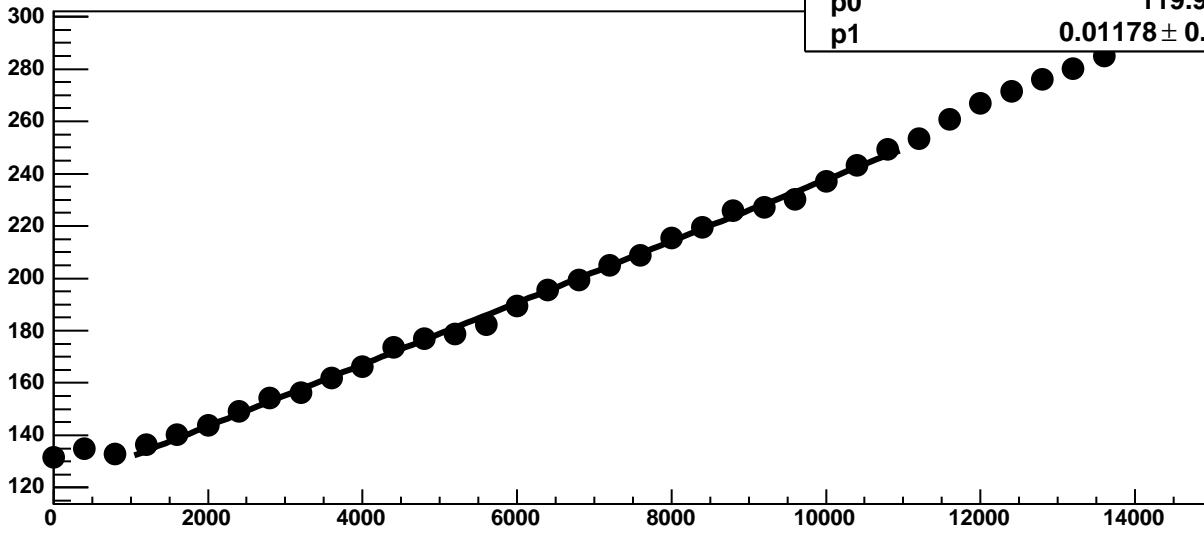
Chip 8, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

17.17 / 23

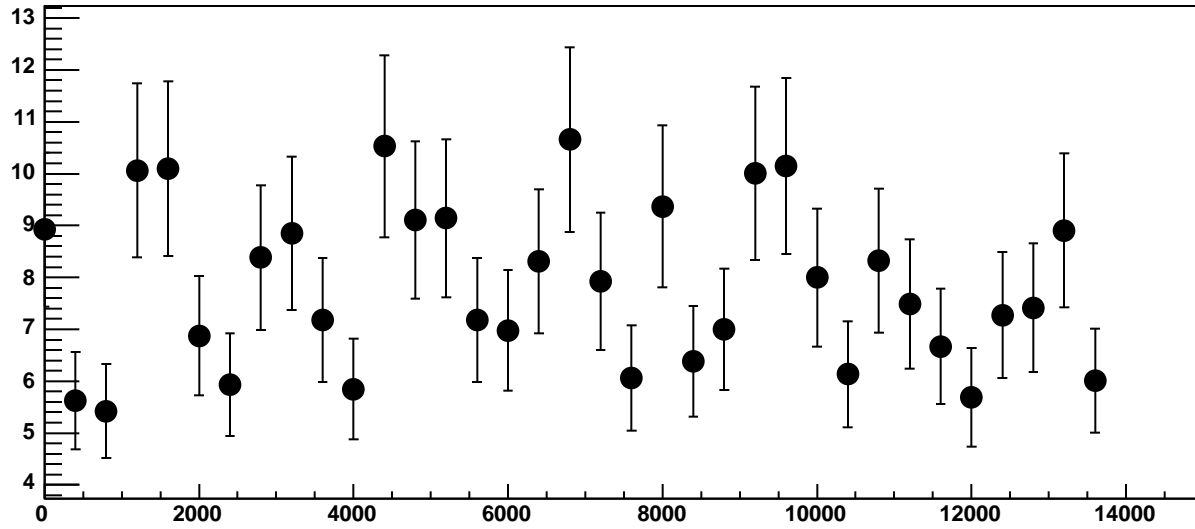
p0

119.9 ± 0.83

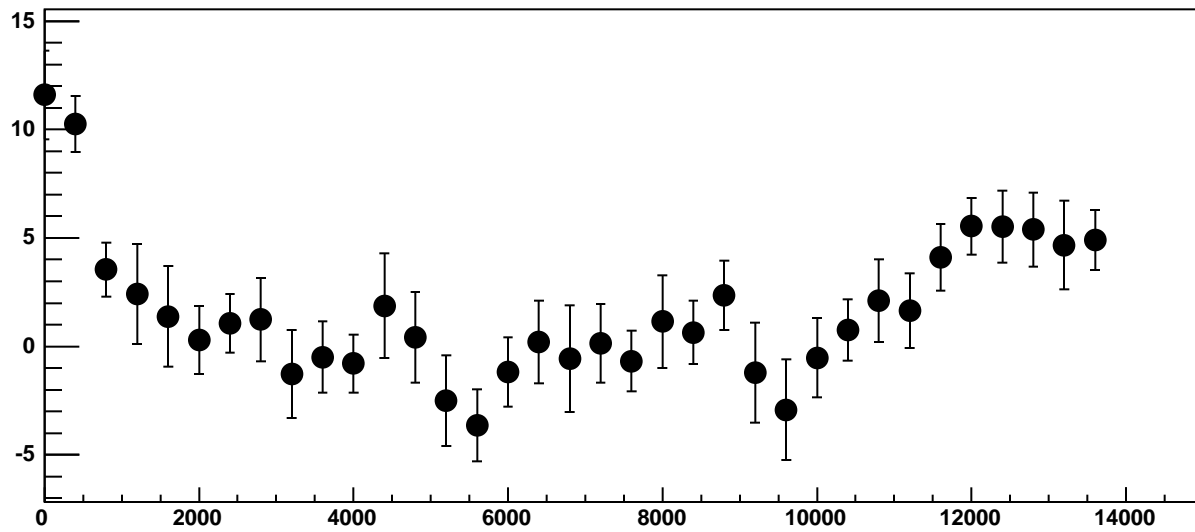
p1

0.01178 ± 0.000124

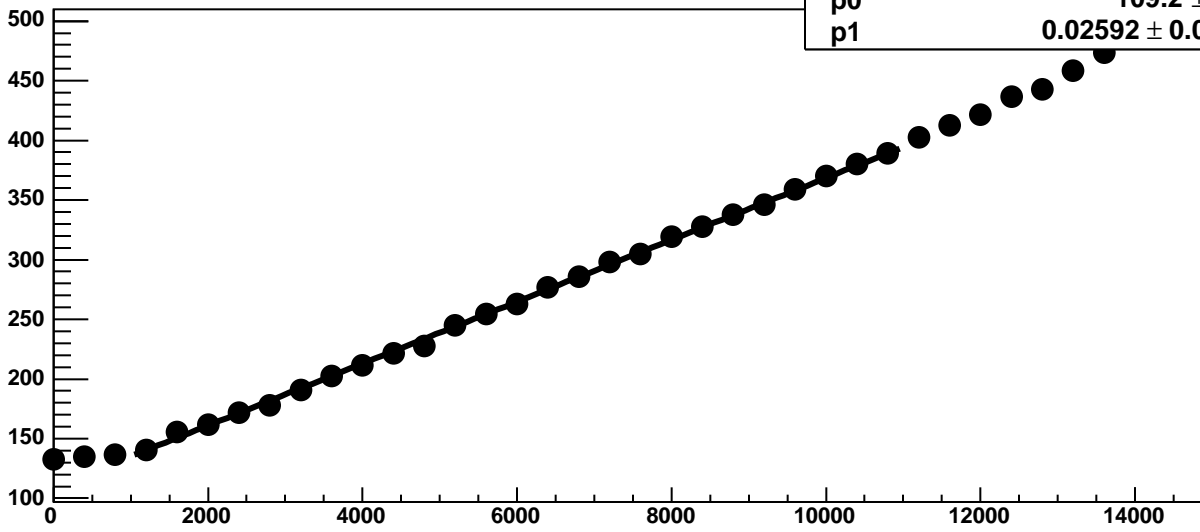
Chip 8, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

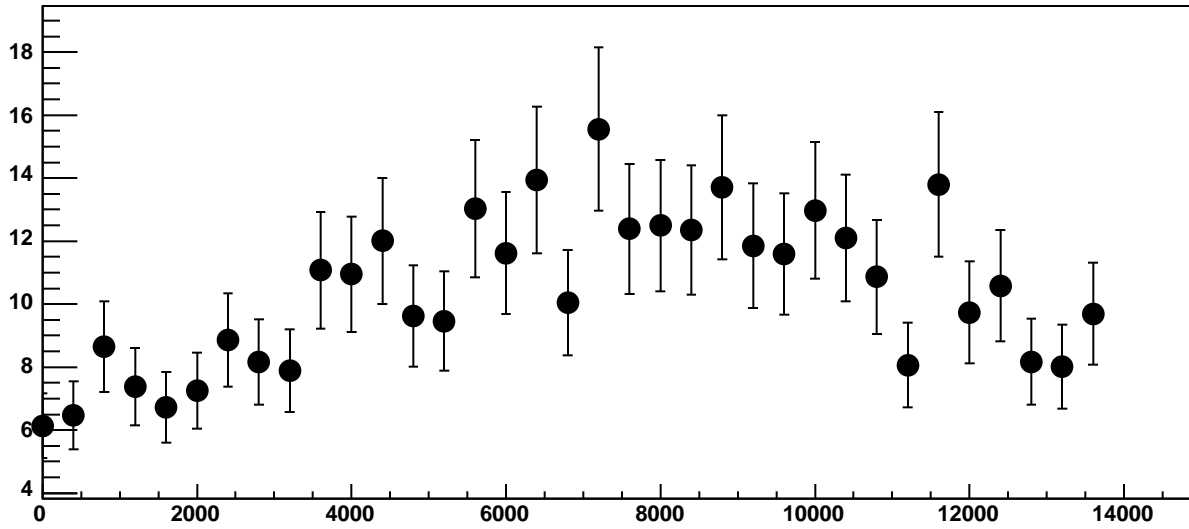


Chip 8, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

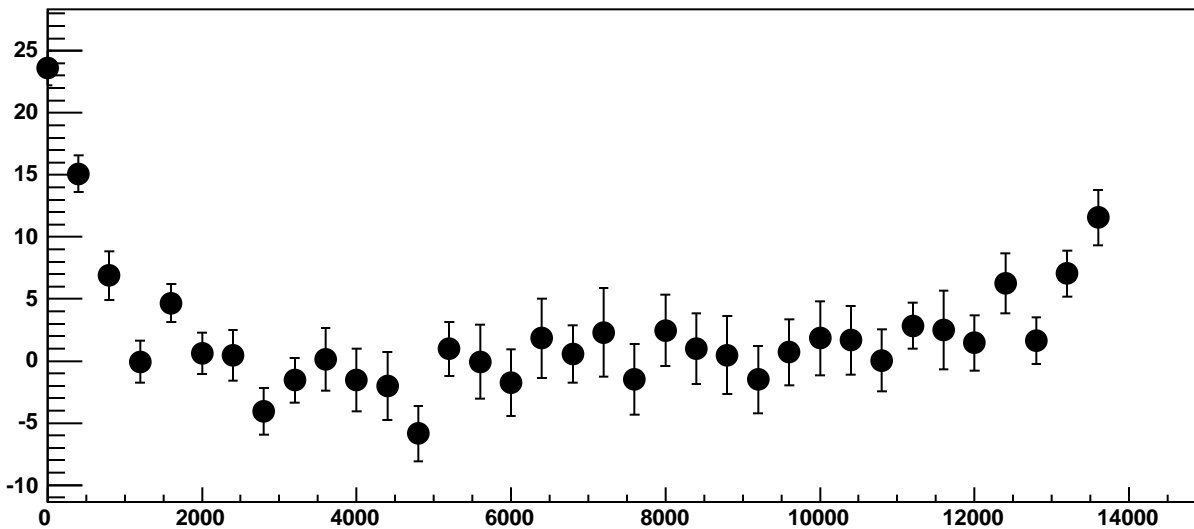


χ^2 / ndf 26.46 / 23
p0 109.2 ± 0.8979
p1 0.02592 ± 0.0001557

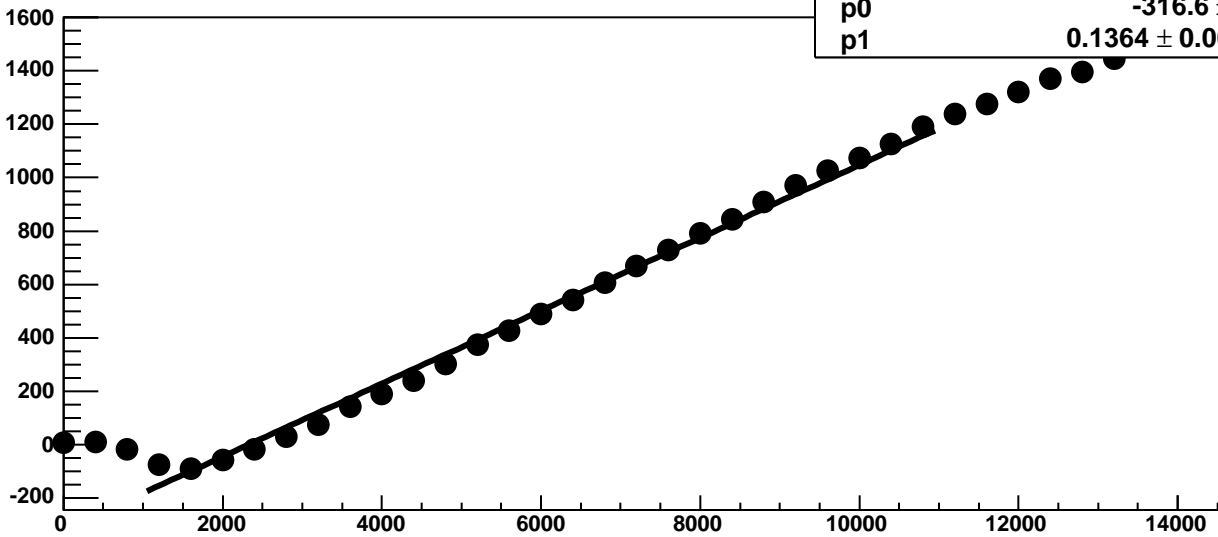
Chip 8, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



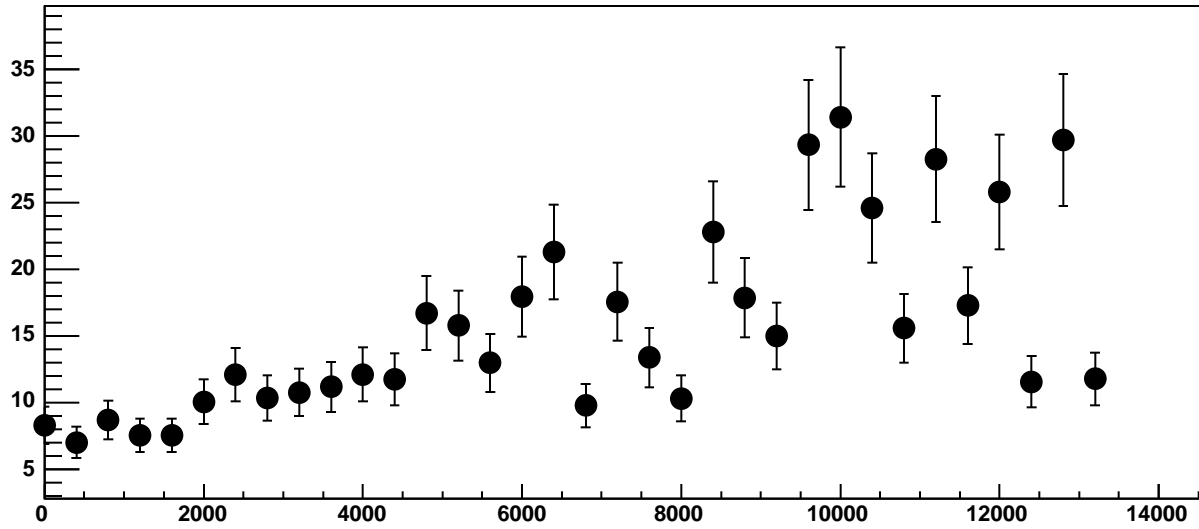
Chip 8, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC



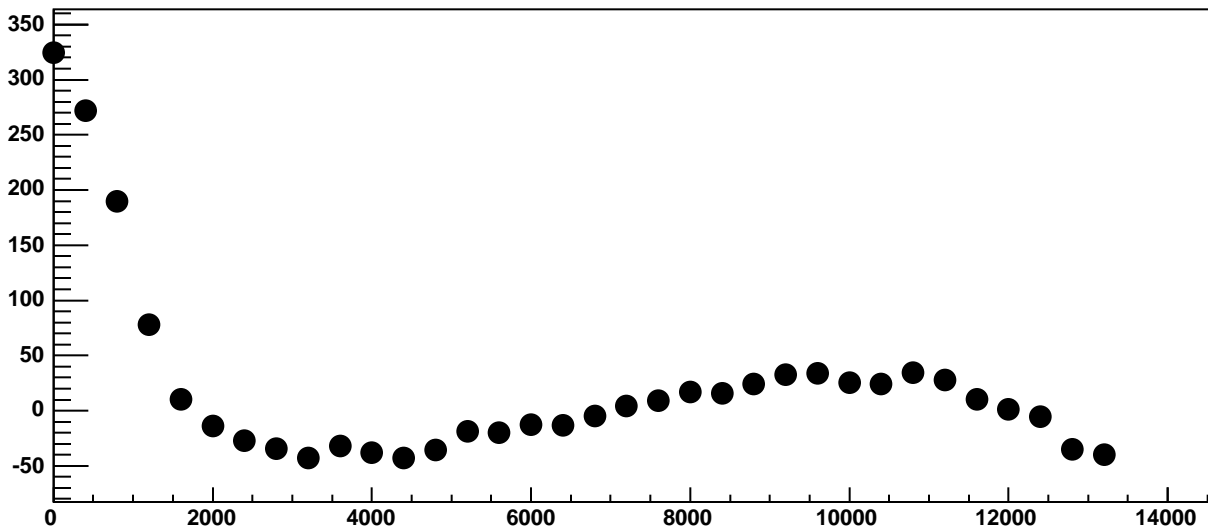
Chip 8, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



Chip 8, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

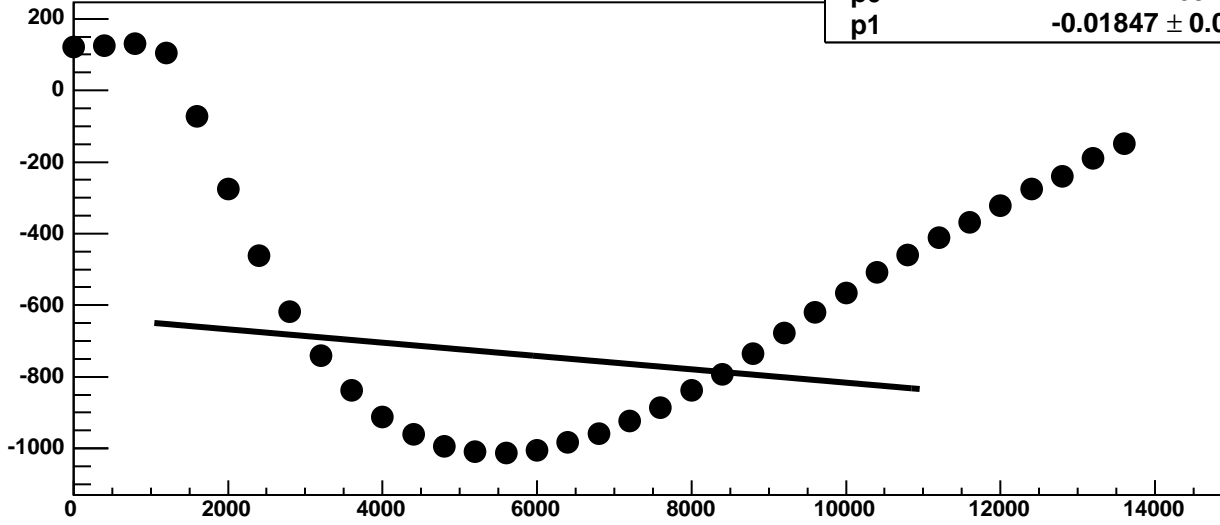


Chip 8, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

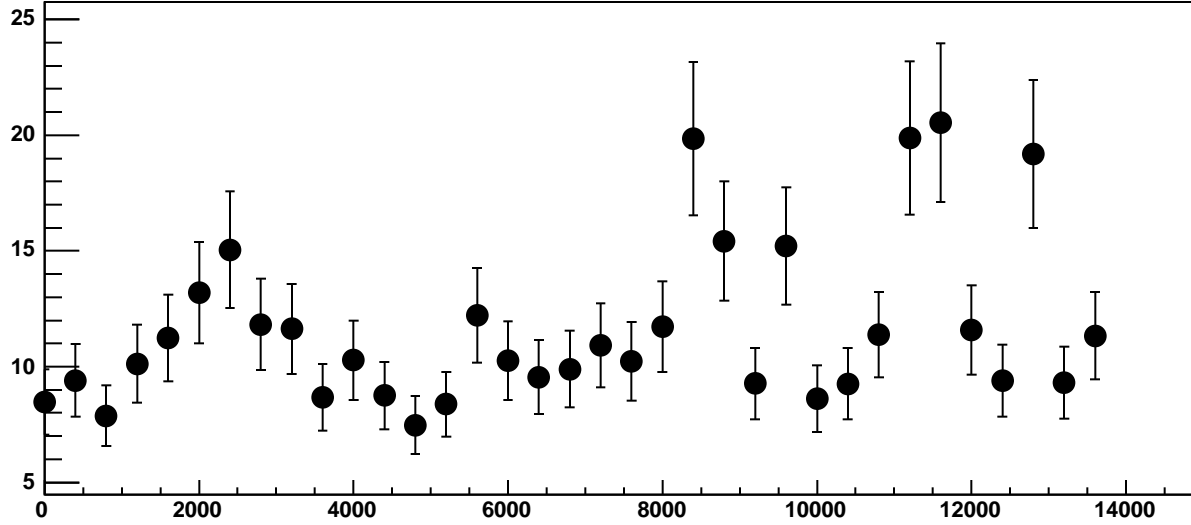


Chip 8, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

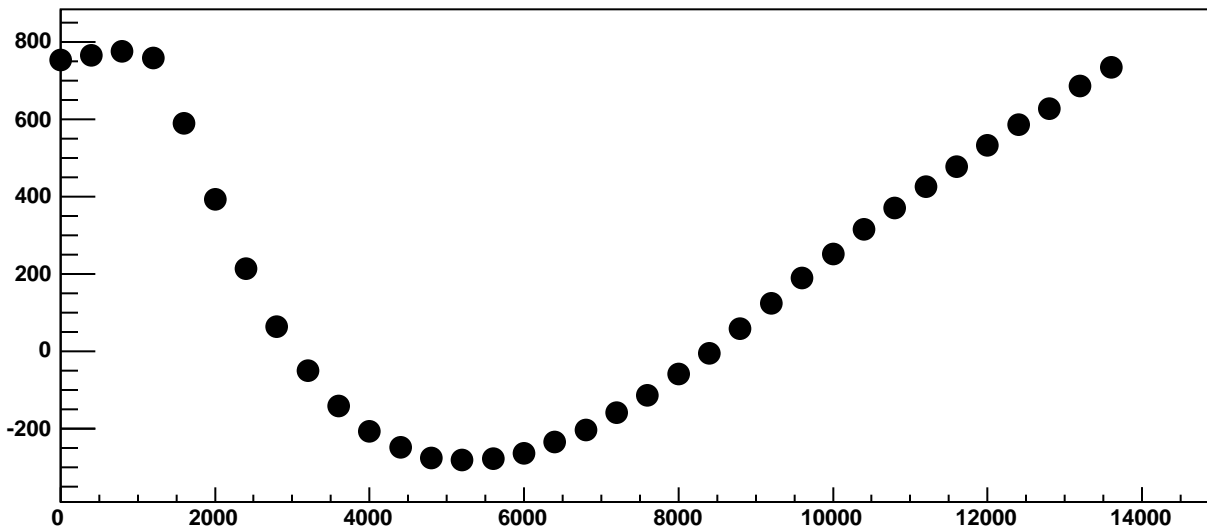
χ^2 / ndf 3.696e+05 / 23
p0 -631 ± 1.138
p1 -0.01847 ± 0.0001726



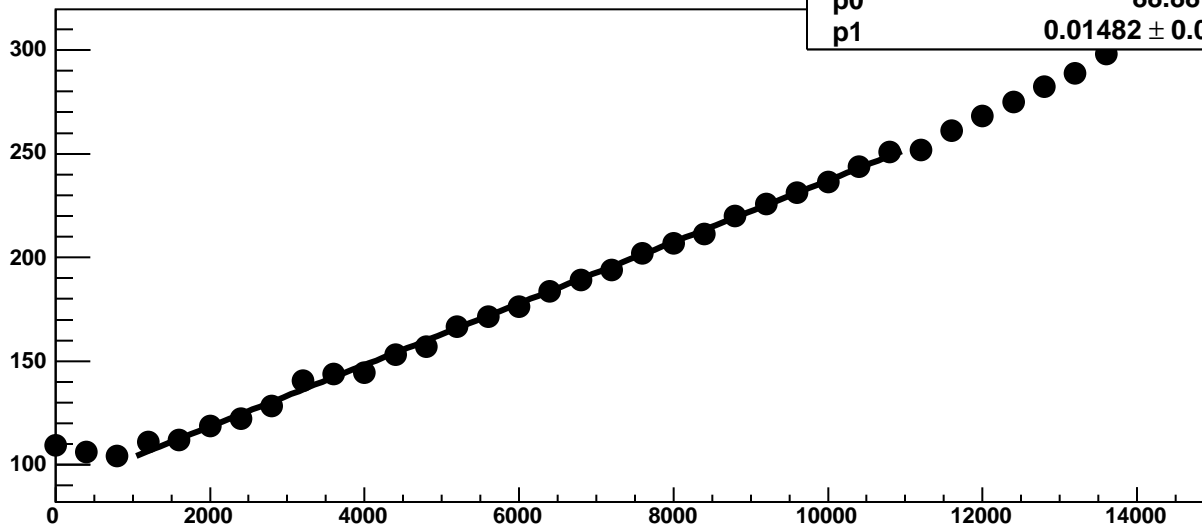
Chip 8, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

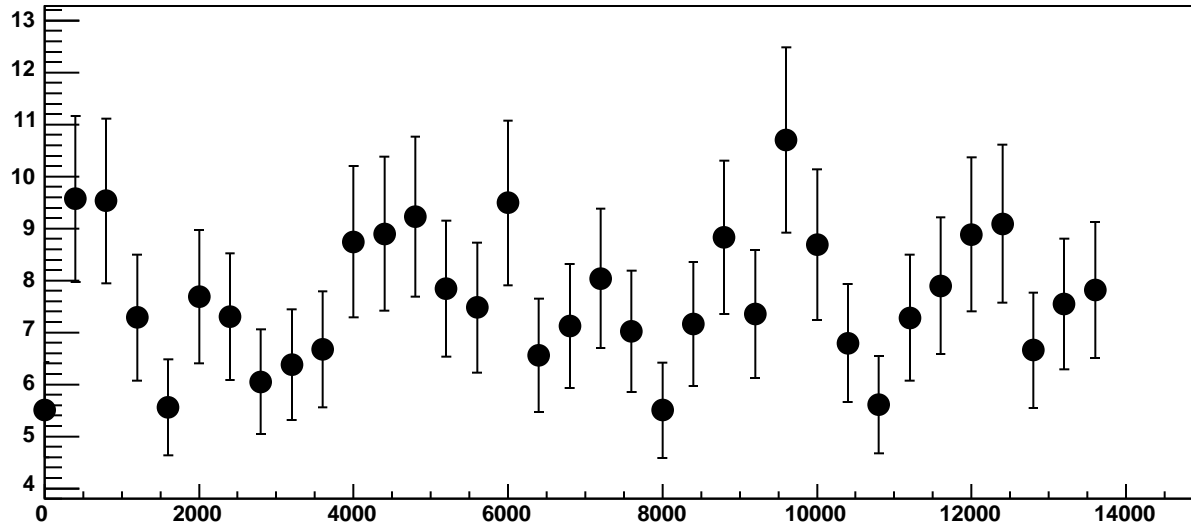


Chip 8, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

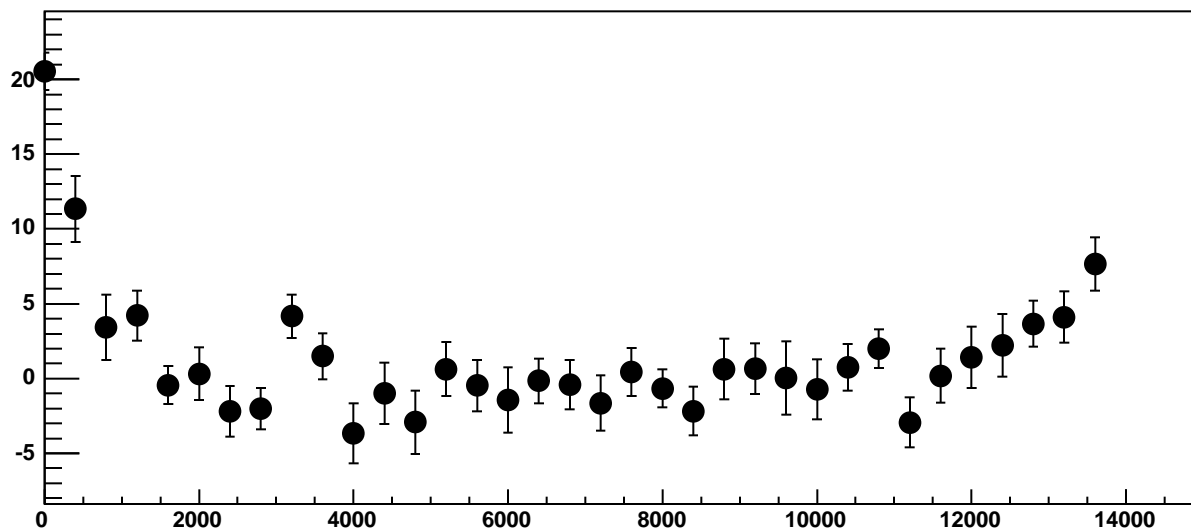


χ^2 / ndf 31.45 / 23
p0 88.88 ± 0.727
p1 0.01482 ± 0.0001095

Chip 8, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

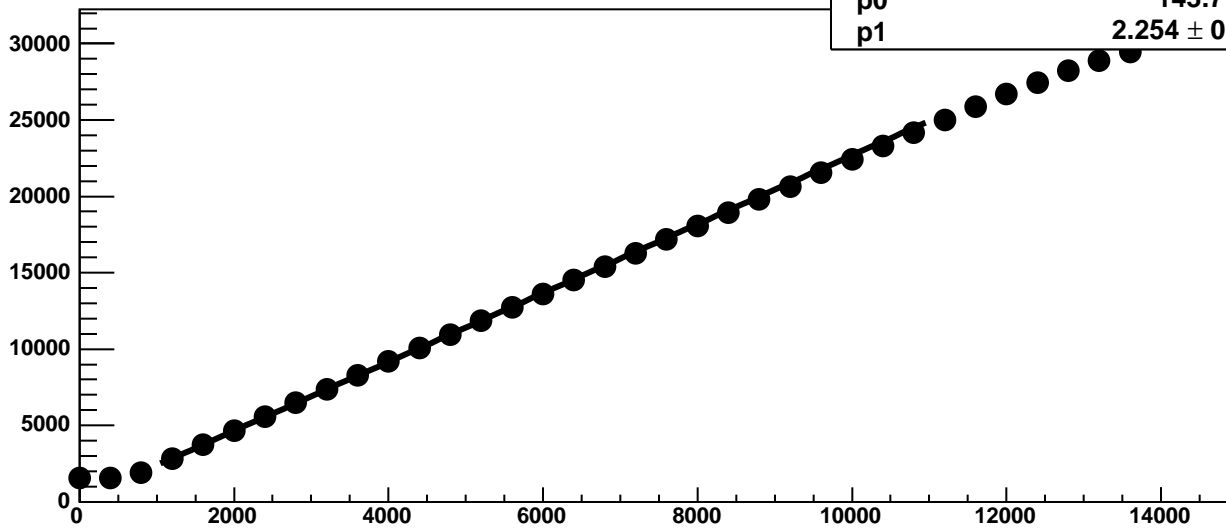


Chip 8, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

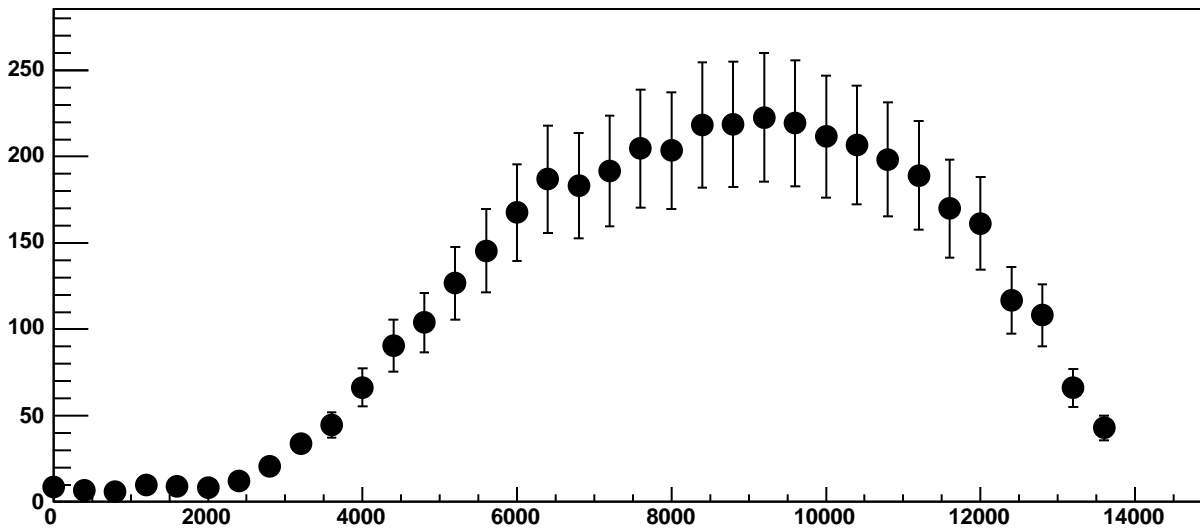


Chip 8, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC

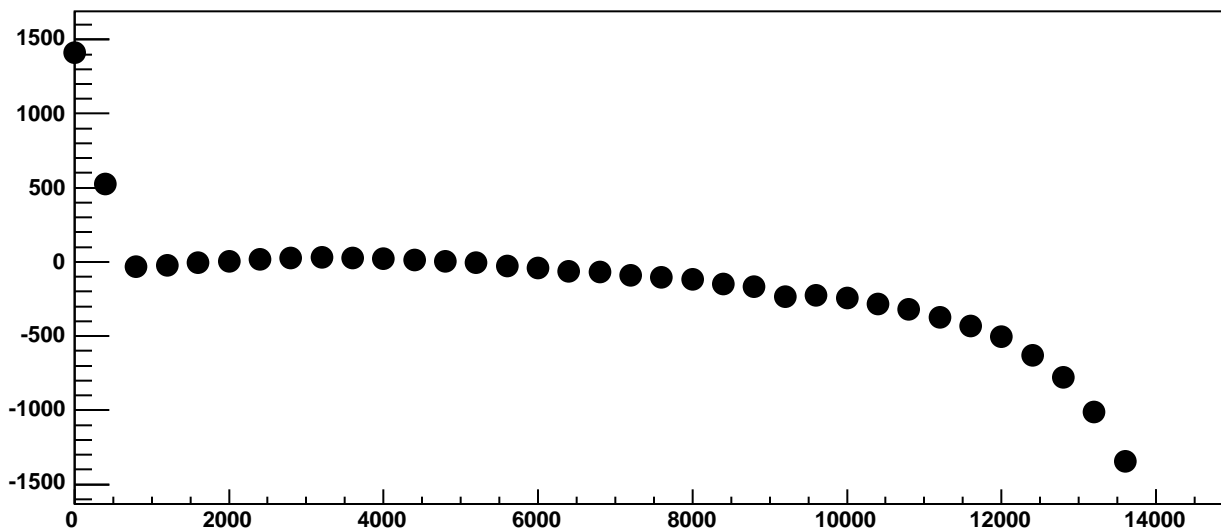
χ^2 / ndf 385.9 / 23
p0 143.7 ± 2.762
p1 2.254 ± 0.001319



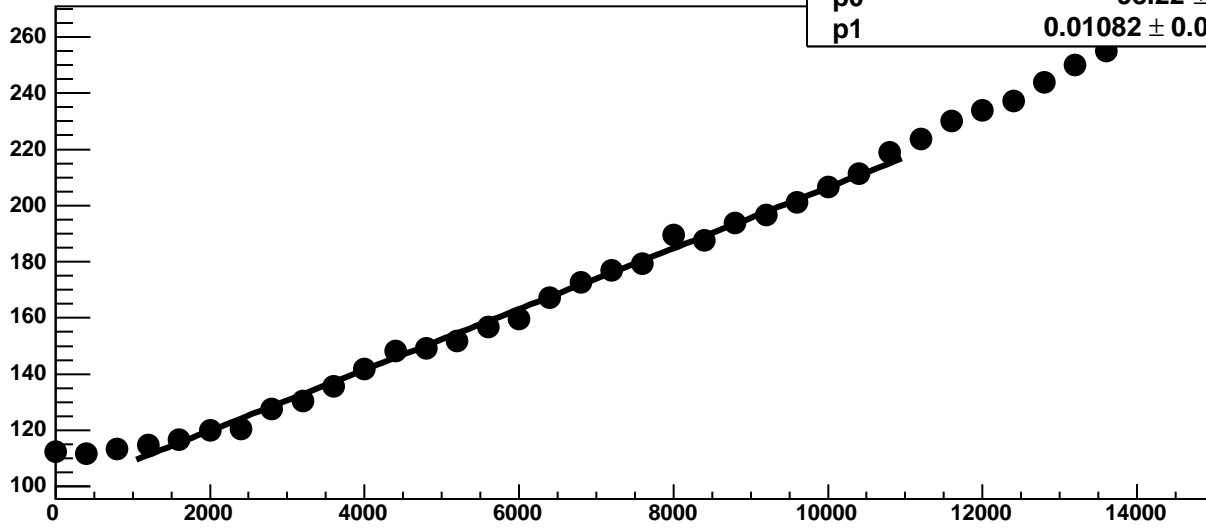
Chip 8, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC

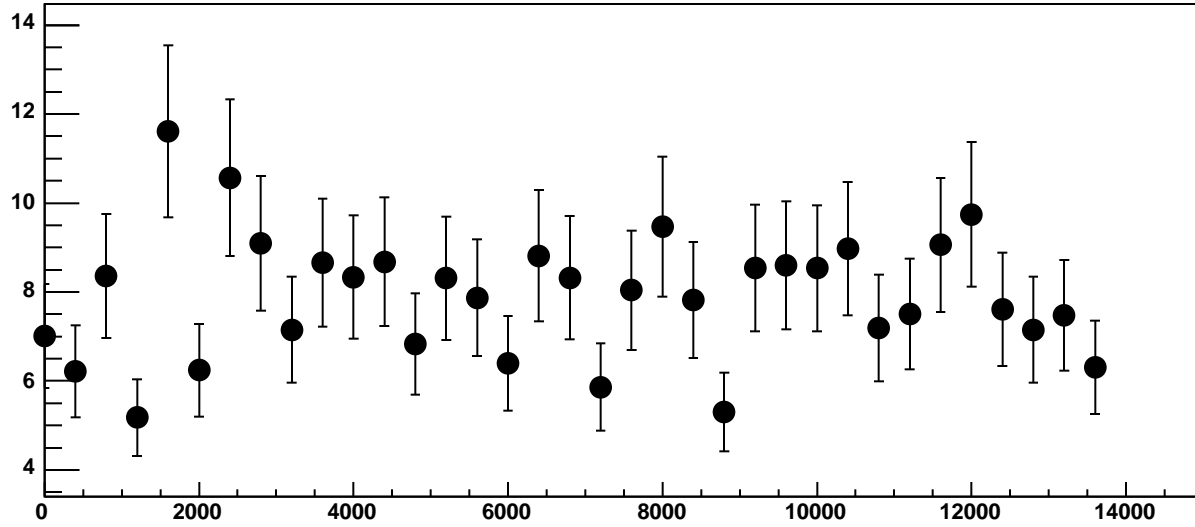


Chip 8, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

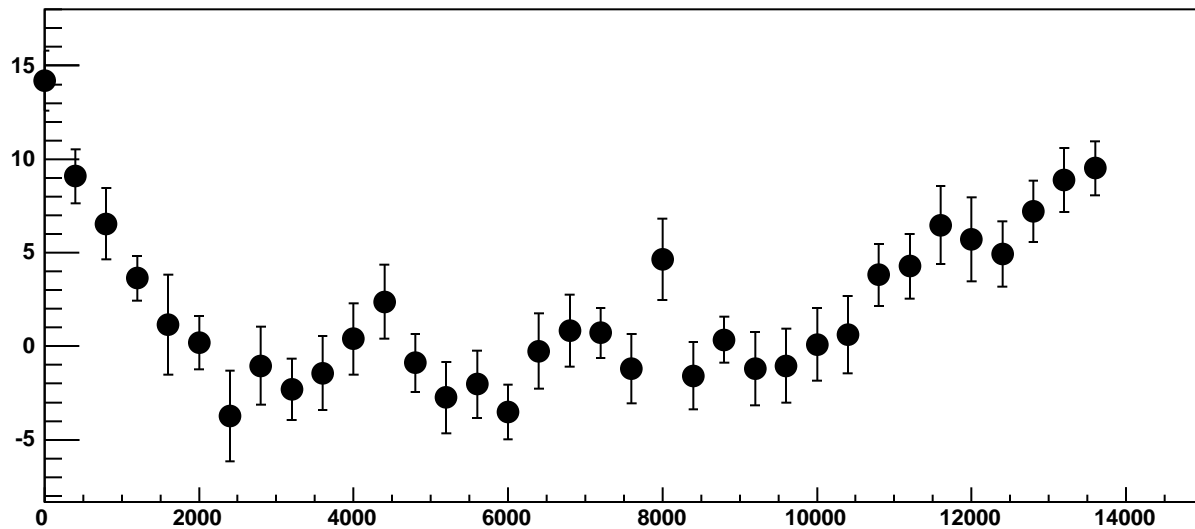


χ^2 / ndf 37.98 / 23
p0 98.22 ± 0.7832
p1 0.01082 ± 0.0001185

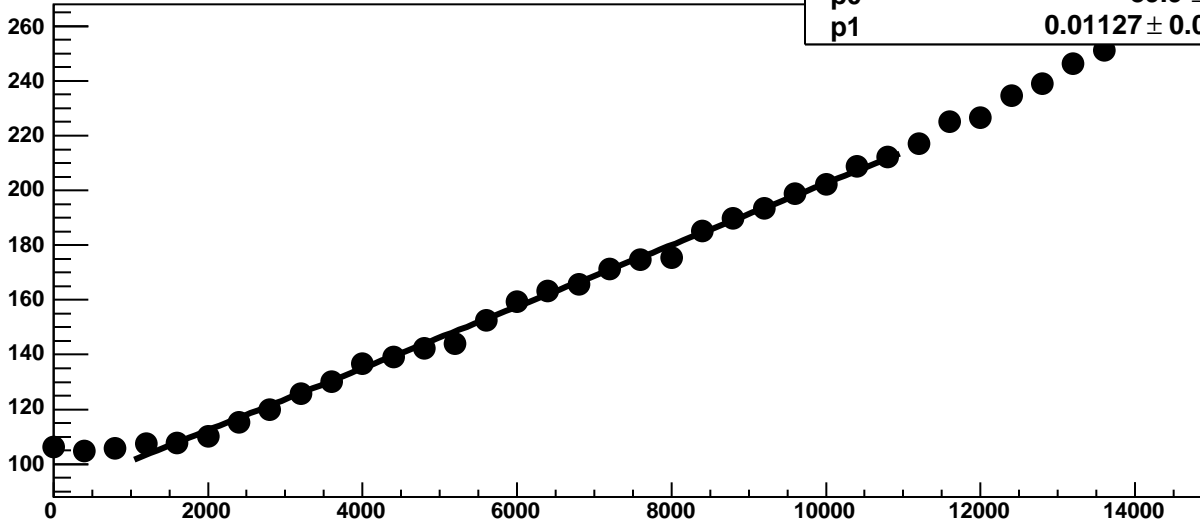
Chip 8, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

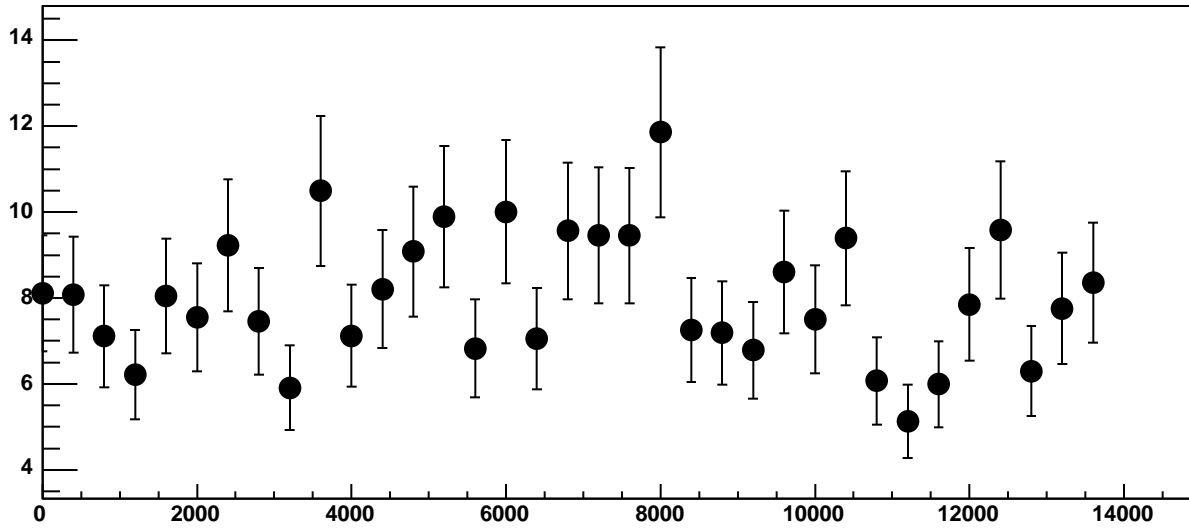


Chip 8, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

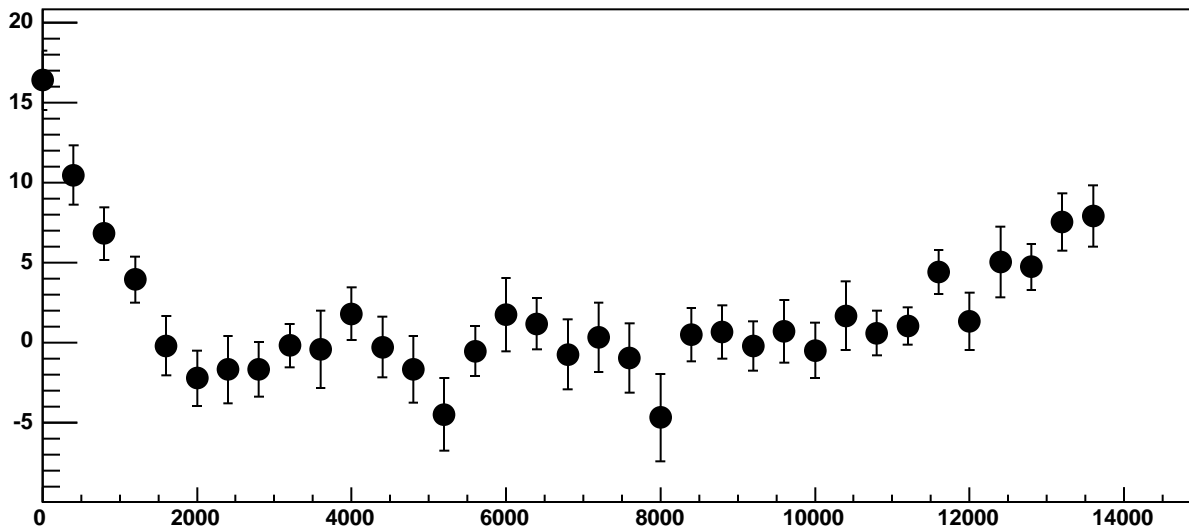


χ^2 / ndf 22.58 / 23
p0 89.9 ± 0.7858
p1 0.01127 ± 0.0001177

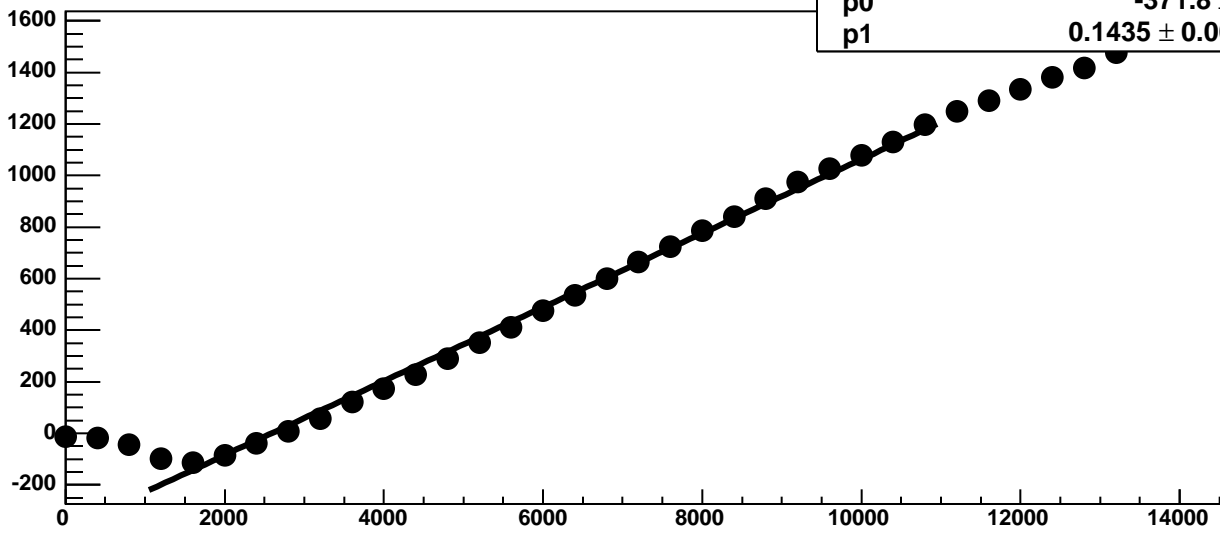
Chip 8, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



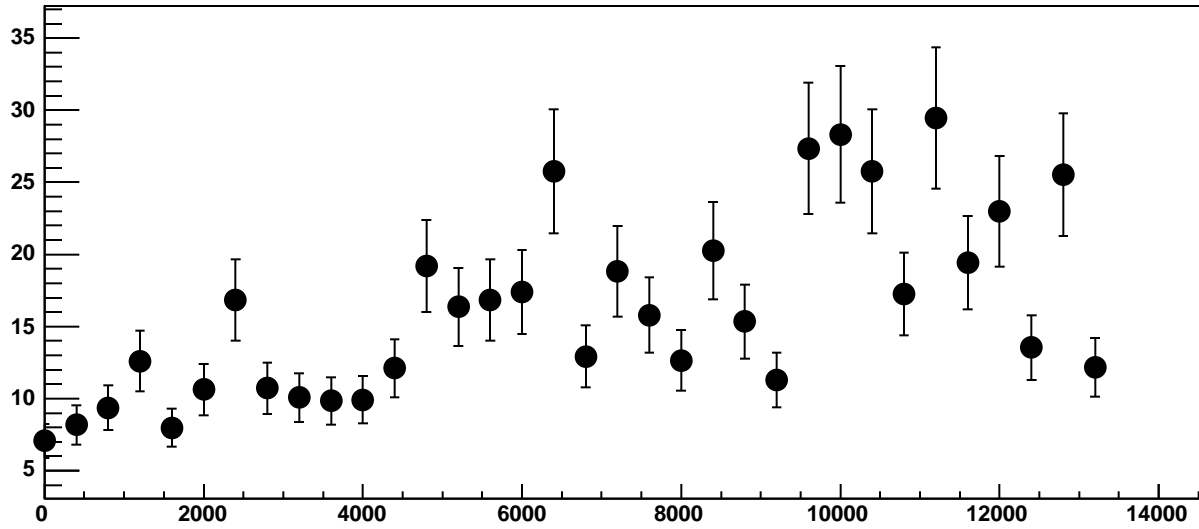
Chip 8, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



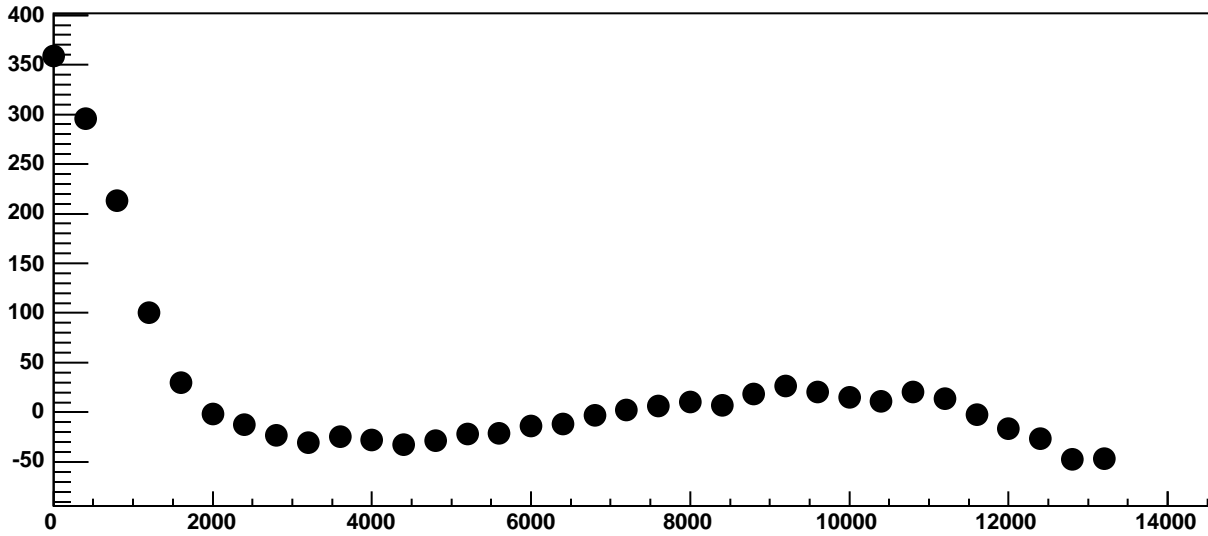
Chip 8, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC



Chip 8, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC

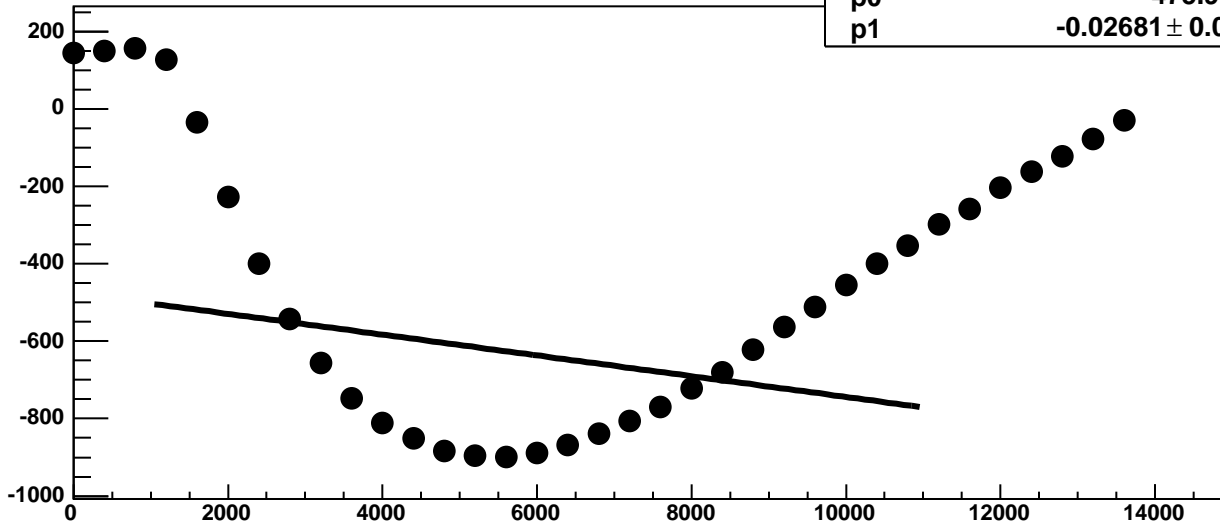


Chip 8, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC

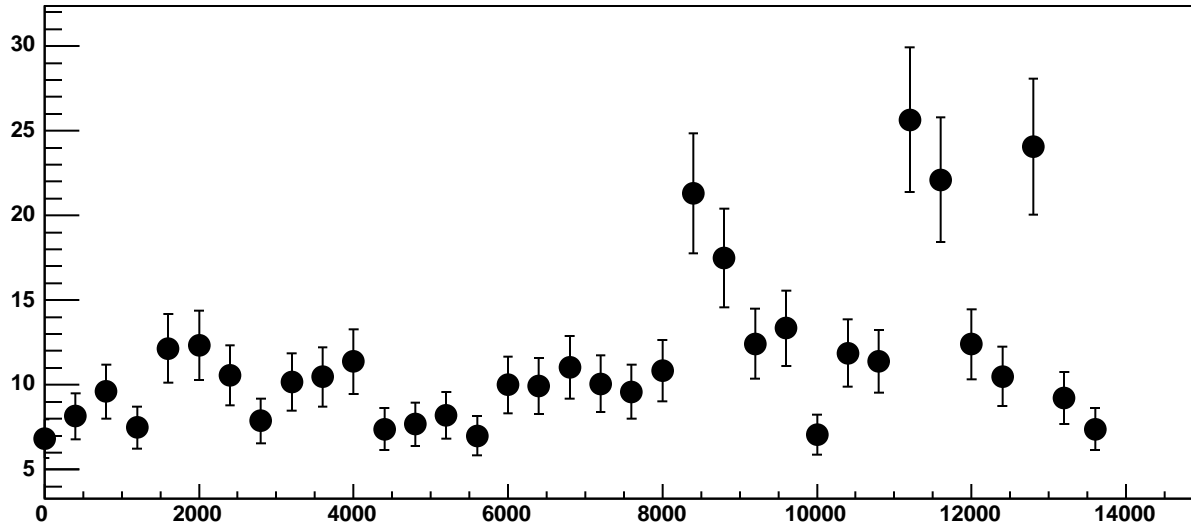


Chip 8, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

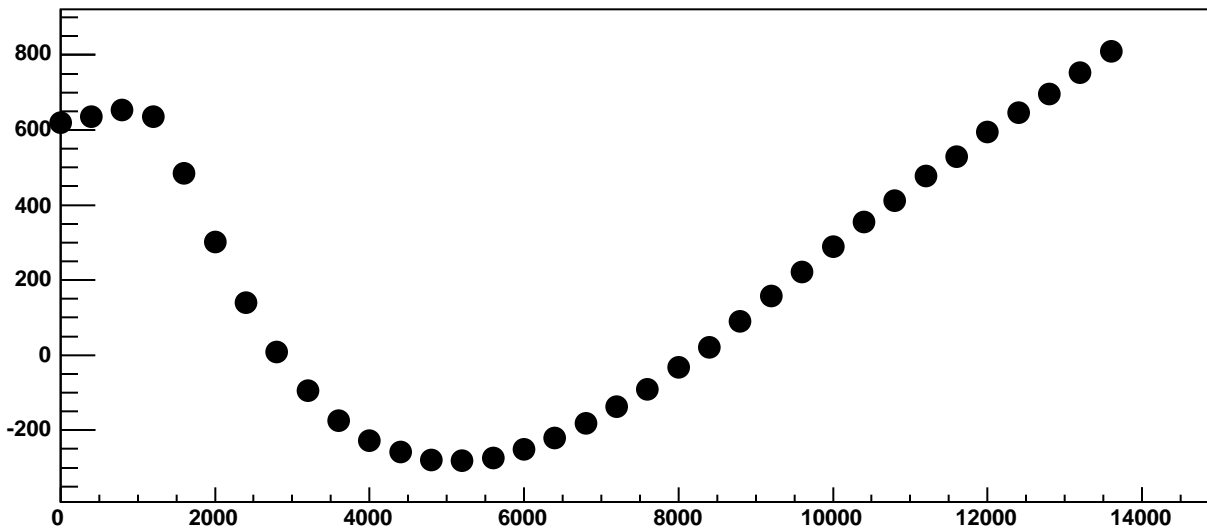
χ^2 / ndf 4.123e+05 / 23
p0 -475.9 ± 1.008
p1 -0.02681 ± 0.0001605



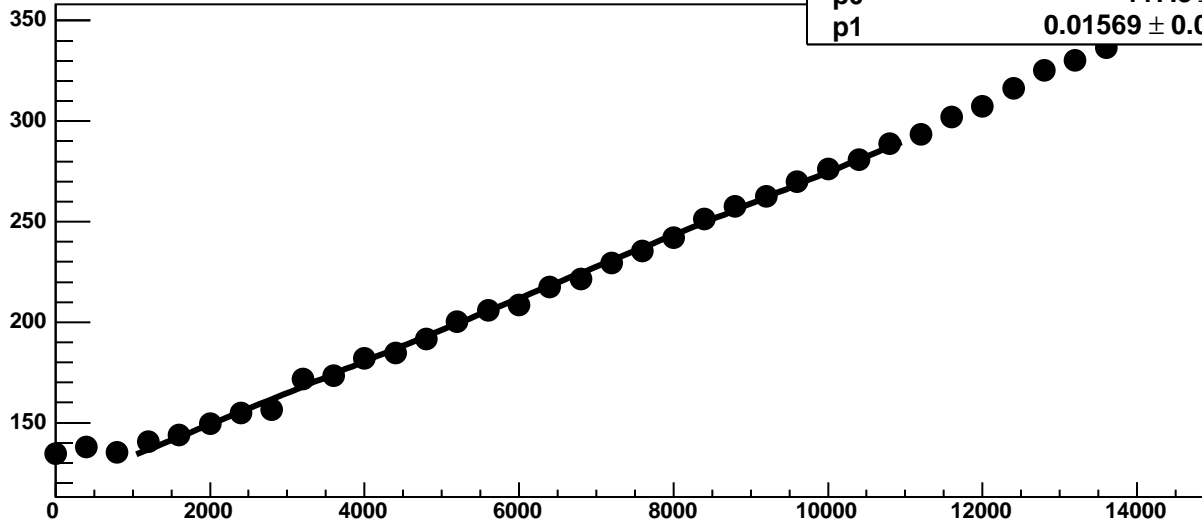
Chip 8, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



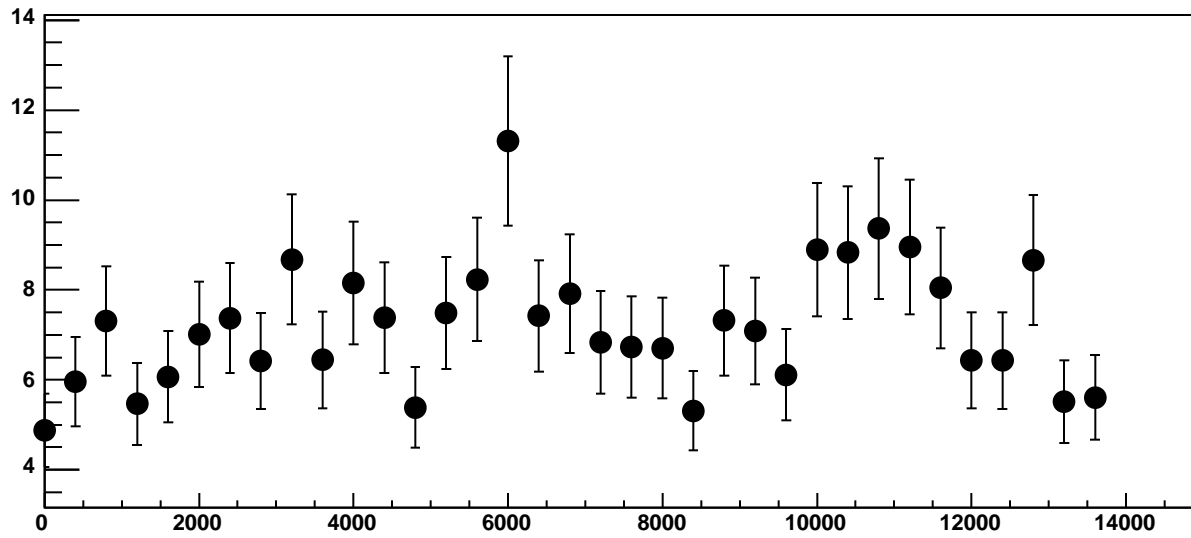
Chip 8, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



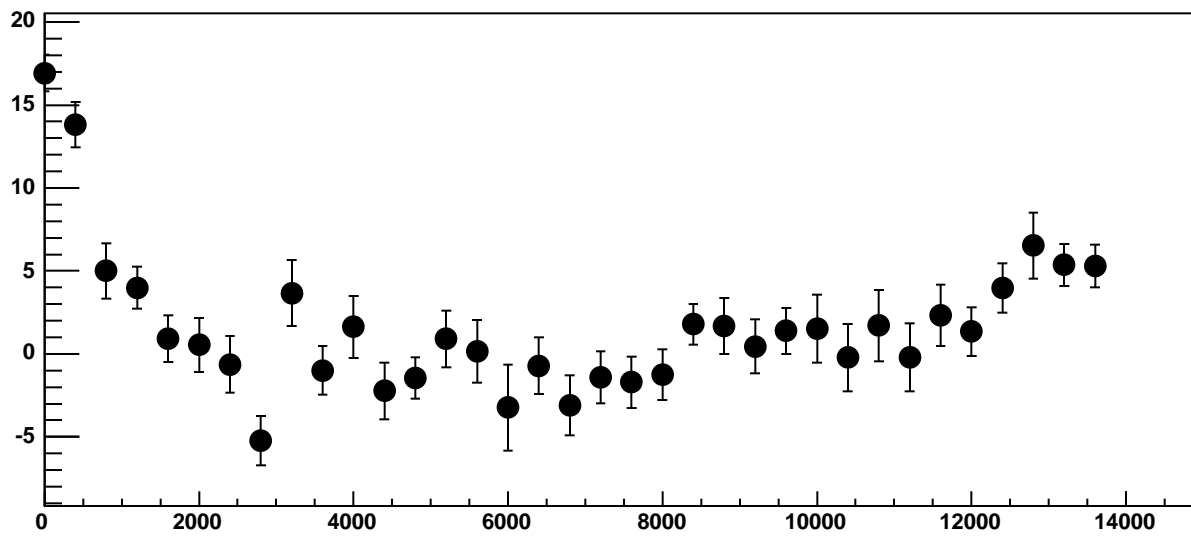
Chip 8, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



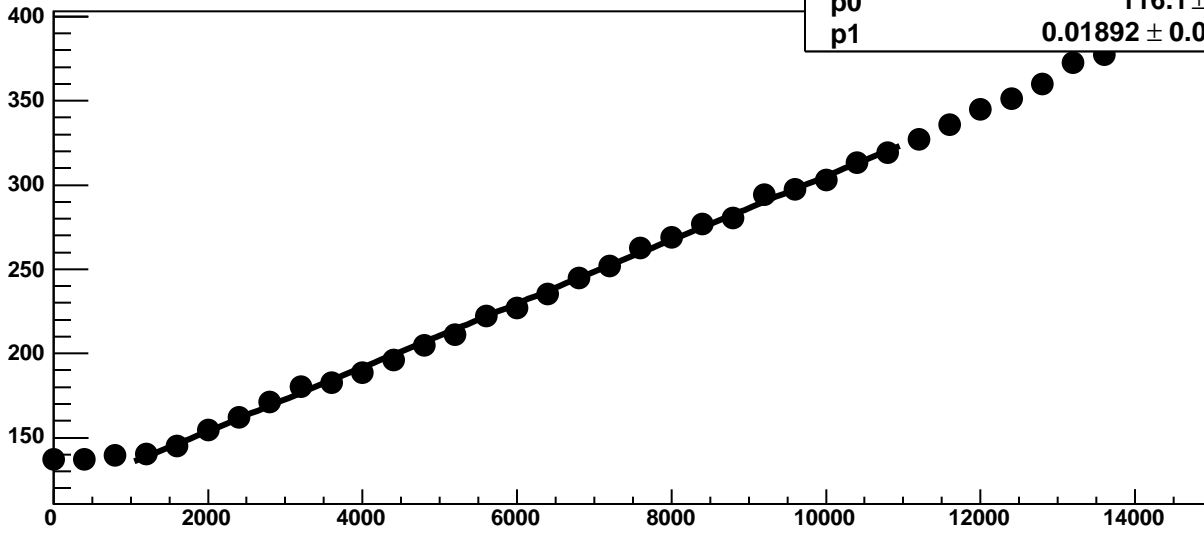
Chip 8, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

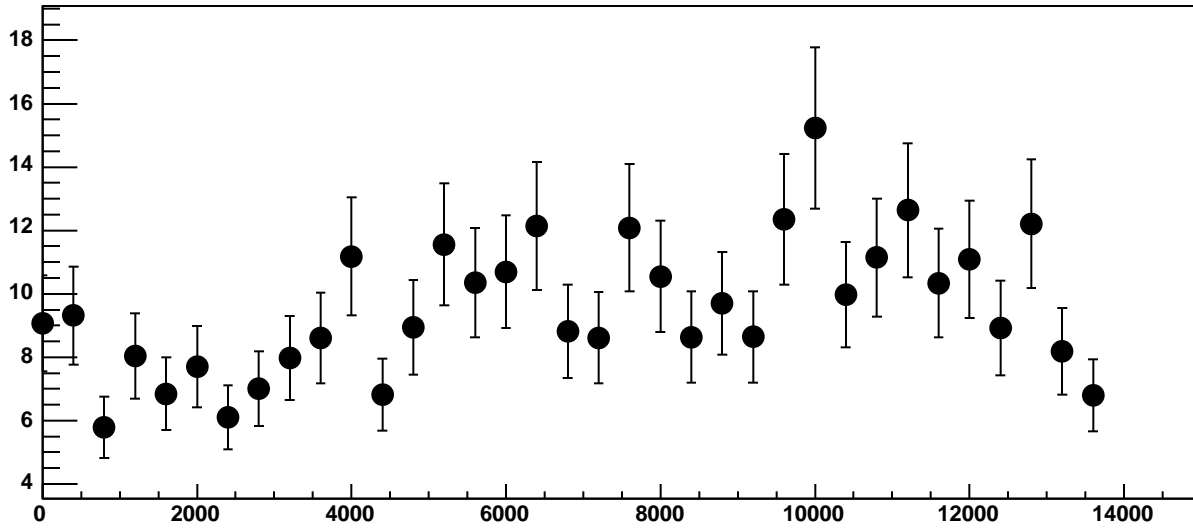


Chip 8, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

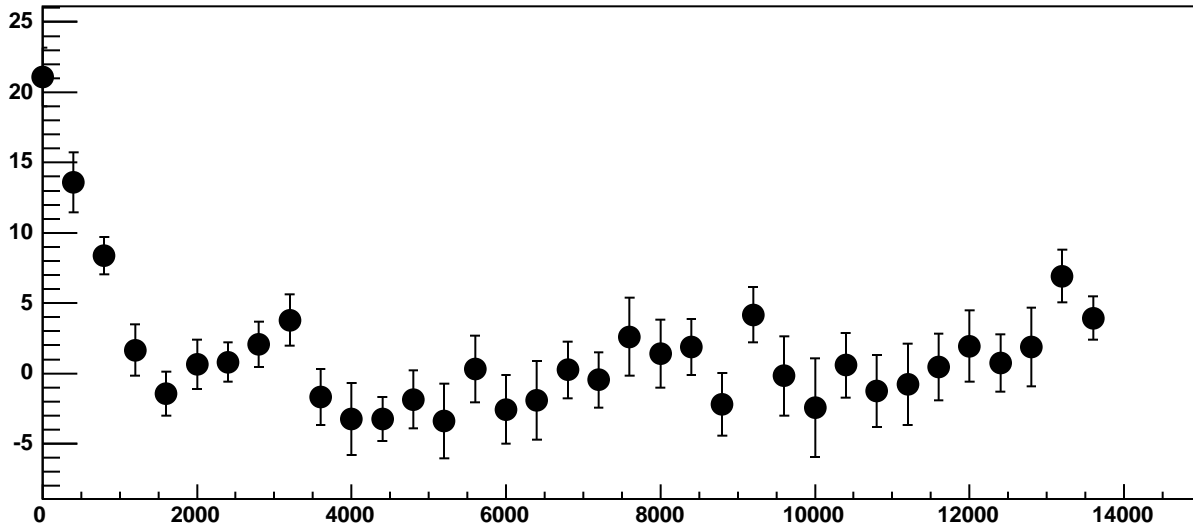


χ^2 / ndf 27.07 / 23
p0 116.1 ± 0.8408
p1 0.01892 ± 0.0001422

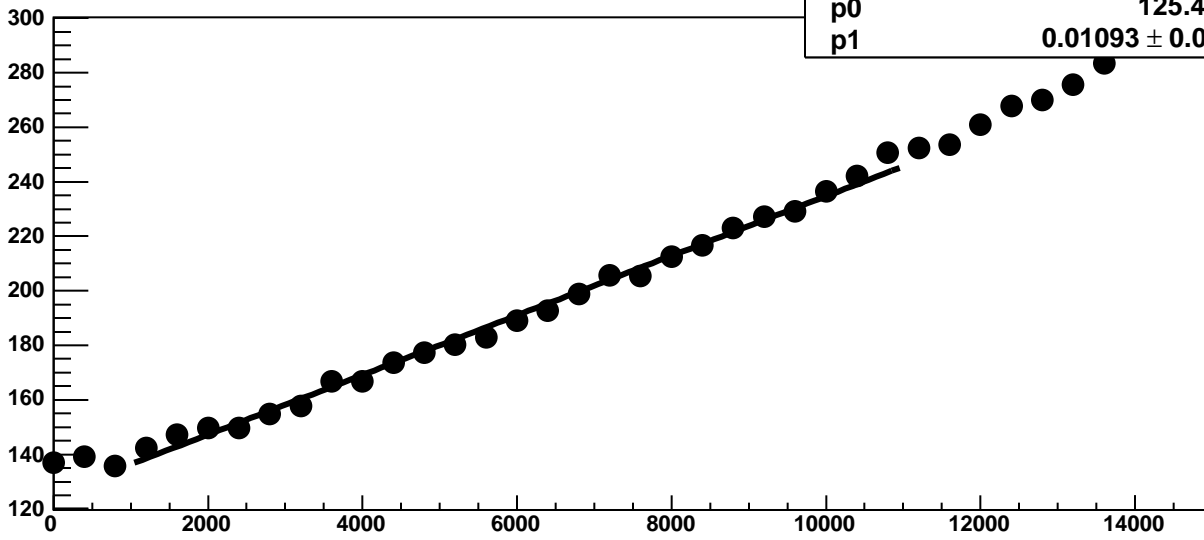
Chip 8, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

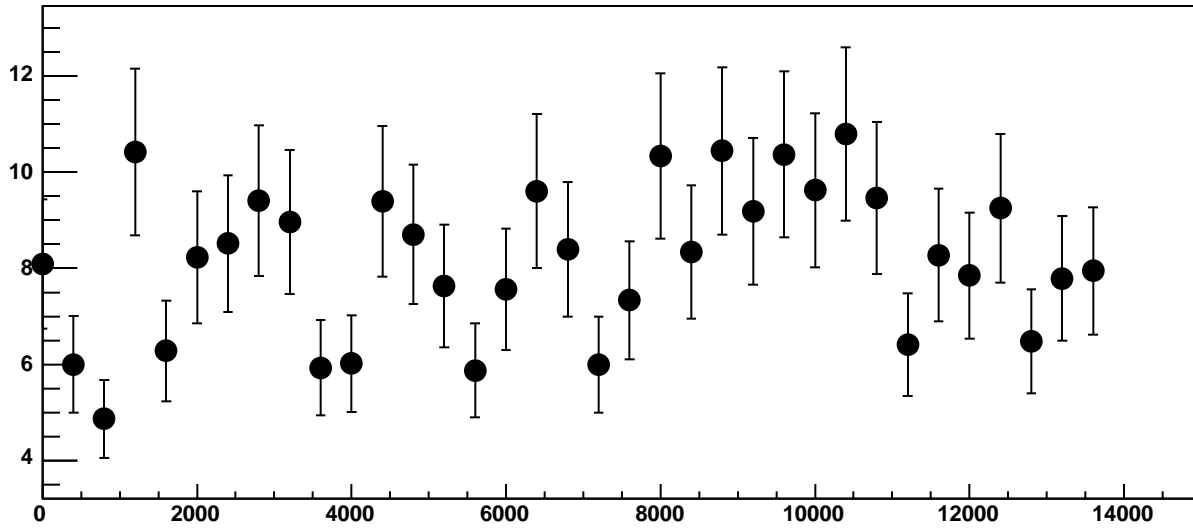


Chip 8, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

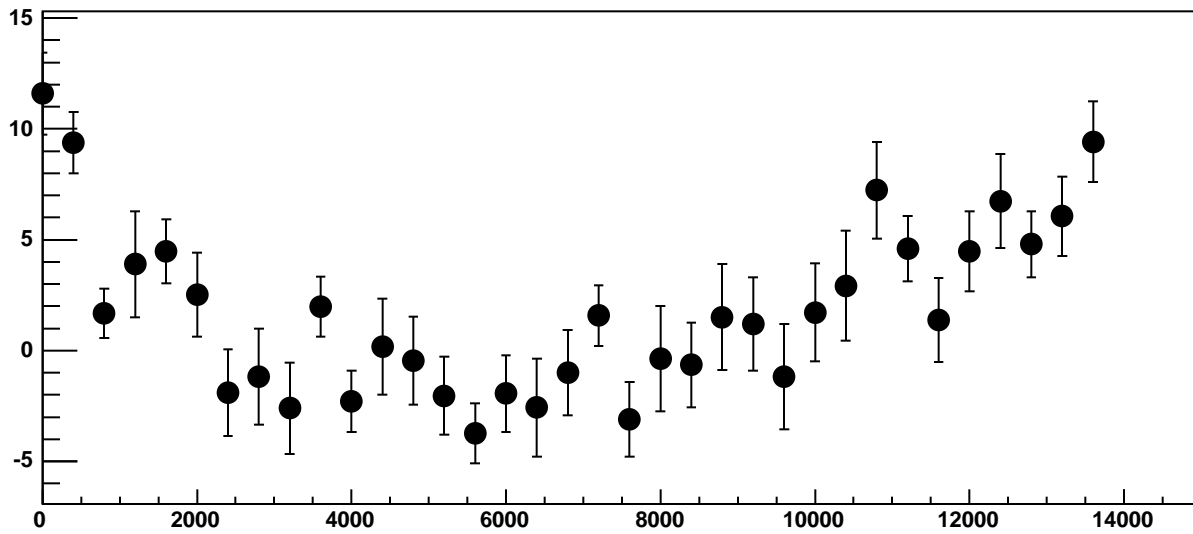


χ^2 / ndf 52.57 / 23
p0 125.4 ± 0.86
p1 0.01093 ± 0.0001387

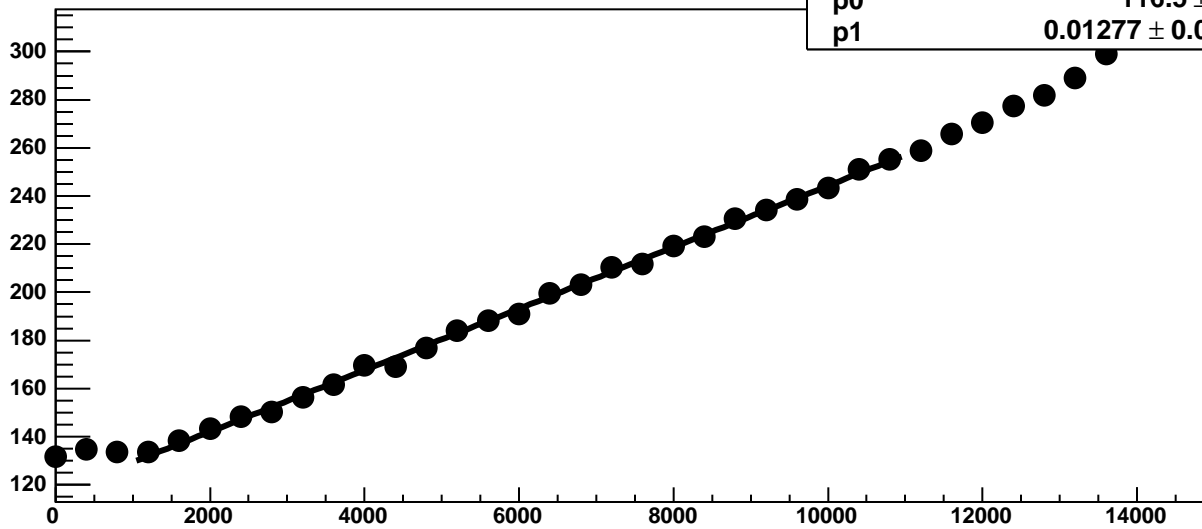
Chip 8, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

18.34 / 23

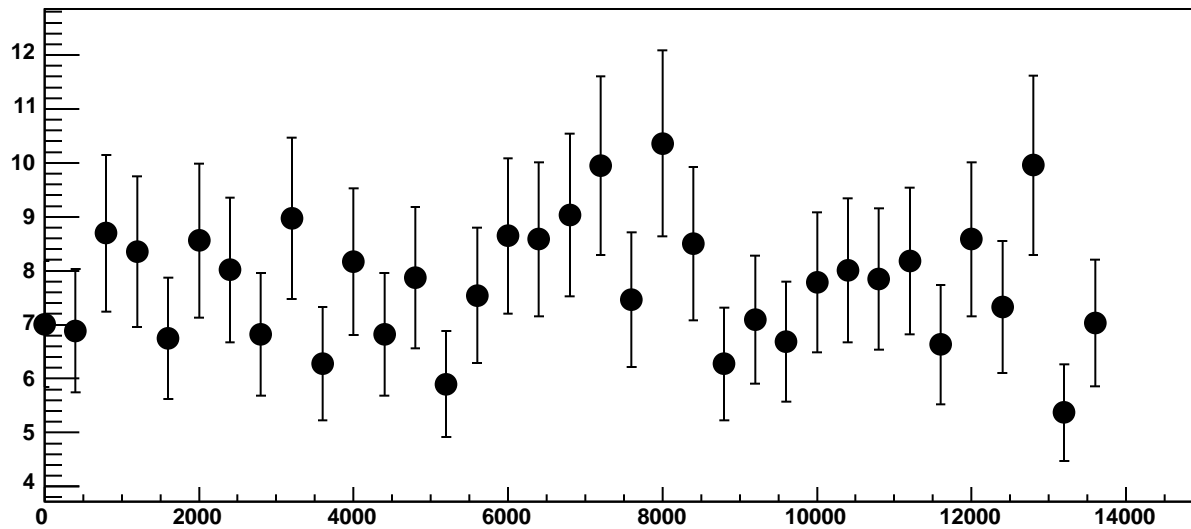
p0

116.5 ± 0.7992

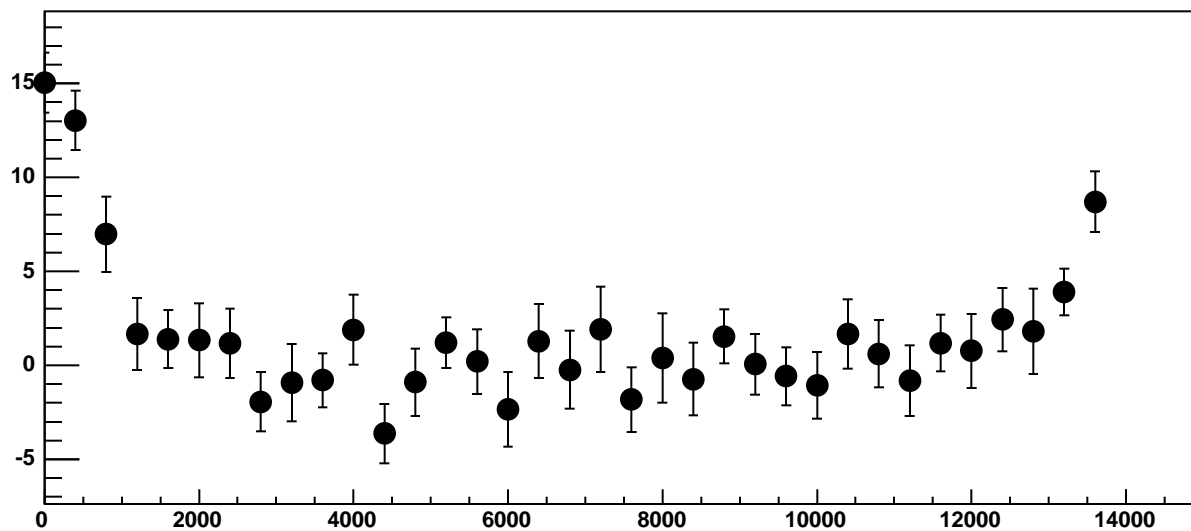
p1

0.01277 ± 0.0001206

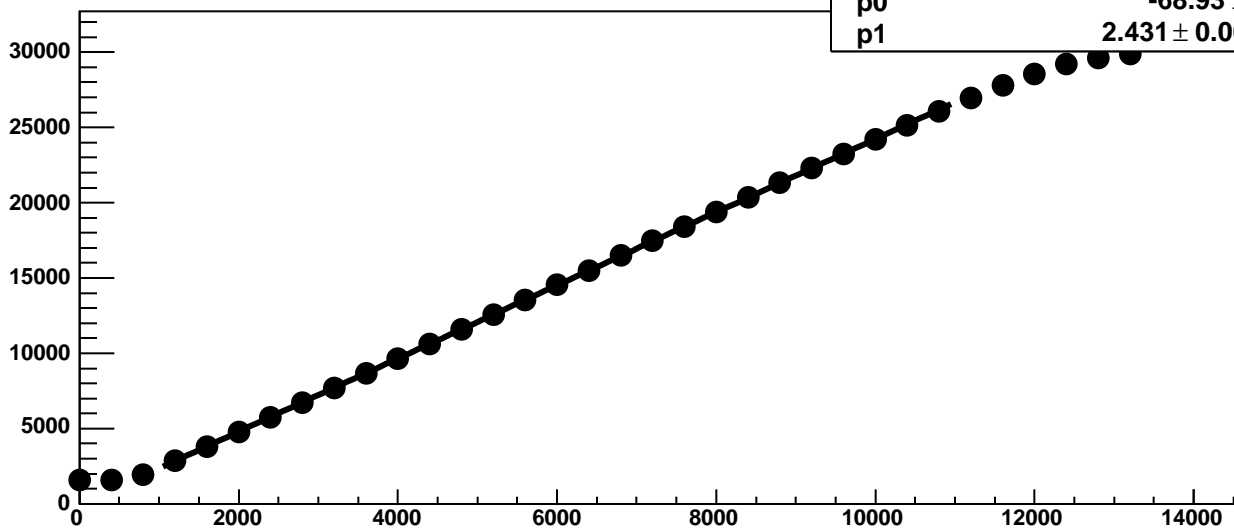
Chip 8, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

2814 / 23

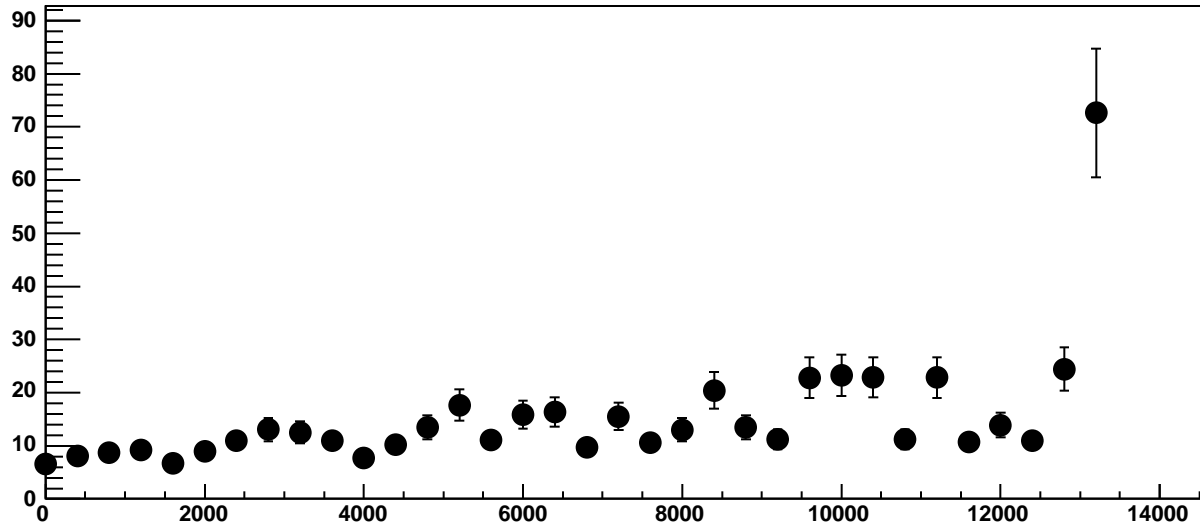
p0

-68.93 ± 1.056

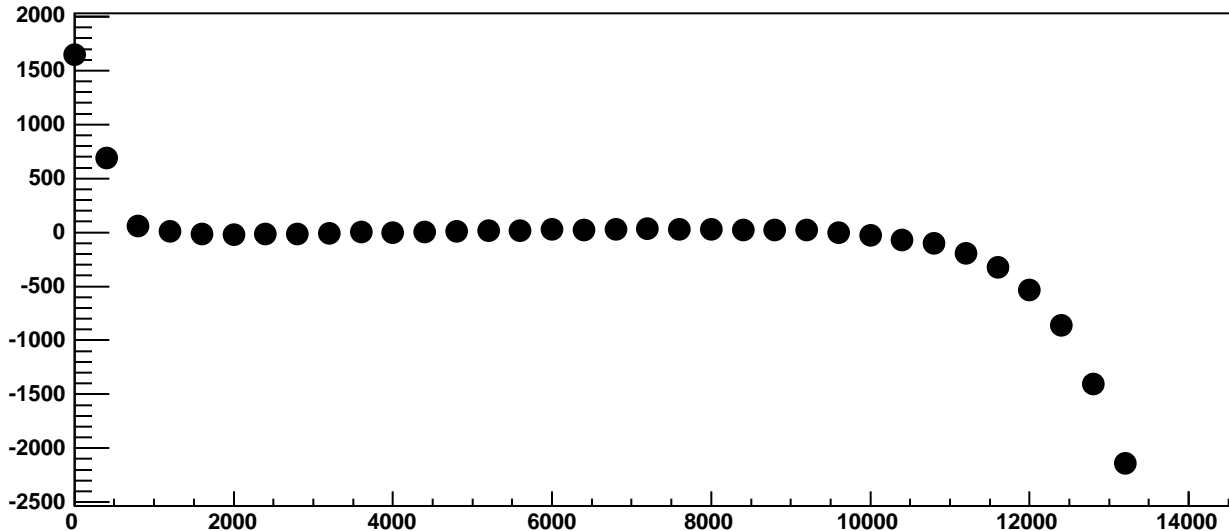
p1

2.431 ± 0.0001862

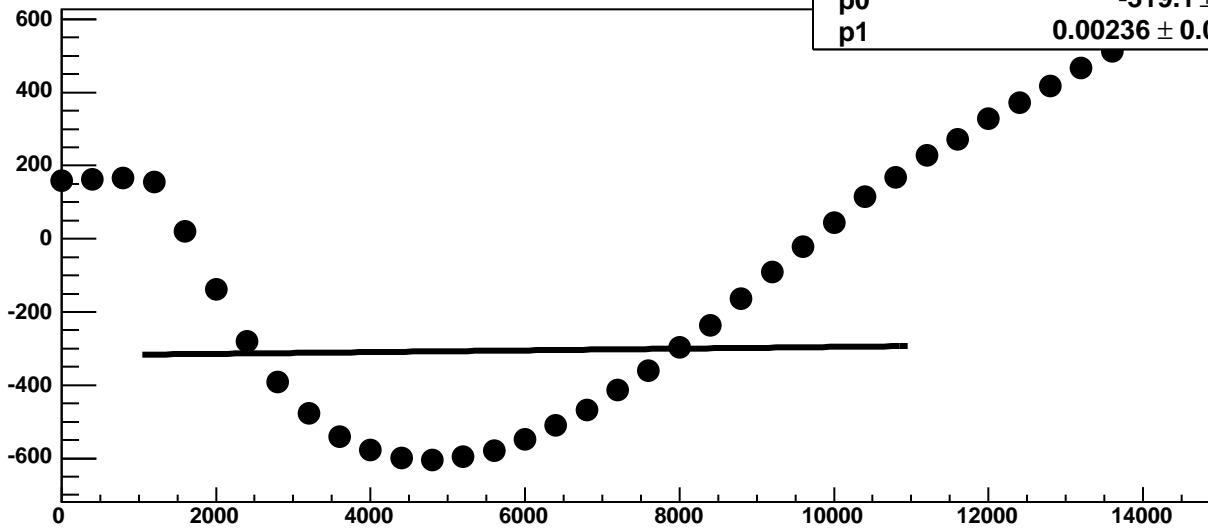
Chip 8, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC

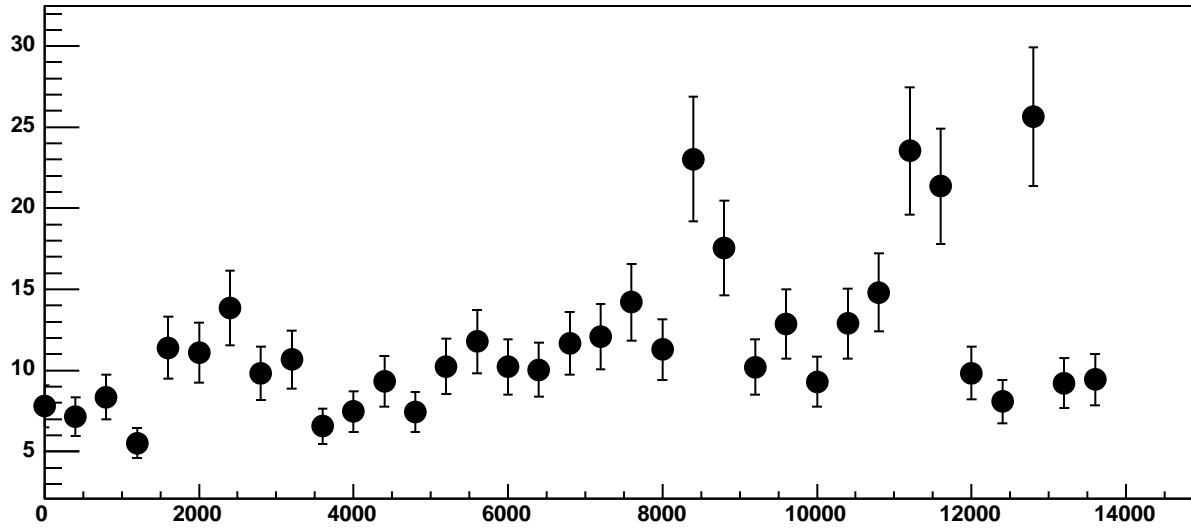


Chip 8, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

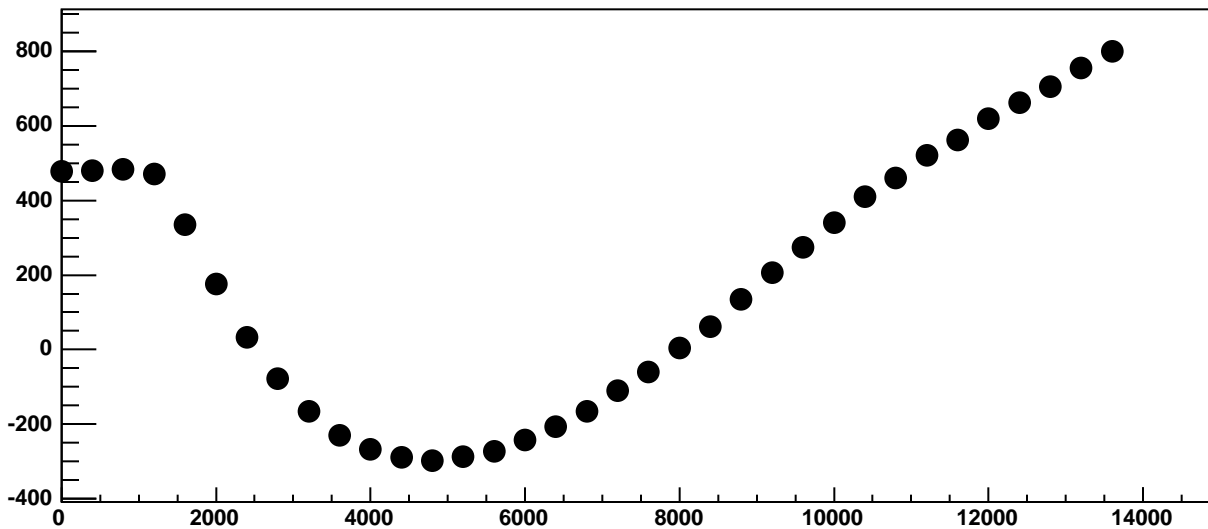


χ^2 / ndf 3.937e+05 / 23
p0 -319.1 ± 0.9378
p1 0.00236 ± 0.0001621

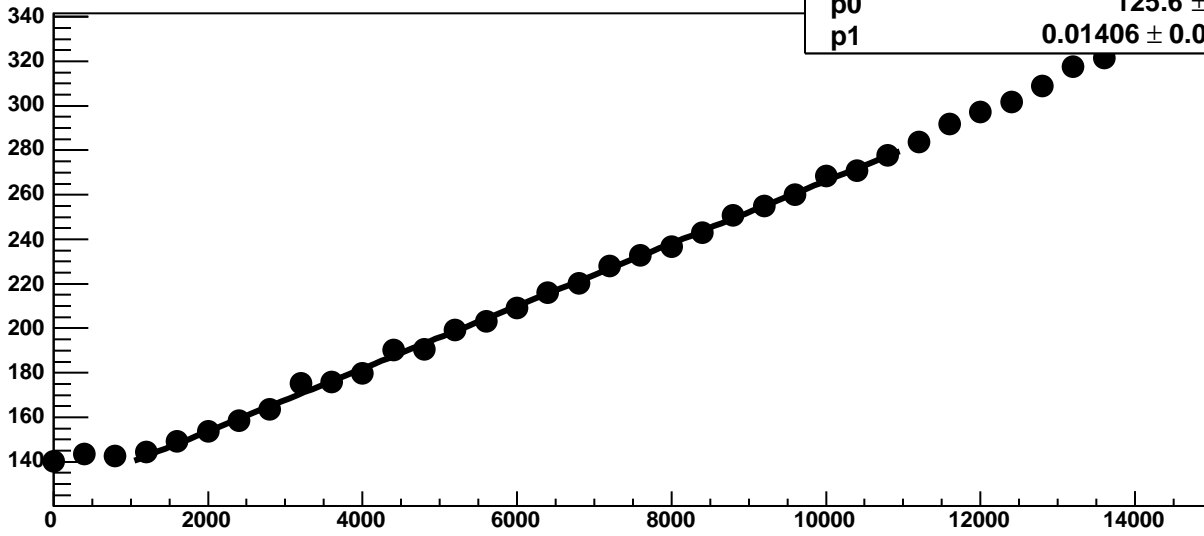
Chip 8, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

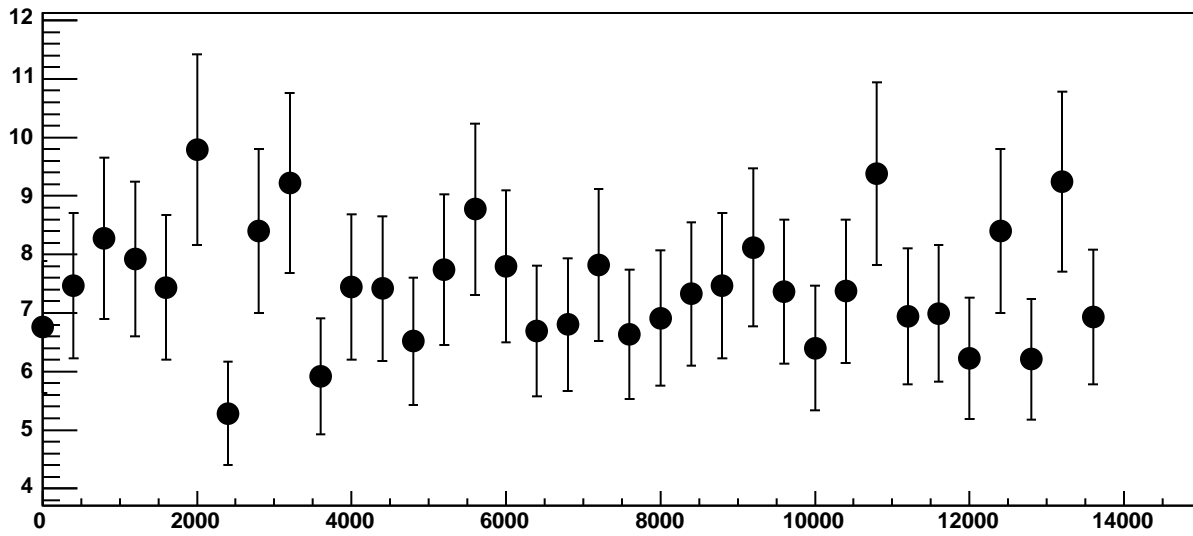


Chip 8, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

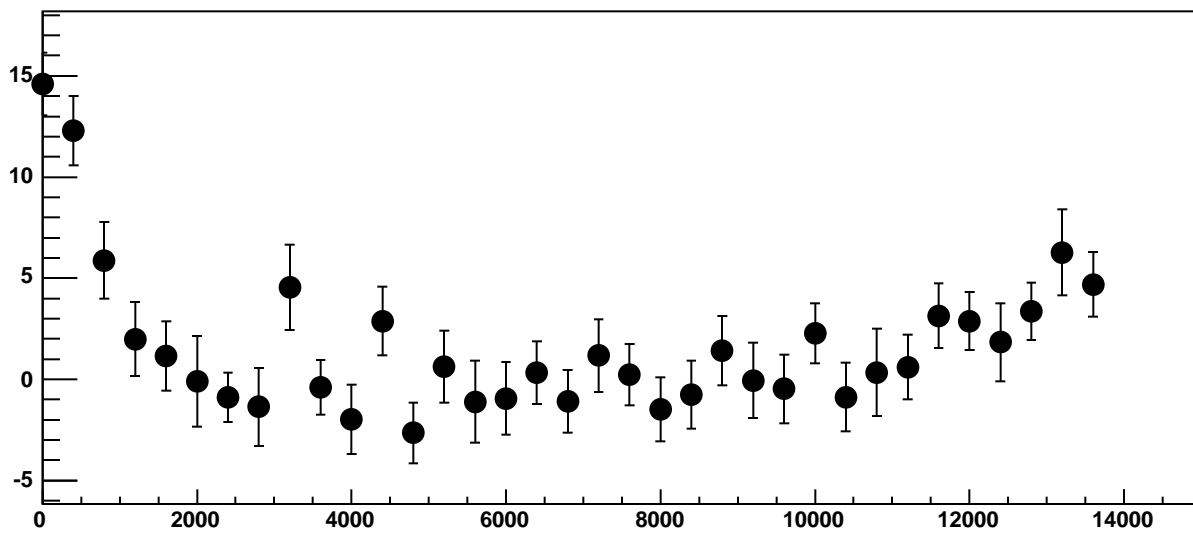


χ^2 / ndf 20.93 / 23
p0 125.6 ± 0.7818
p1 0.01406 ± 0.0001185

Chip 8, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

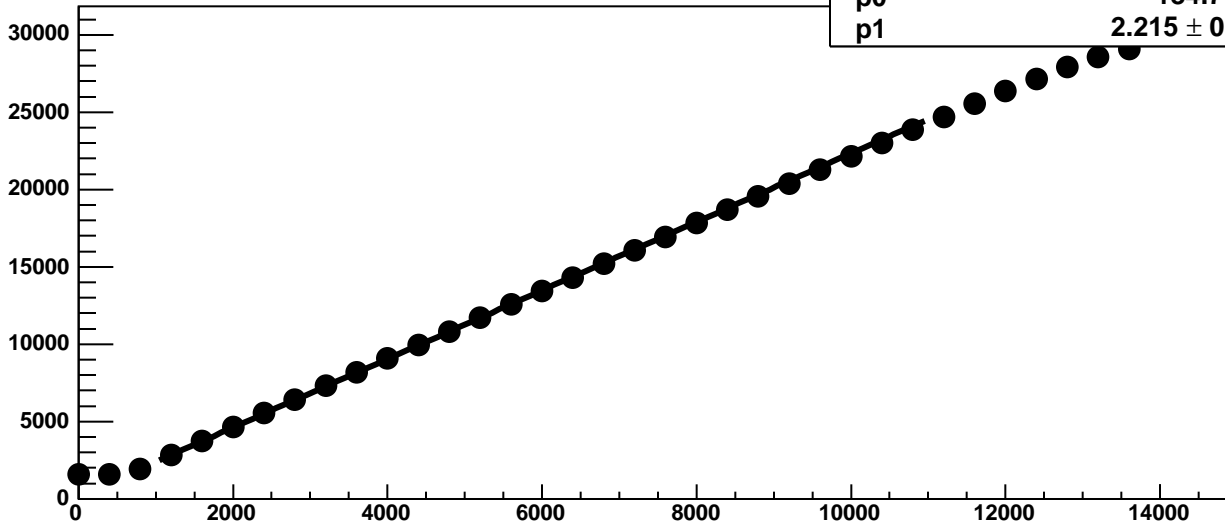


Chip 8, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

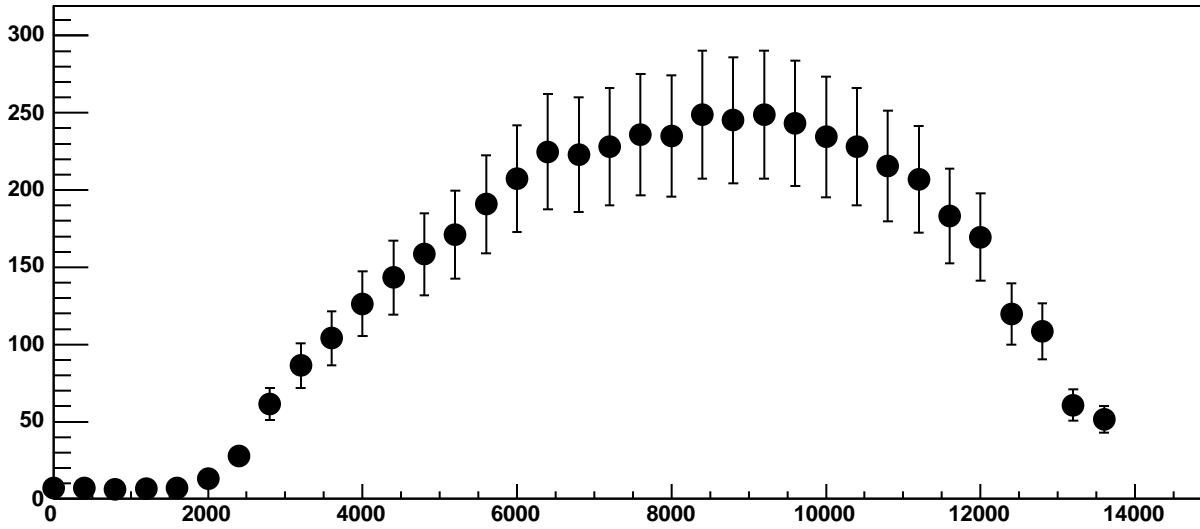


Chip 8, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC

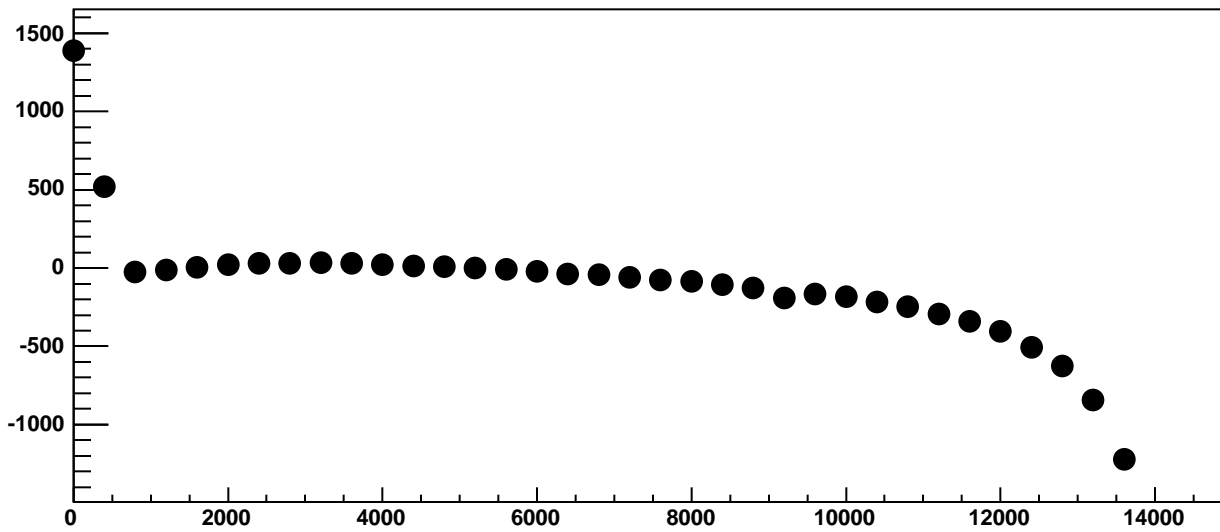
χ^2 / ndf 250.7 / 23
p0 184.7 ± 2.682
p1 2.215 ± 0.001615



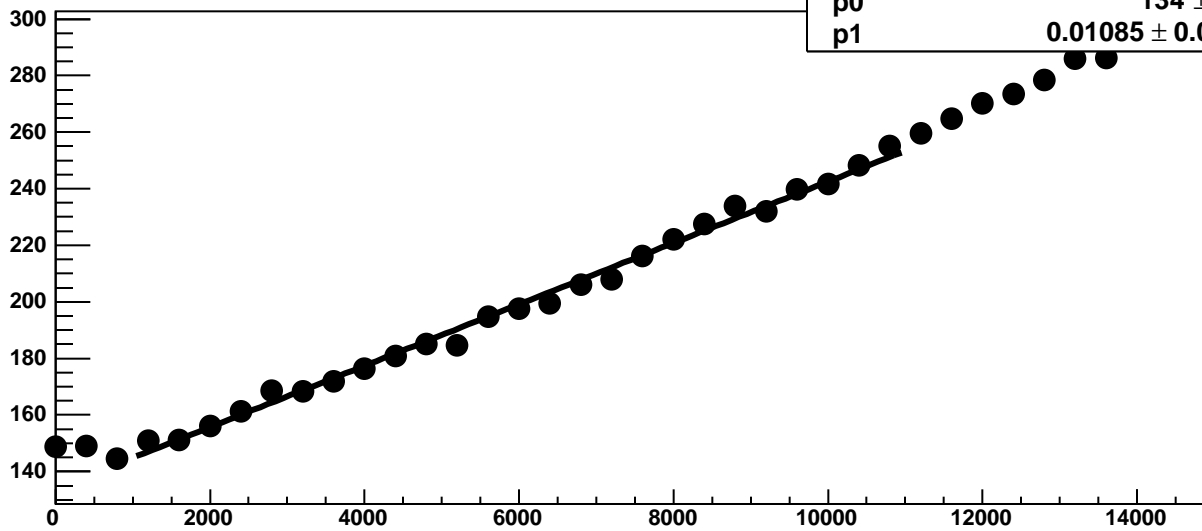
Chip 8, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC

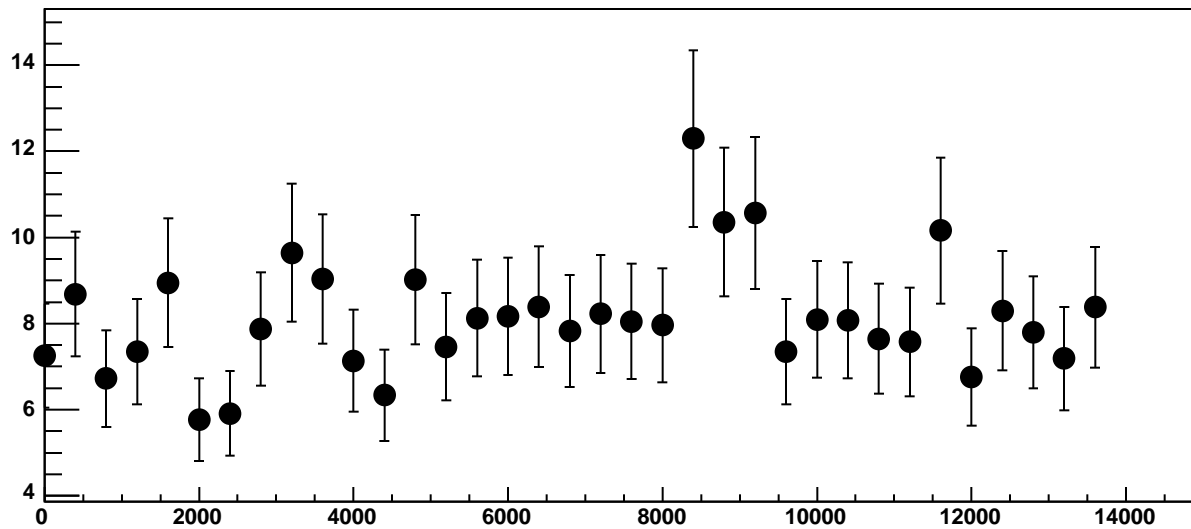


Chip 8, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

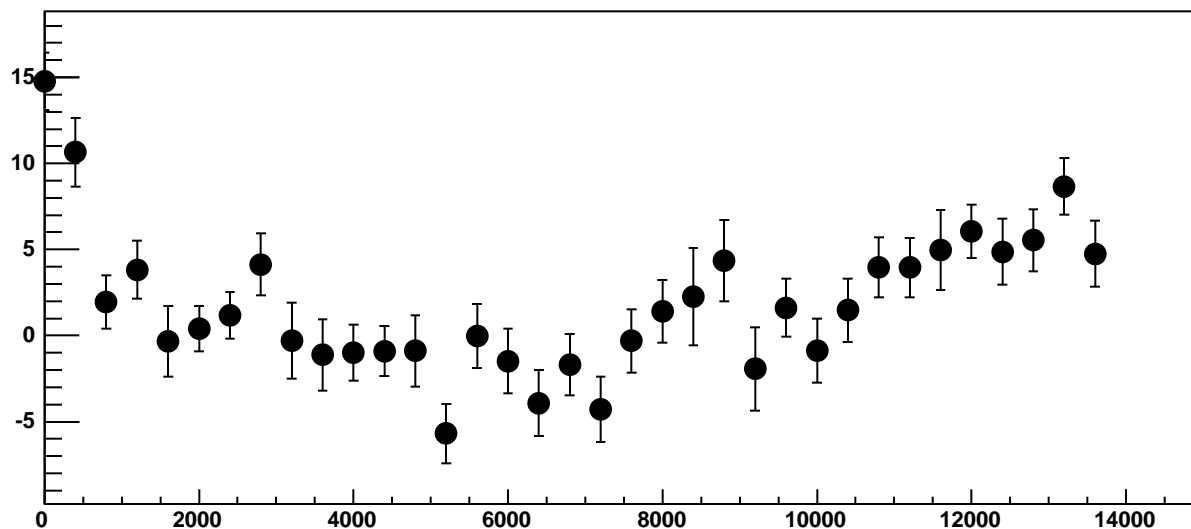


χ^2 / ndf 46.49 / 23
p0 134 ± 0.7813
p1 0.01085 ± 0.0001231

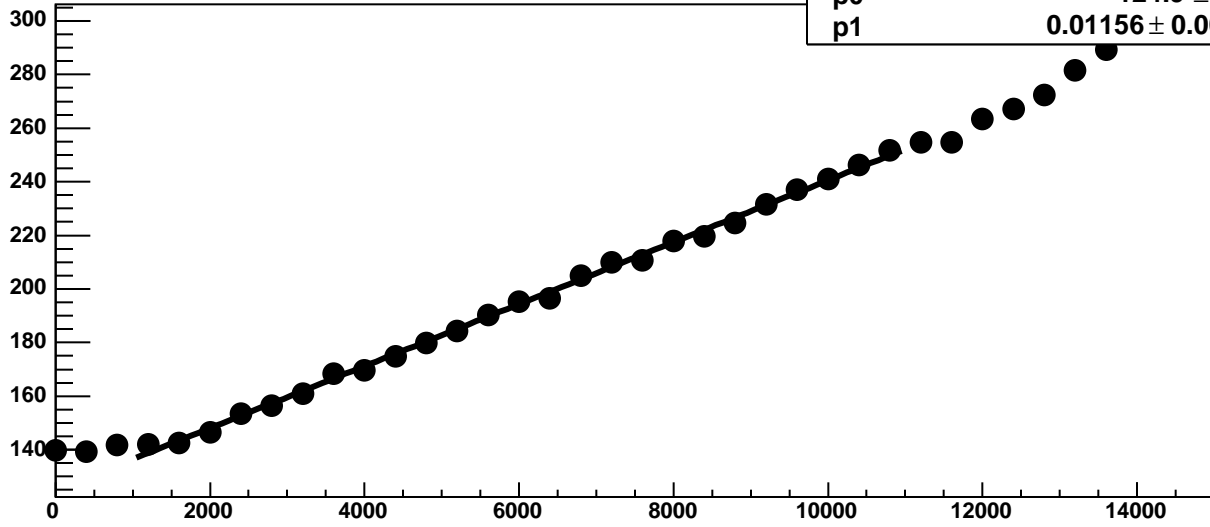
Chip 8, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

18.22 / 23

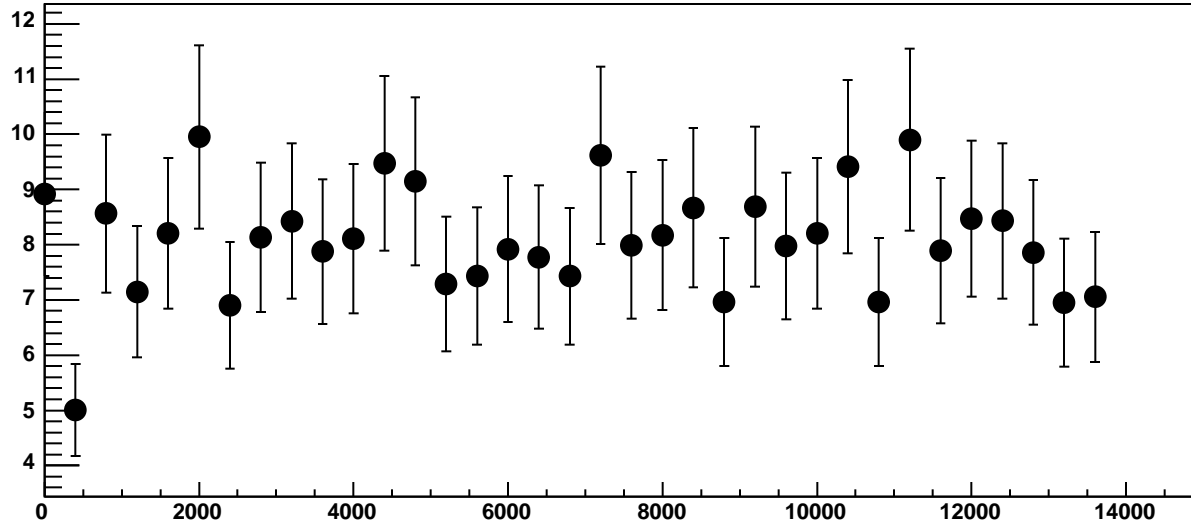
p0

124.9 ± 0.8433

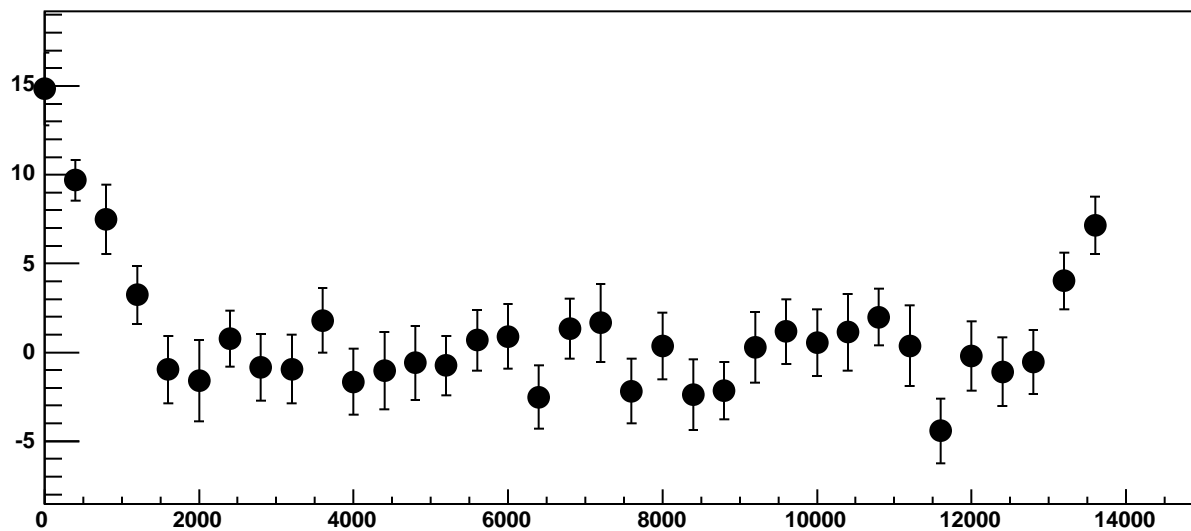
p1

0.01156 ± 0.0001265

Chip 8, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

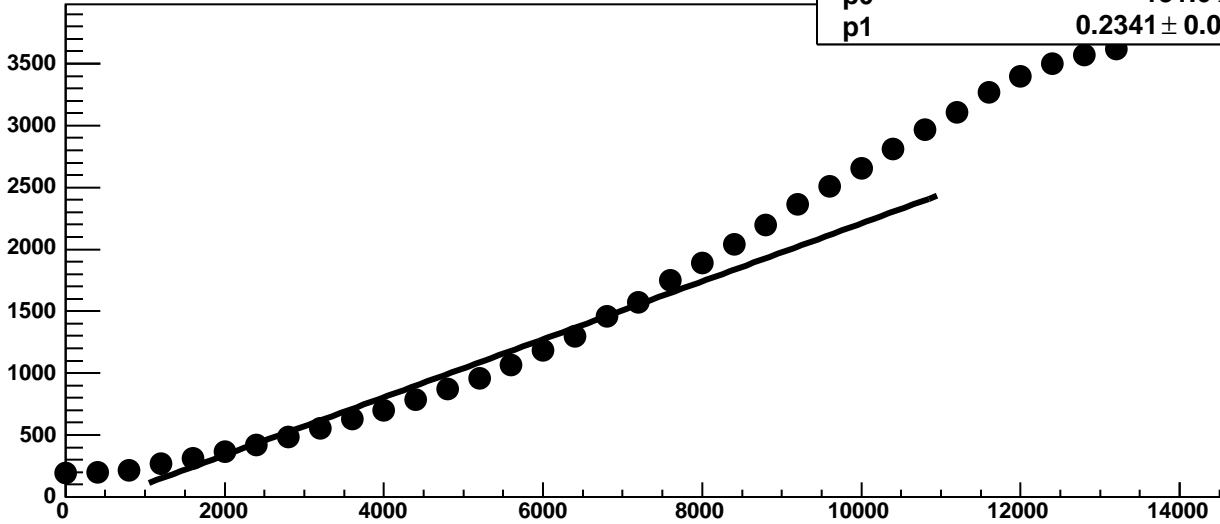


Chip 8, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

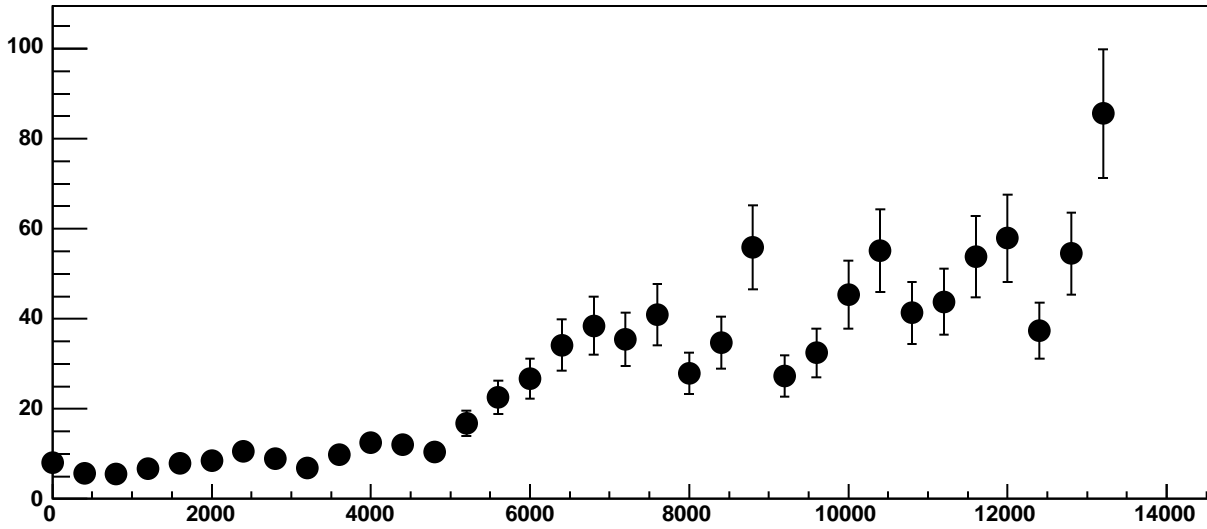


Chip 8, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC

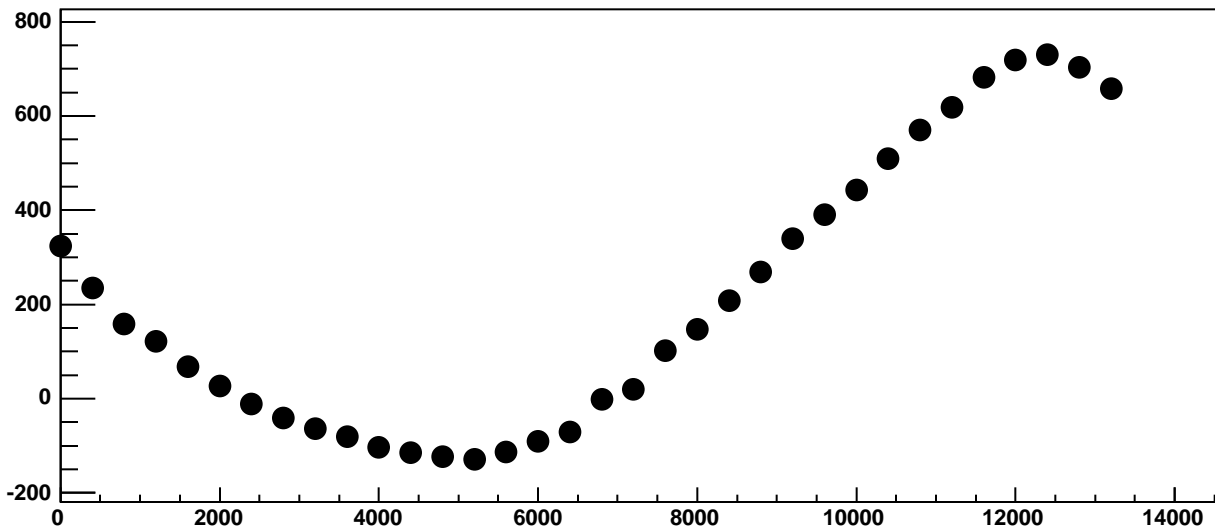
χ^2 / ndf 3.325e+04 / 23
p0 -131.6 ± 1.196
p1 0.2341 ± 0.0003251



Chip 8, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC

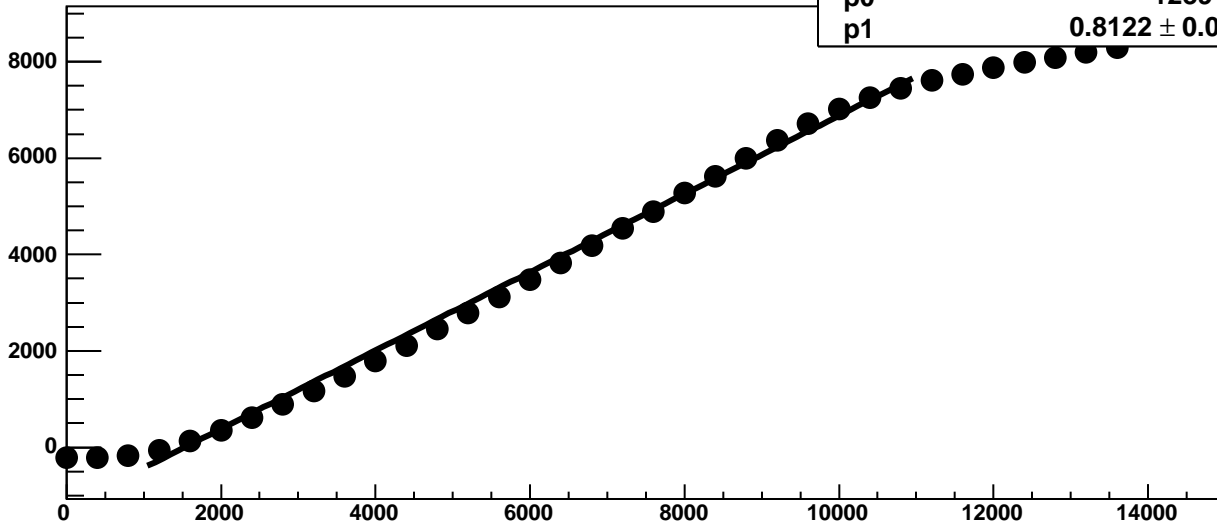


Chip 8, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC

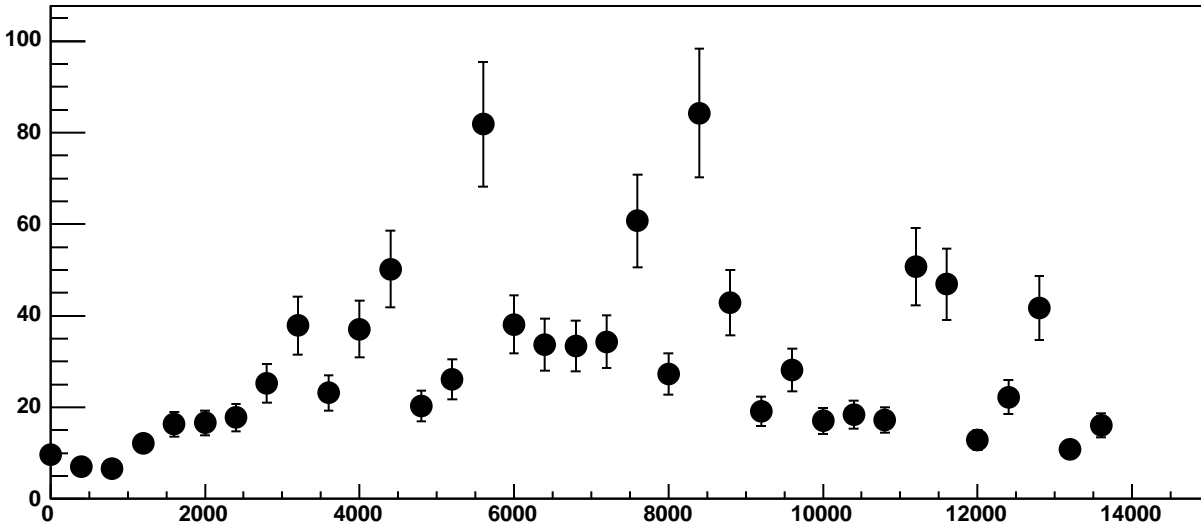


Chip 9, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

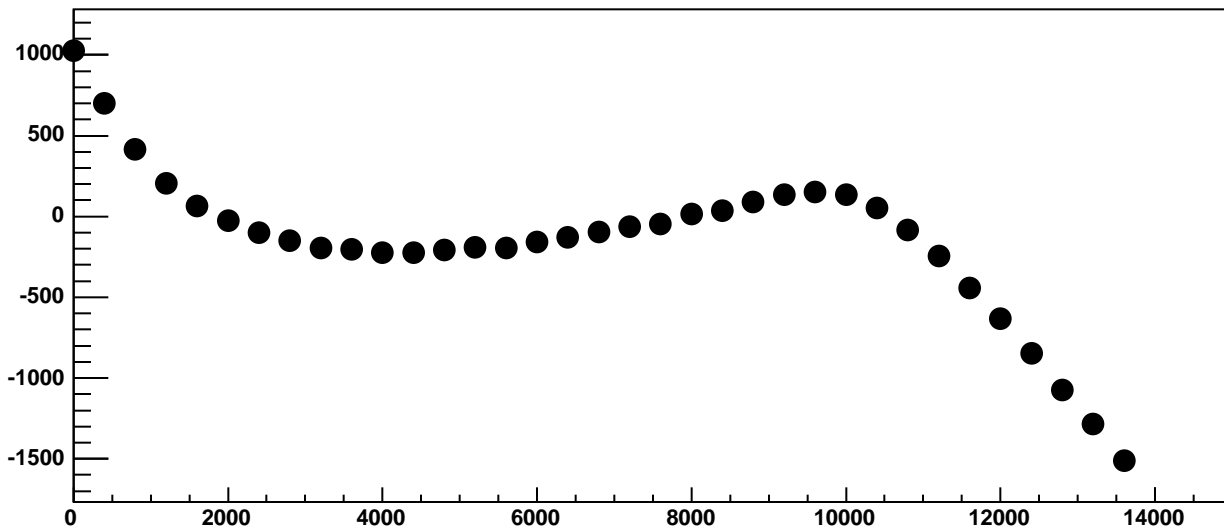
χ^2 / ndf 1.759e+04 / 23
p0 -1239 ± 1.917
p1 0.8122 ± 0.0002958



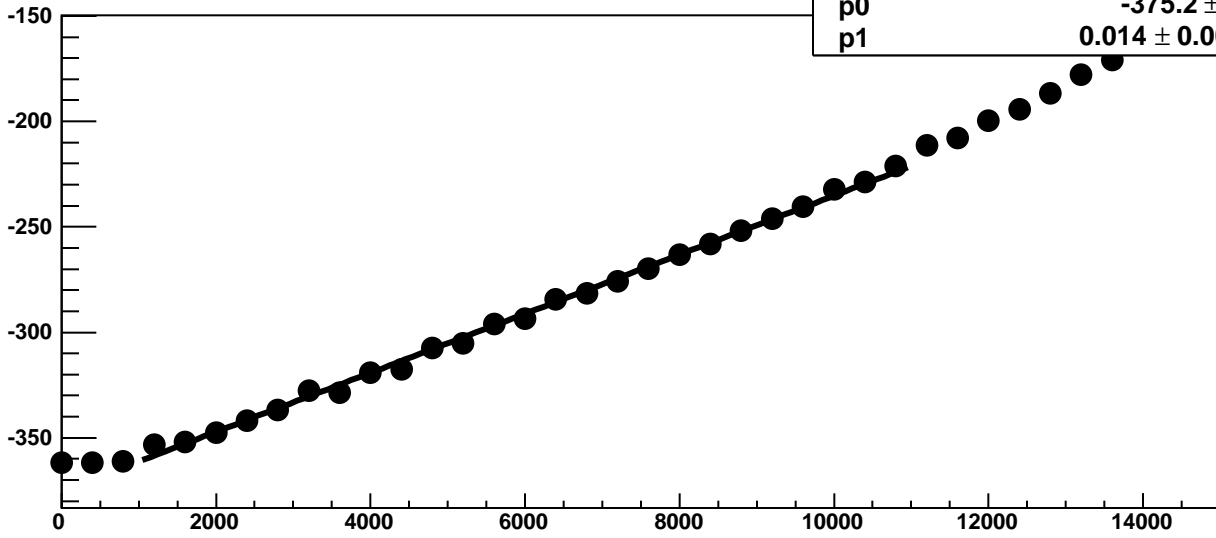
Chip 9, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

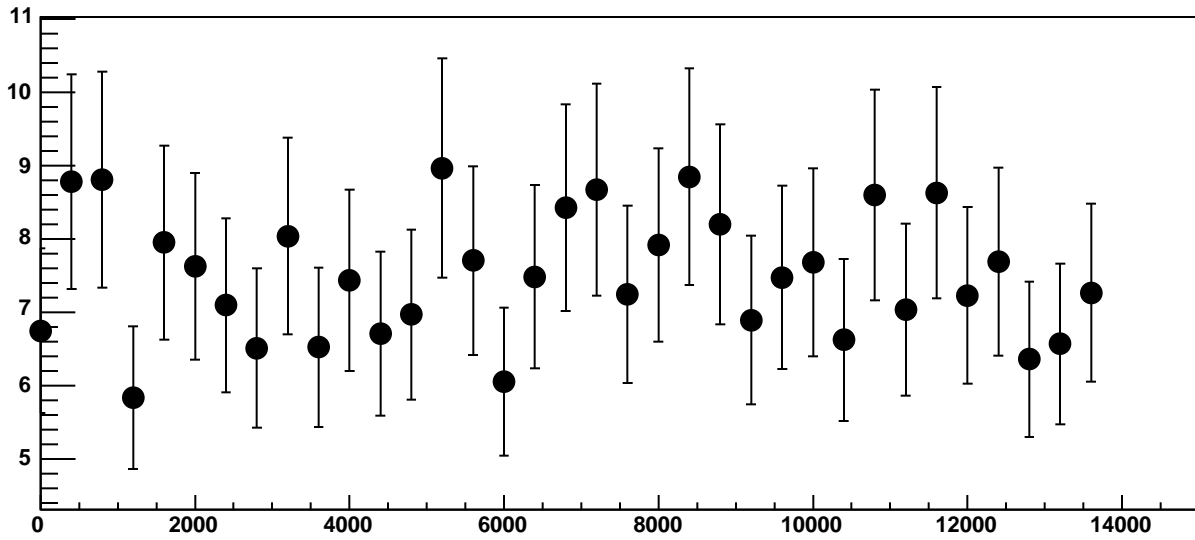


Chip 9, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

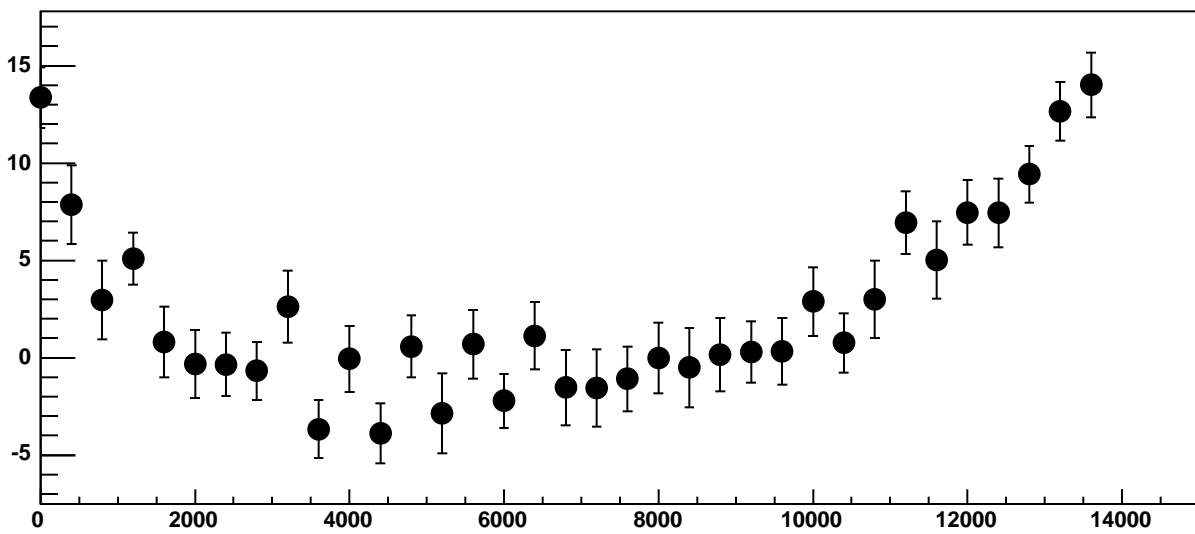


χ^2 / ndf 41.48 / 23
p0 -375.2 ± 0.7461
p1 0.014 ± 0.0001153

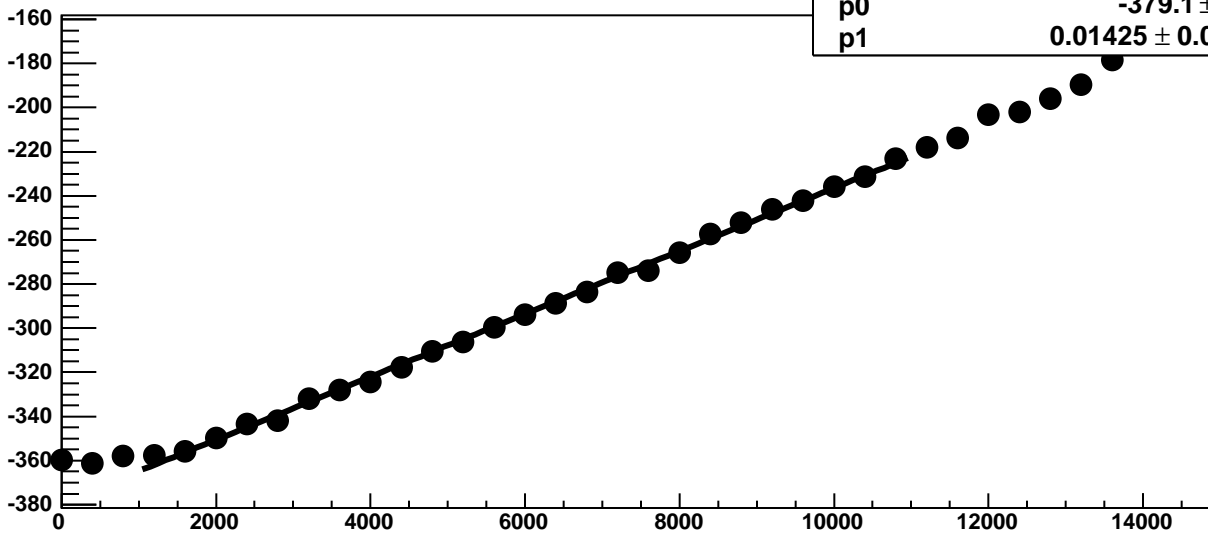
Chip 9, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 9, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

25 / 23

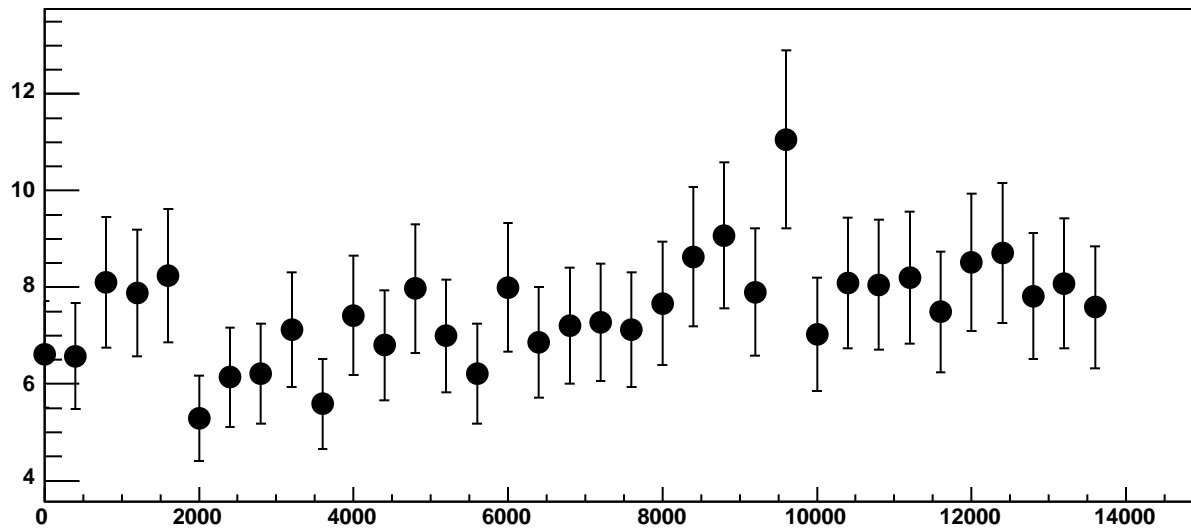
p0

-379.1 ± 0.7279

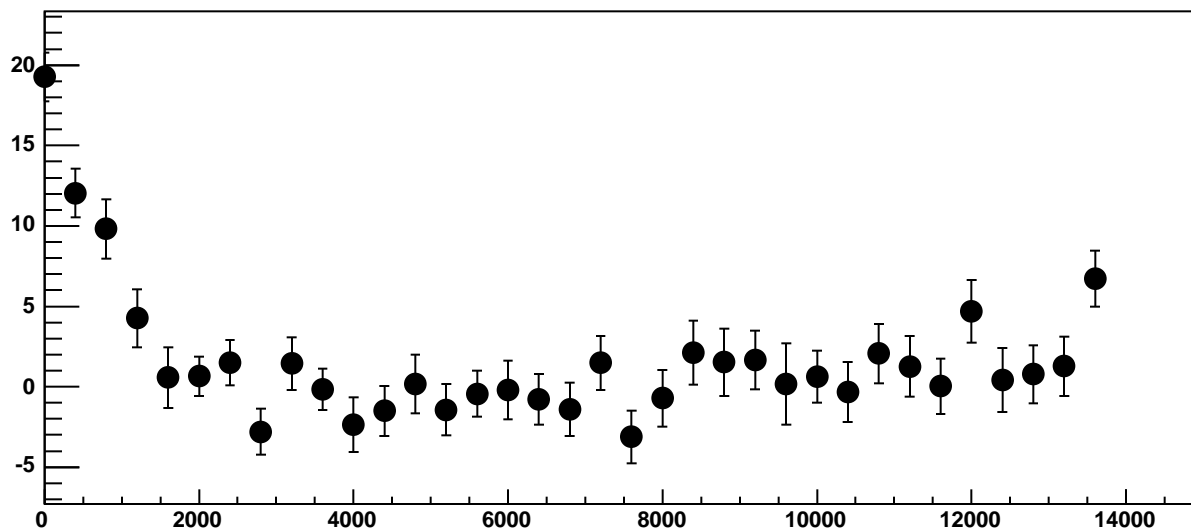
p1

0.01425 ± 0.0001175

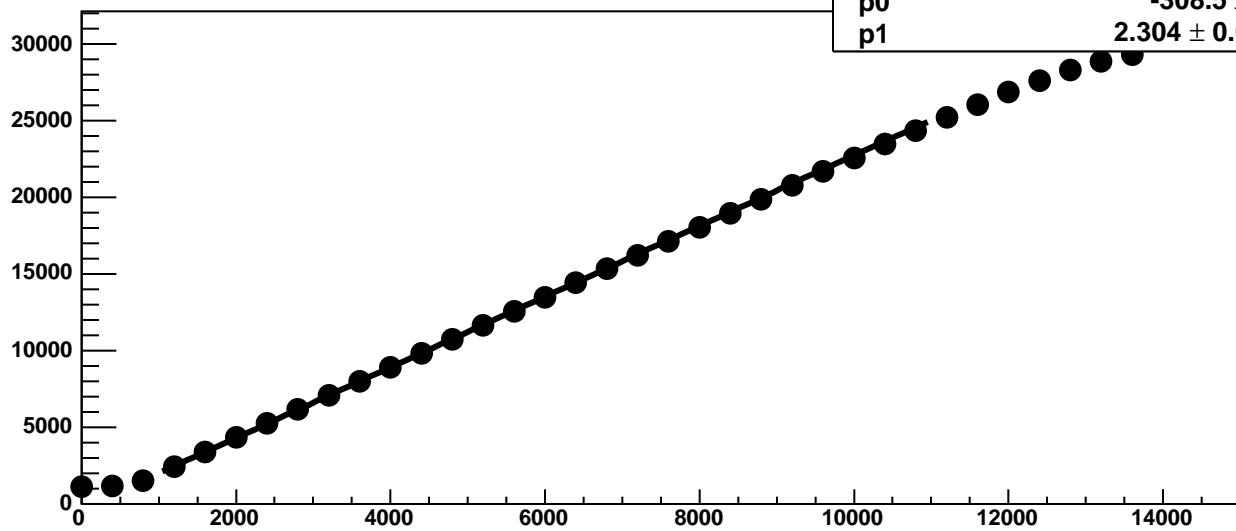
Chip 9, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



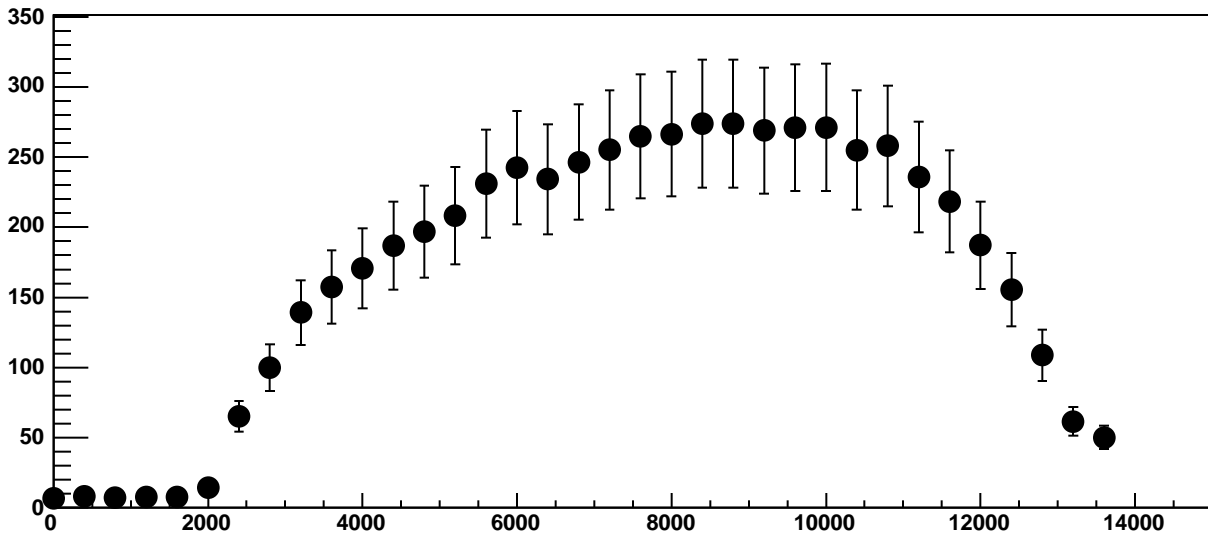
Chip 9, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



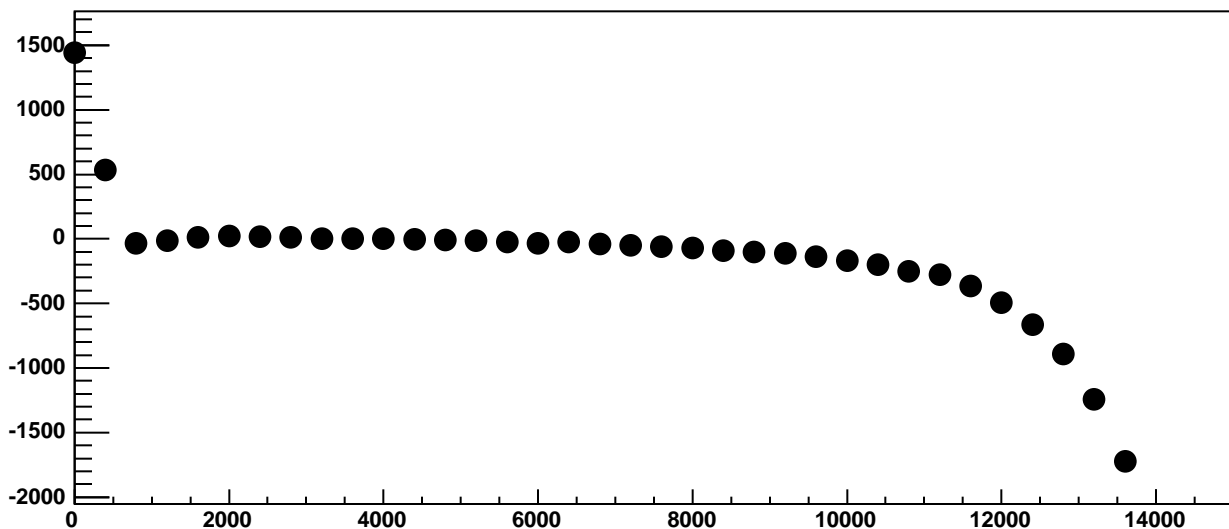
Chip 9, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC



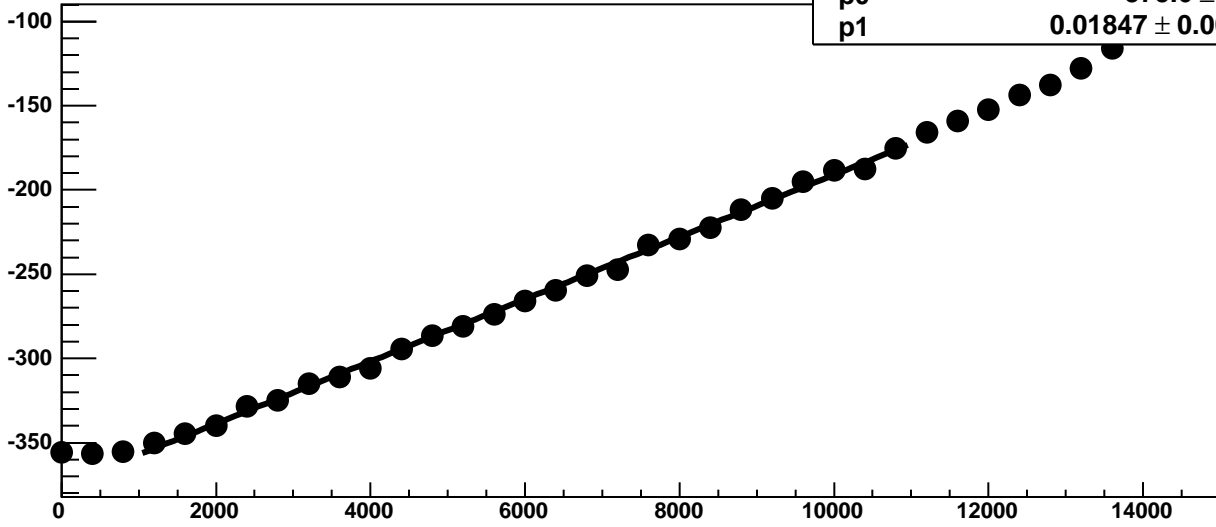
Chip 9, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC

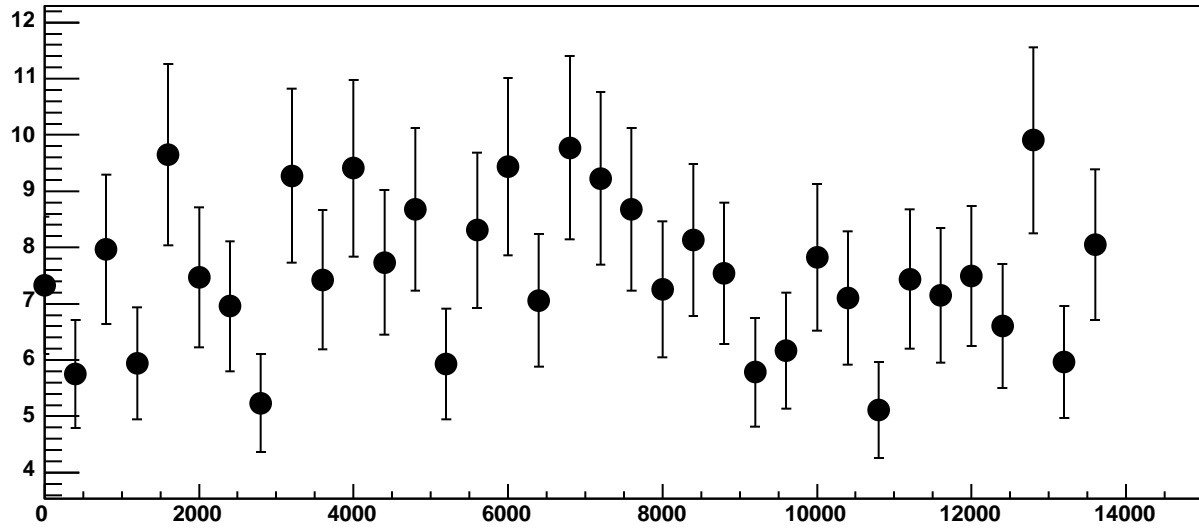


Chip 9, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

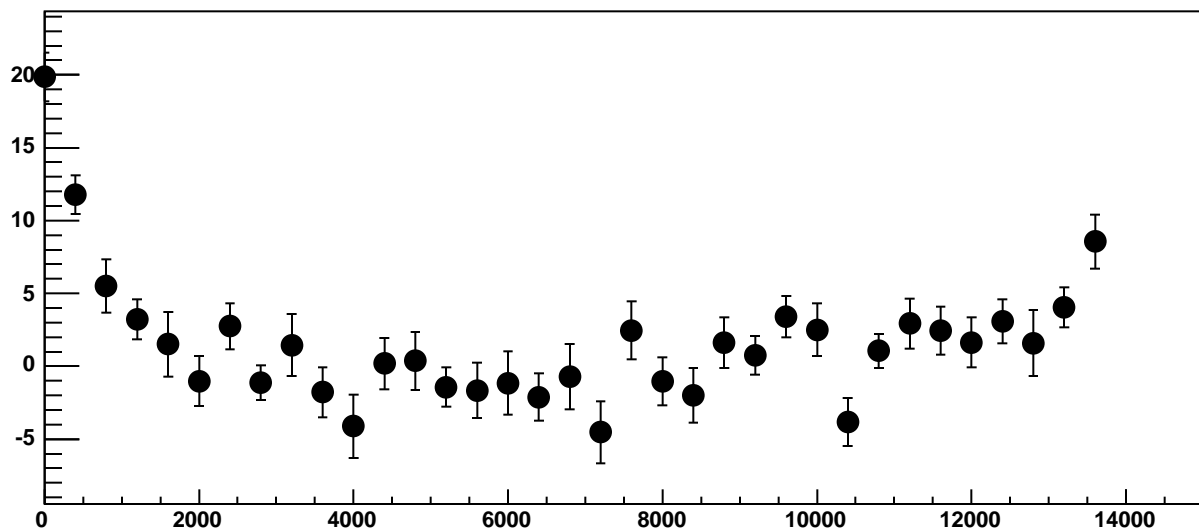


χ^2 / ndf 42.37 / 23
p0 -375.6 ± 0.7377
p1 0.01847 ± 0.0001066

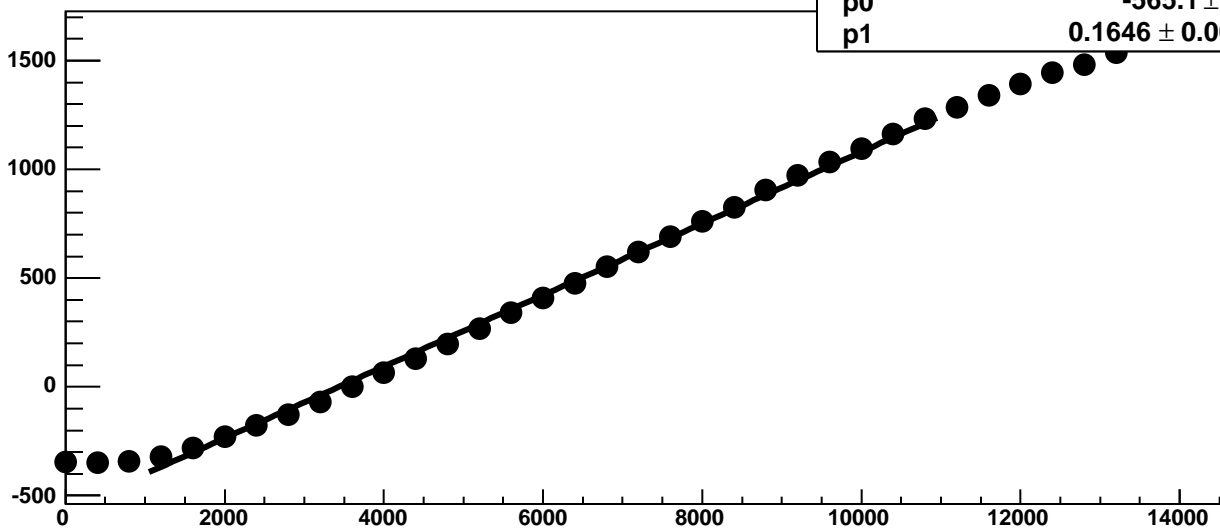
Chip 9, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



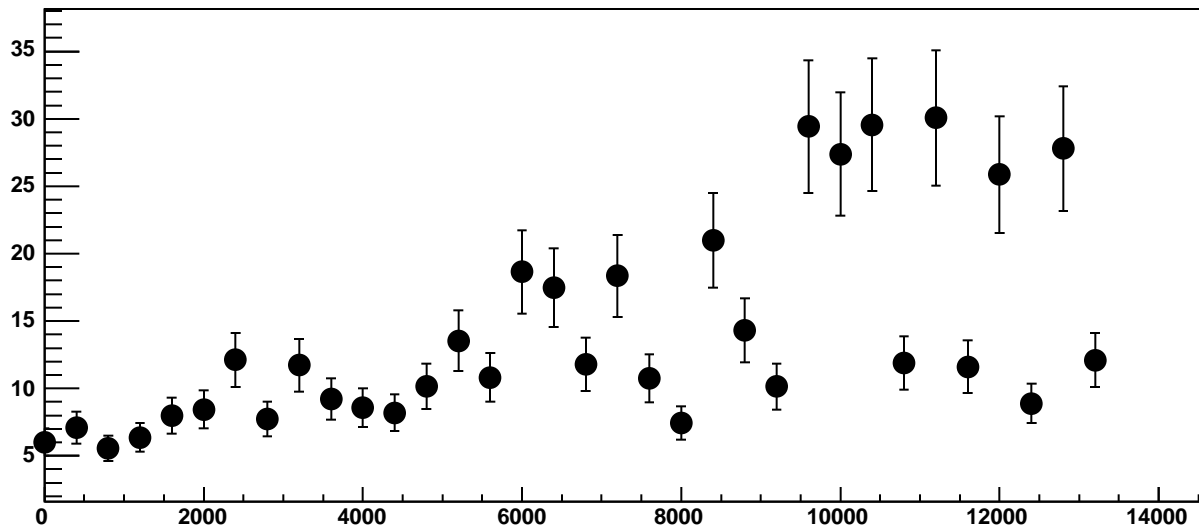
Chip 9, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



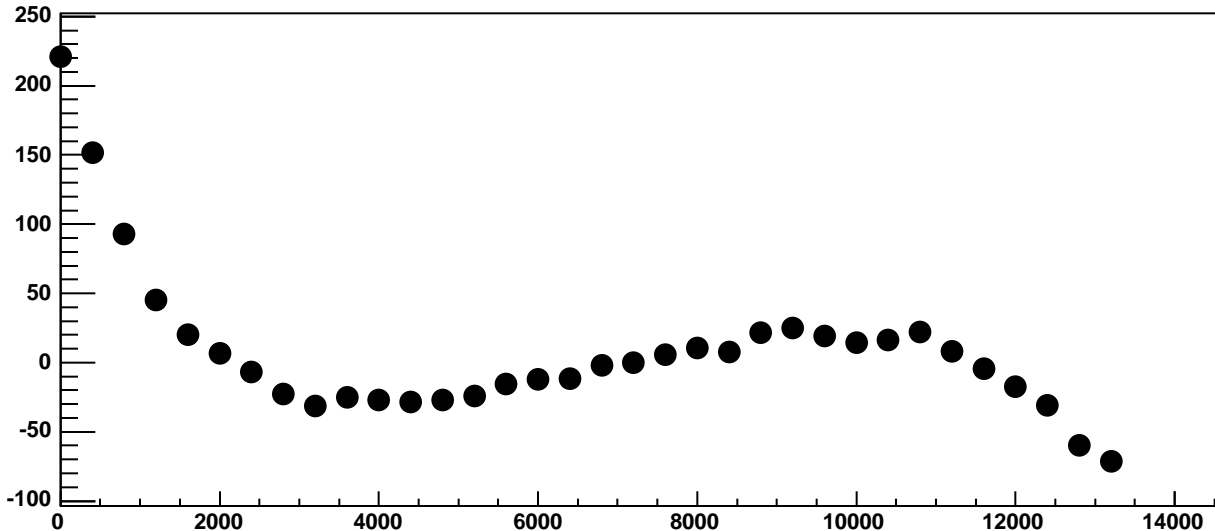
Chip 9, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



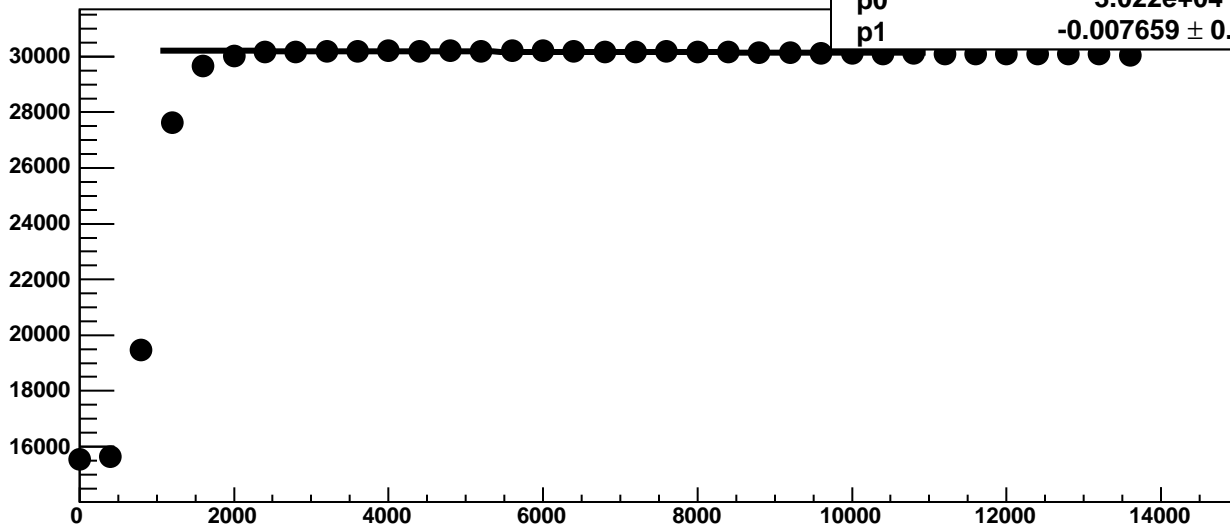
Chip 9, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



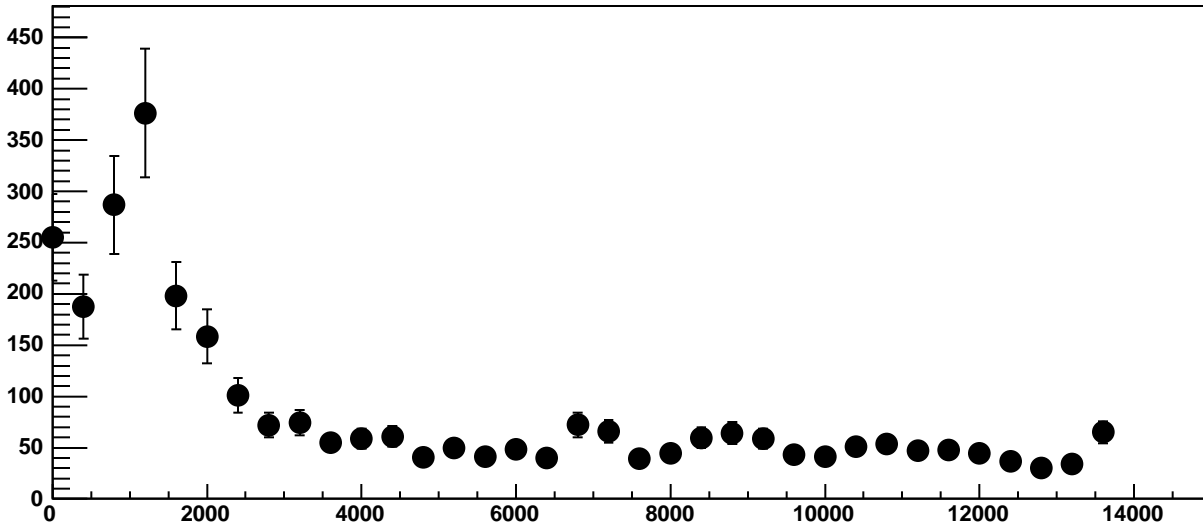
Chip 9, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



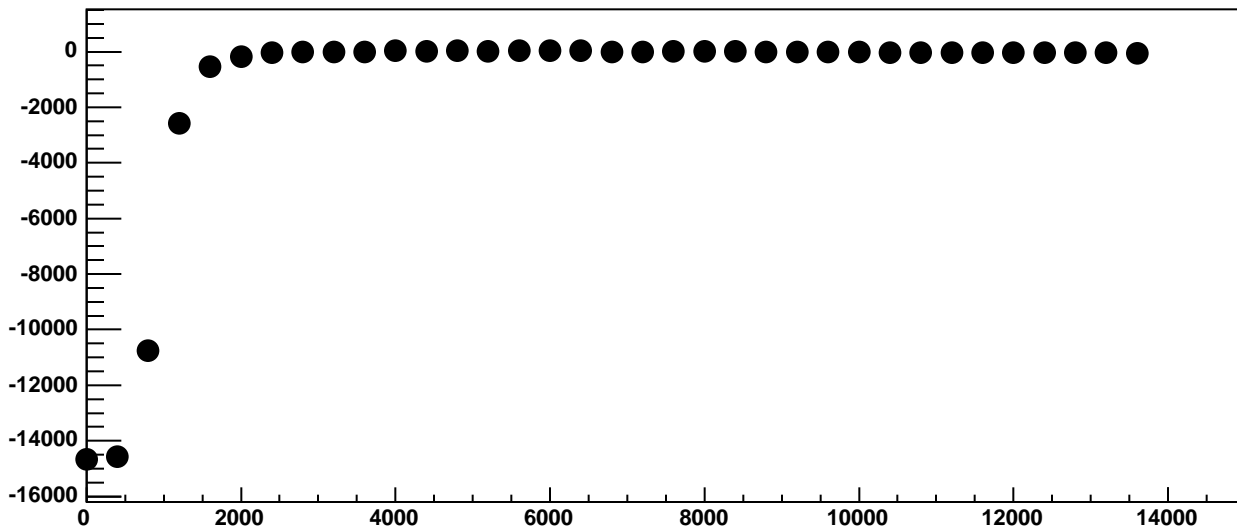
Chip 9, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



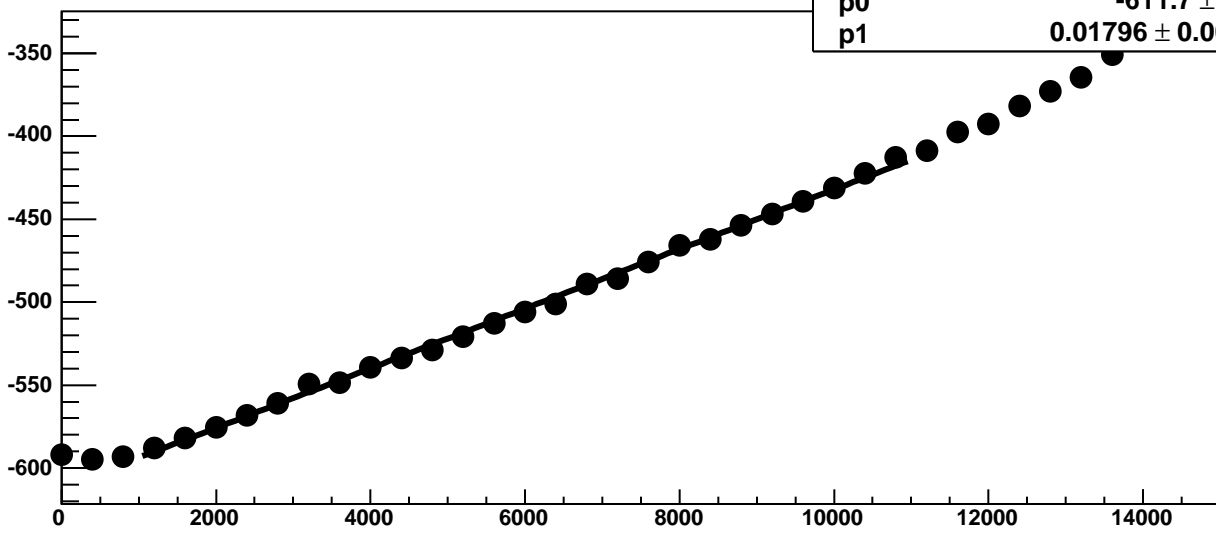
Chip 9, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC

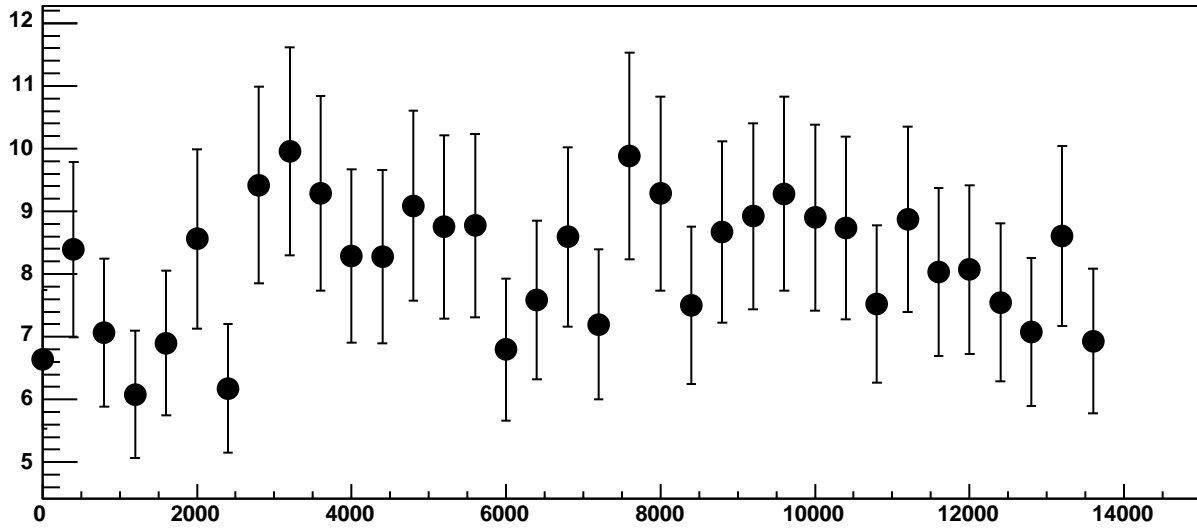


Chip 9, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

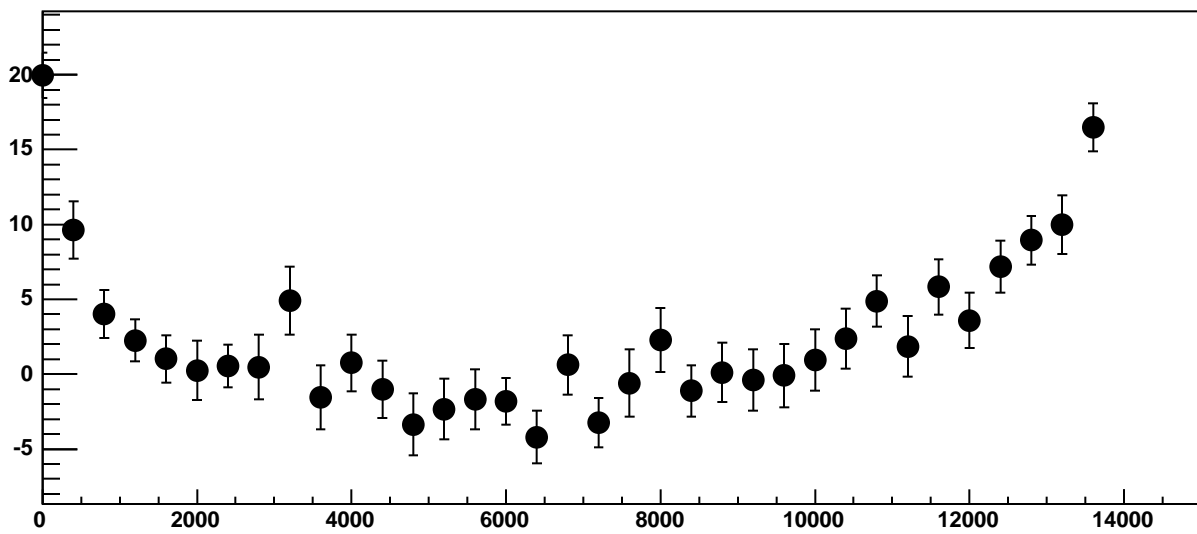


χ^2 / ndf 35.85 / 23
p0 -611.7 ± 0.8018
p1 0.01796 ± 0.0001244

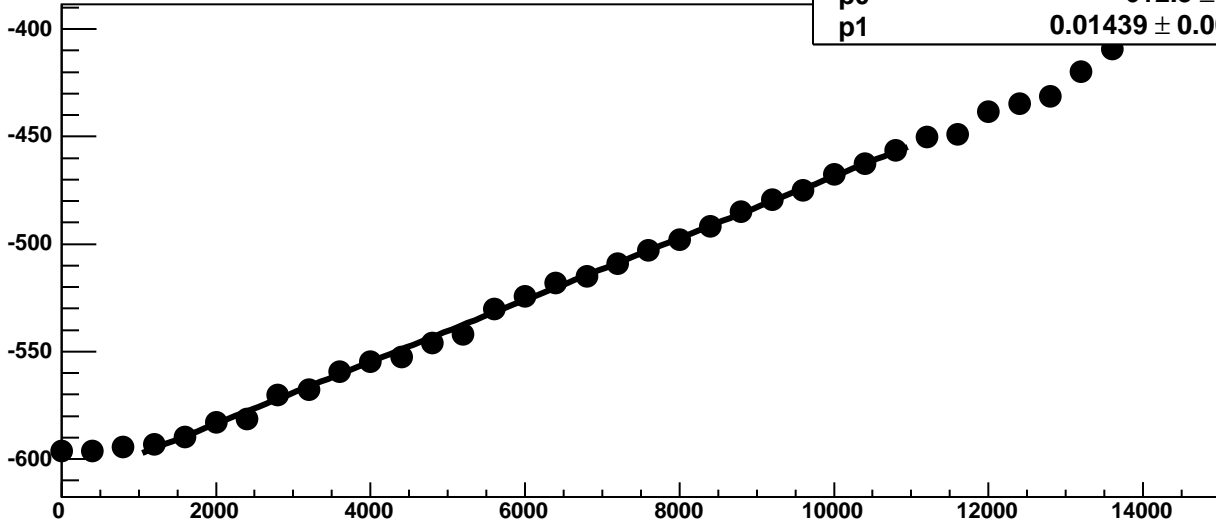
Chip 9, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



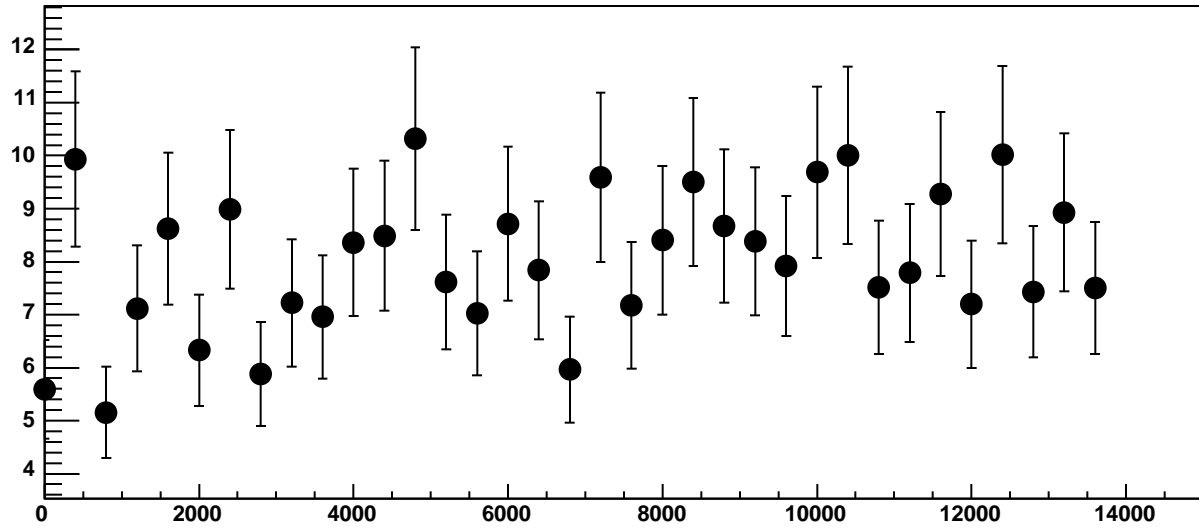
Chip 9, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



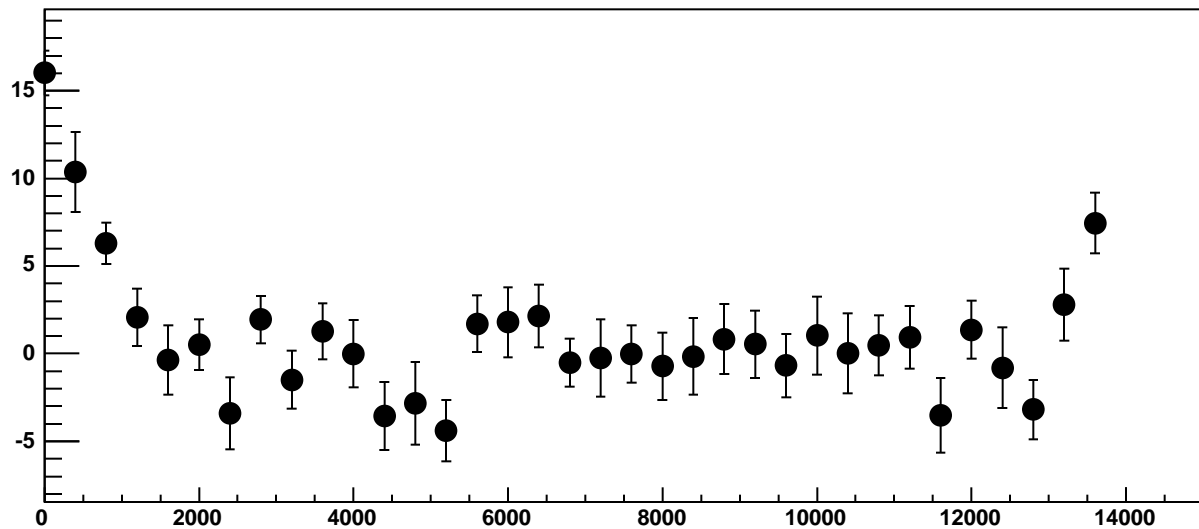
Chip 9, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



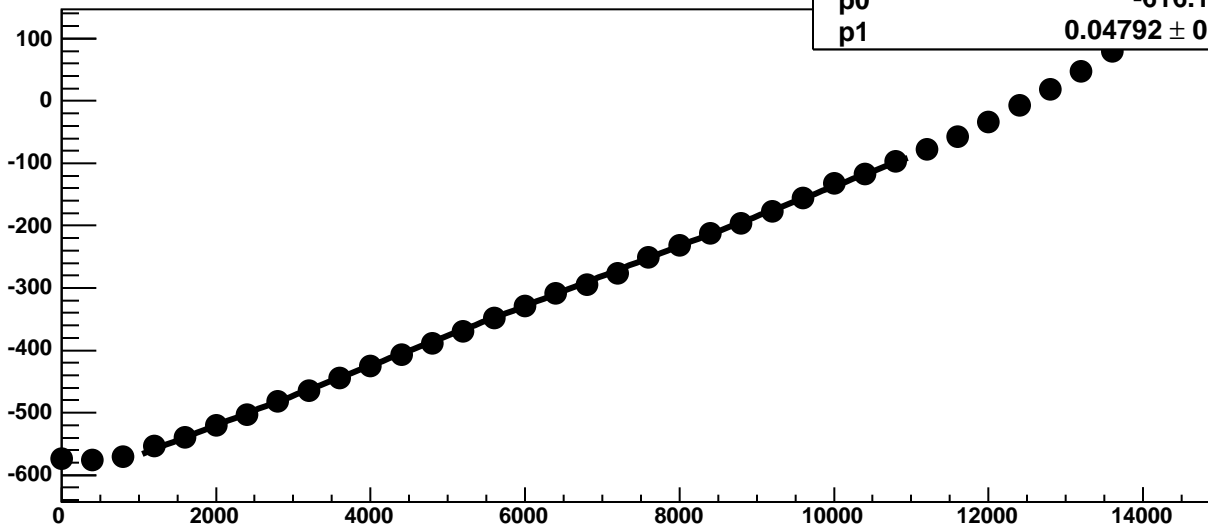
Chip 9, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



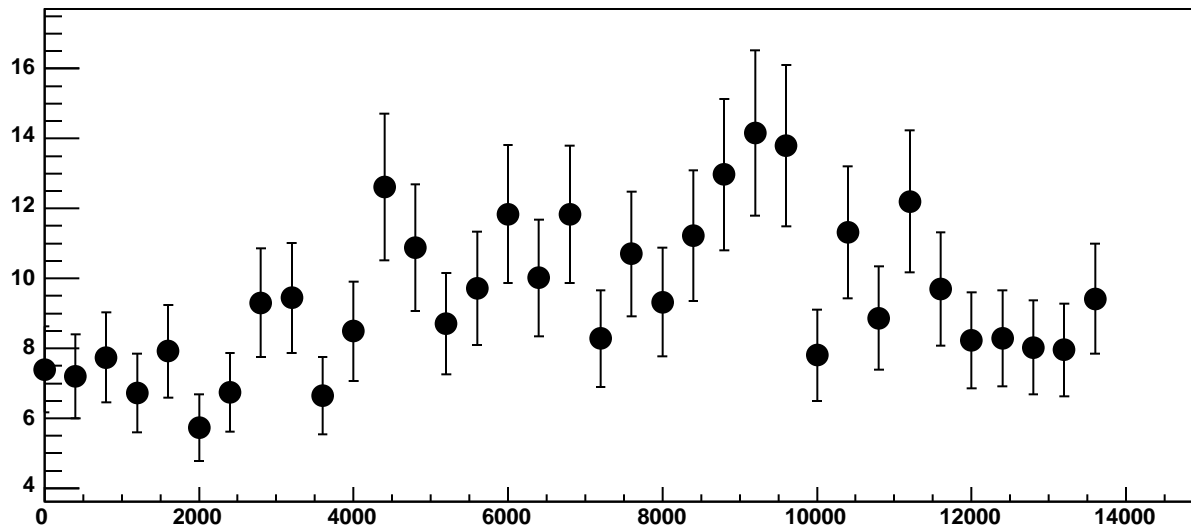
Chip 9, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



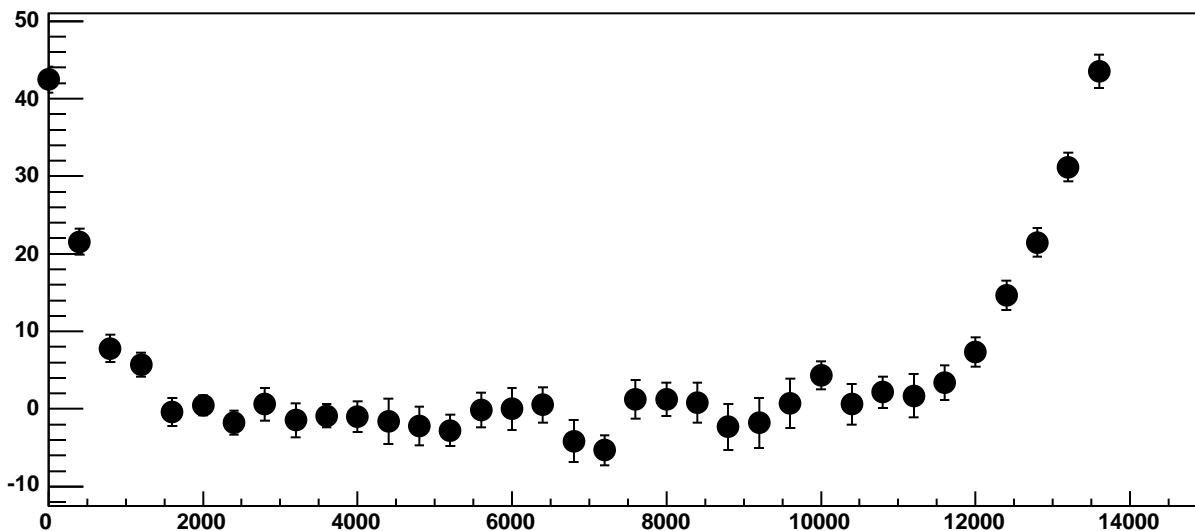
Chip 9, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC



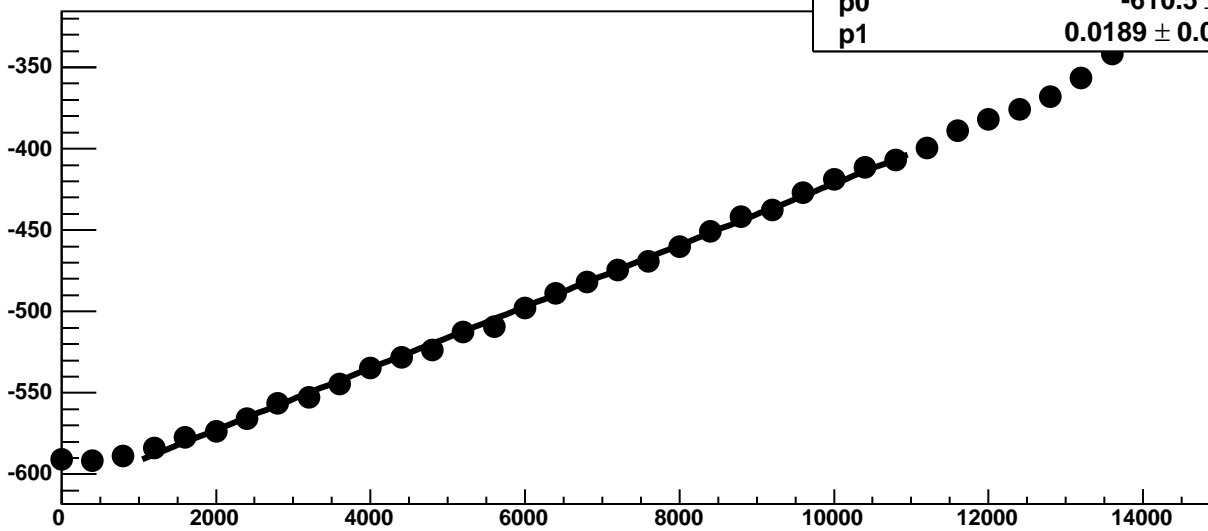
Chip 9, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC

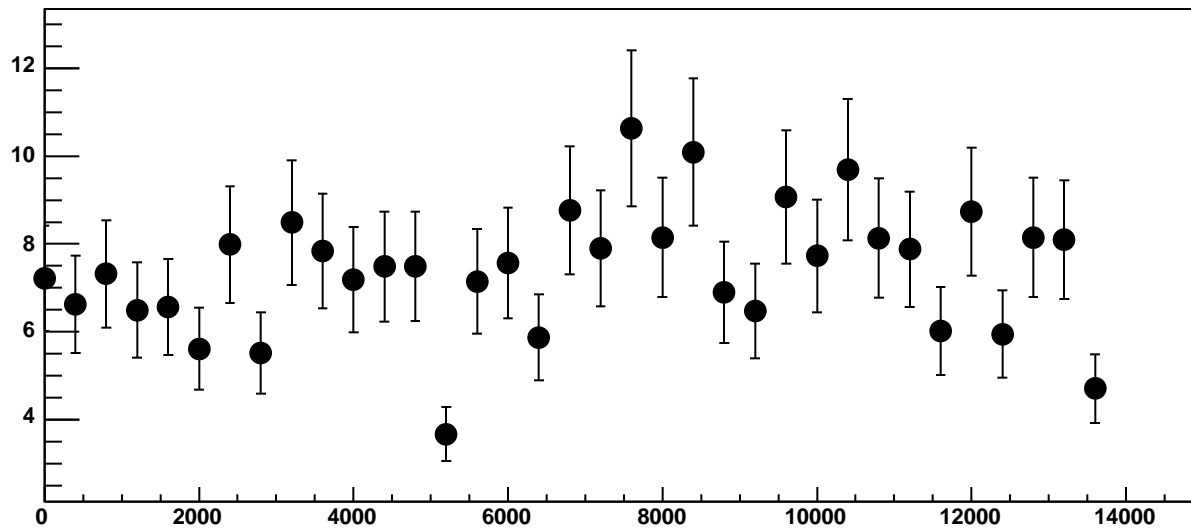


Chip 9, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

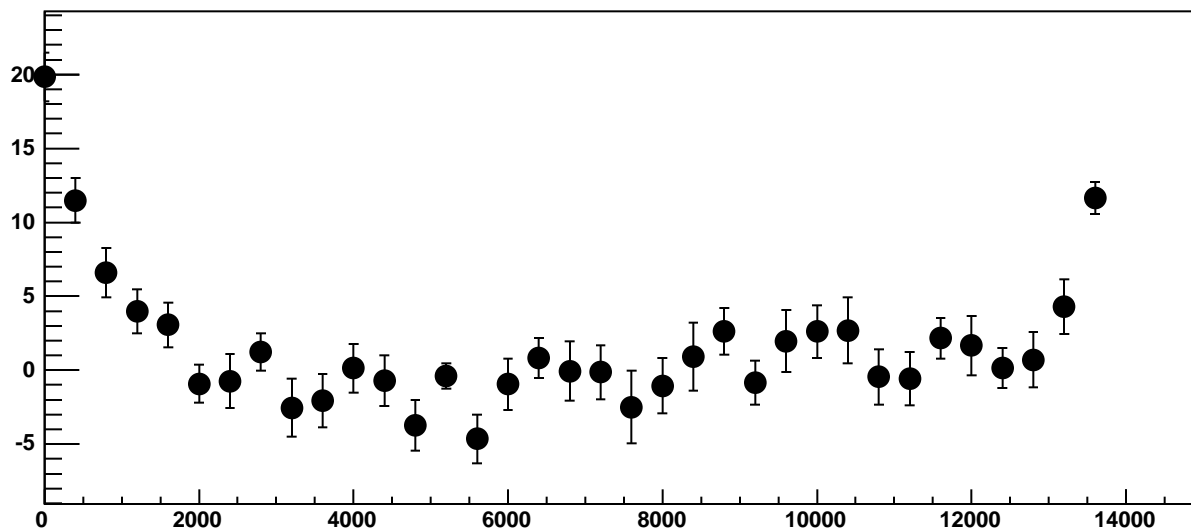


χ^2 / ndf 38.89 / 23
p0 -610.5 ± 0.7151
p1 0.0189 ± 0.0001165

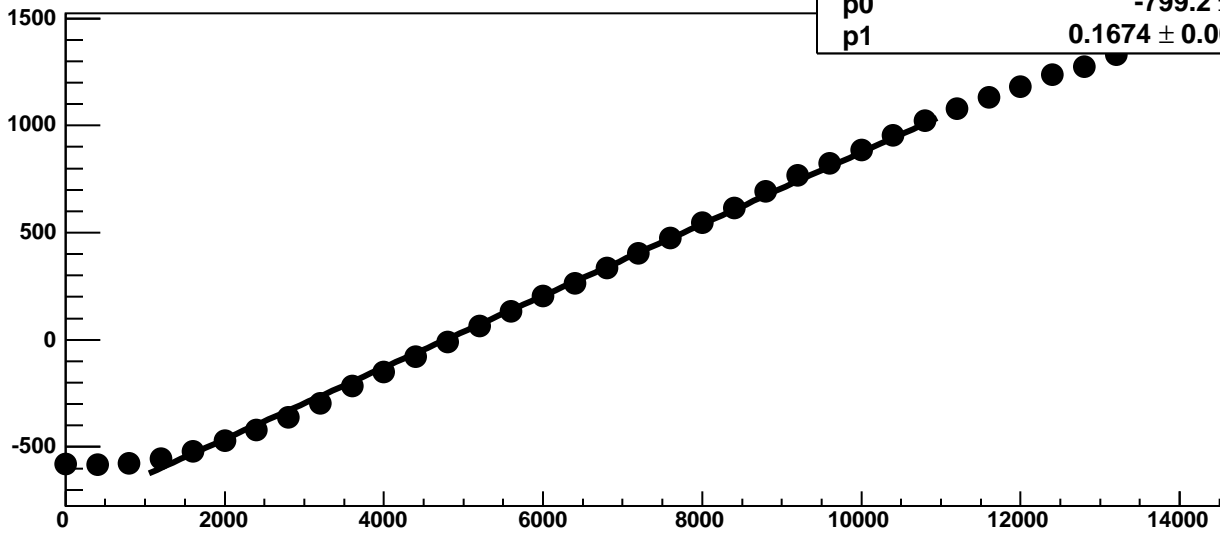
Chip 9, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



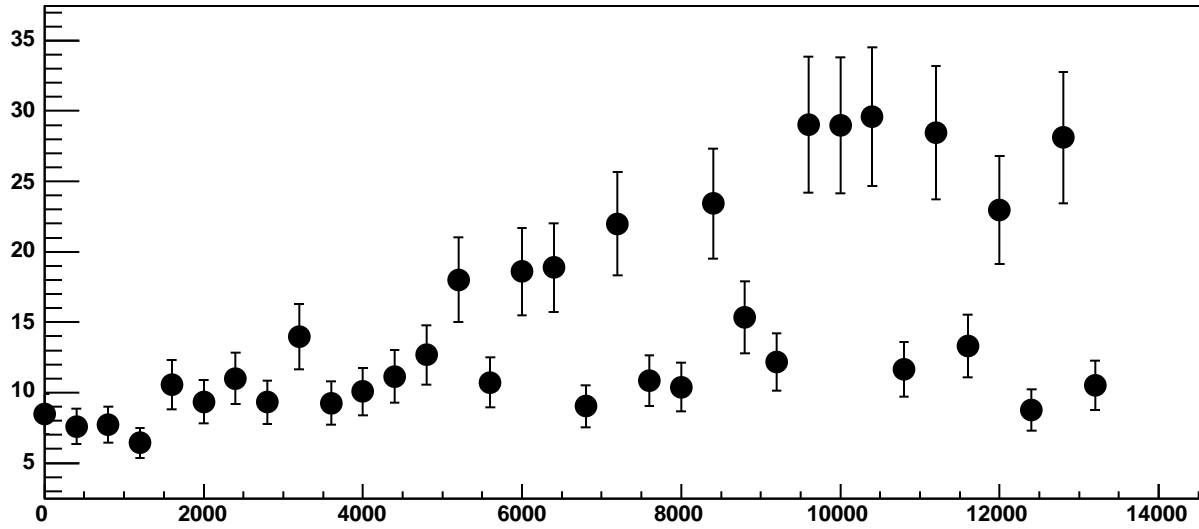
Chip 9, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



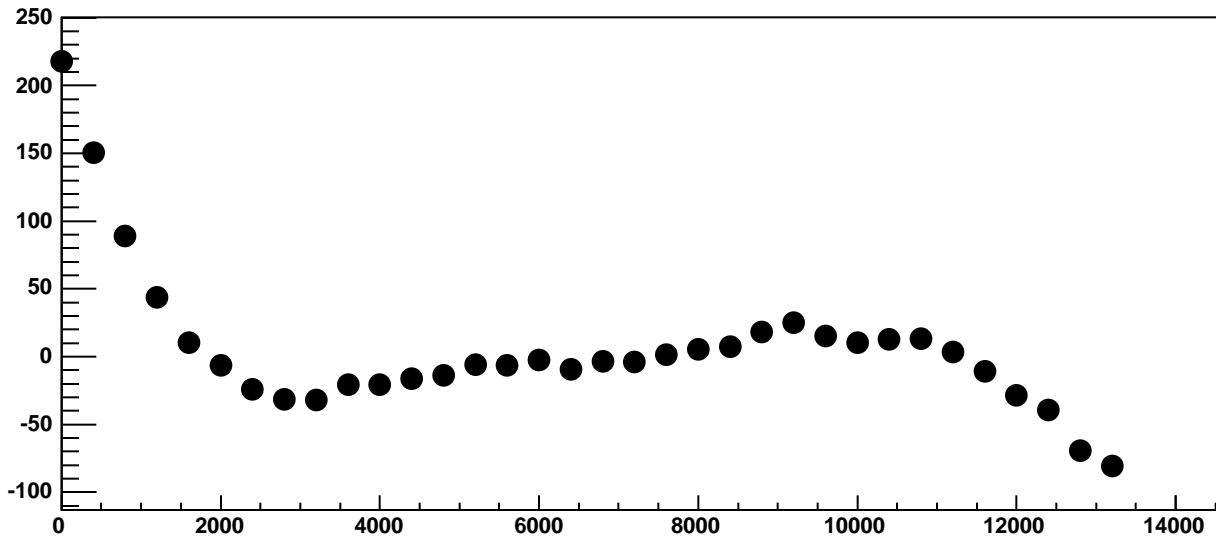
Chip 9, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



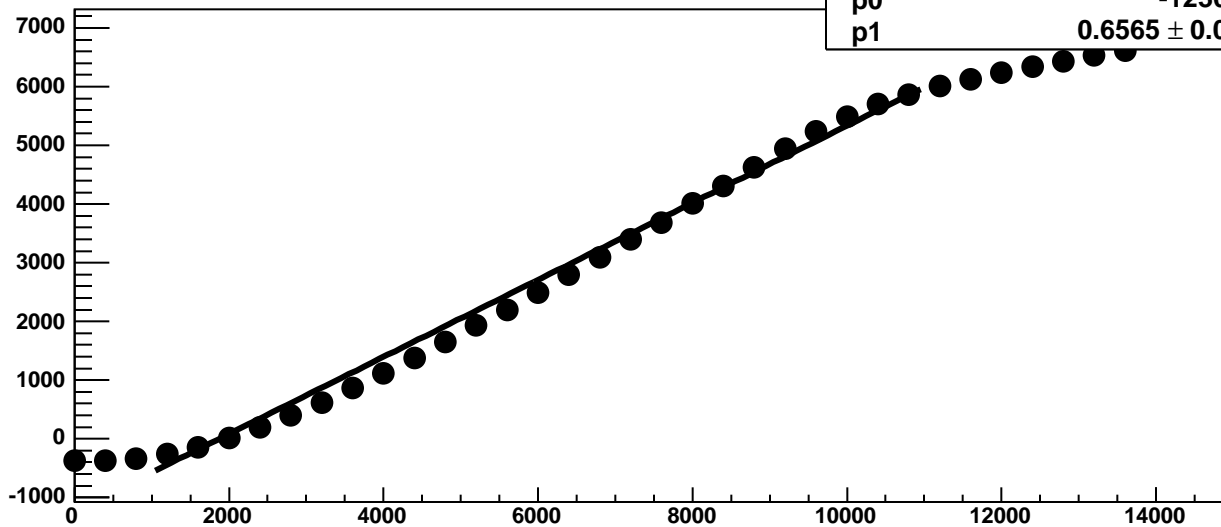
Chip 9, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



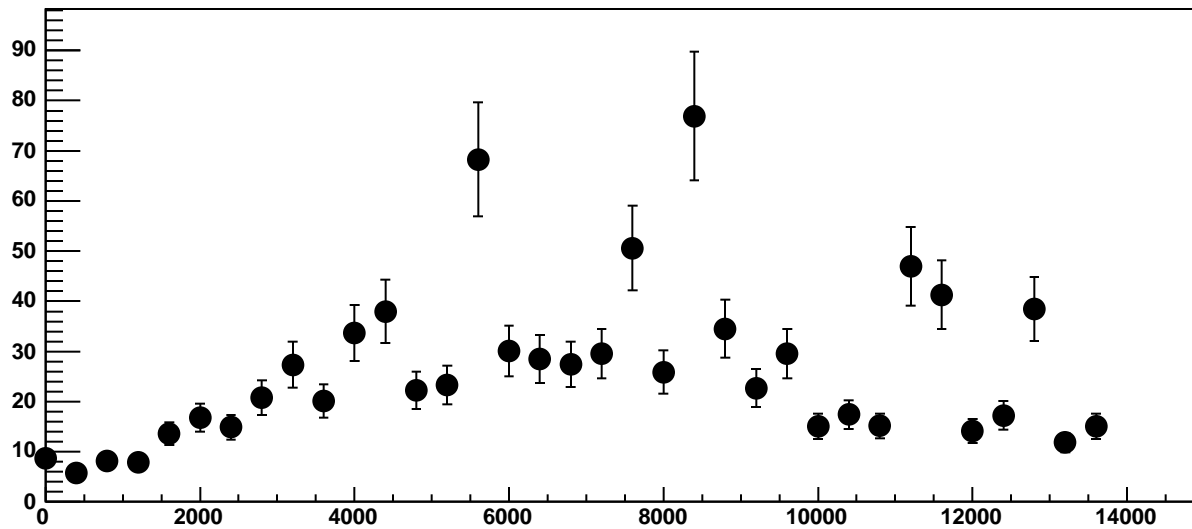
Chip 9, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



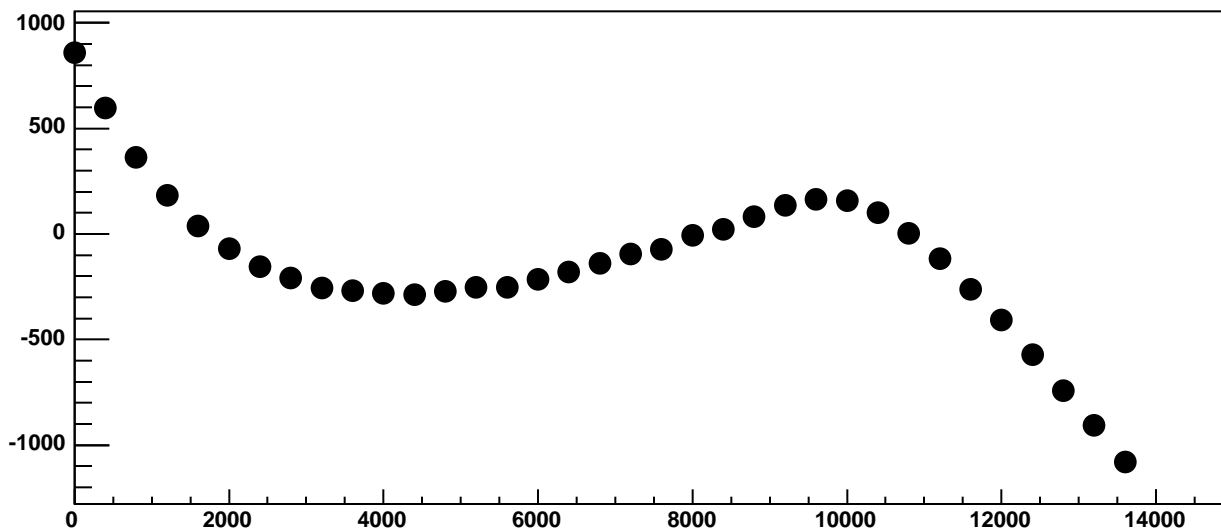
Chip 9, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC



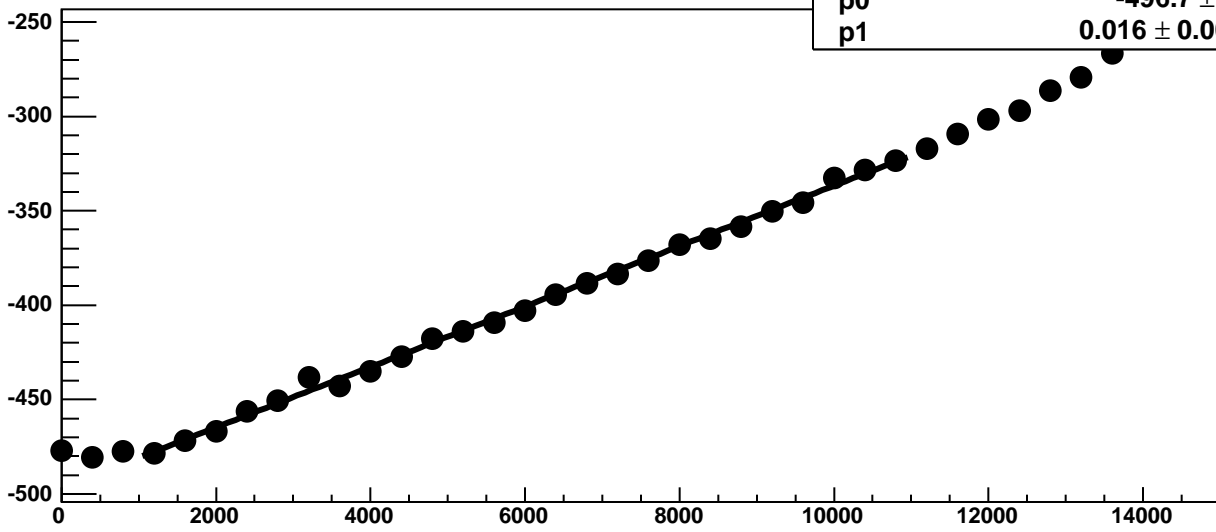
Chip 9, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

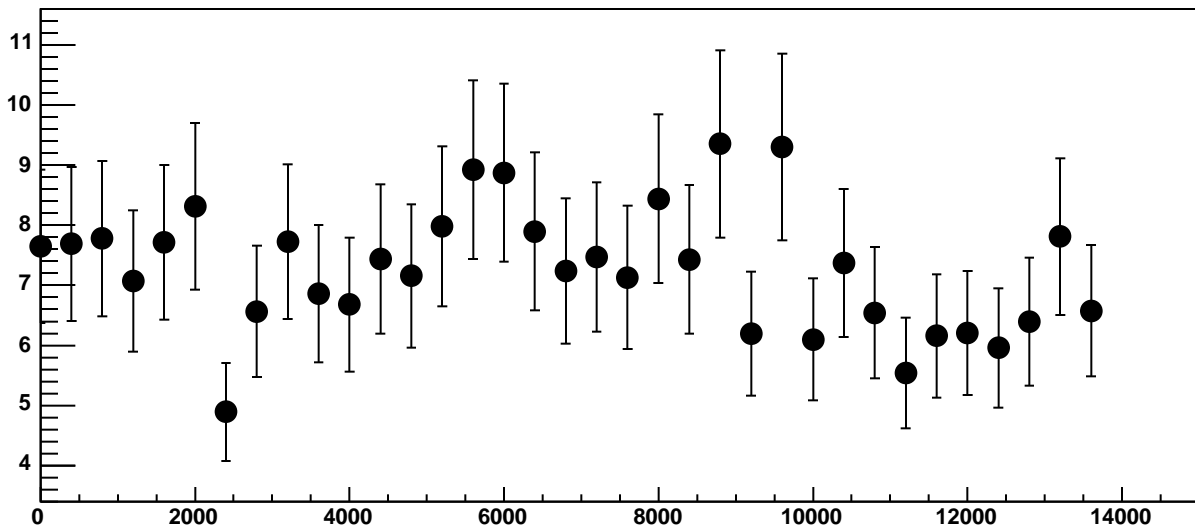


Chip 9, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

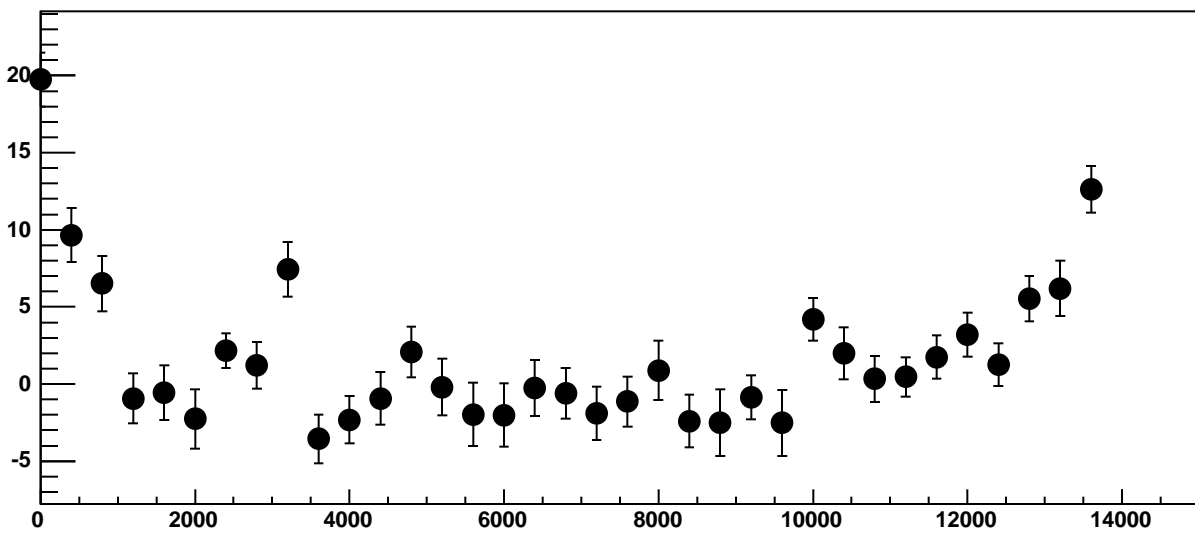


χ^2 / ndf 52.62 / 23
p0 -496.7 ± 0.7294
p1 0.016 ± 0.0001106

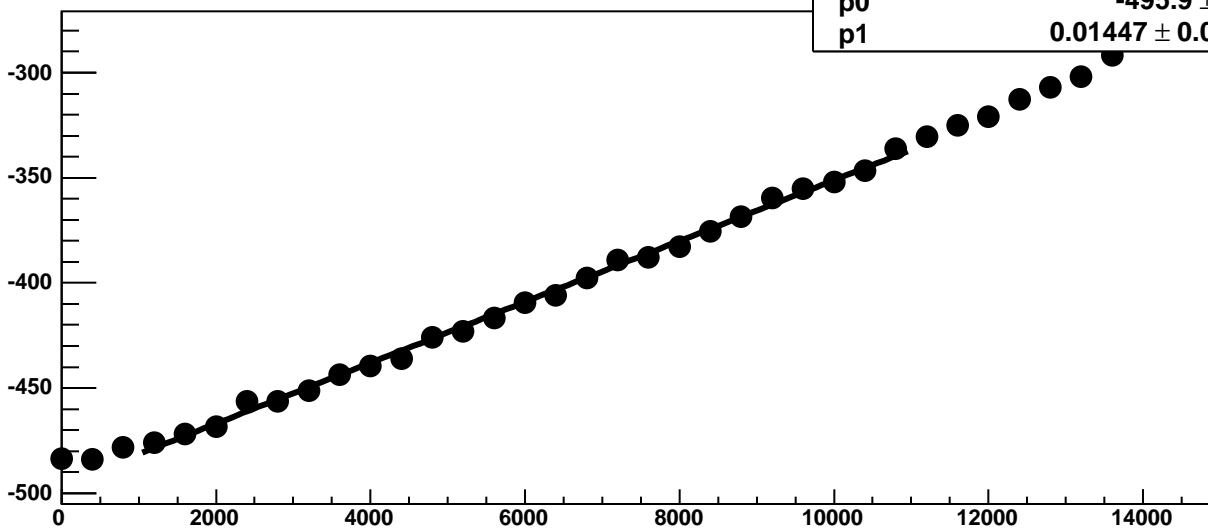
Chip 9, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

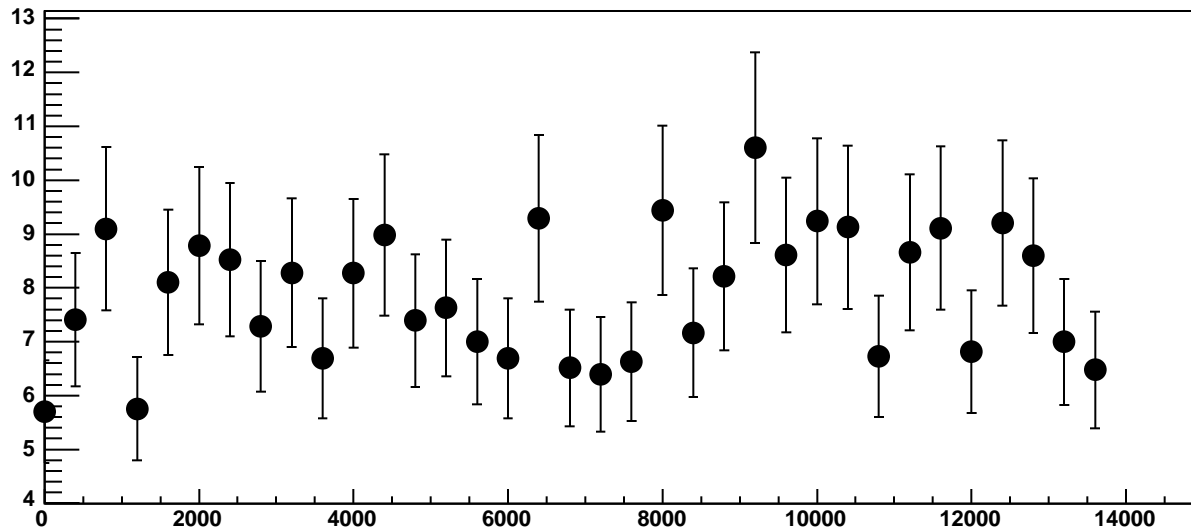


Chip 9, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

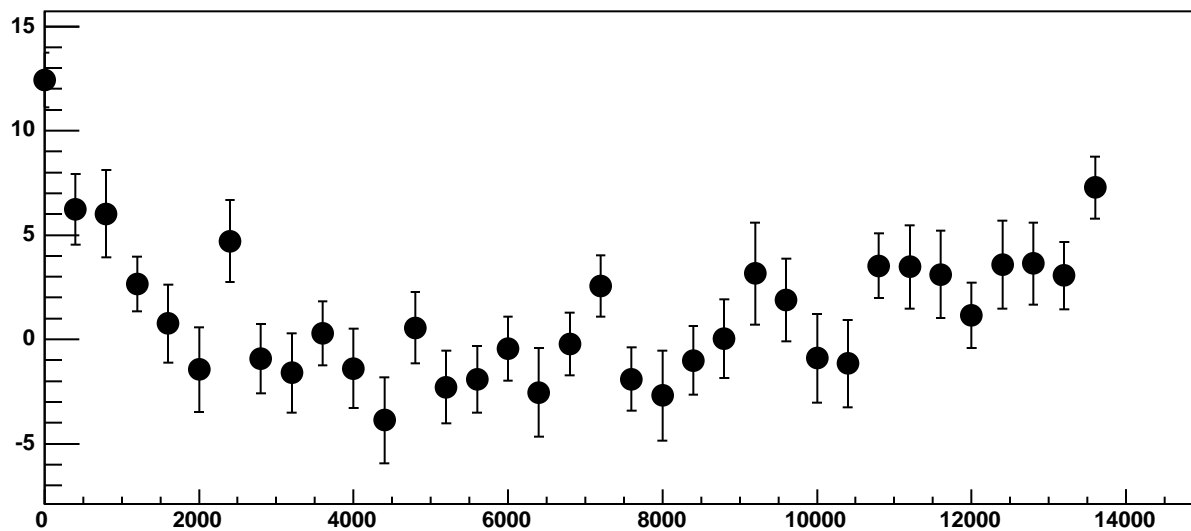


χ^2 / ndf 35.22 / 23
p0 -495.9 ± 0.7942
p1 0.01447 ± 0.0001225

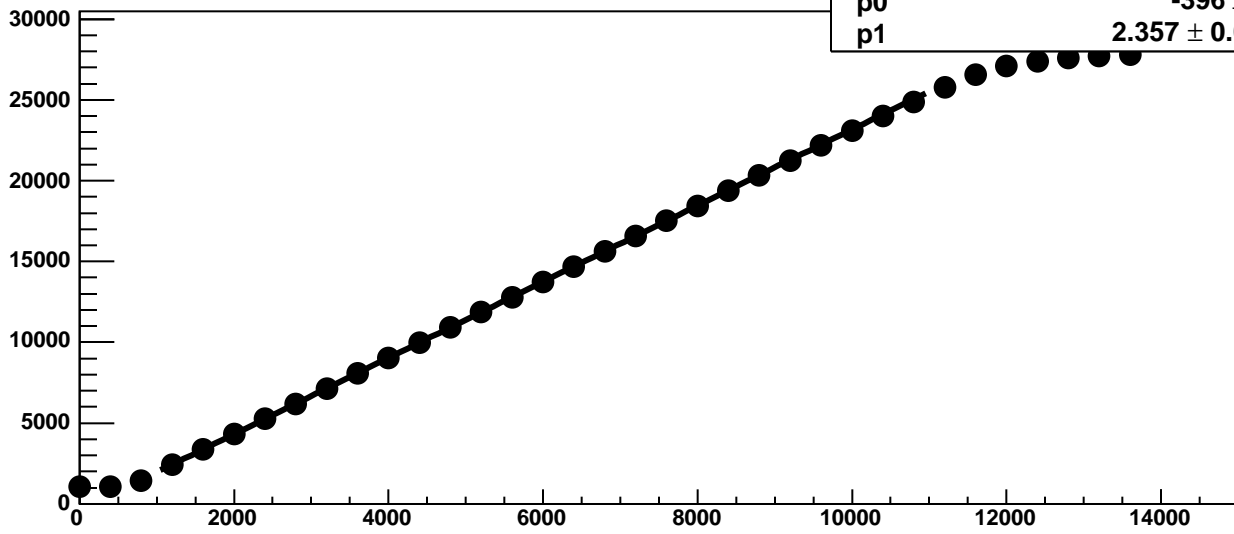
Chip 9, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



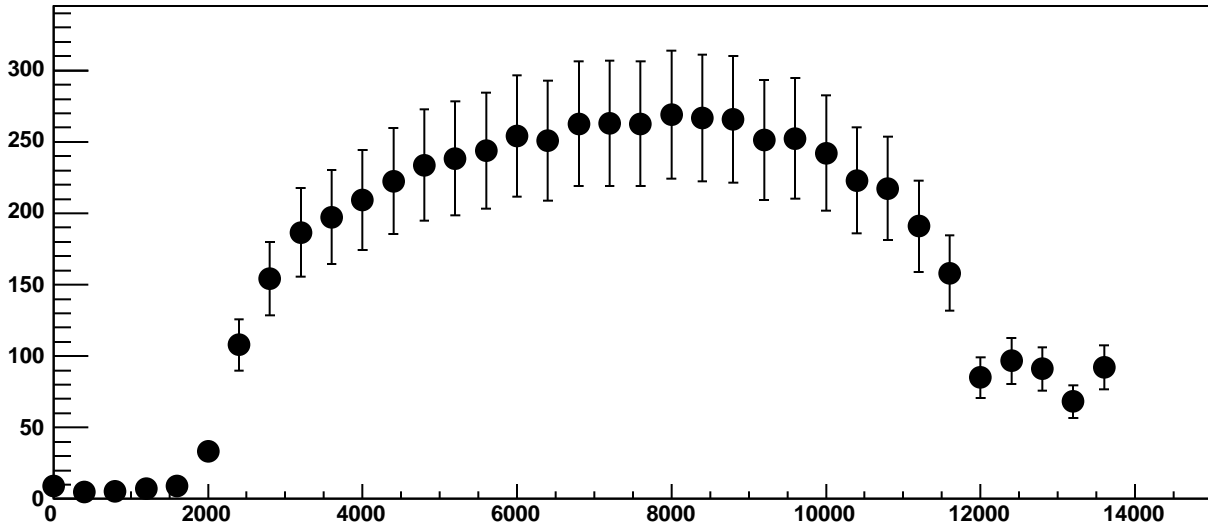
Chip 9, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



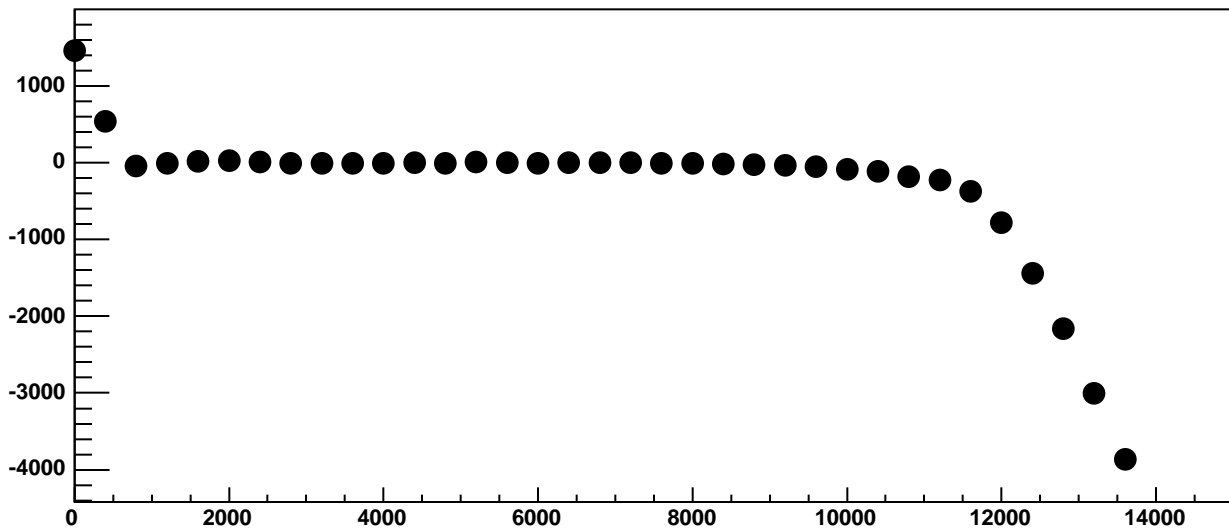
Chip 9, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC



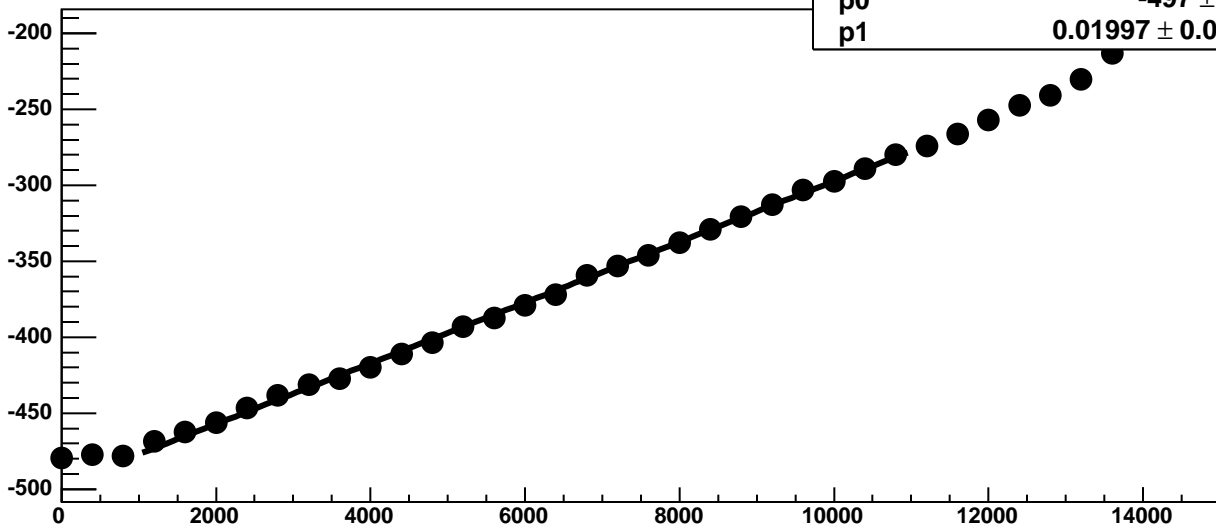
Chip 9, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC

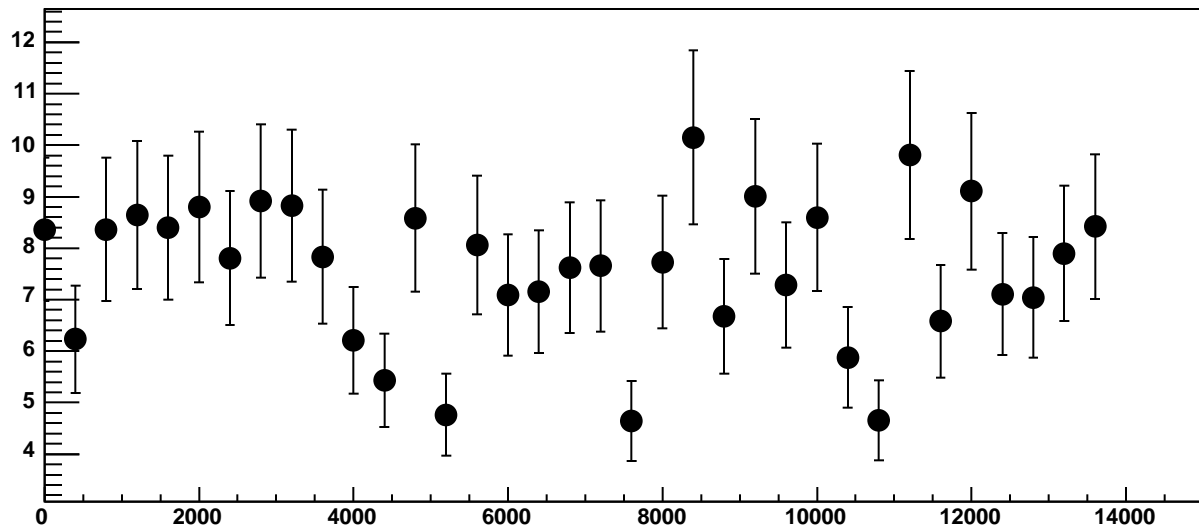


Chip 9, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

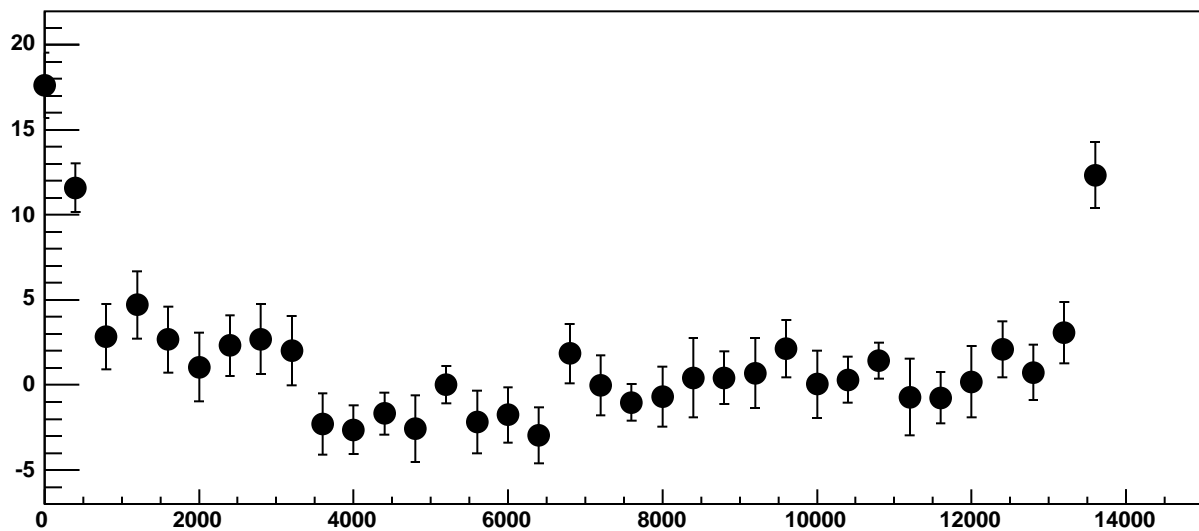


χ^2 / ndf 32.47 / 23
p0 -497 ± 0.7936
p1 0.01997 ± 0.0001129

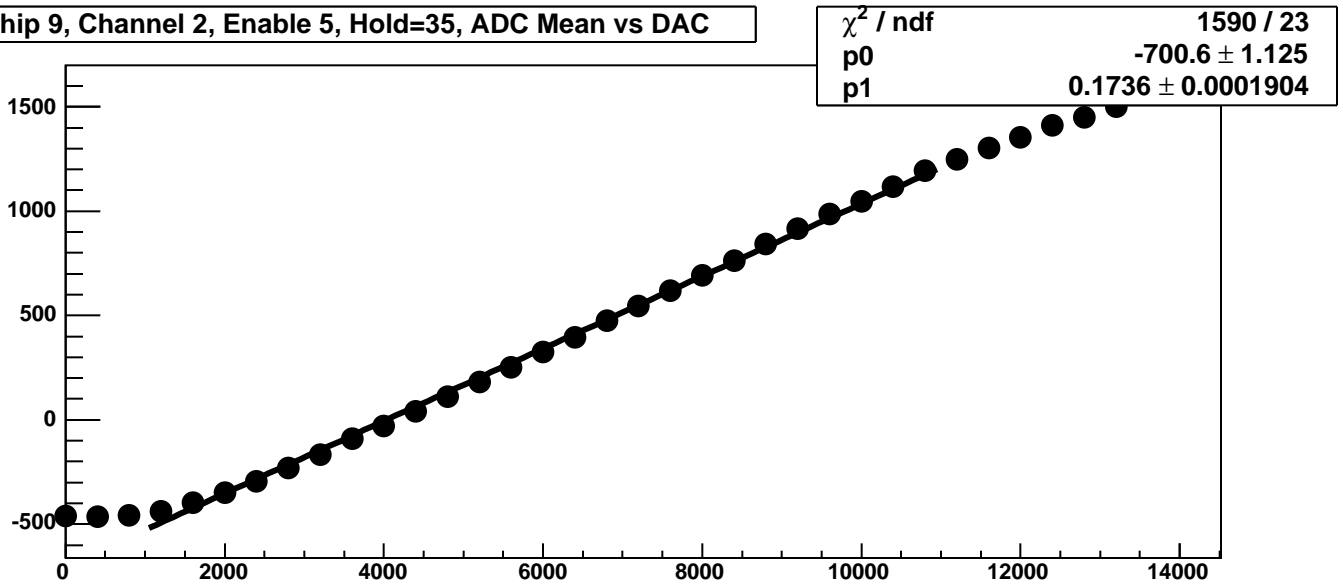
Chip 9, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



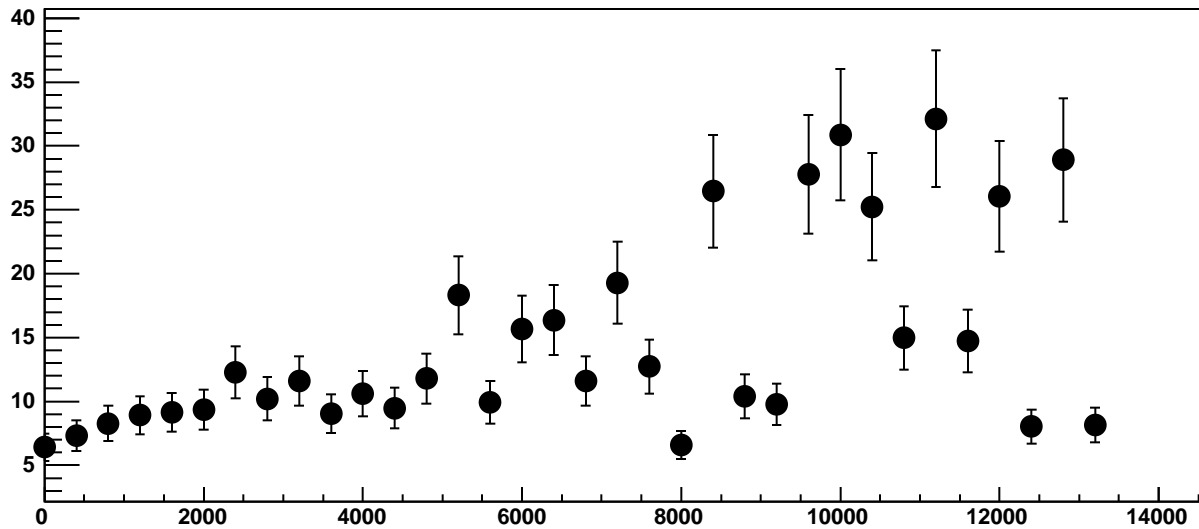
Chip 9, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



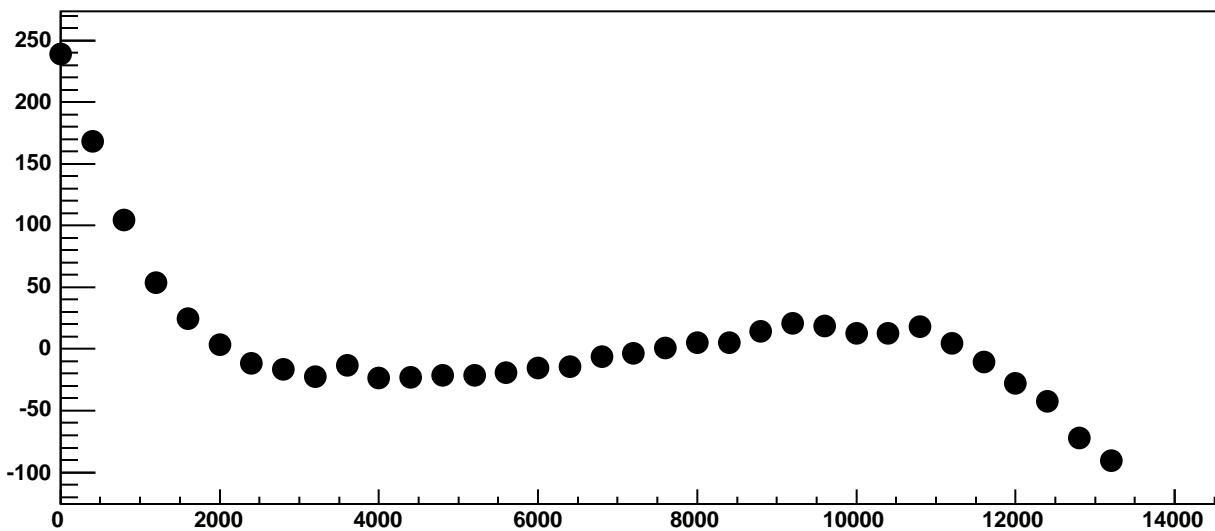
Chip 9, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



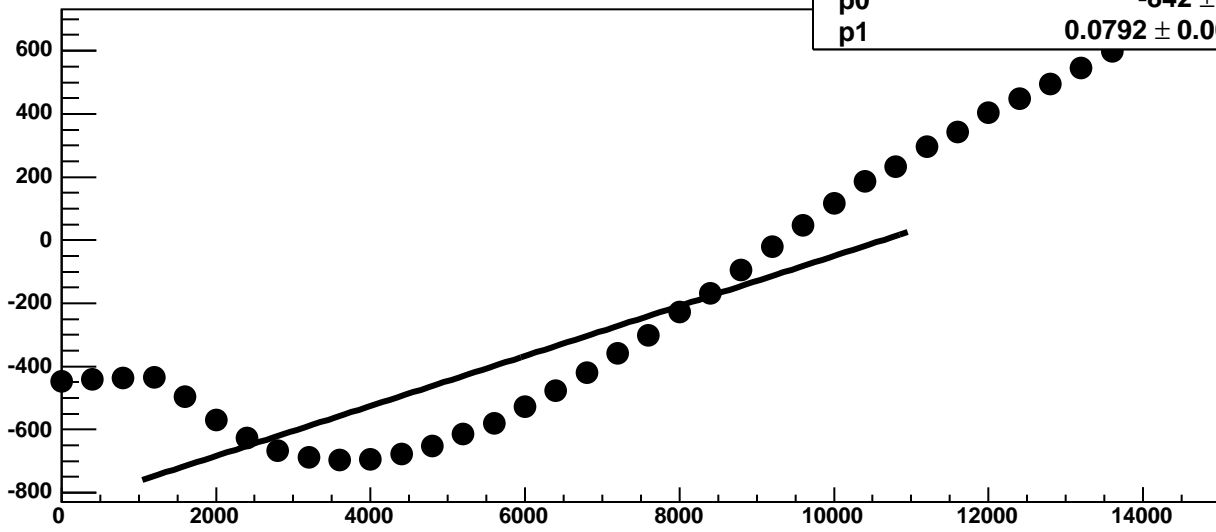
Chip 9, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



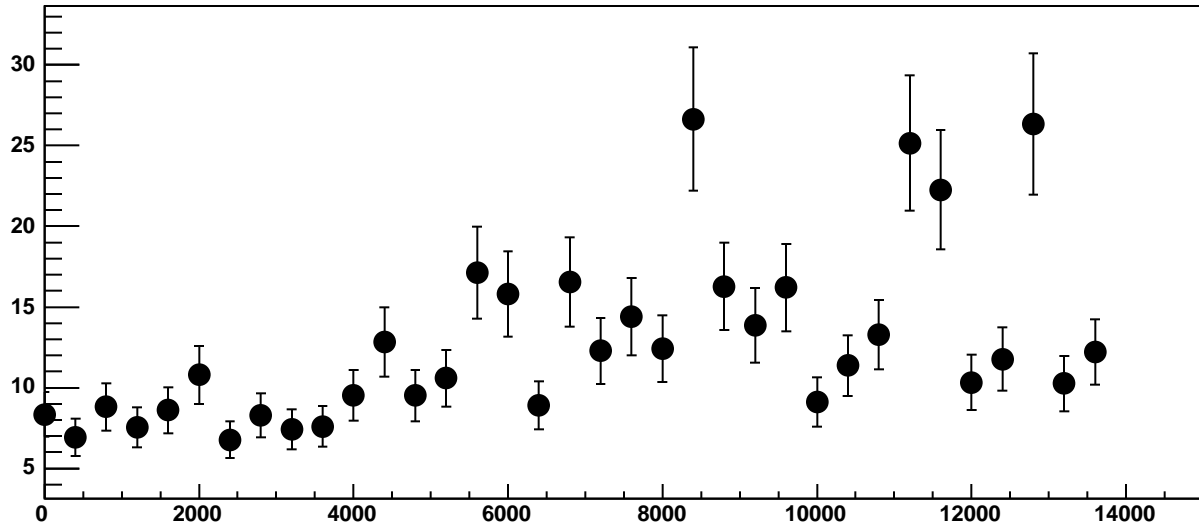
Chip 9, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



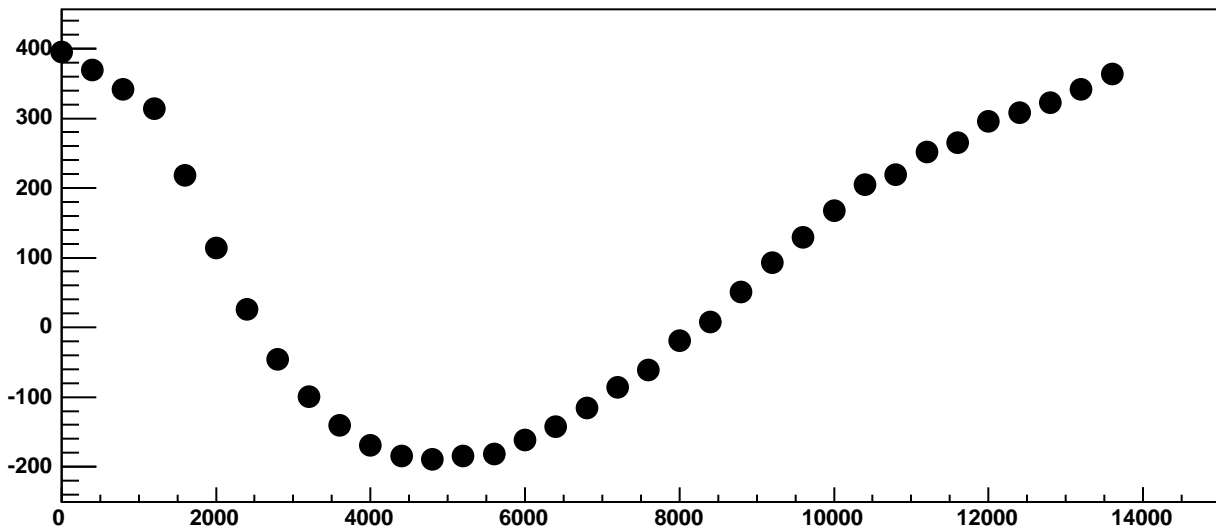
Chip 9, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



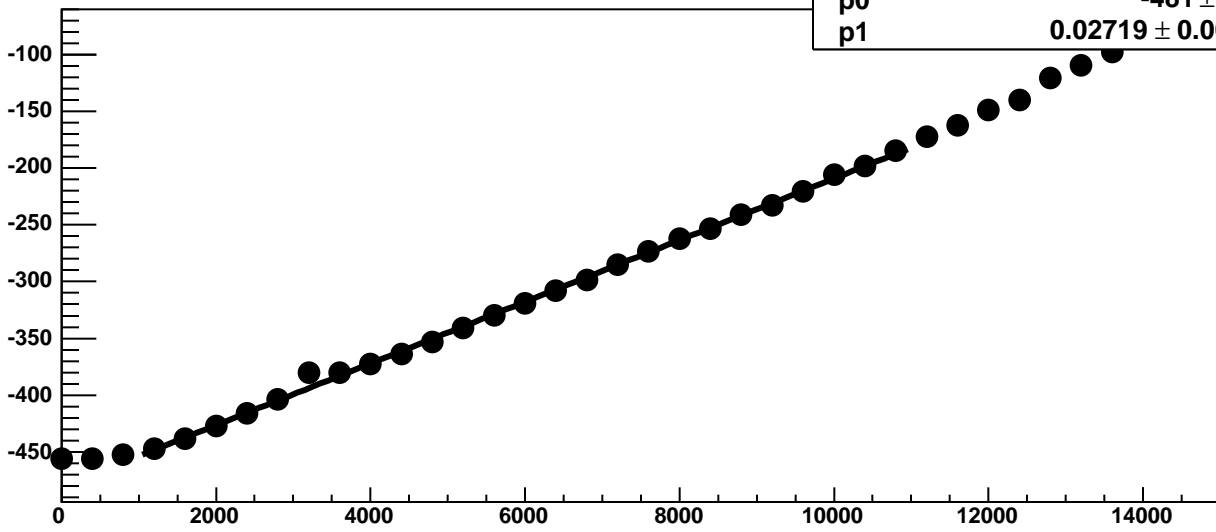
Chip 9, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

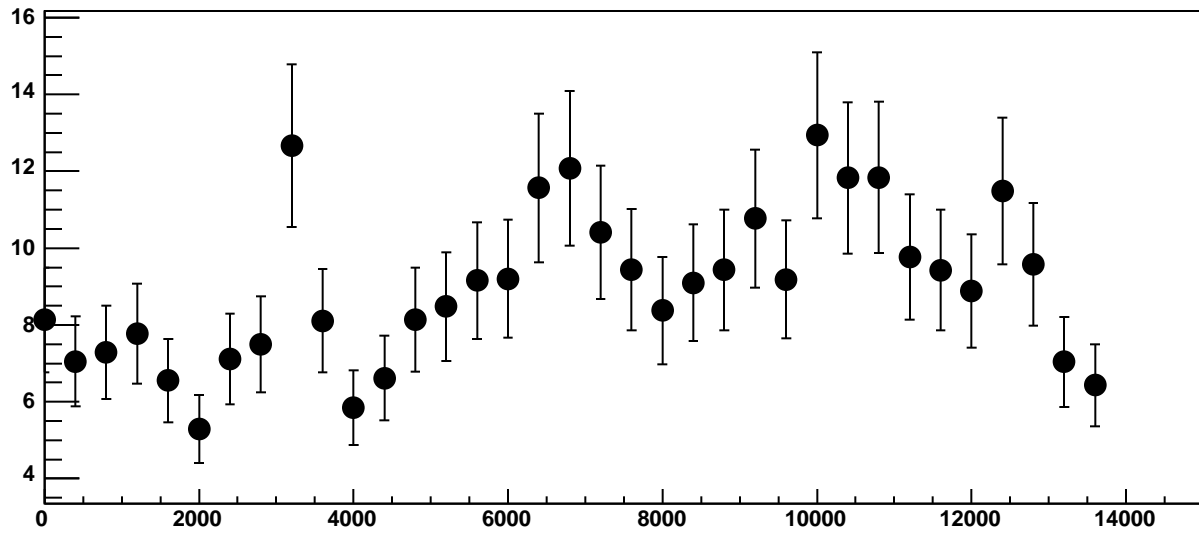


Chip 9, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC

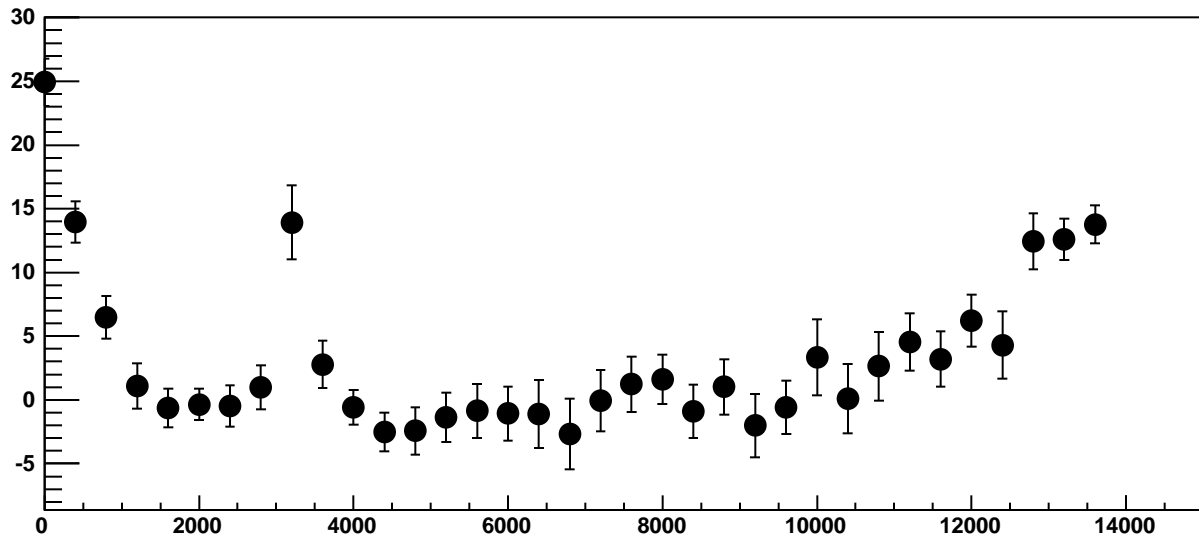


χ^2 / ndf 37.19 / 23
p0 -481 ± 0.7943
p1 0.02719 ± 0.0001385

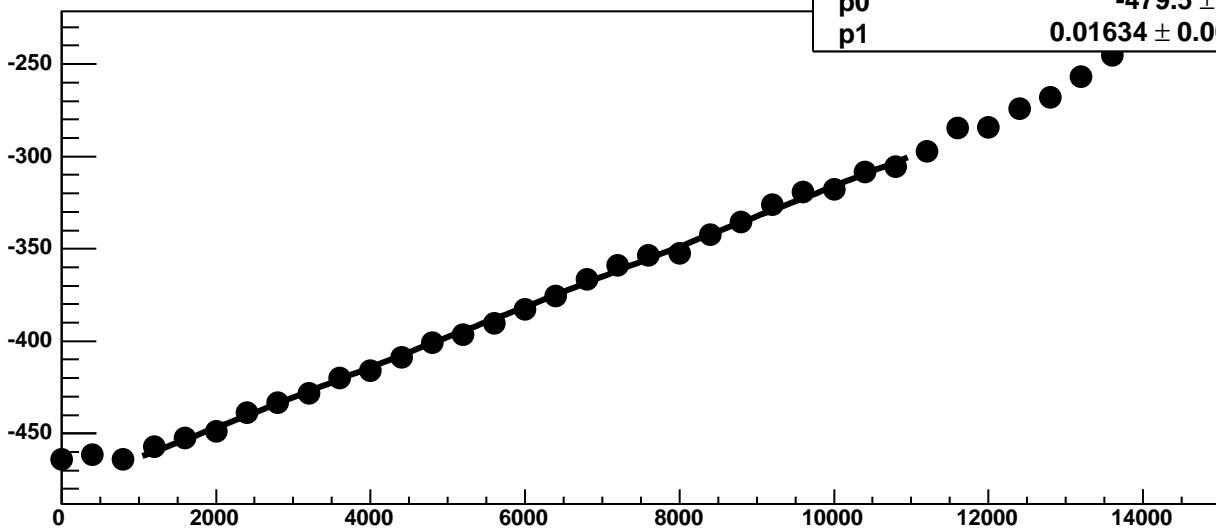
Chip 9, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC

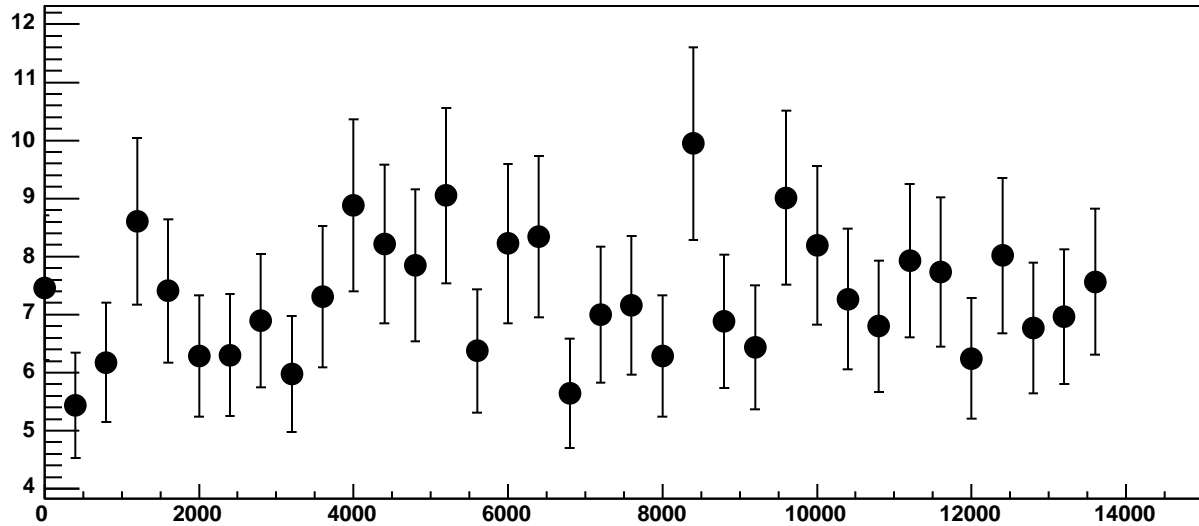


Chip 9, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

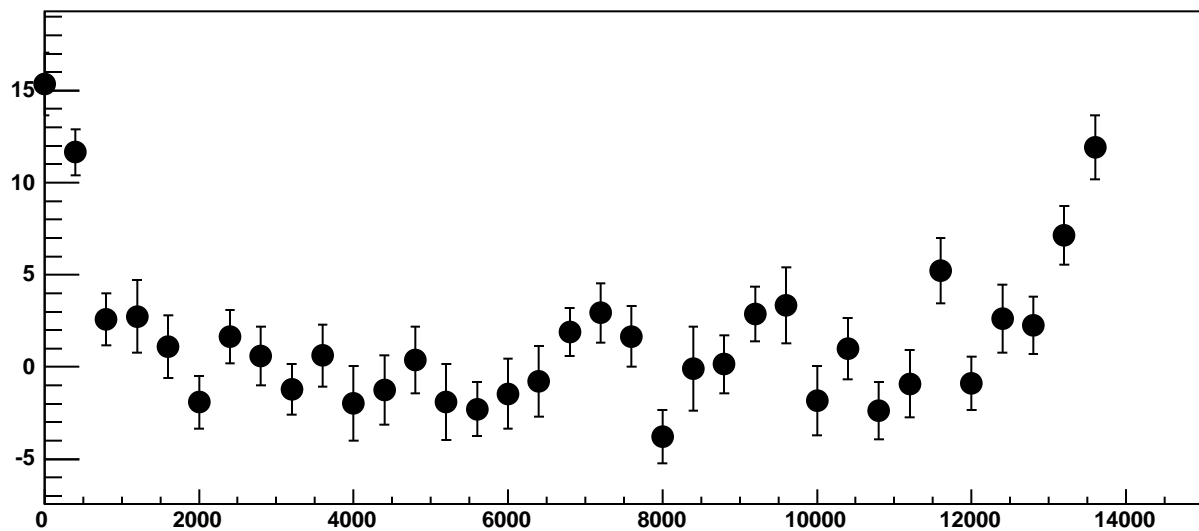


χ^2 / ndf 35.32 / 23
p0 -479.5 ± 0.7583
p1 0.01634 ± 0.0001148

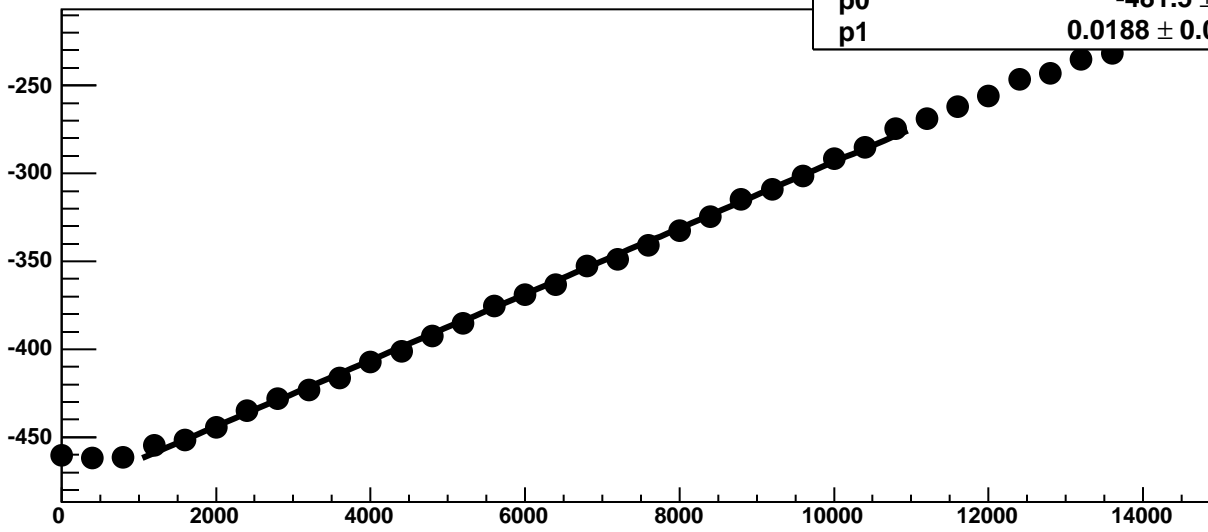
Chip 9, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

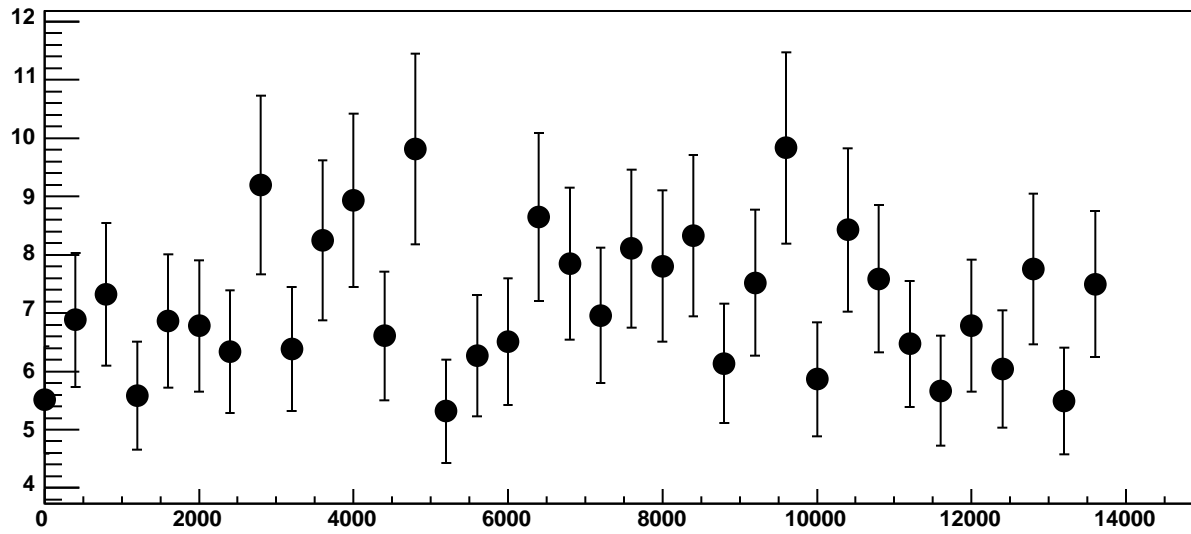


Chip 9, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

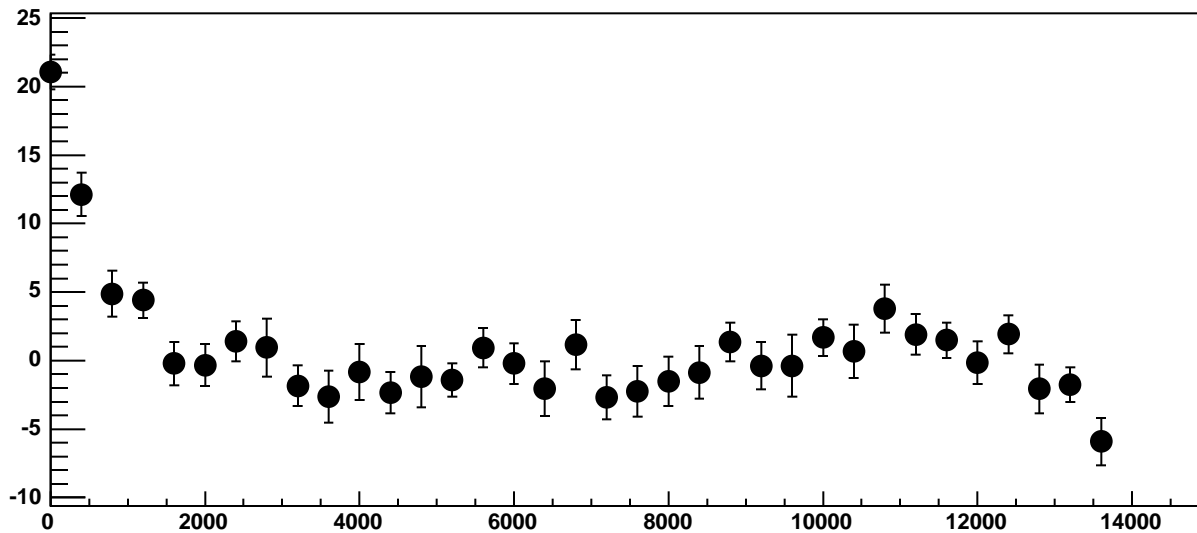


χ^2 / ndf 35.29 / 23
p0 -481.5 ± 0.7214
p1 0.0188 ± 0.0001115

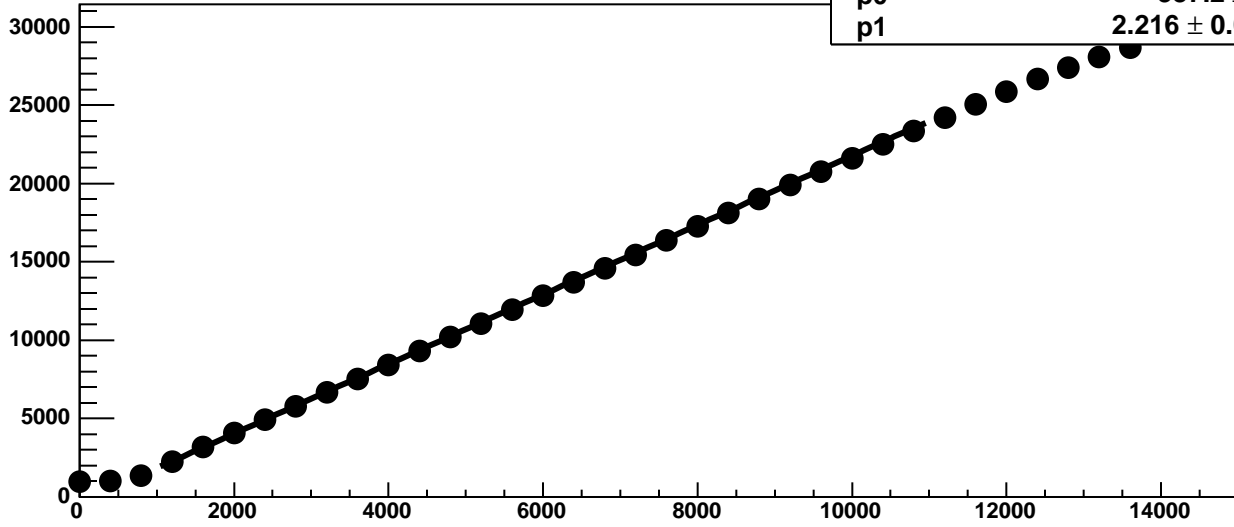
Chip 9, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



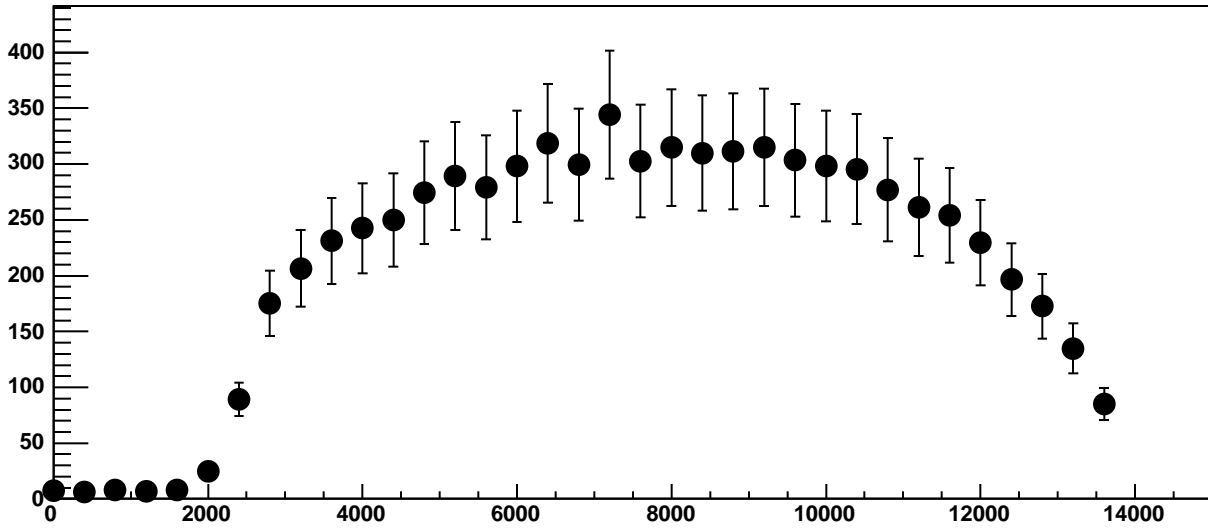
Chip 9, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



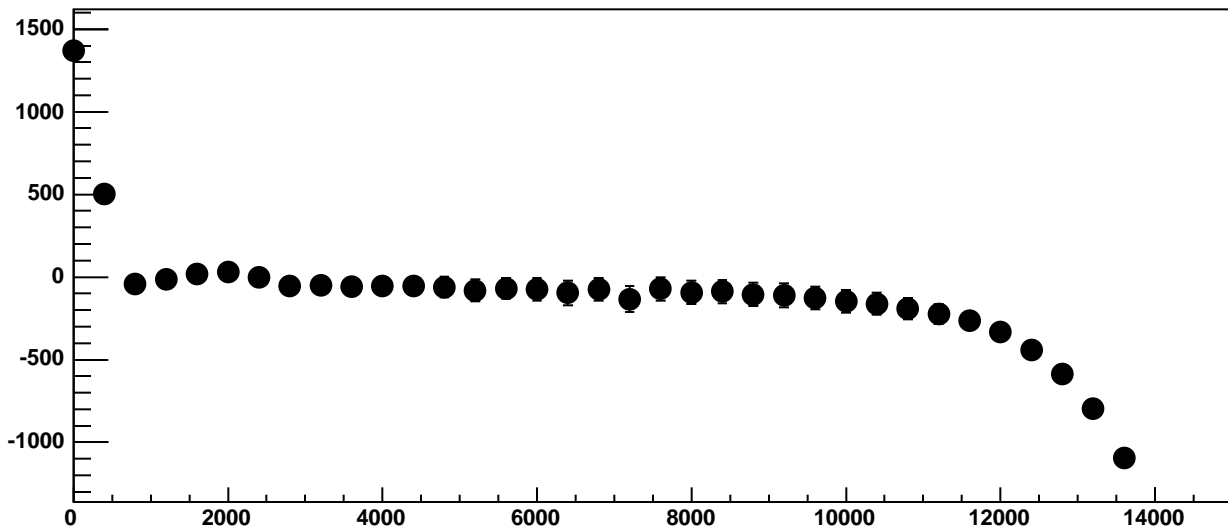
Chip 9, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC



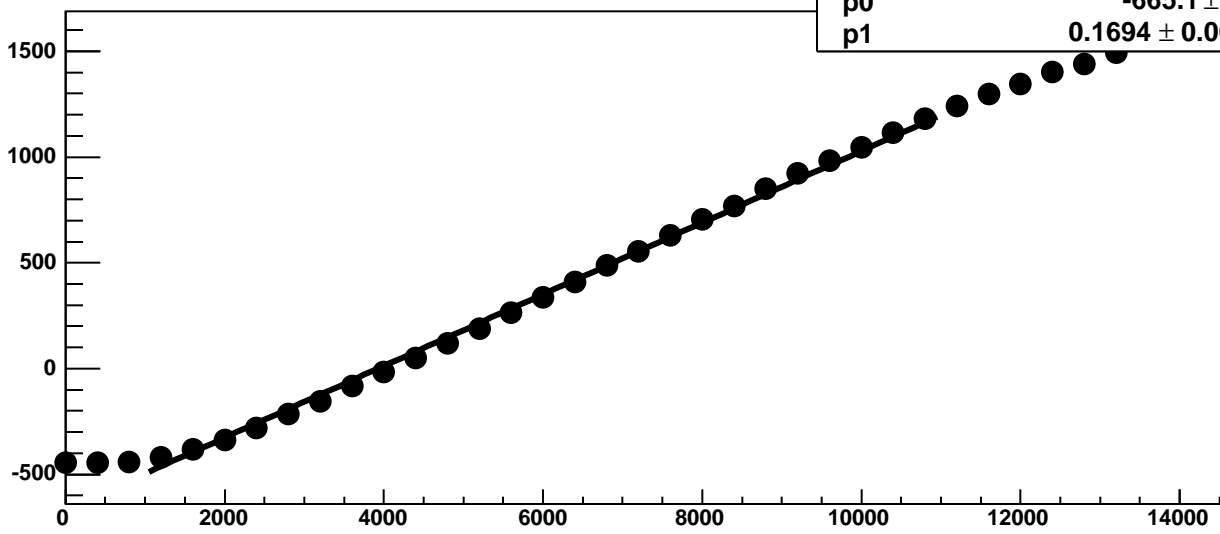
Chip 9, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



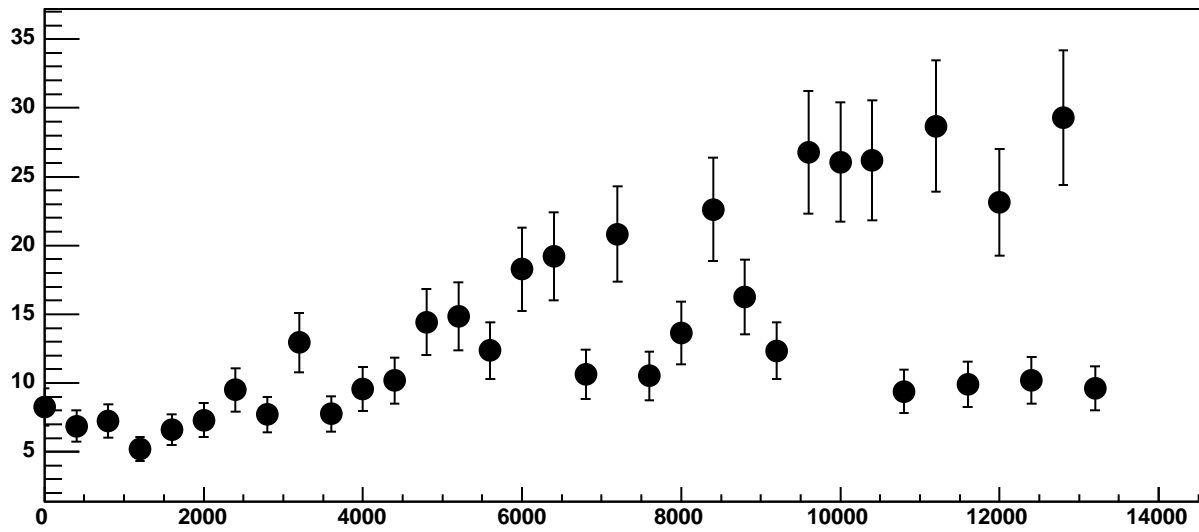
Chip 9, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC



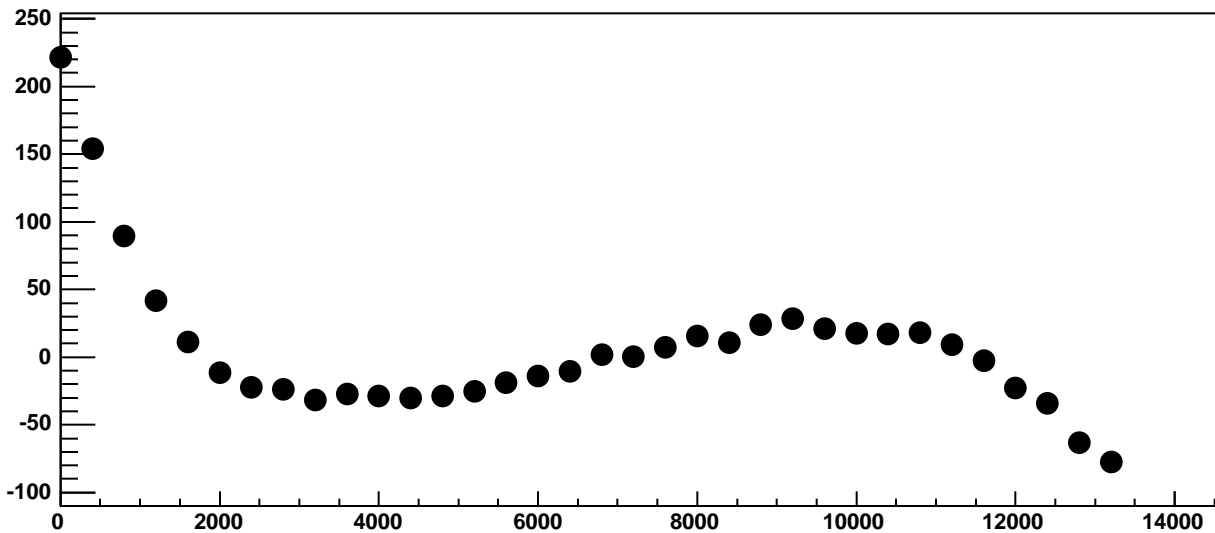
Chip 9, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



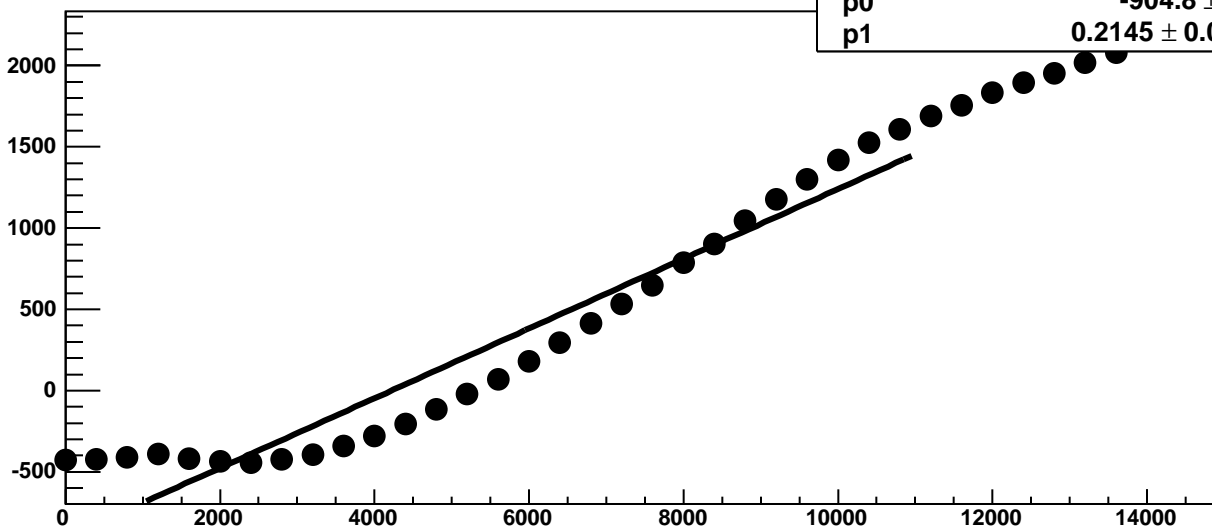
Chip 9, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



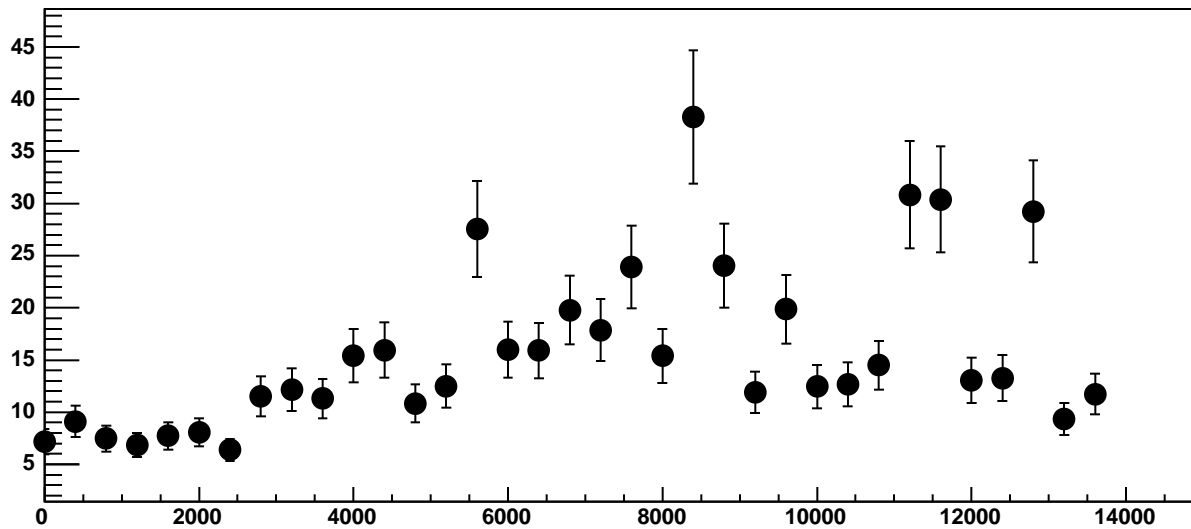
Chip 9, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



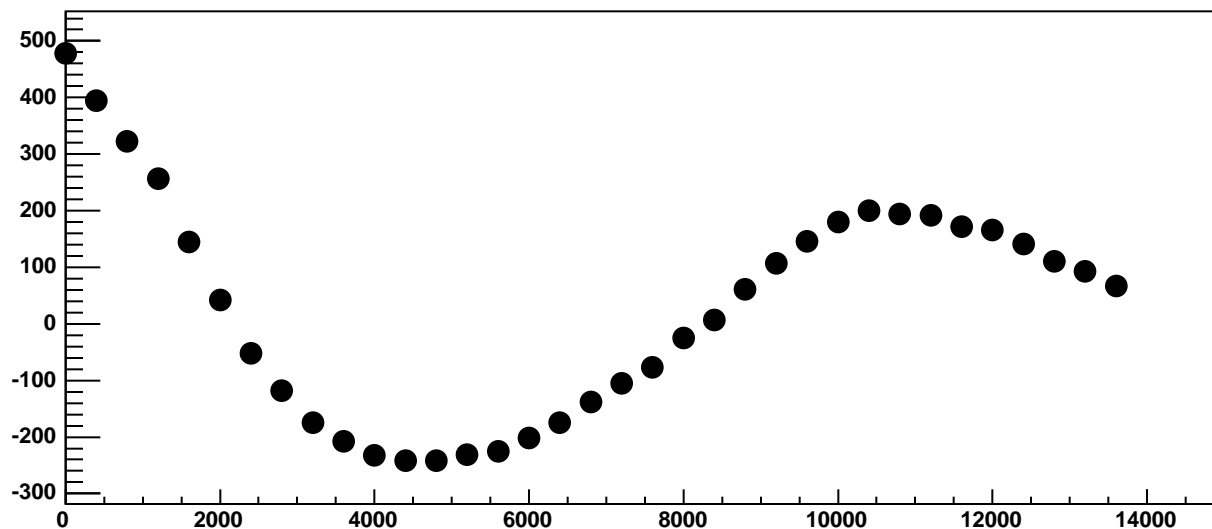
Chip 9, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



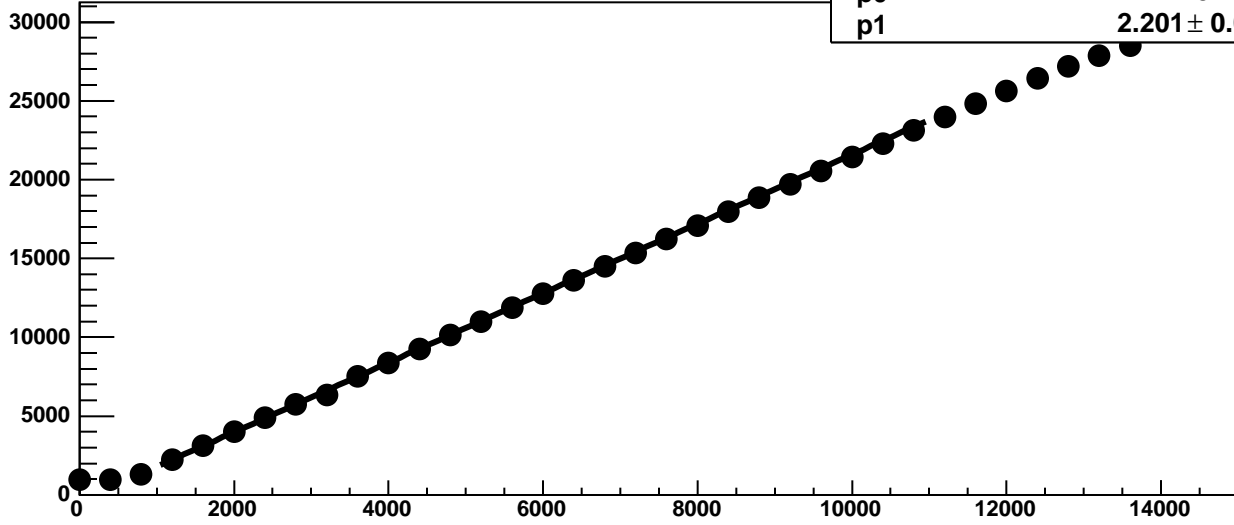
Chip 9, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



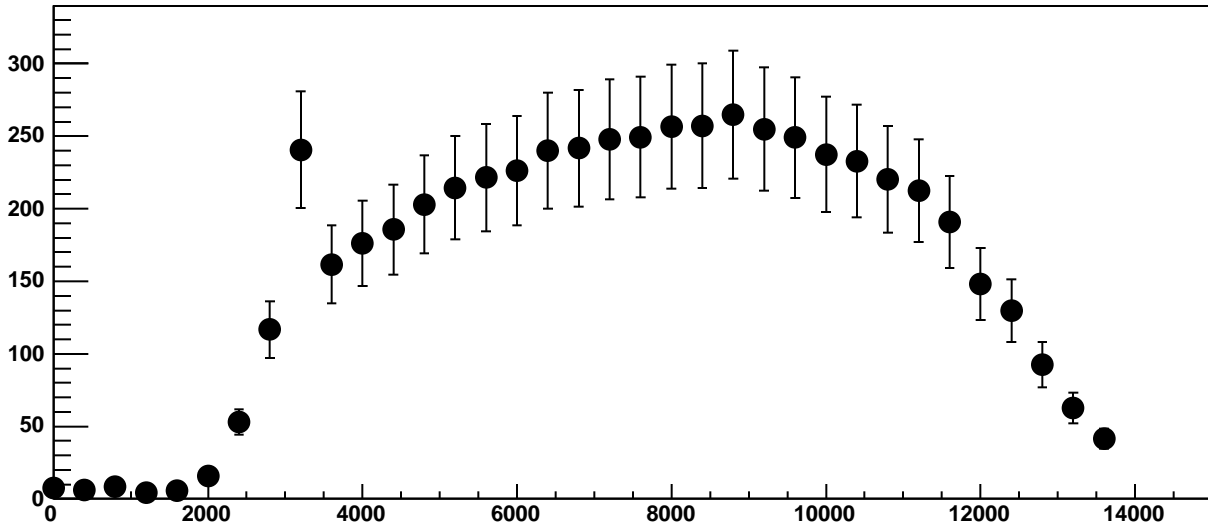
Chip 9, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



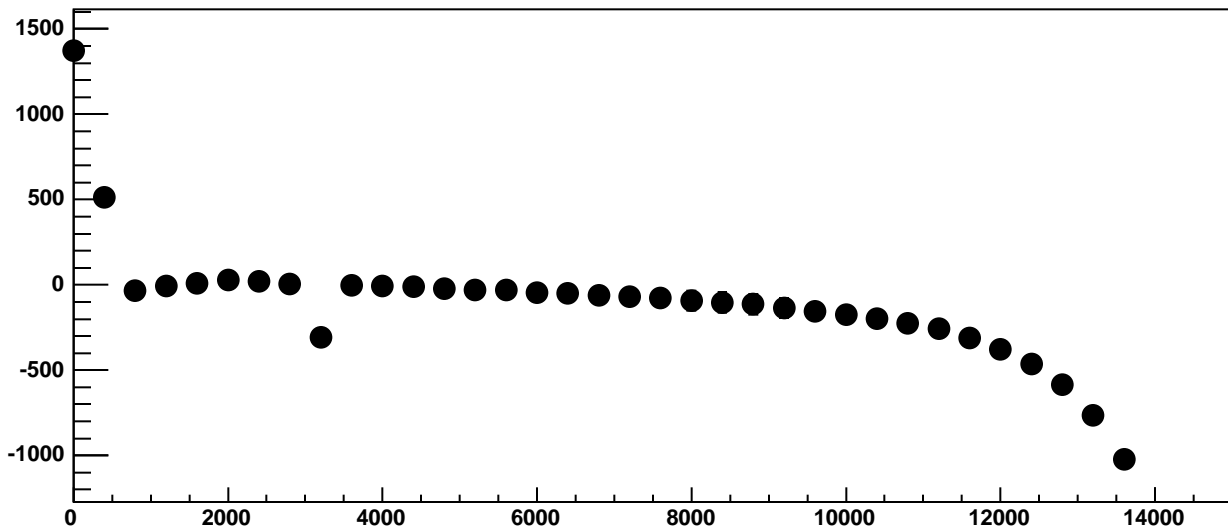
Chip 9, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC



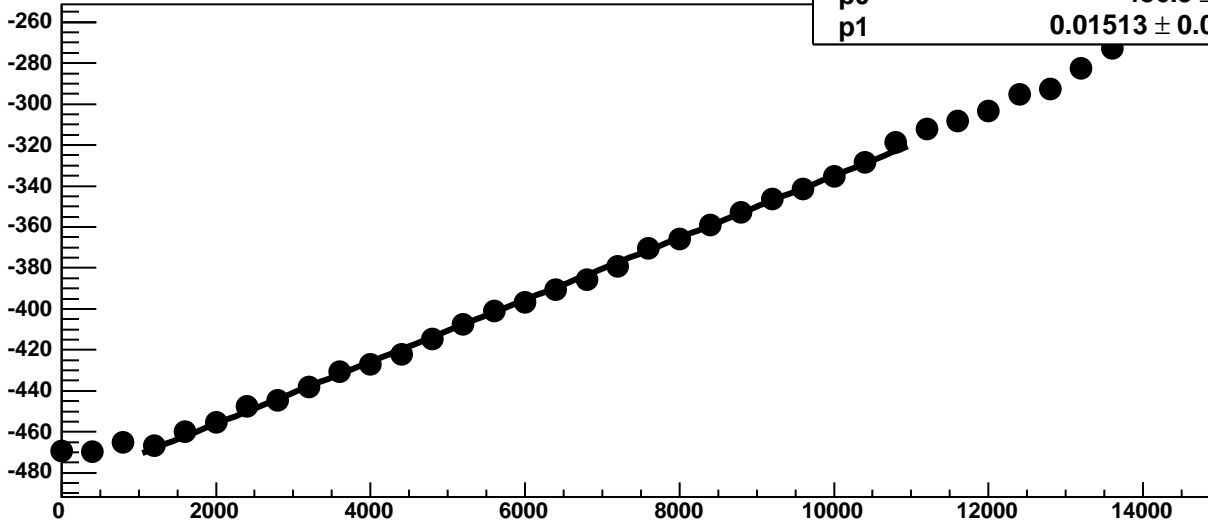
Chip 9, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC

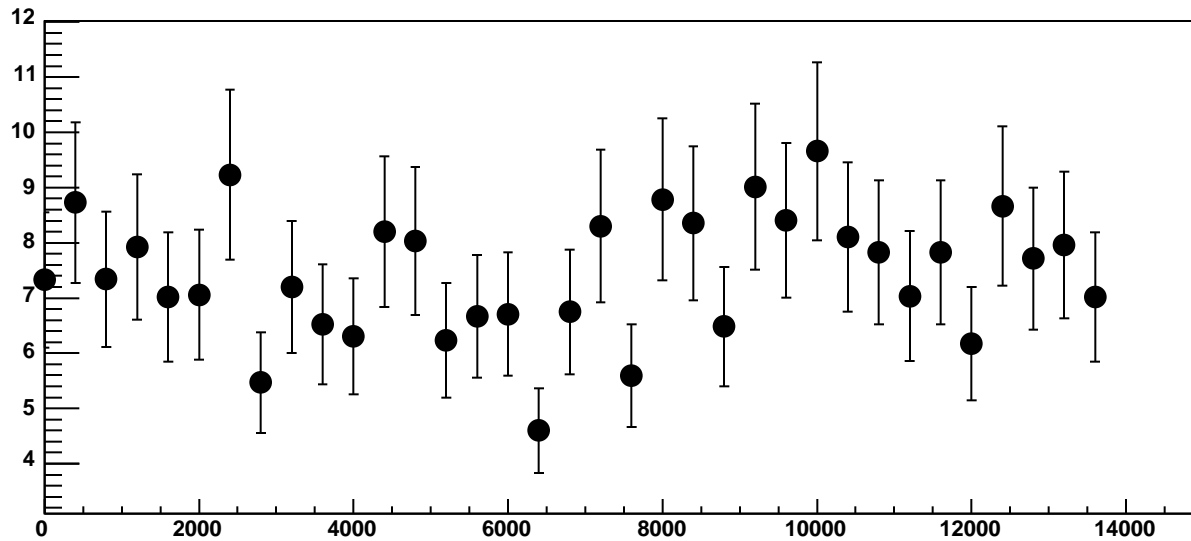


Chip 9, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

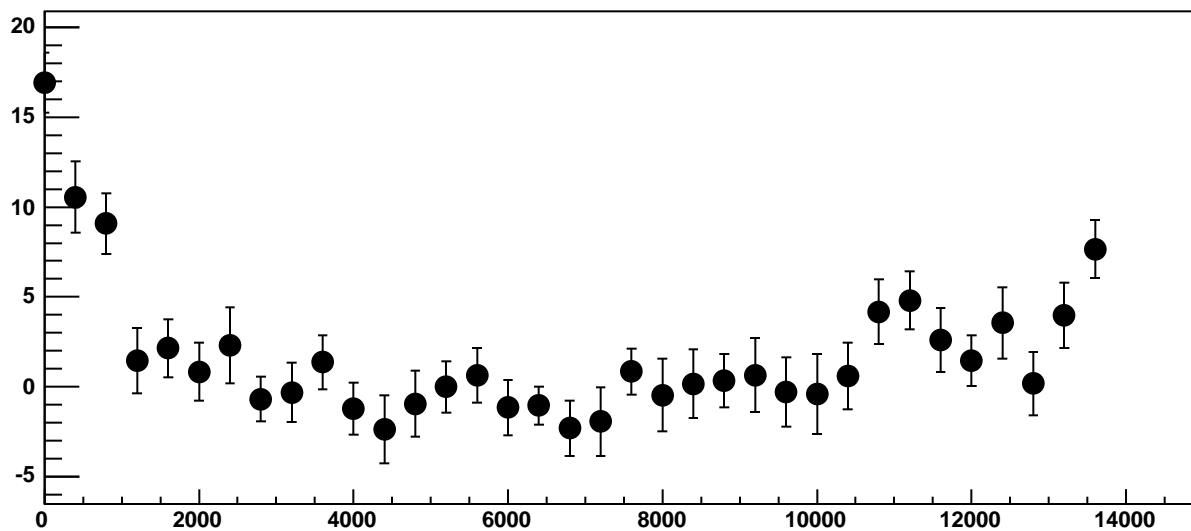


χ^2 / ndf 18.8 / 23
p0 -486.3 ± 0.7673
p1 0.01513 ± 0.0001207

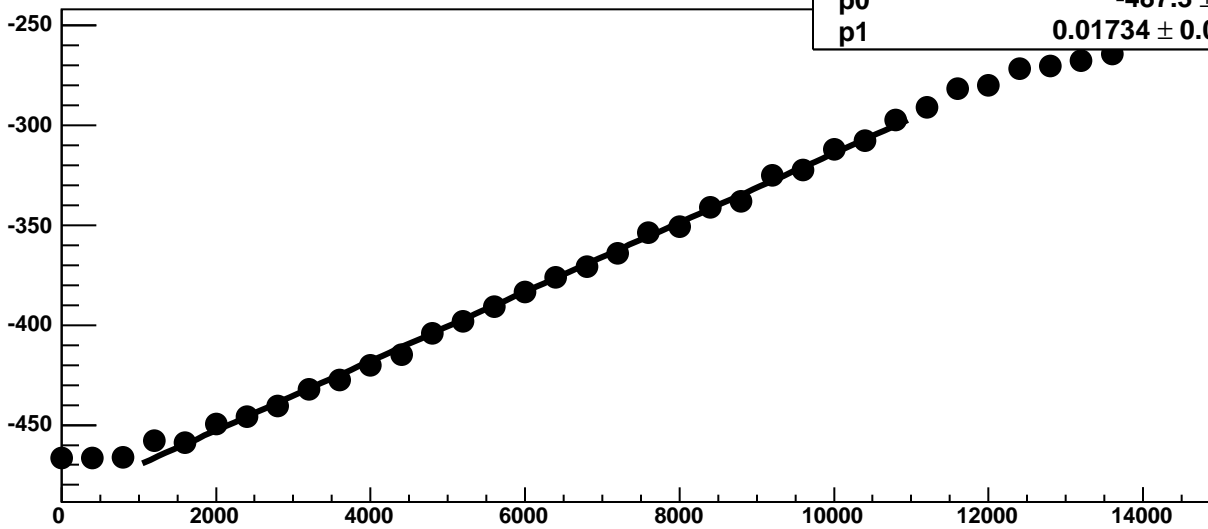
Chip 9, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



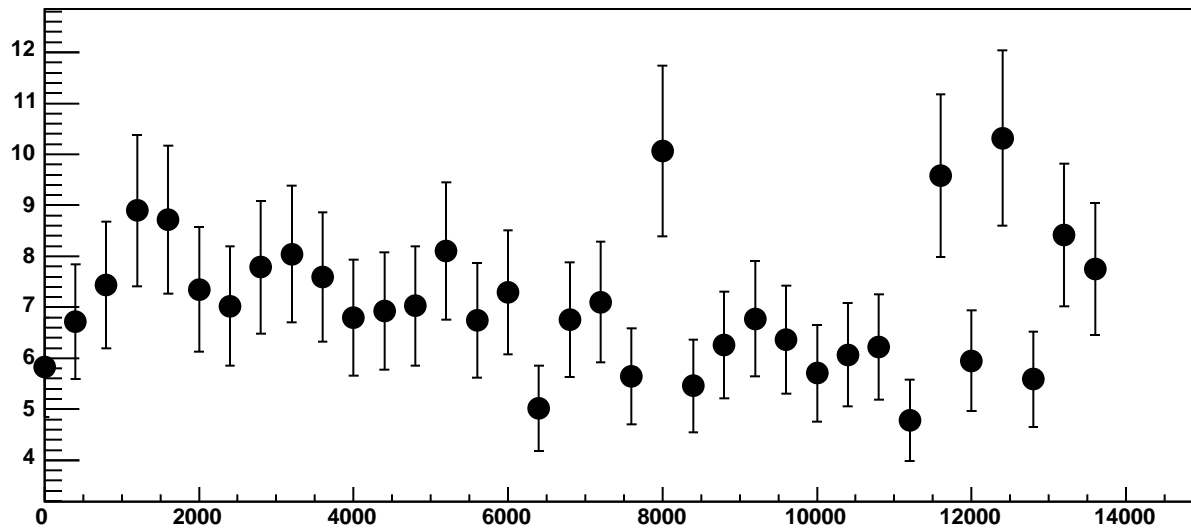
Chip 9, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



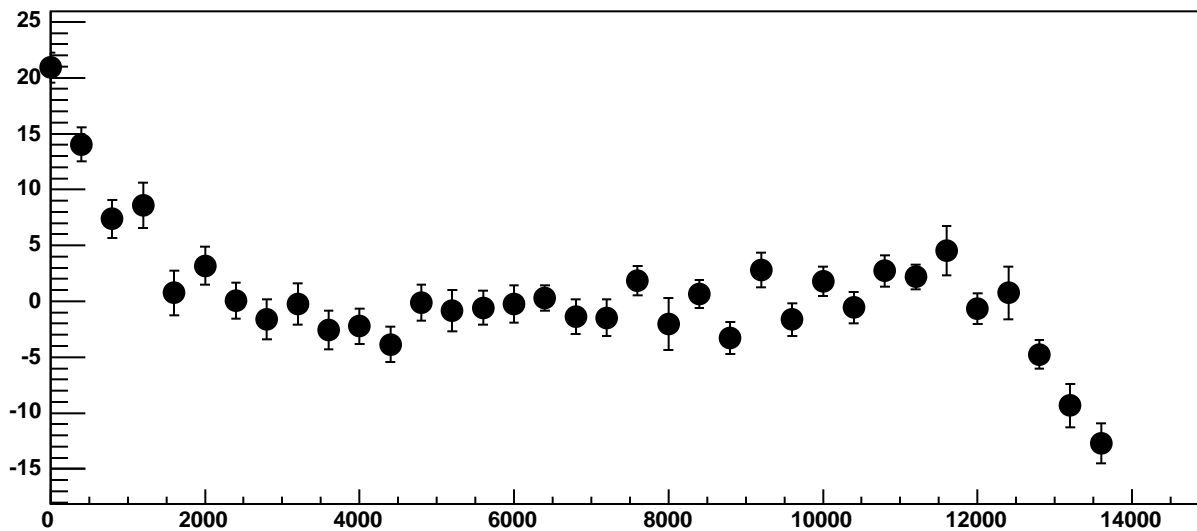
Chip 9, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



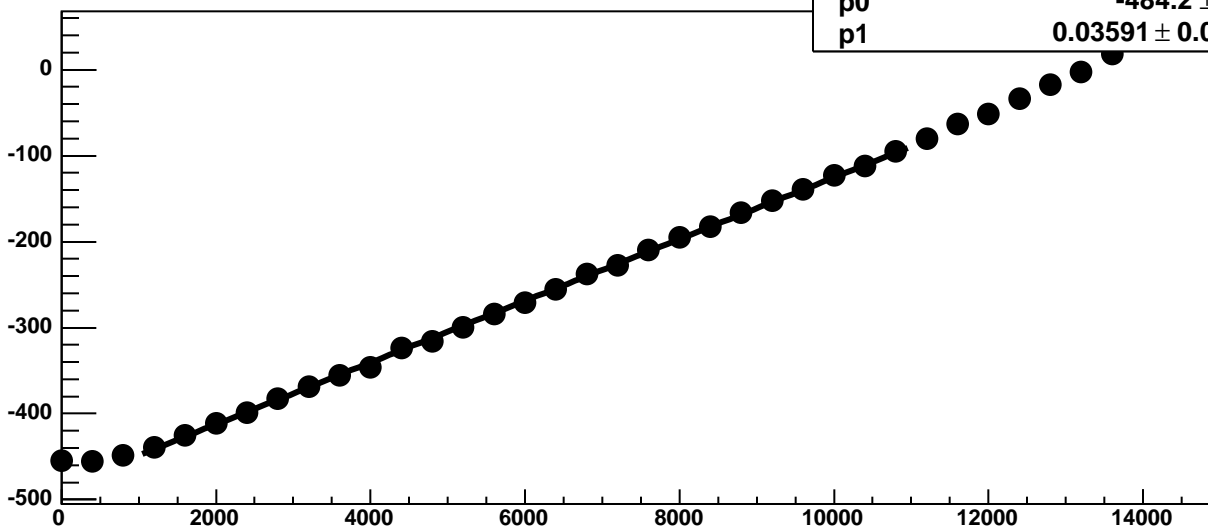
Chip 9, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

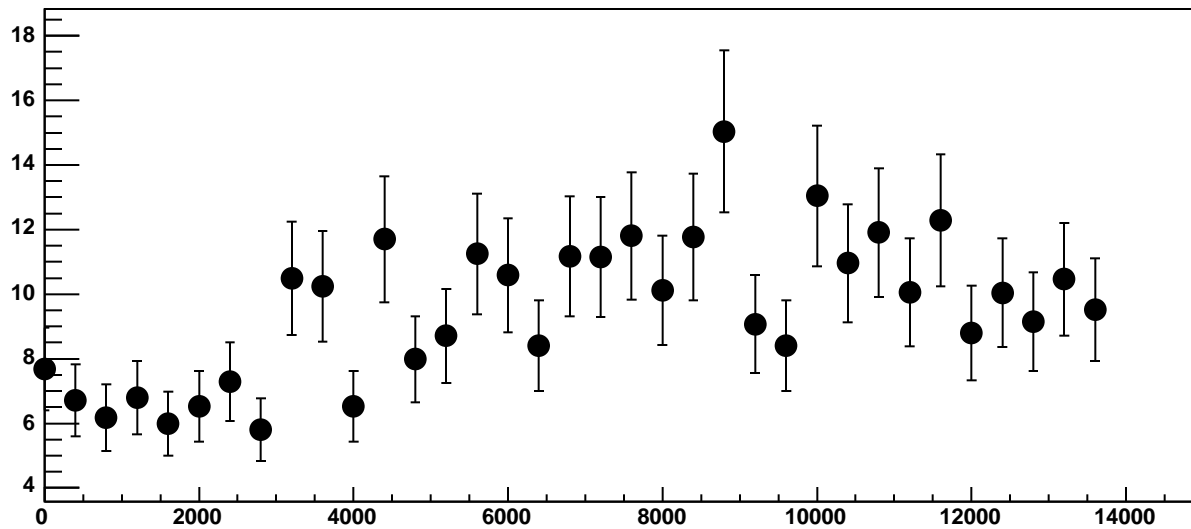


Chip 9, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

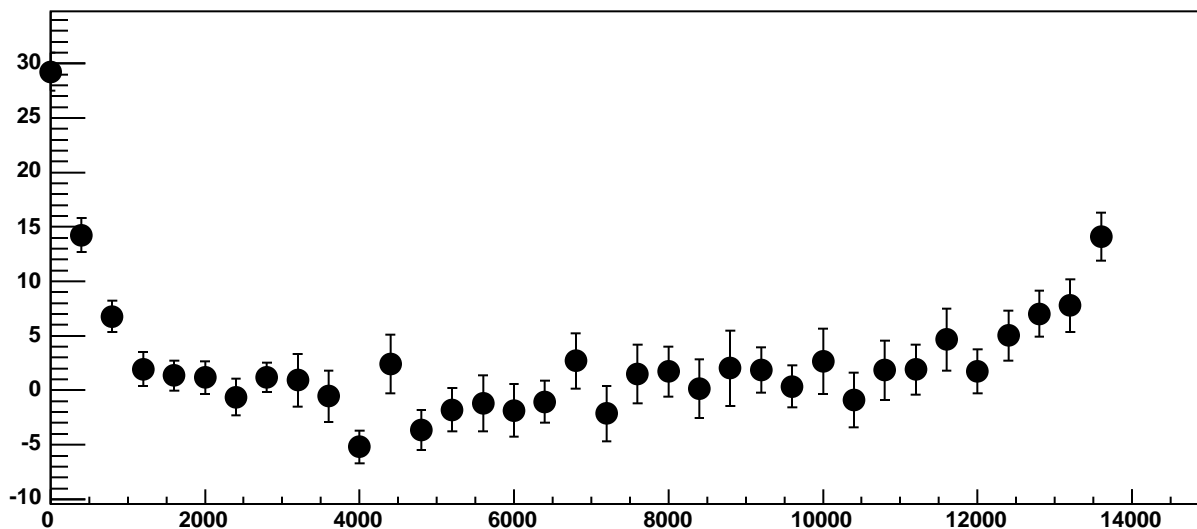


χ^2 / ndf 28.12 / 23
p0 -484.2 ± 0.7923
p1 0.03591 ± 0.0001392

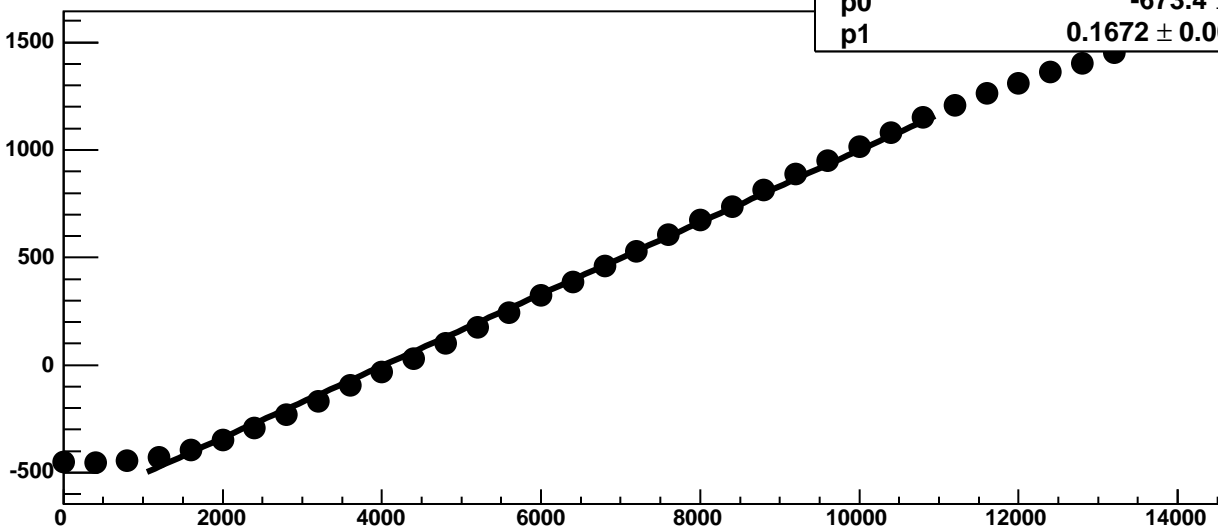
Chip 9, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



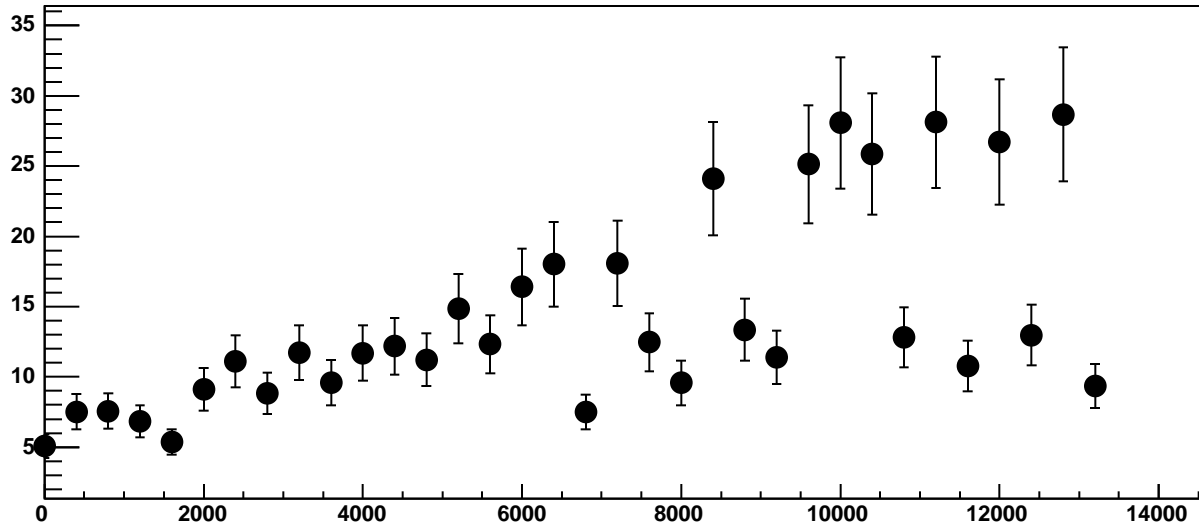
Chip 9, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC



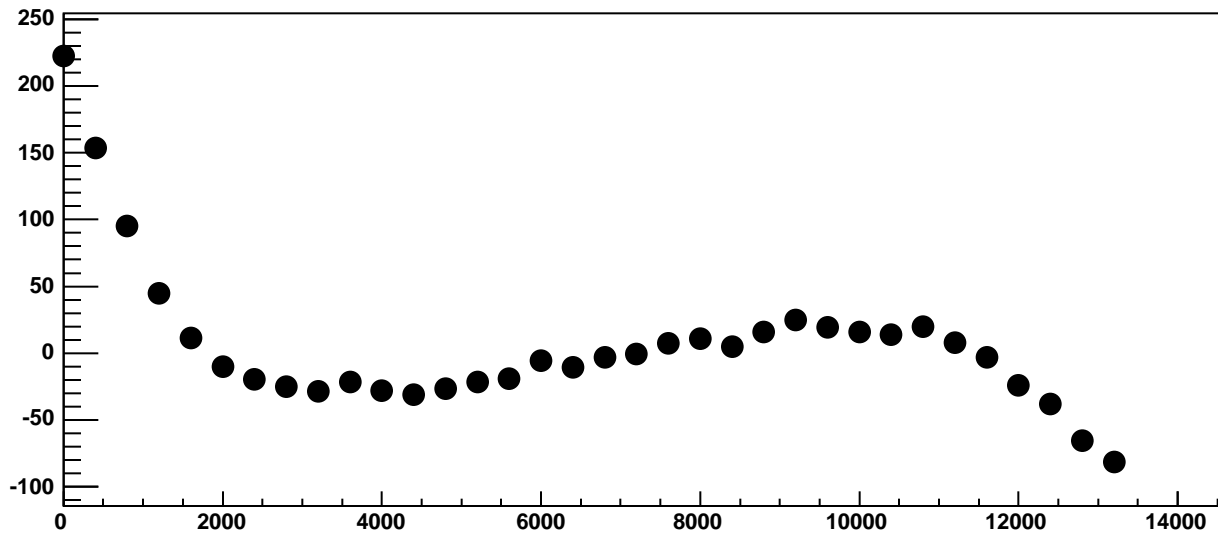
Chip 9, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



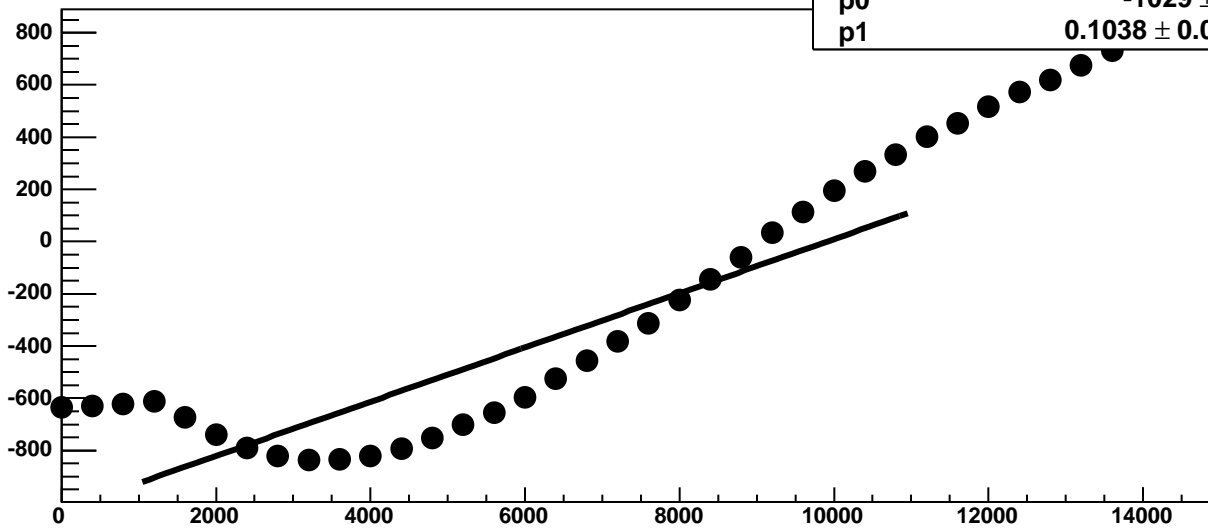
Chip 9, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



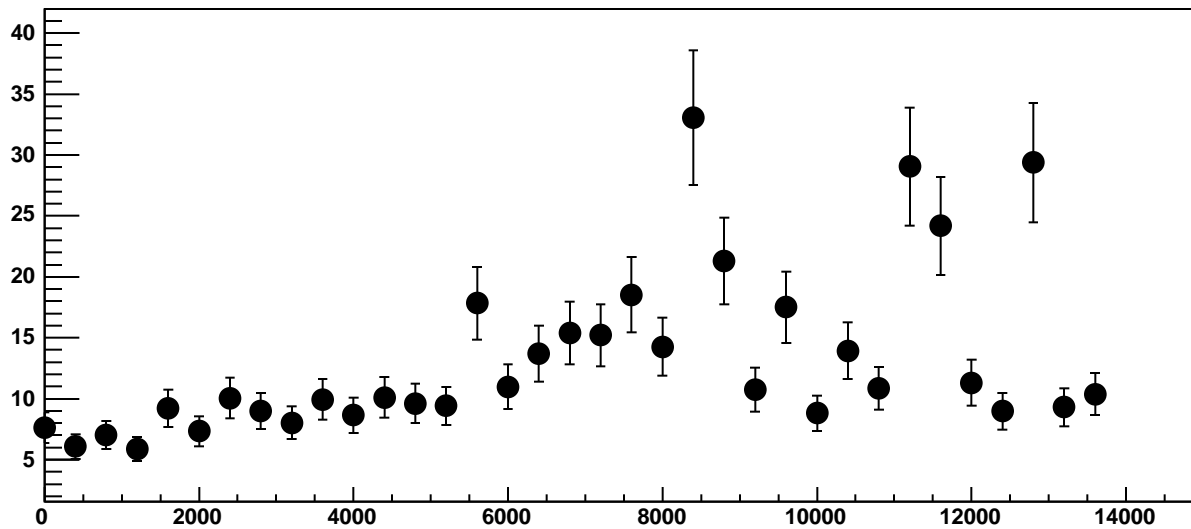
Chip 9, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



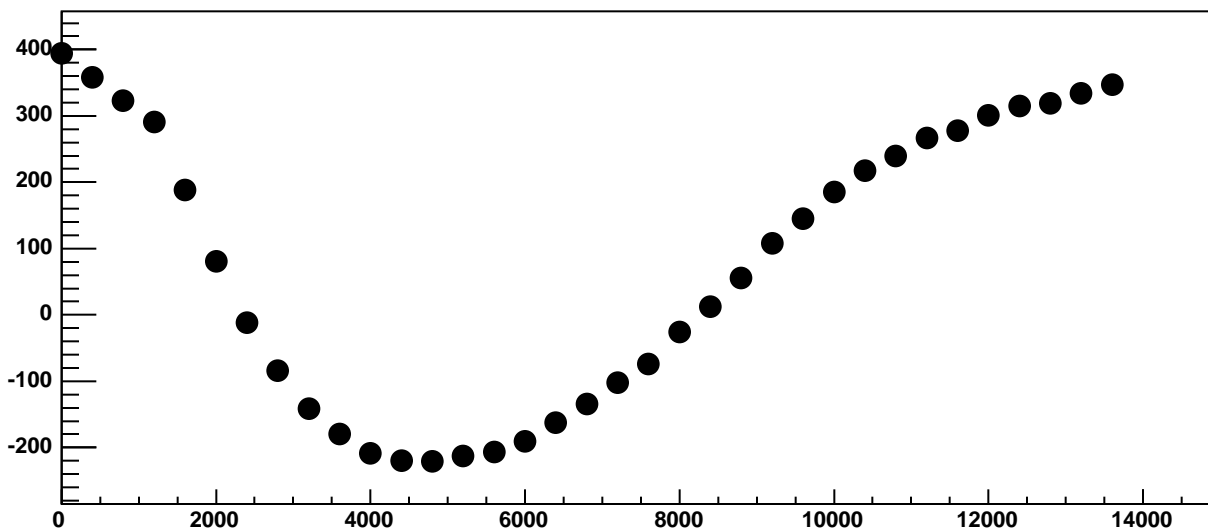
Chip 9, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



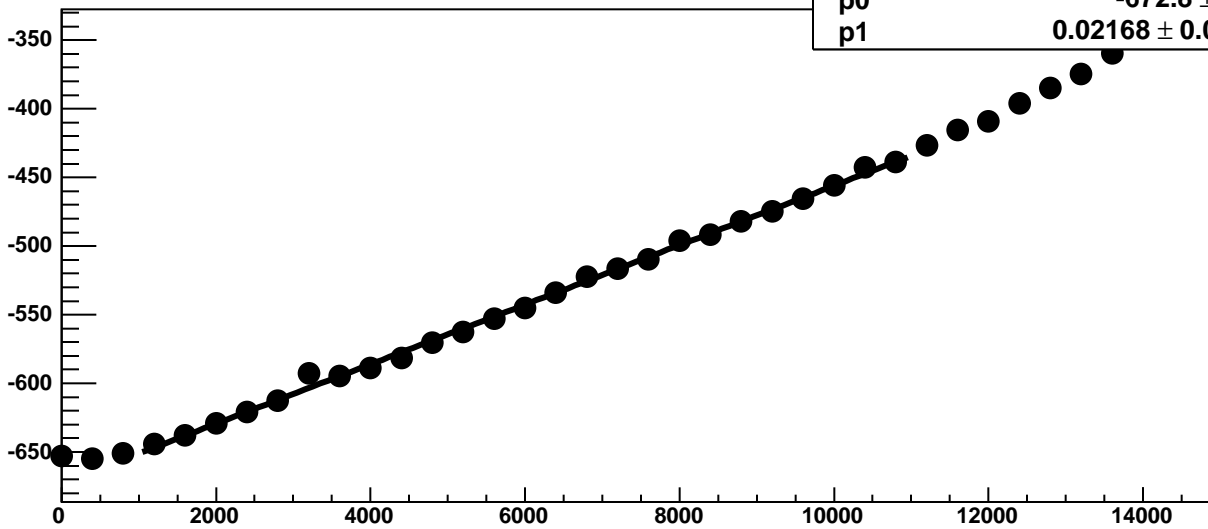
Chip 9, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

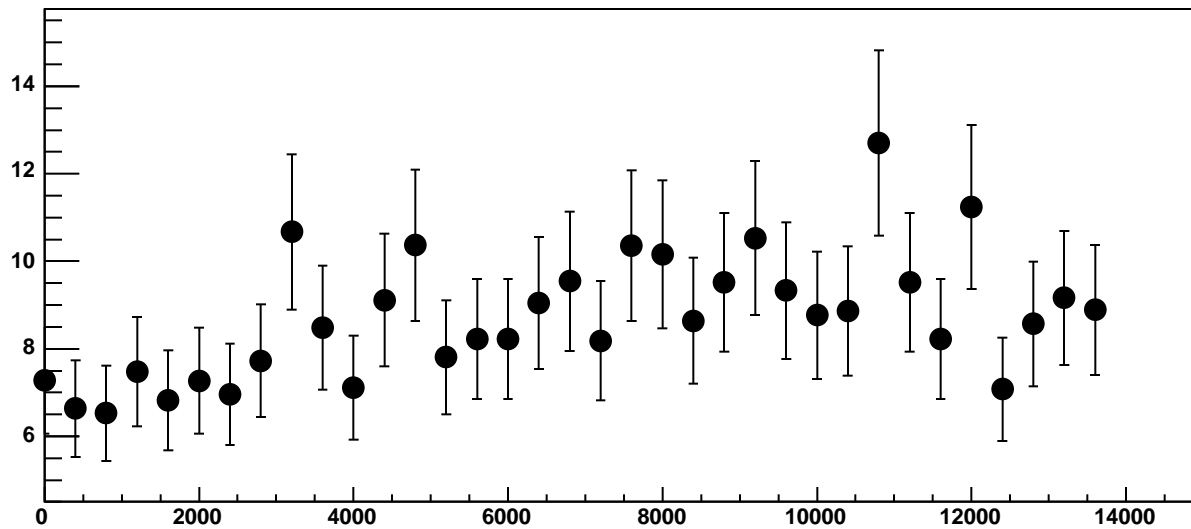


Chip 9, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

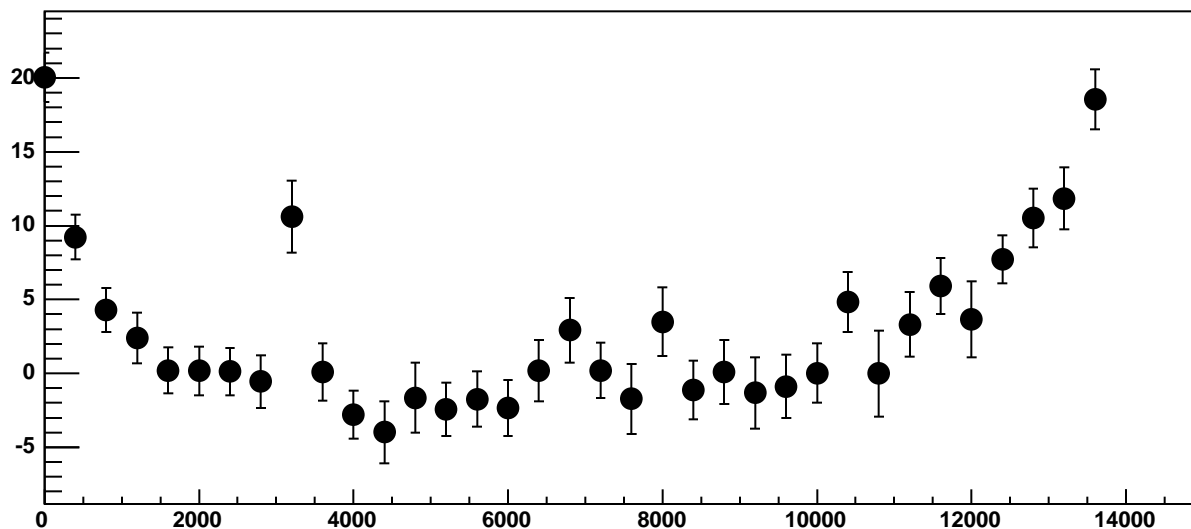


χ^2 / ndf 43.18 / 23
p0 -672.8 ± 0.8373
p1 0.02168 ± 0.0001361

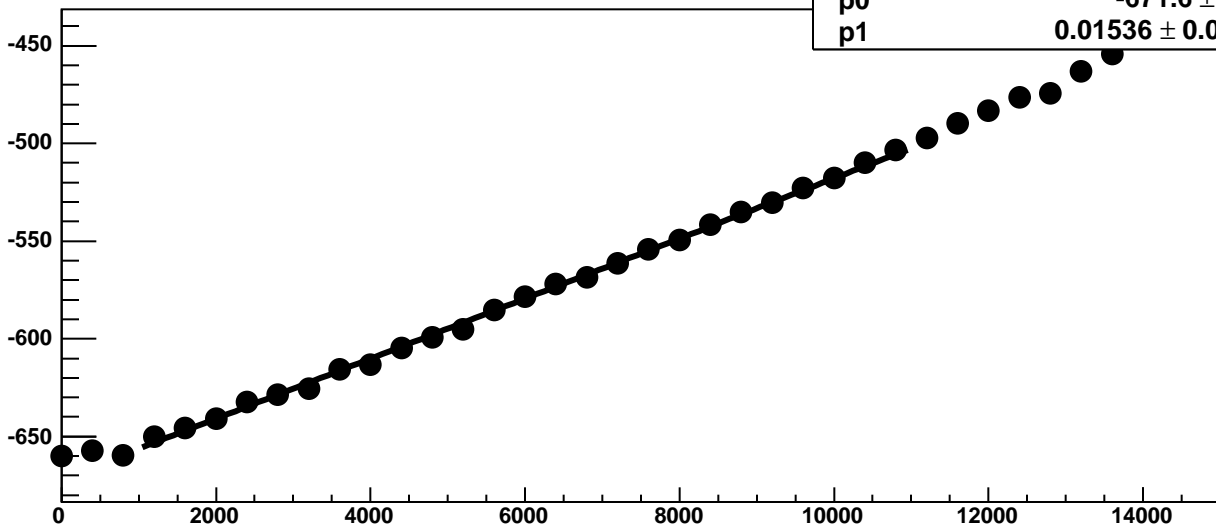
Chip 9, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

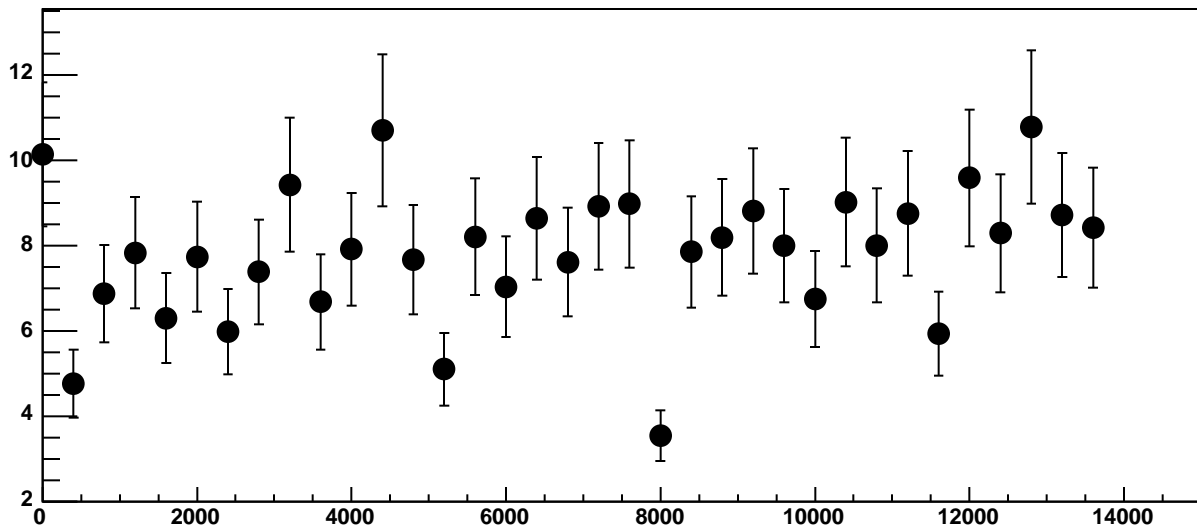


Chip 9, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

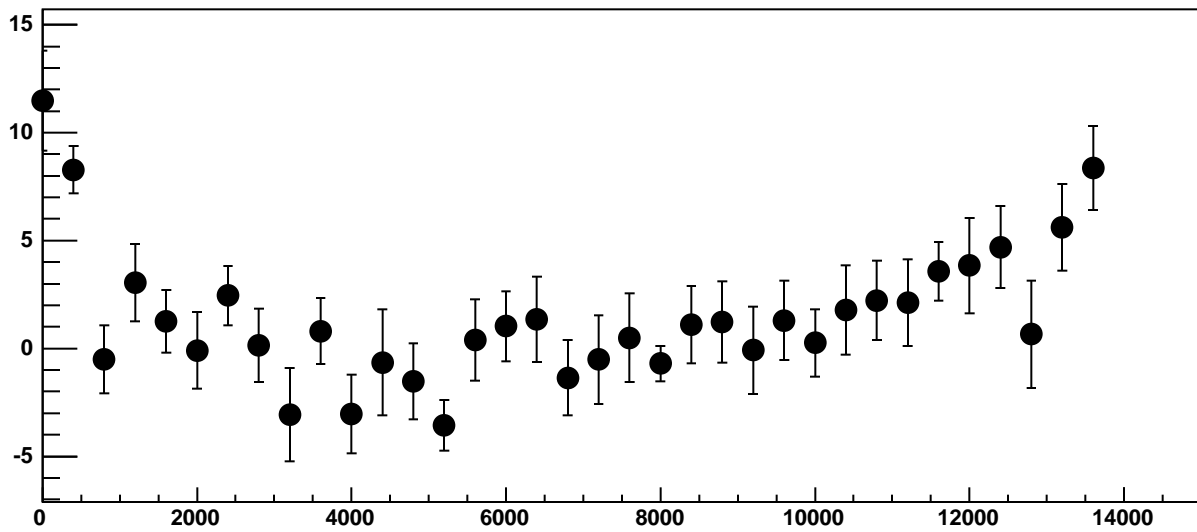


χ^2 / ndf 27.92 / 23
p0 -671.6 ± 0.7699
p1 0.01536 ± 0.0001161

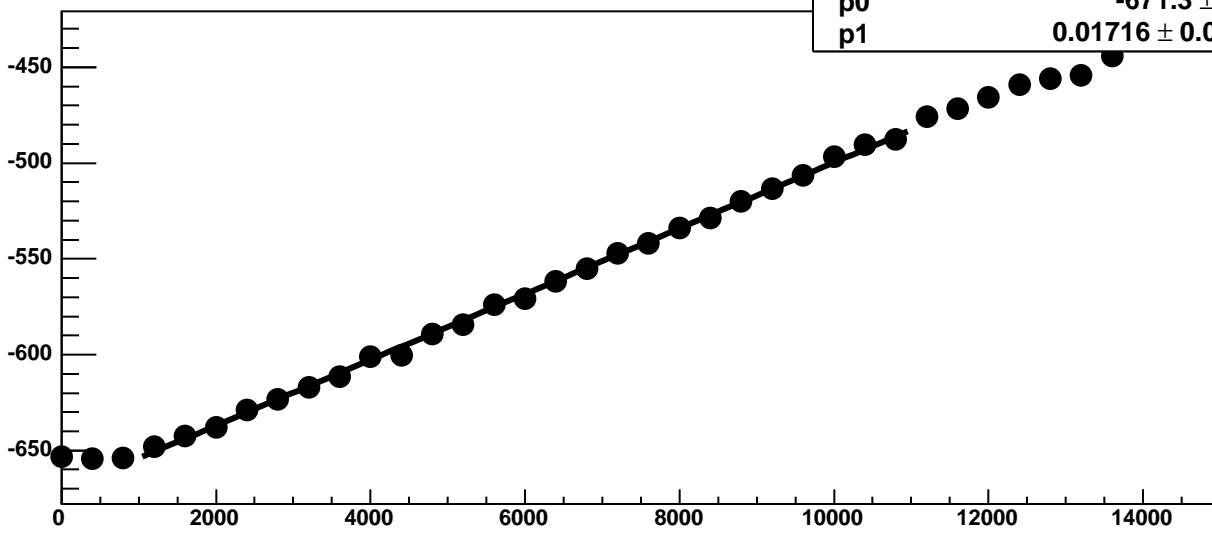
Chip 9, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

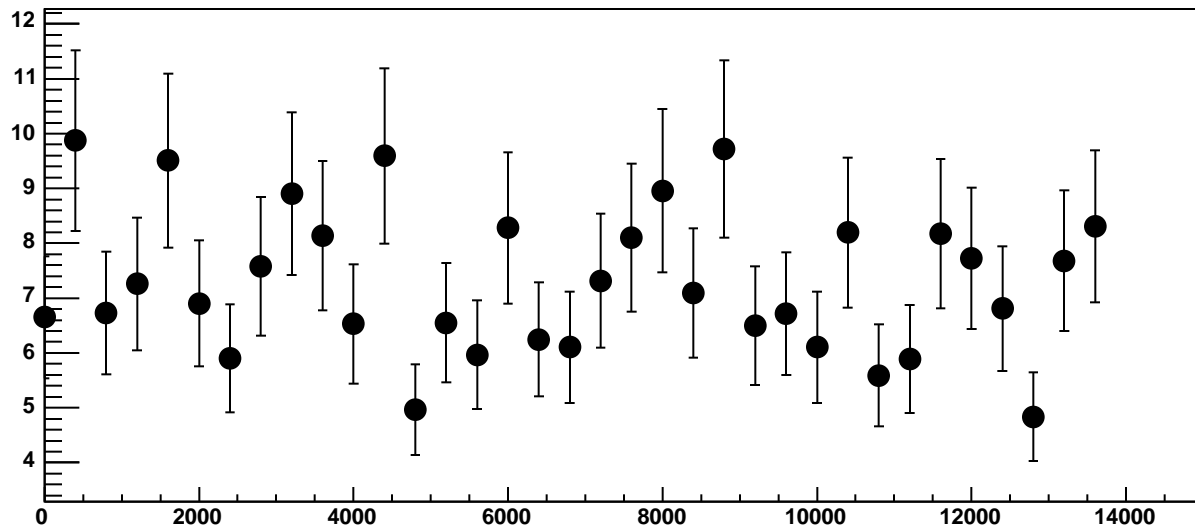


Chip 9, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

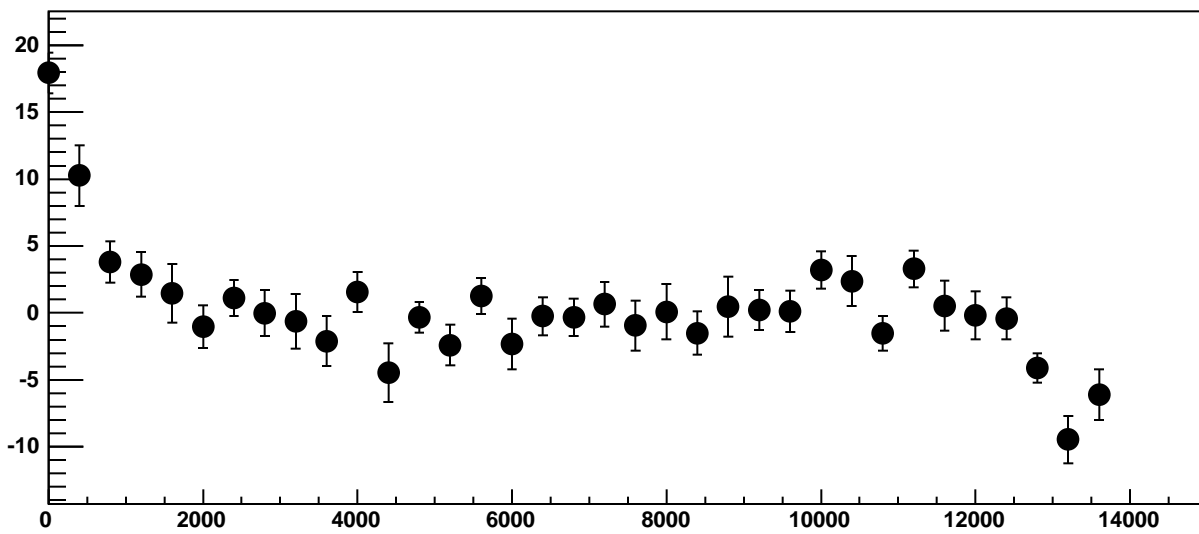


χ^2 / ndf 25.71 / 23
p0 -671.3 ± 0.7545
p1 0.01716 ± 0.0001115

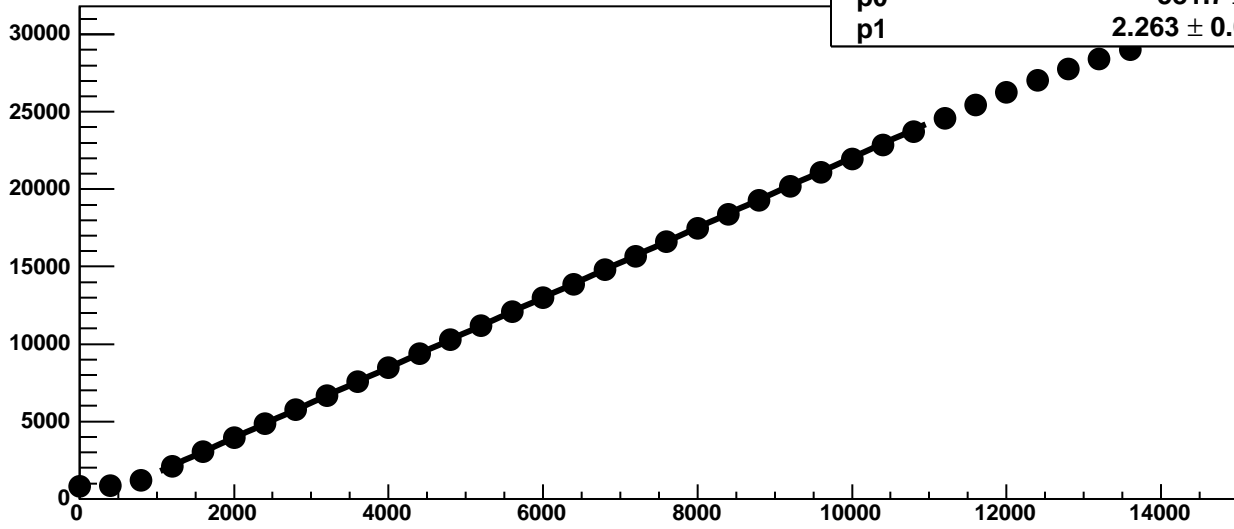
Chip 9, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



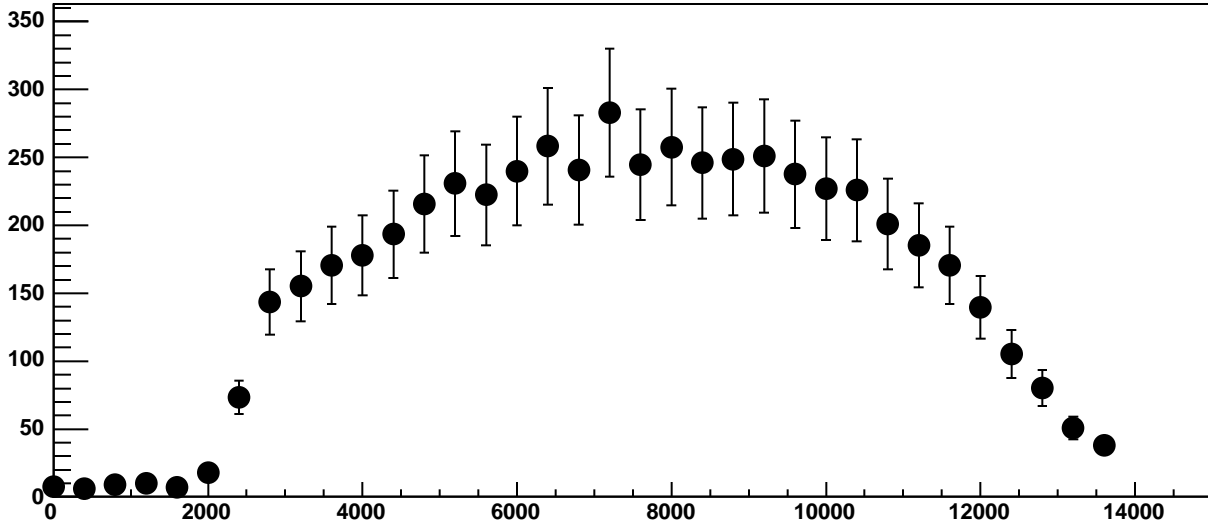
Chip 9, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



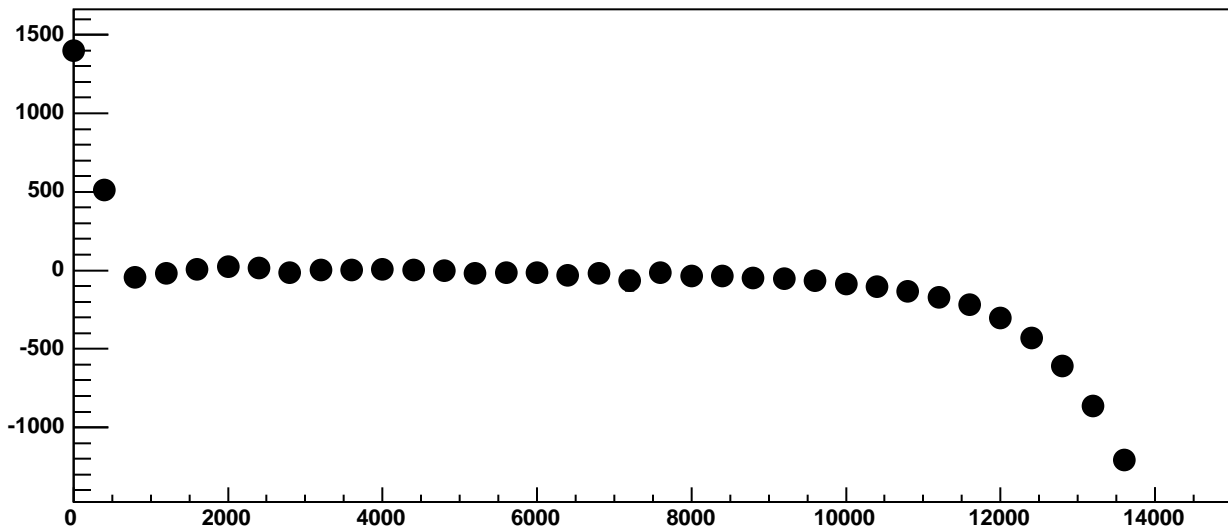
Chip 9, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC



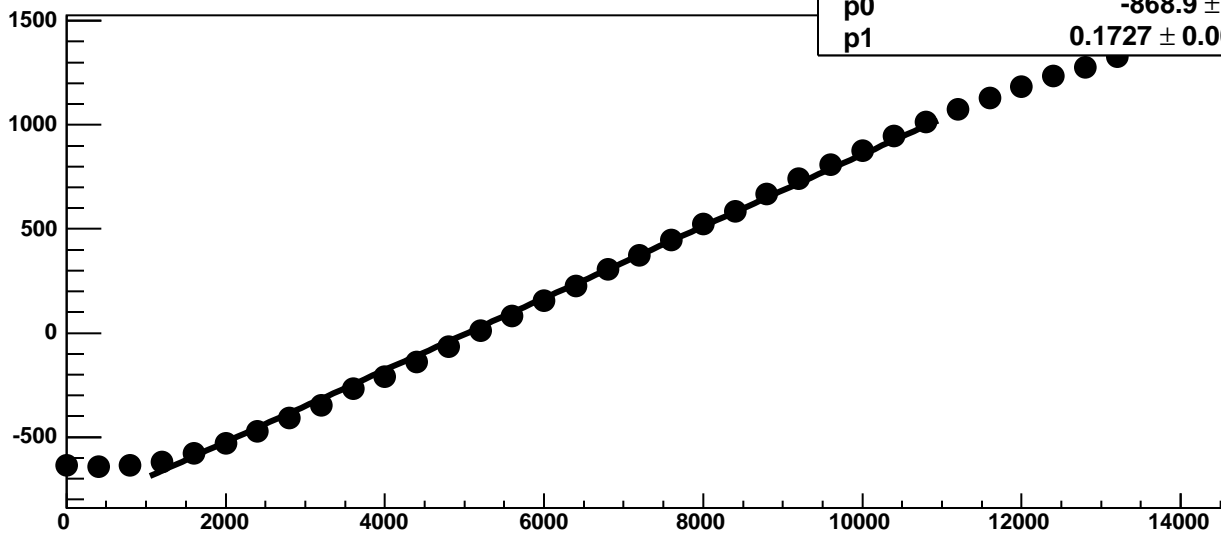
Chip 9, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC



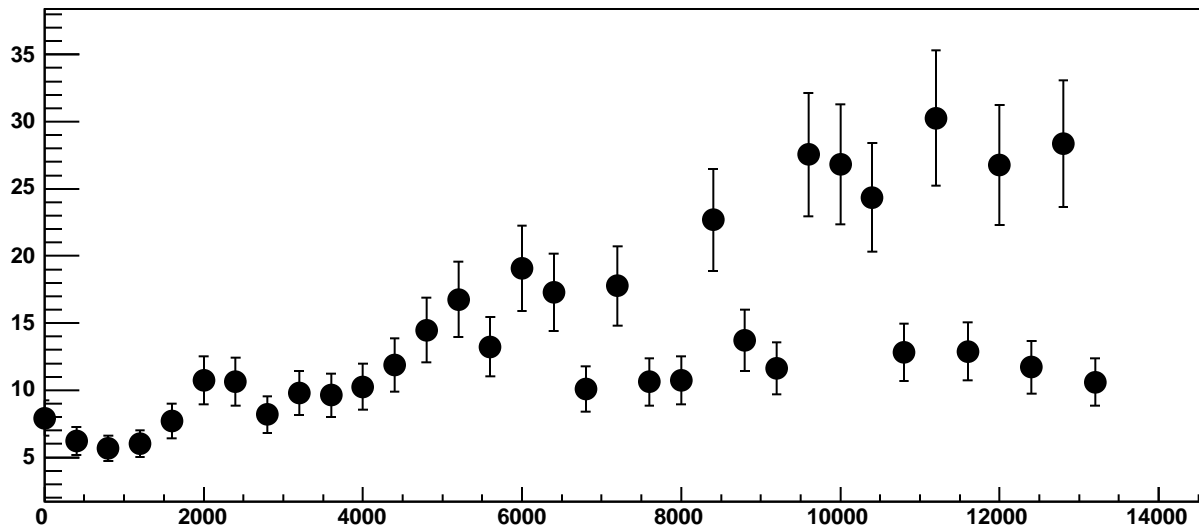
Chip 9, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC



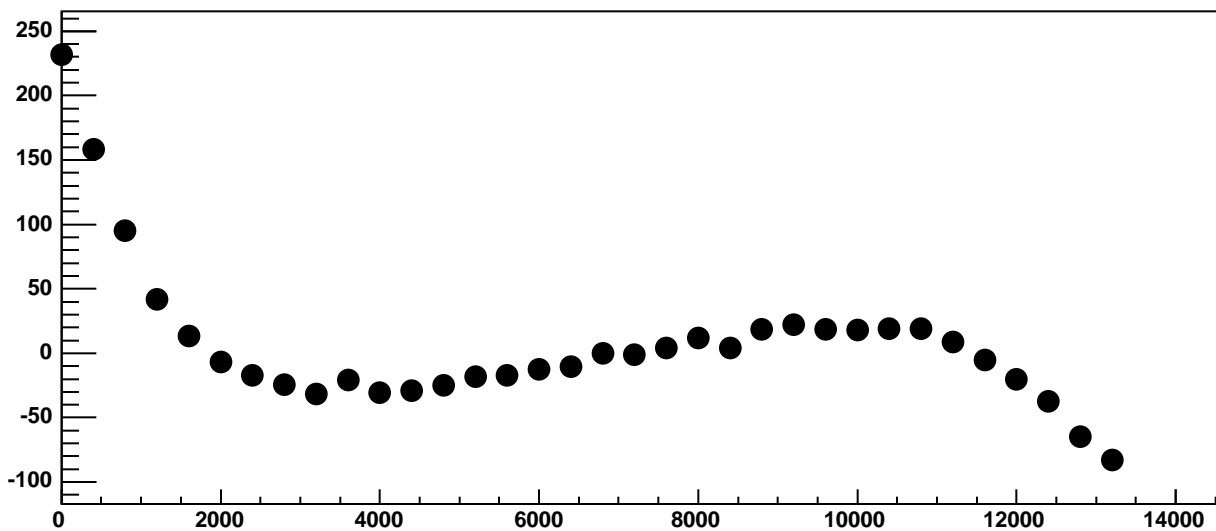
Chip 9, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



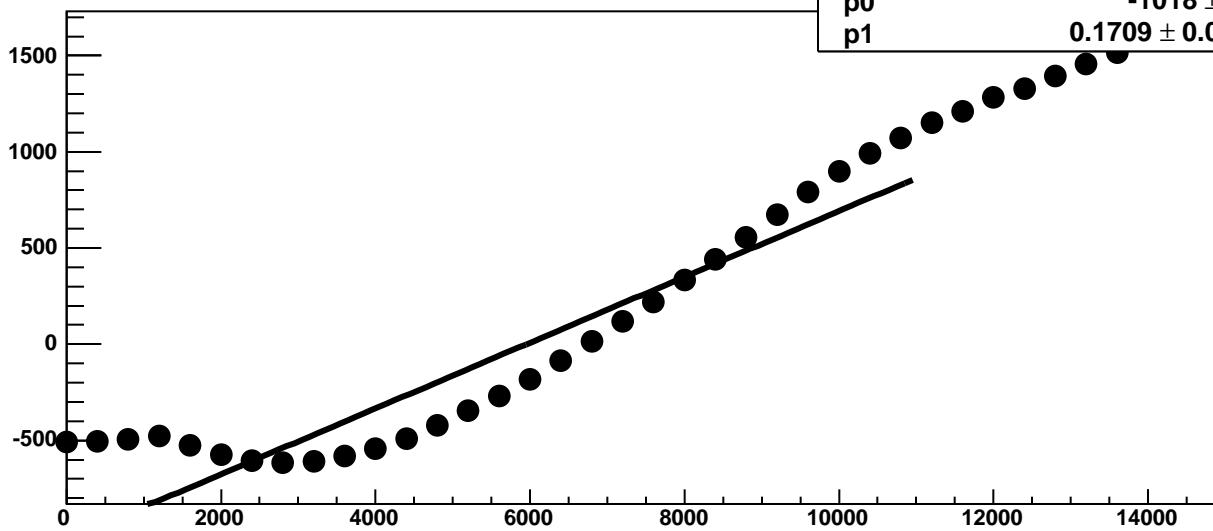
Chip 9, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



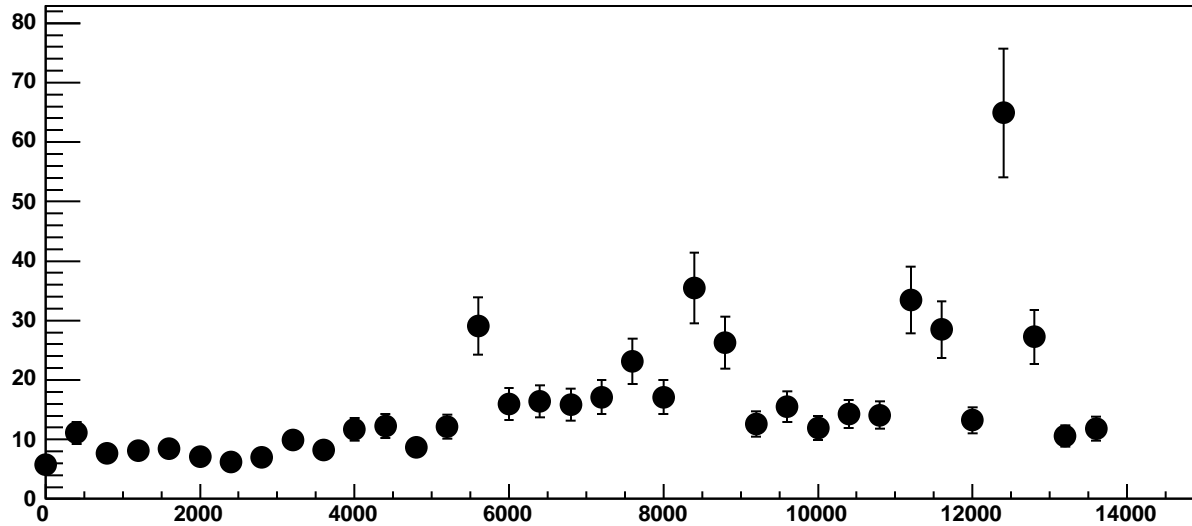
Chip 9, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



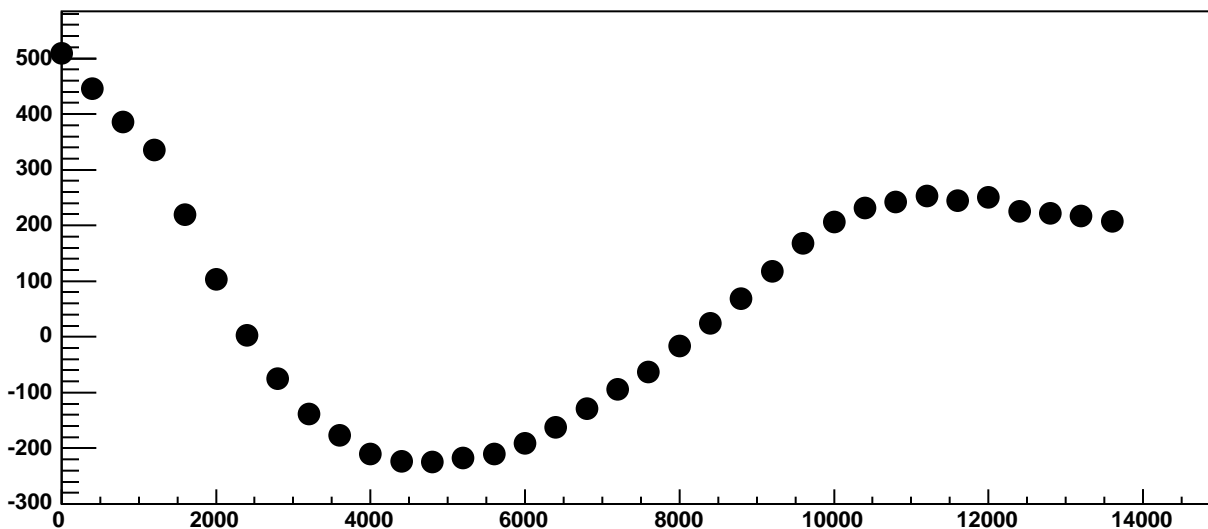
Chip 9, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



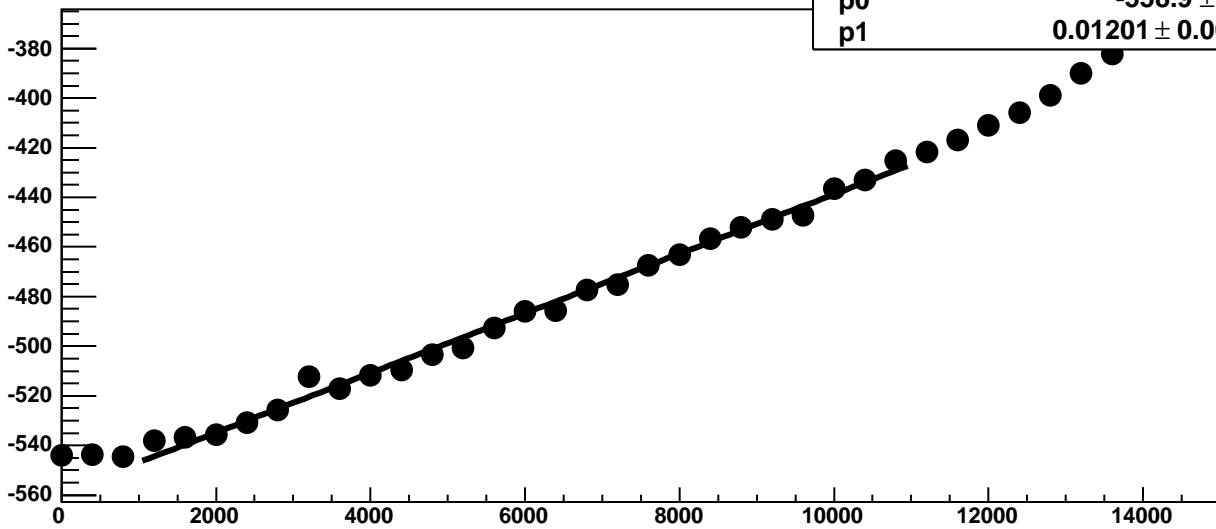
Chip 9, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

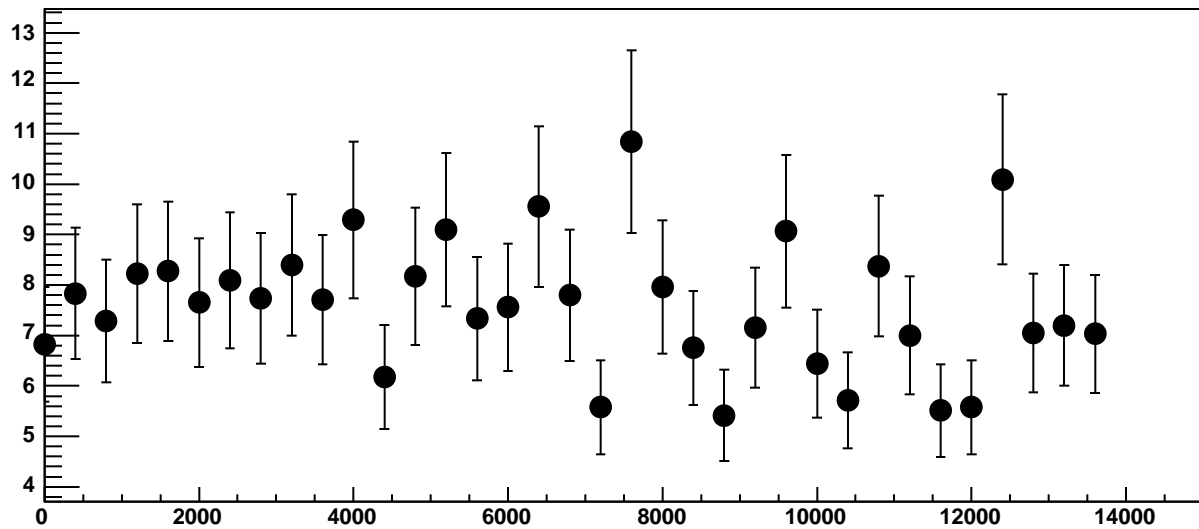


Chip 9, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

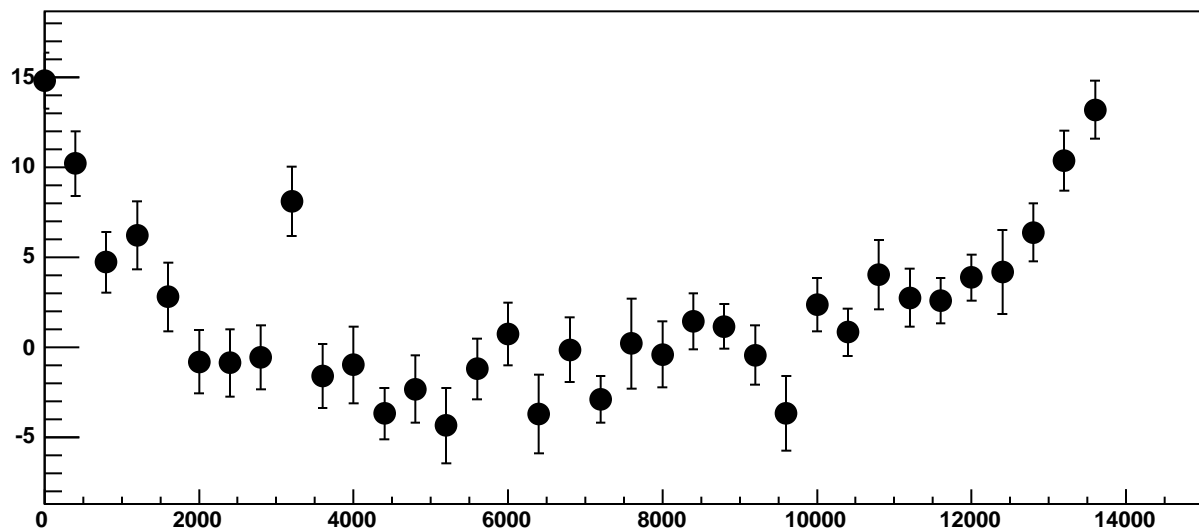


χ^2 / ndf 65.9 / 23
p0 -558.9 ± 0.8263
p1 0.01201 ± 0.0001183

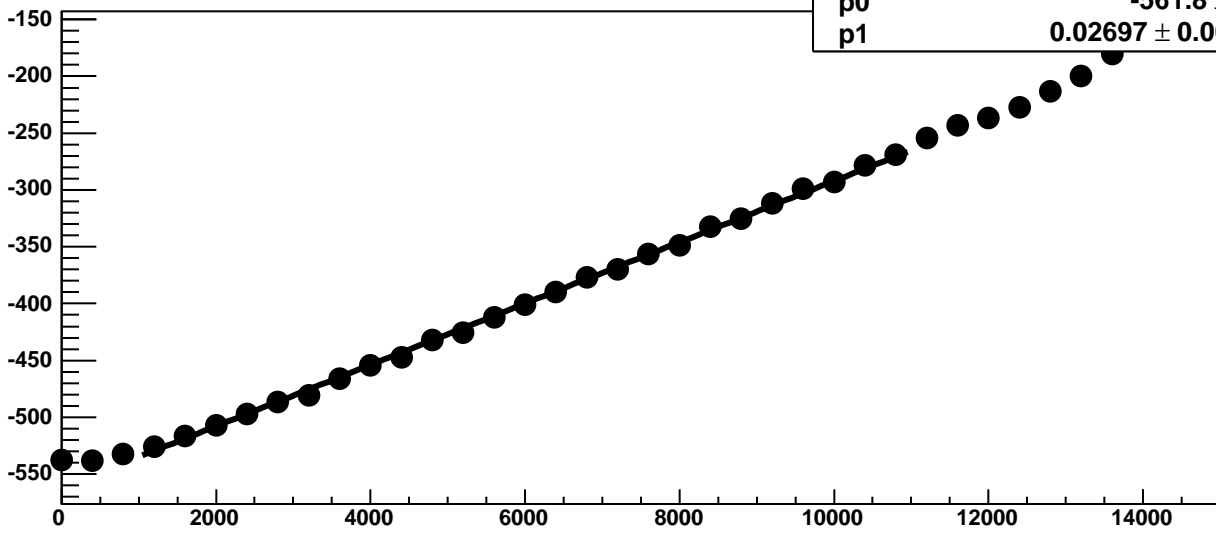
Chip 9, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



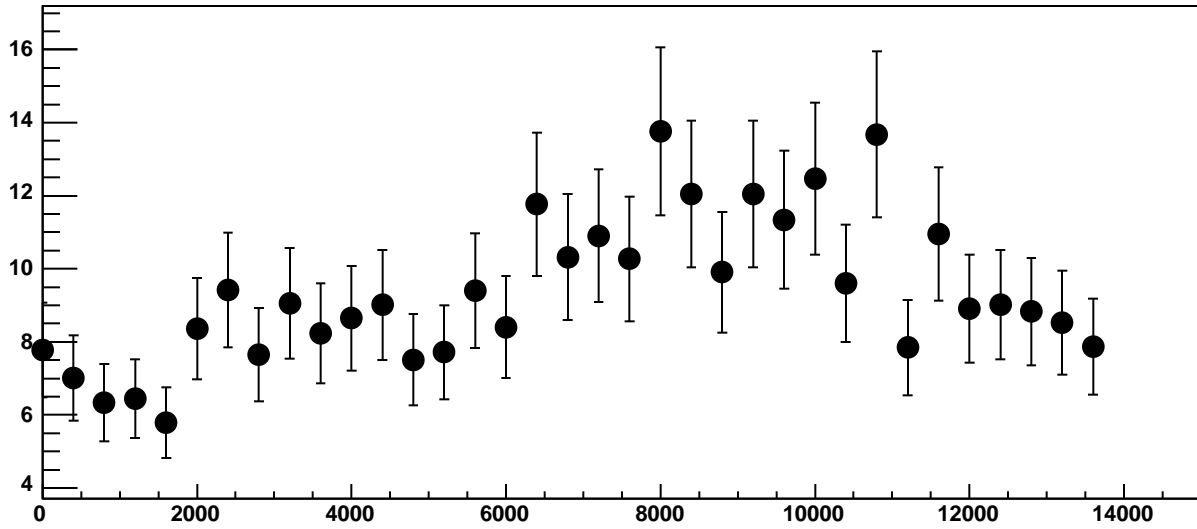
Chip 9, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



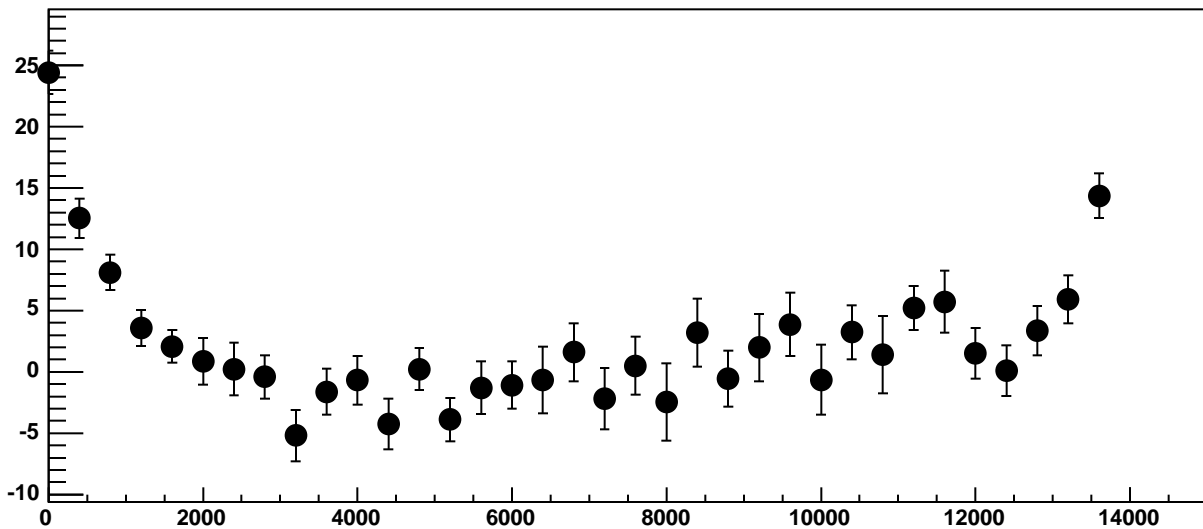
Chip 9, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC



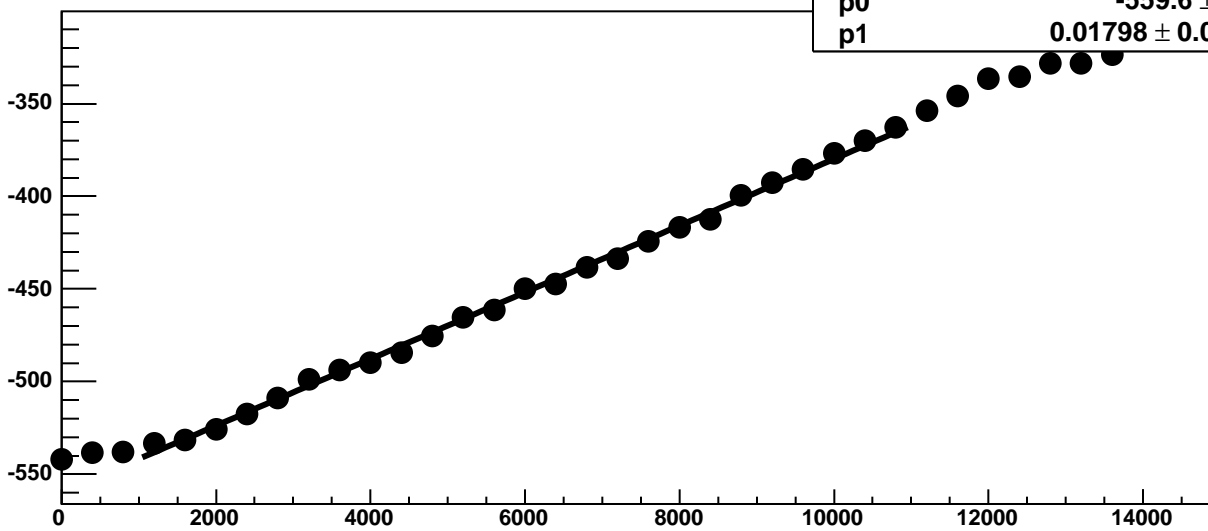
Chip 9, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC

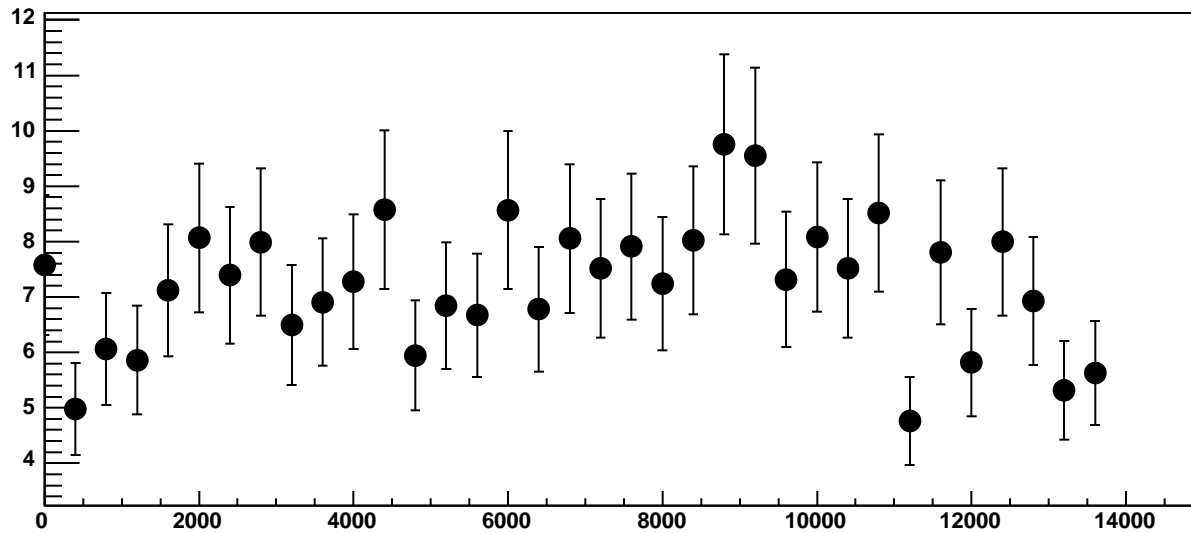


Chip 9, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

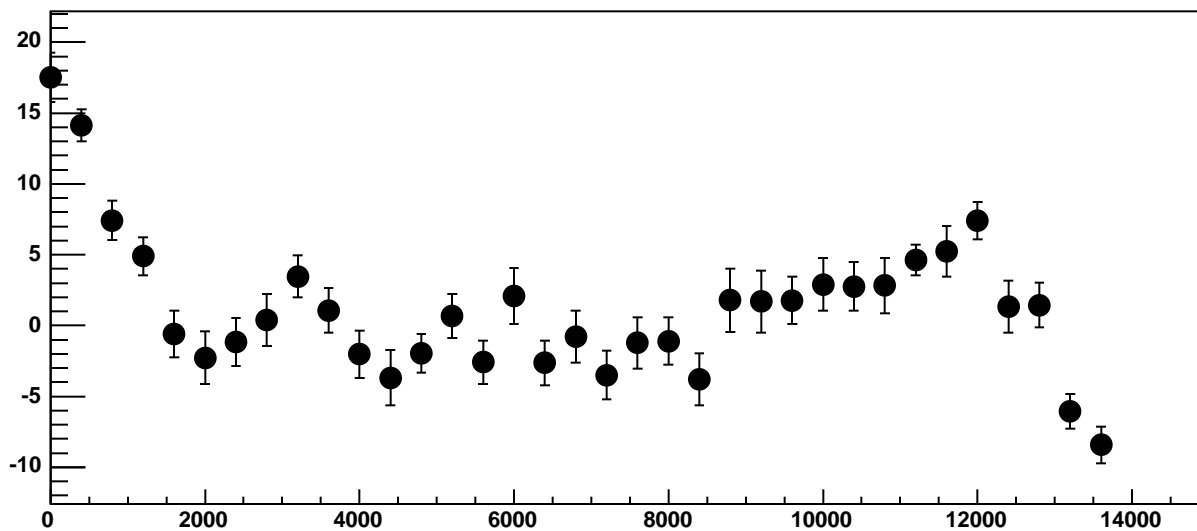


χ^2 / ndf 54.29 / 23
p0 -559.6 ± 0.7548
p1 0.01798 ± 0.0001193

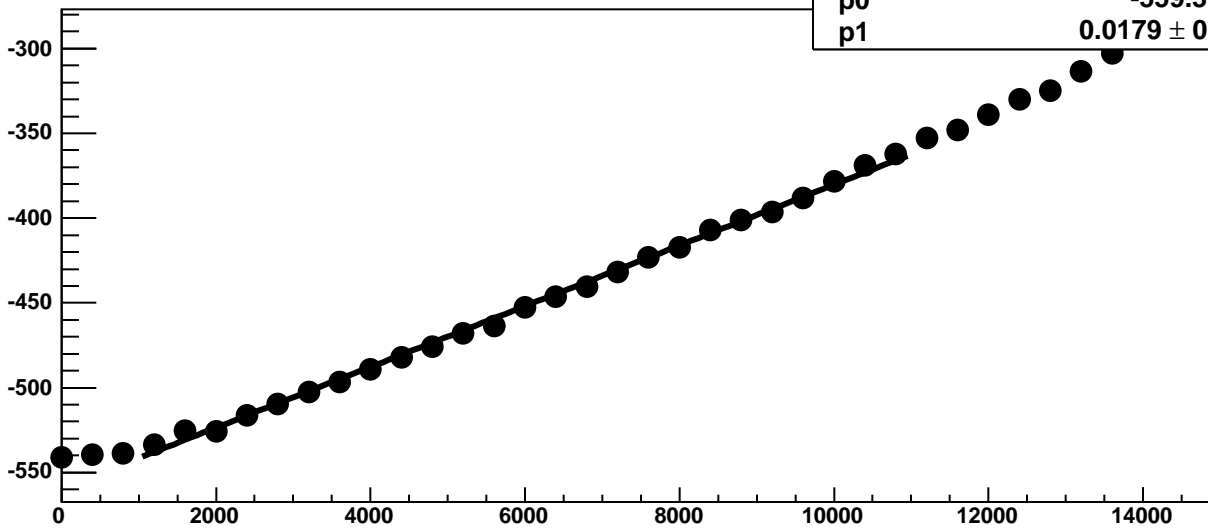
Chip 9, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

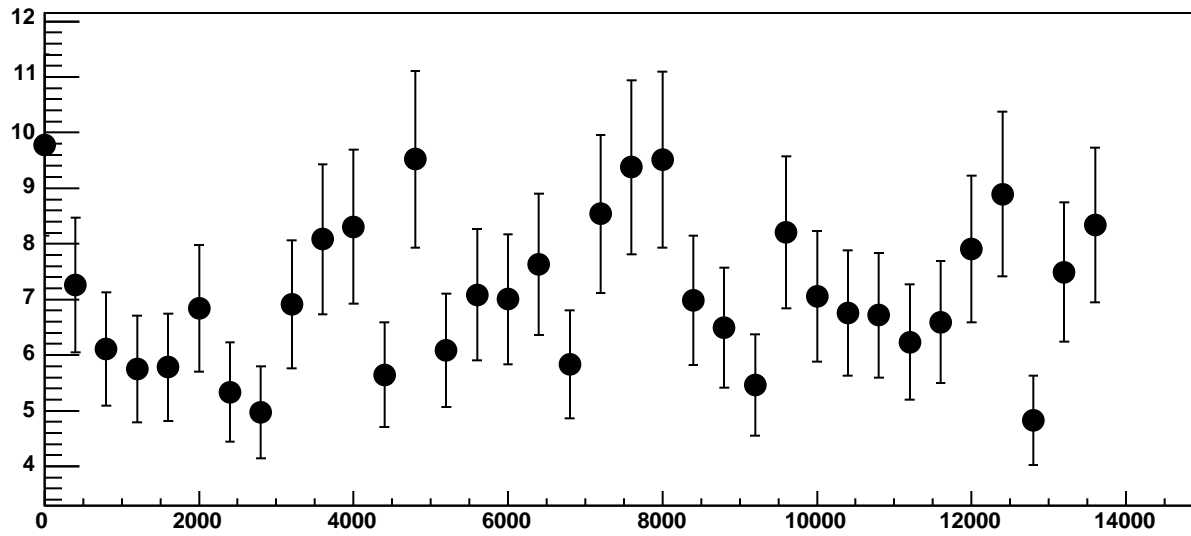


Chip 9, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

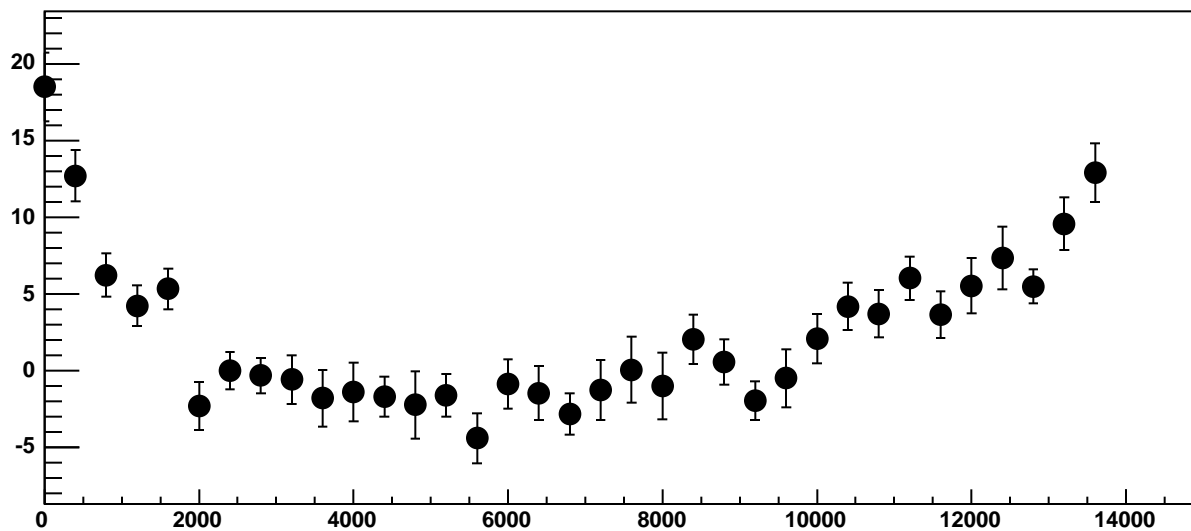


χ^2 / ndf 66.77 / 23
p0 -559.3 ± 0.654
p1 0.0179 ± 0.000102

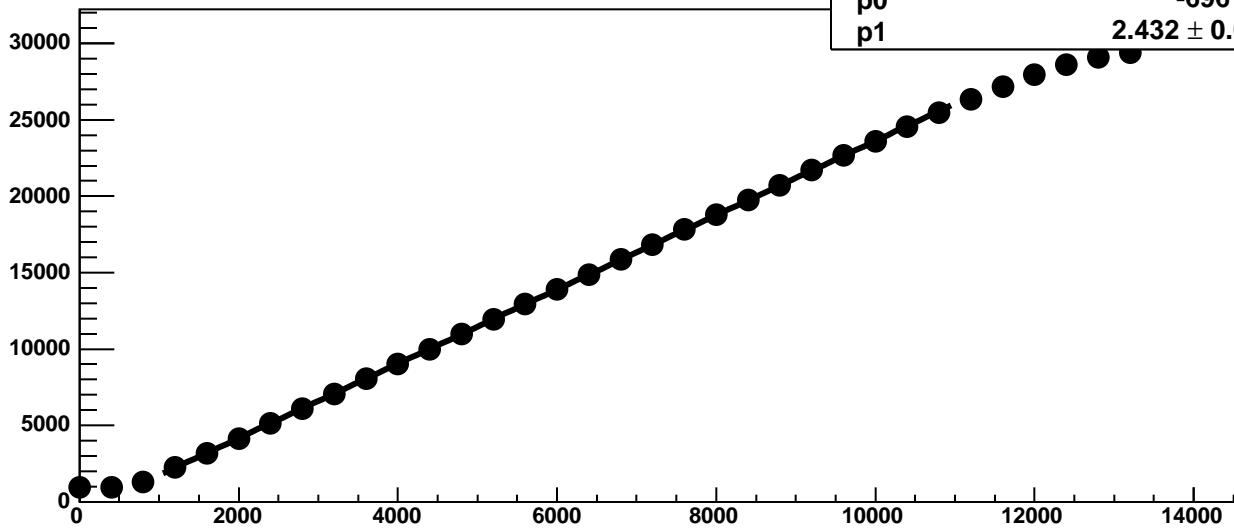
Chip 9, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



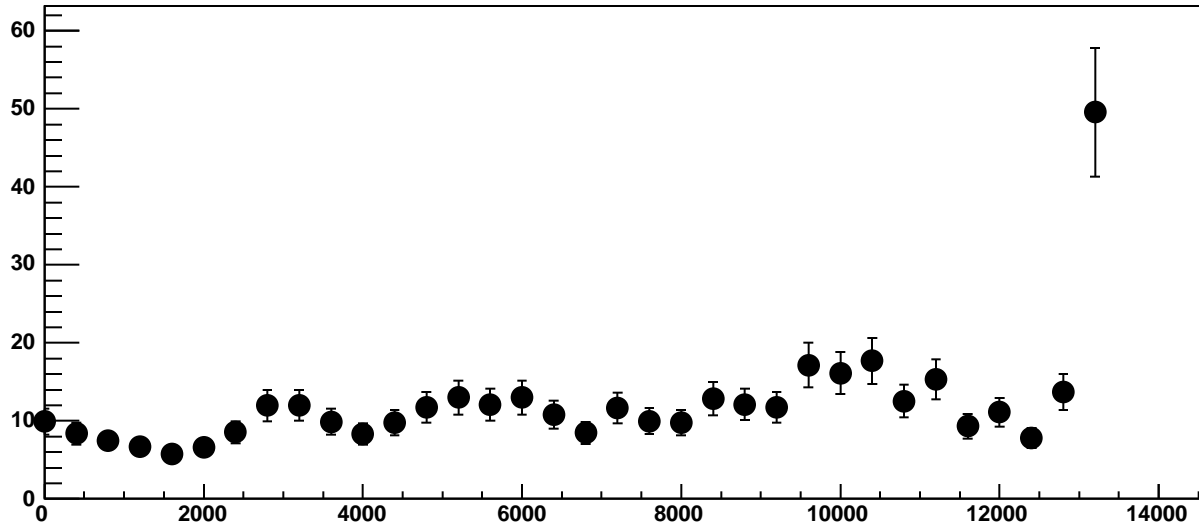
Chip 9, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



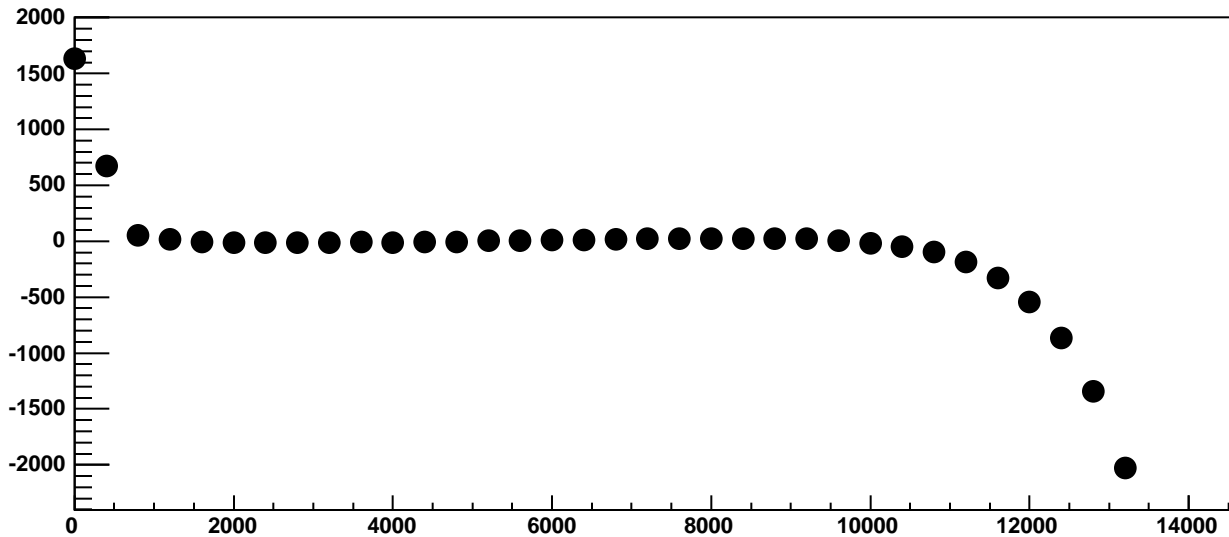
Chip 9, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC



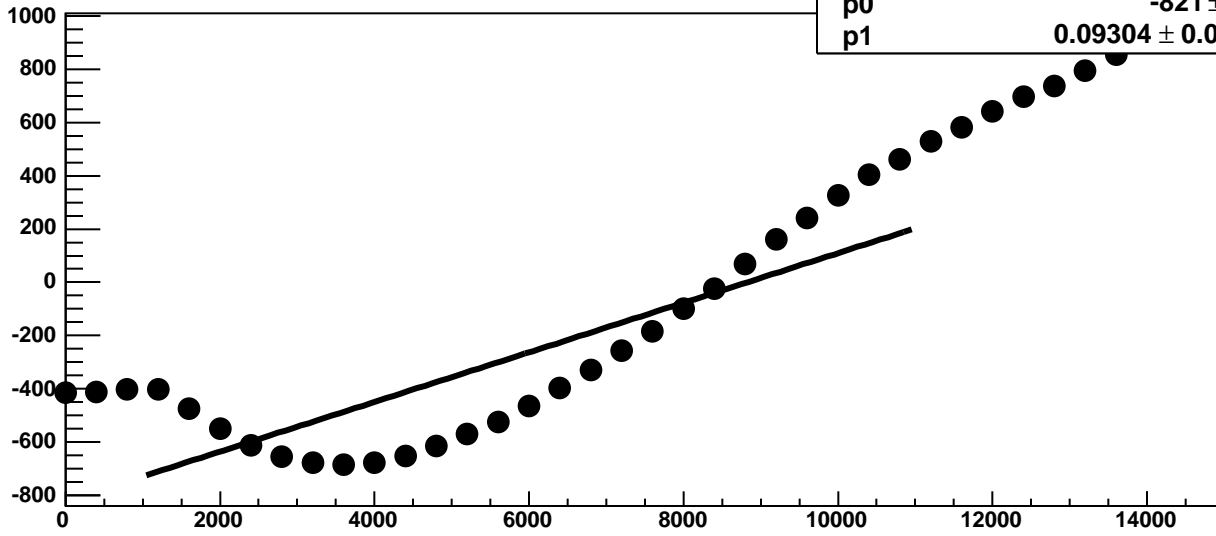
Chip 9, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC

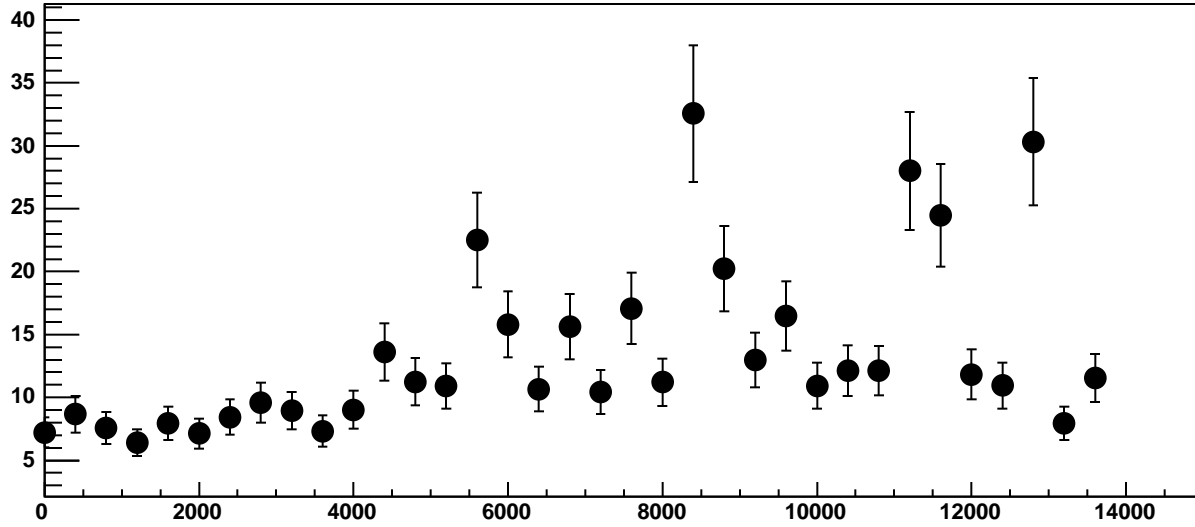


Chip 9, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

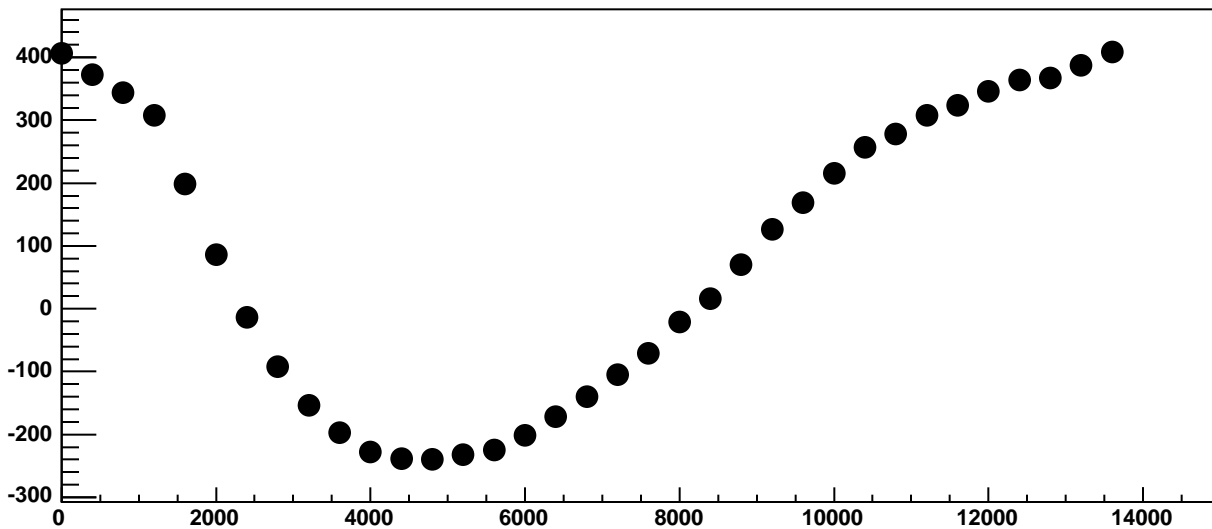


χ^2 / ndf 1.589e+05 / 23
p0 -821 ± 0.9011
p1 0.09304 ± 0.0001632

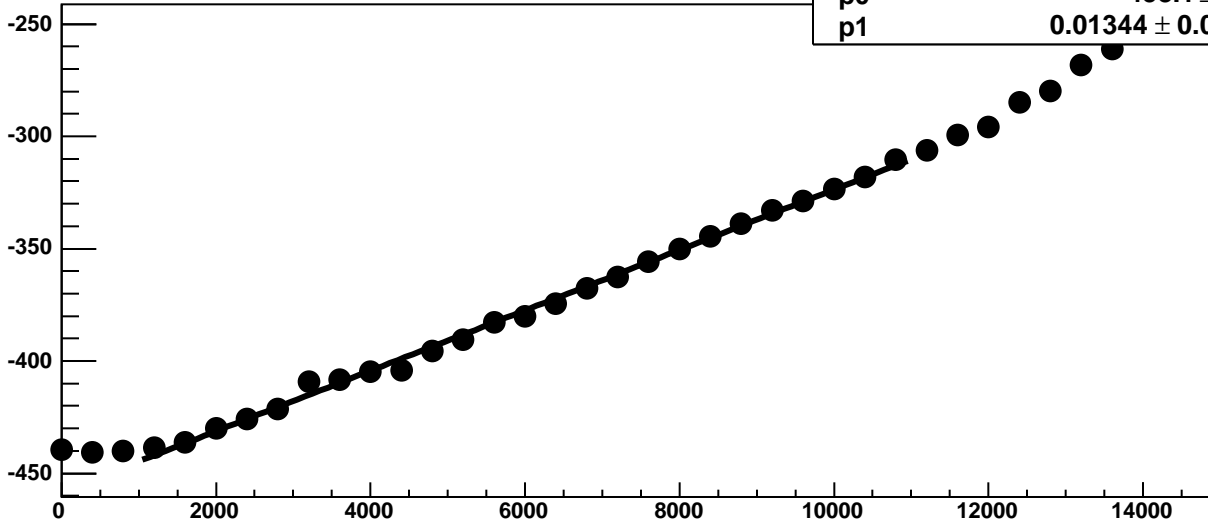
Chip 9, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

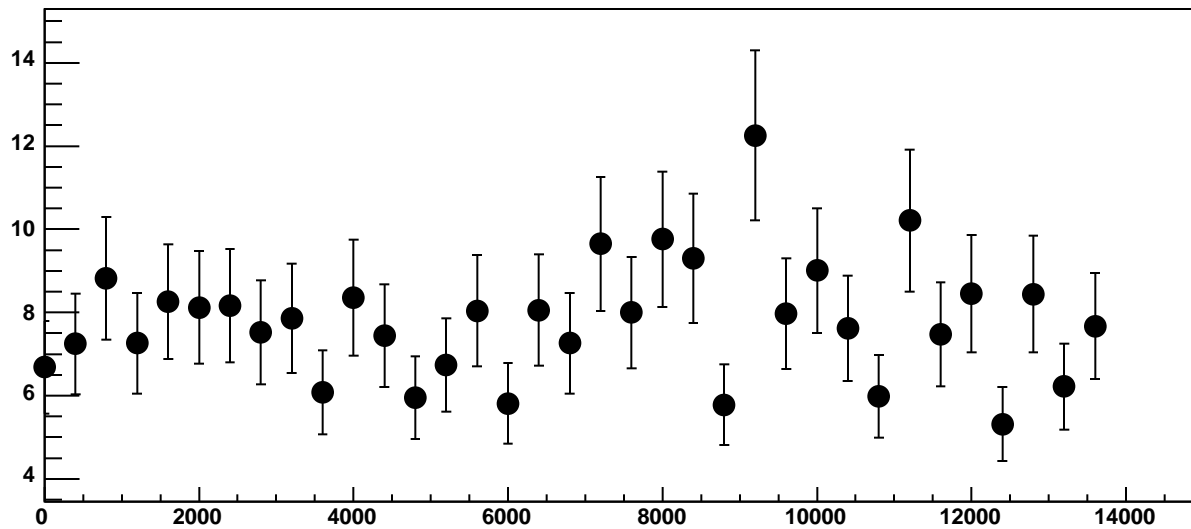


Chip 9, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

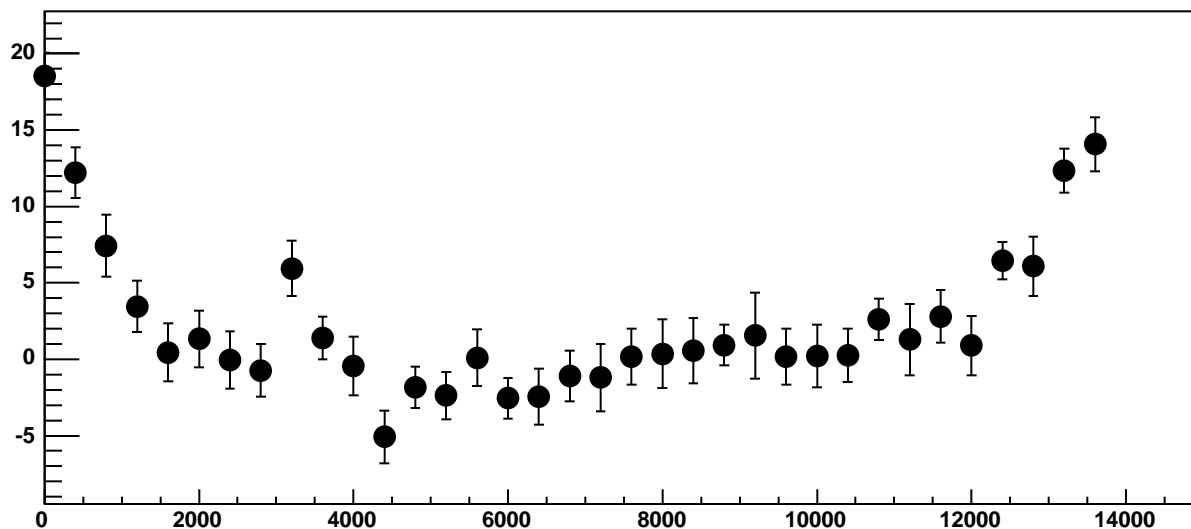


χ^2 / ndf 40.66 / 23
p0 -458.1 ± 0.7934
p1 0.01344 ± 0.0001202

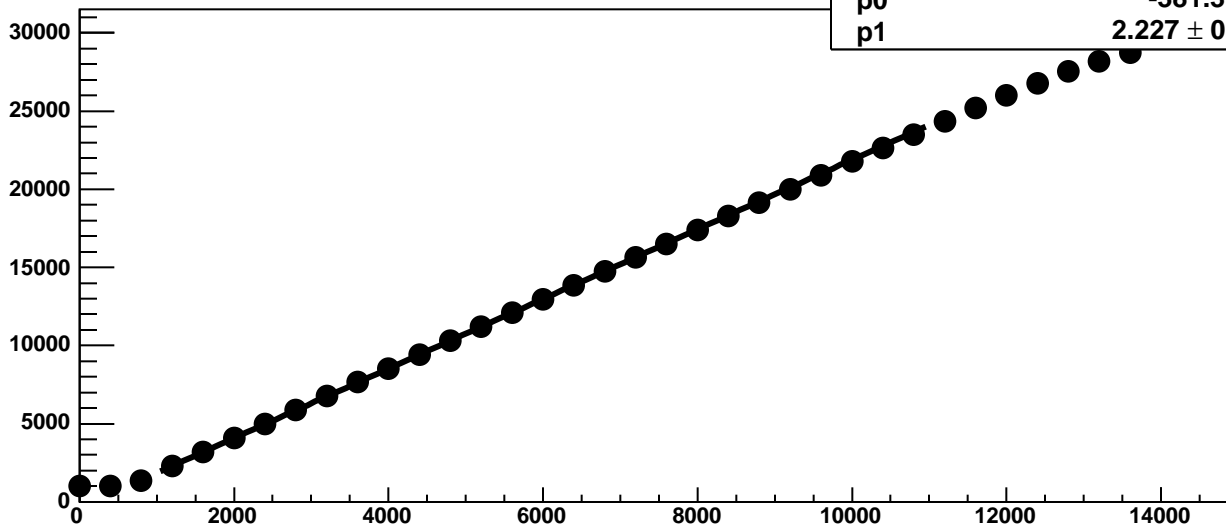
Chip 9, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



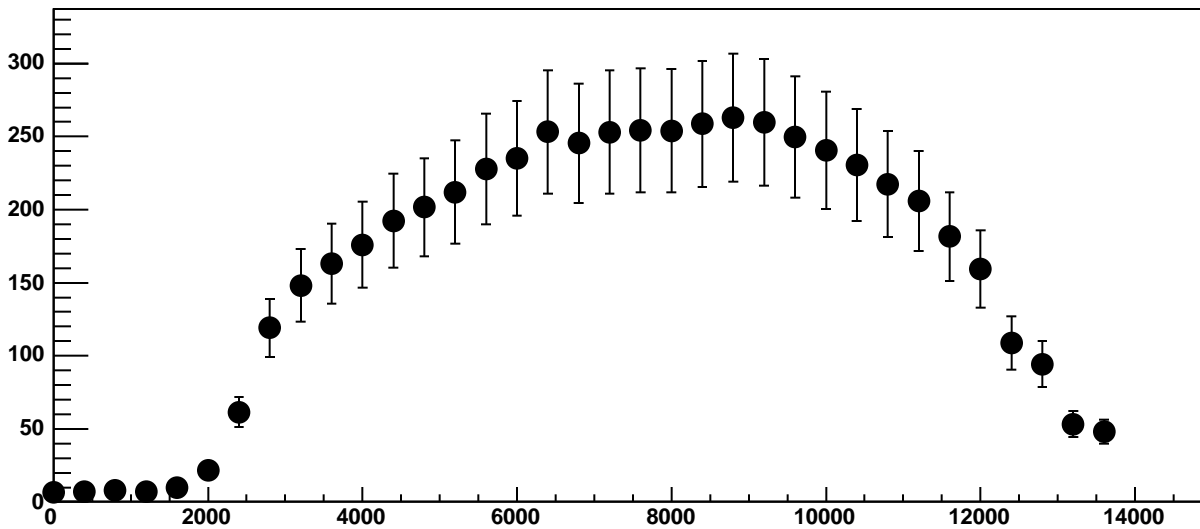
Chip 9, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



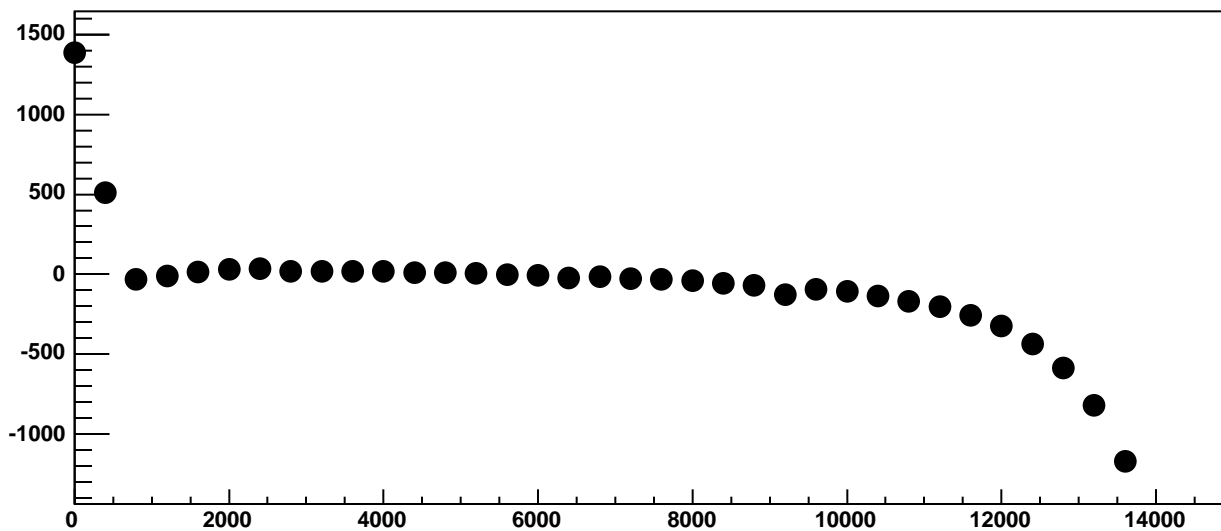
Chip 9, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC



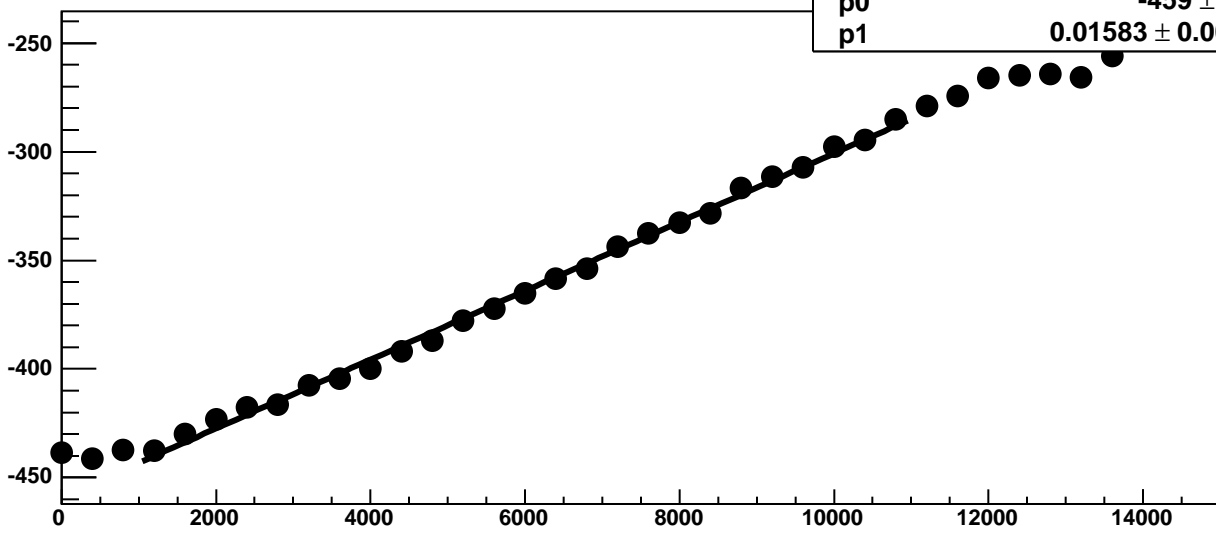
Chip 9, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC

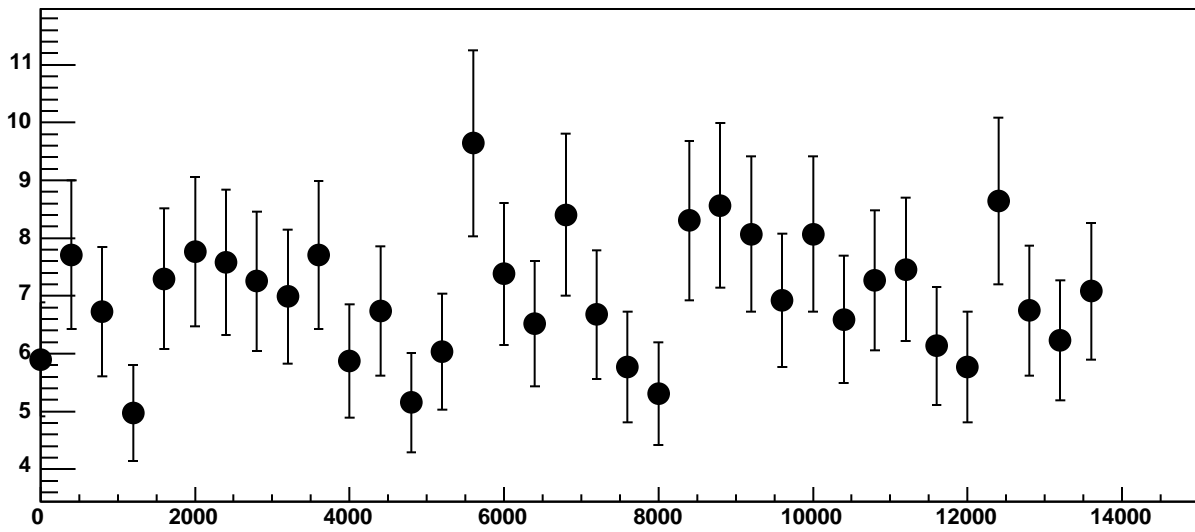


Chip 9, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

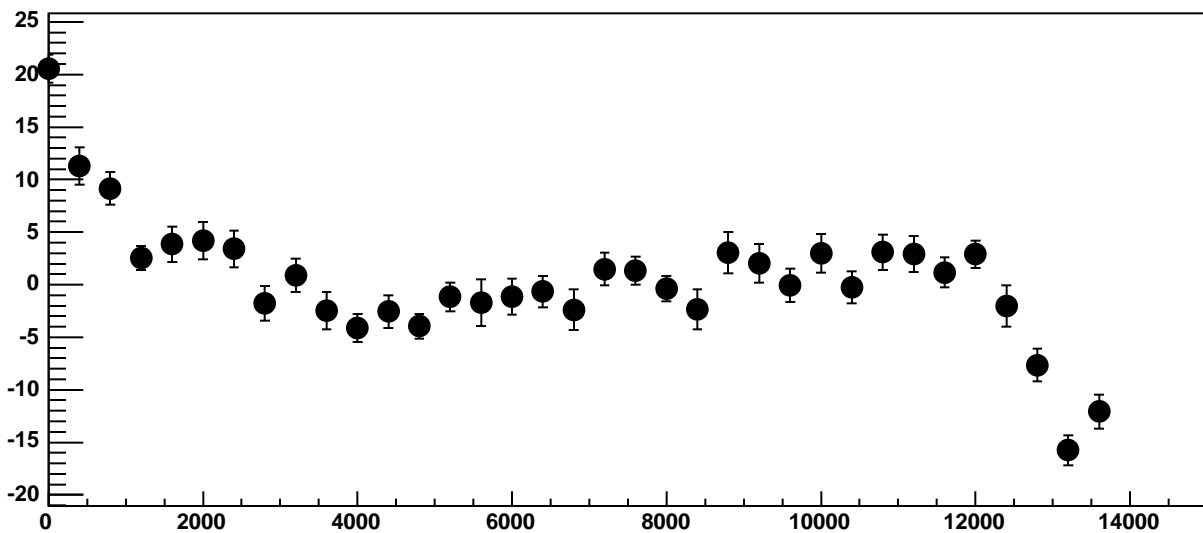


χ^2 / ndf 63.19 / 23
p0 -459 ± 0.7015
p1 0.01583 ± 0.0001085

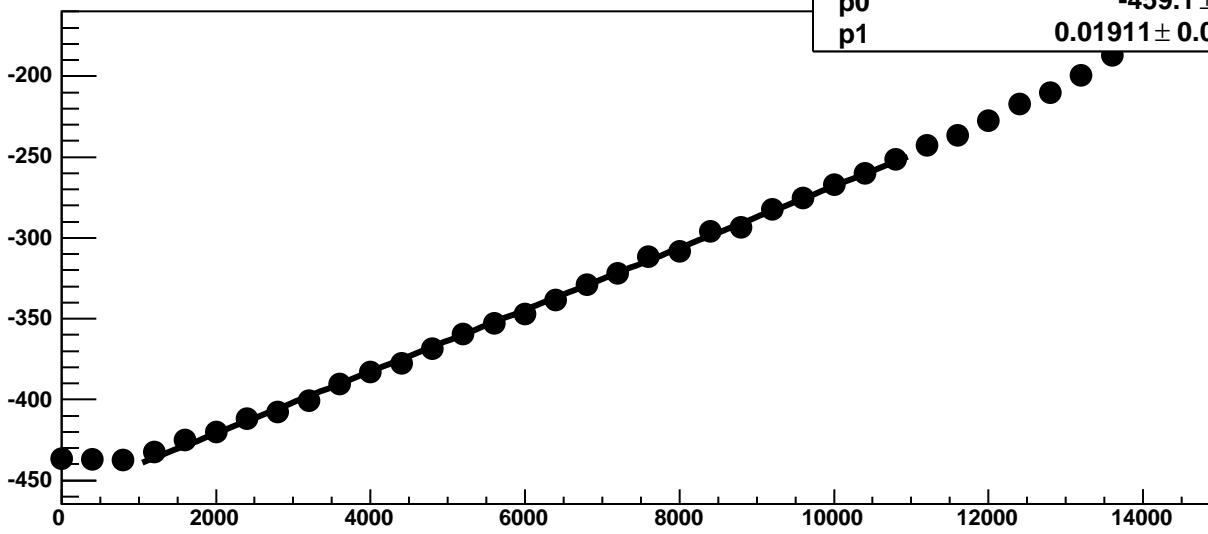
Chip 9, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

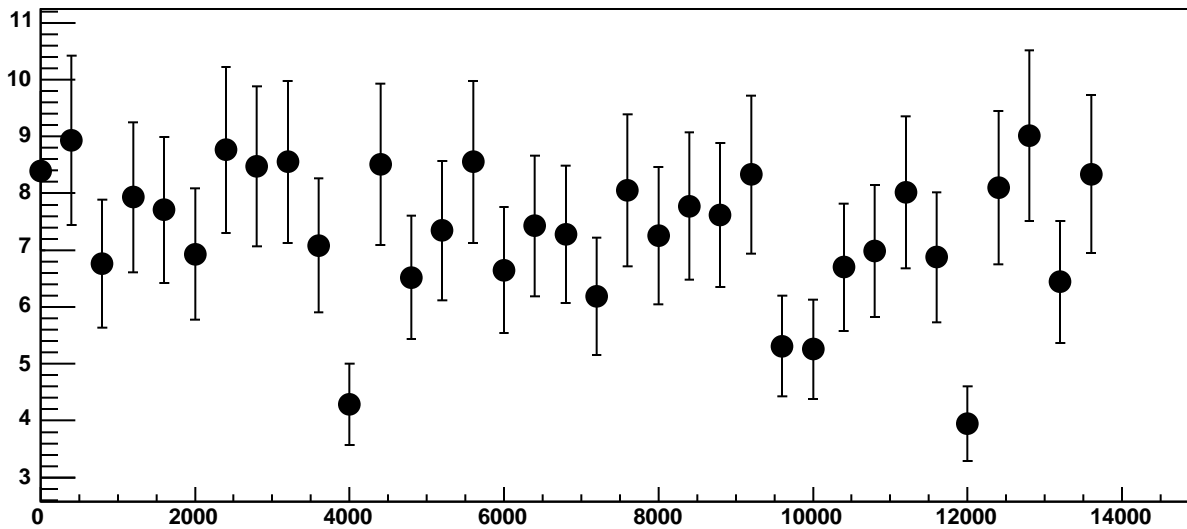


Chip 9, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

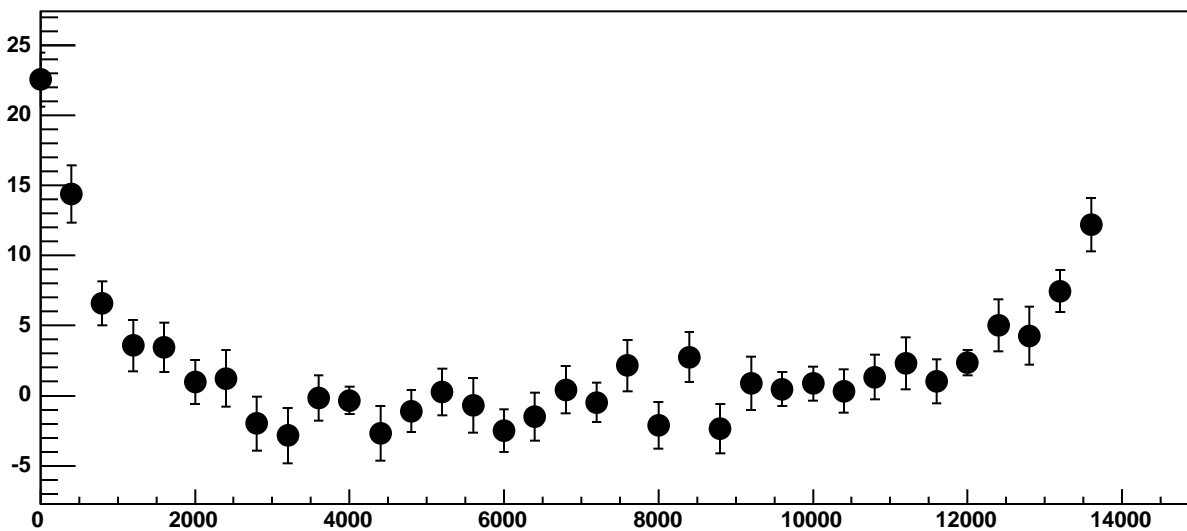


χ^2 / ndf 26.73 / 23
p0 -459.1 ± 0.7596
p1 0.01911 ± 0.0001104

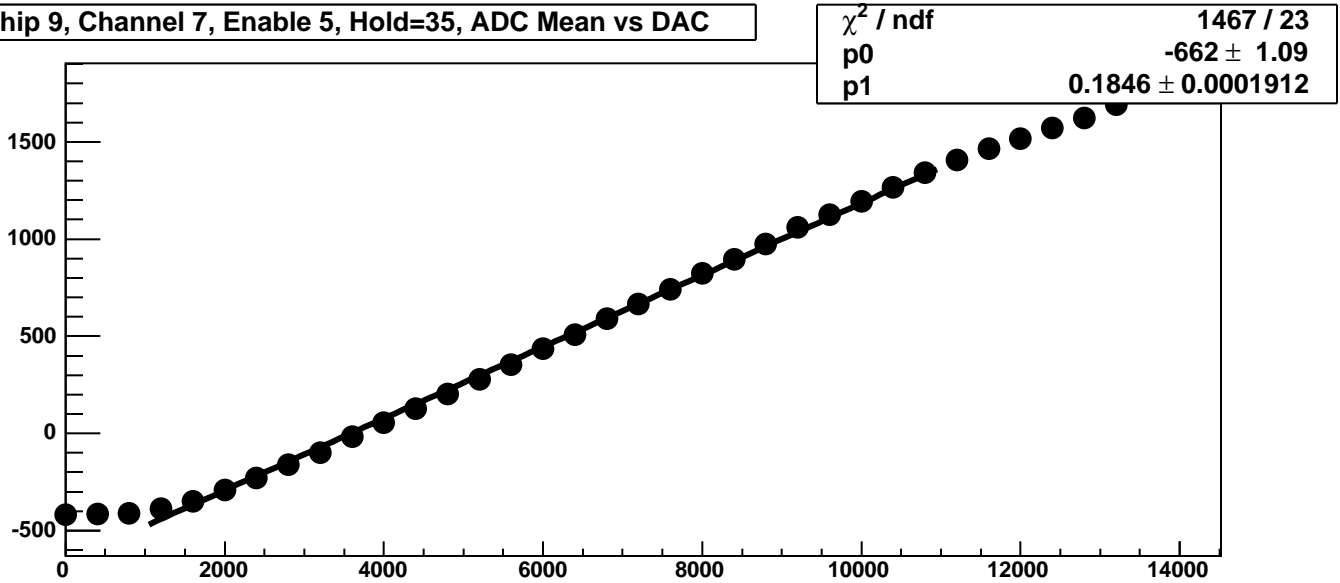
Chip 9, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



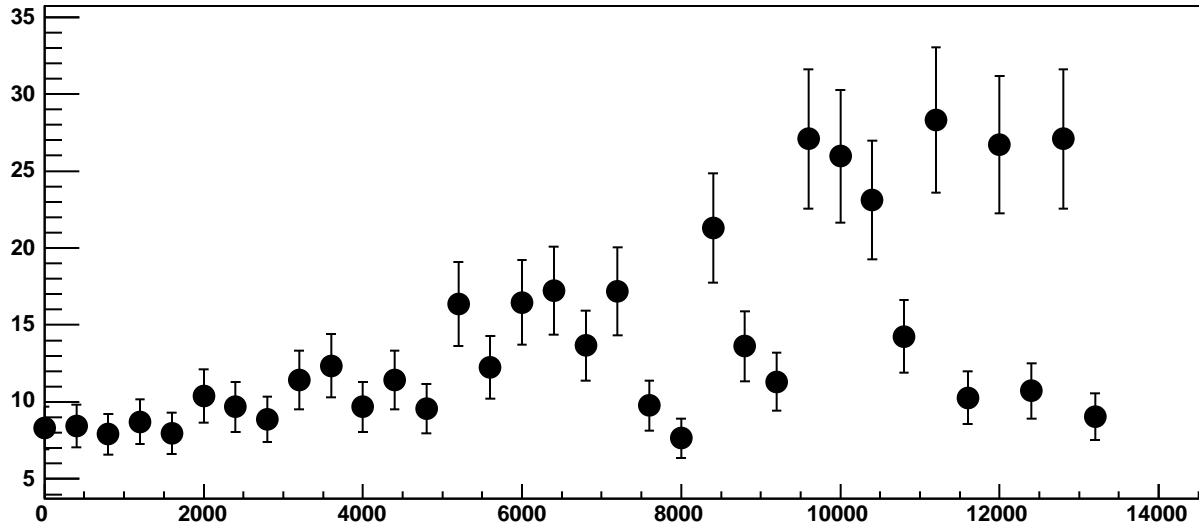
Chip 9, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



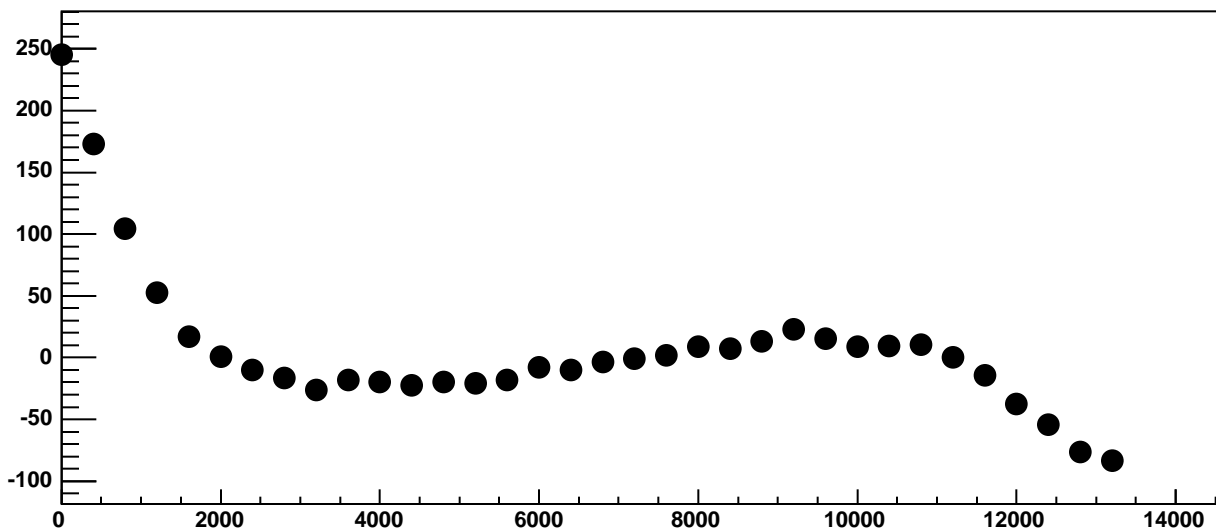
Chip 9, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC



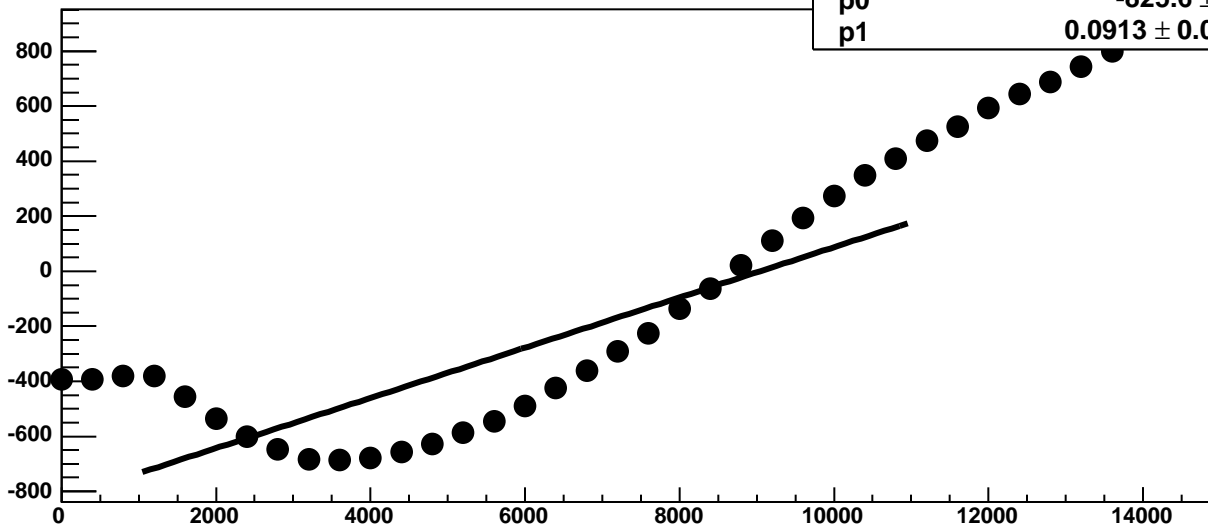
Chip 9, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC

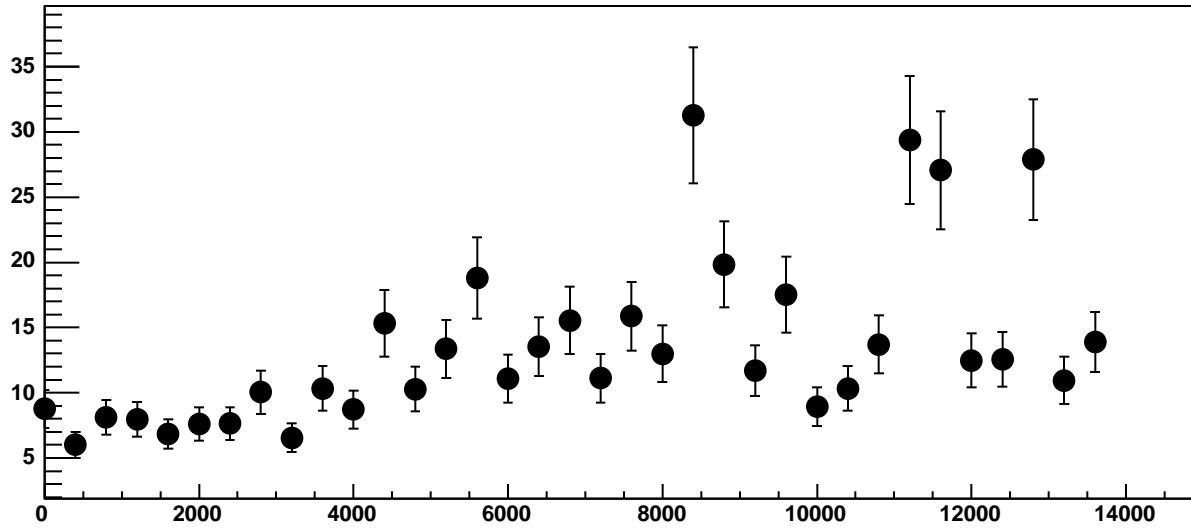


Chip 9, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

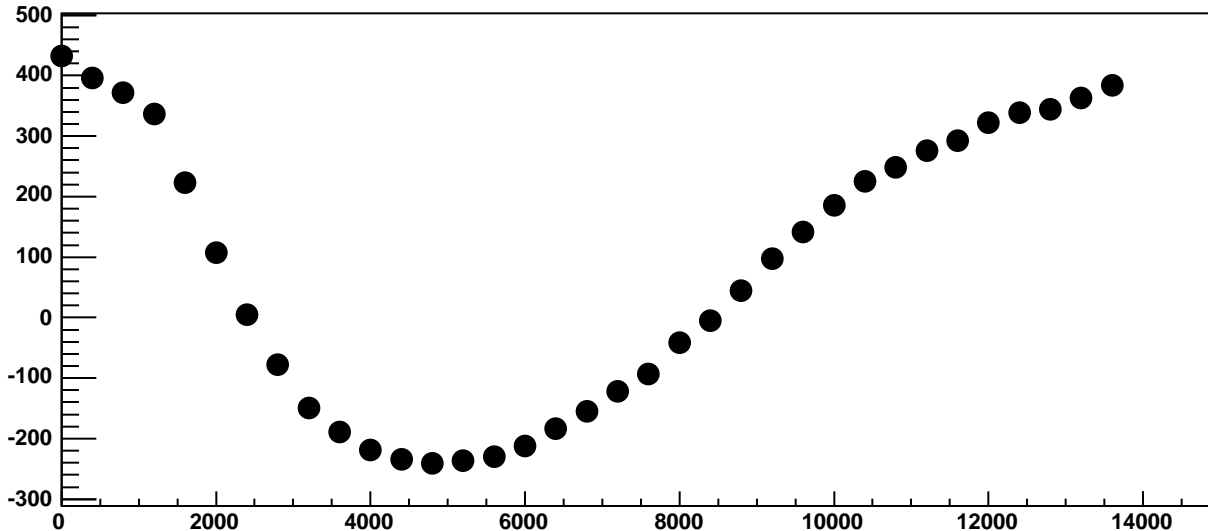


χ^2 / ndf 1.531e+05 / 23
p0 -825.6 ± 0.9032
p1 0.0913 ± 0.0001595

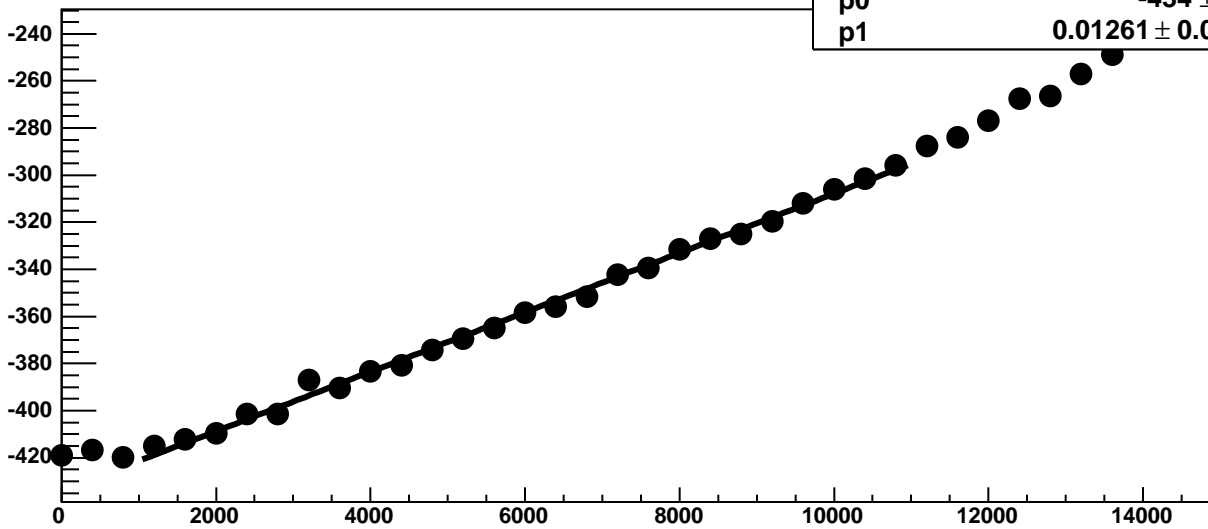
Chip 9, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



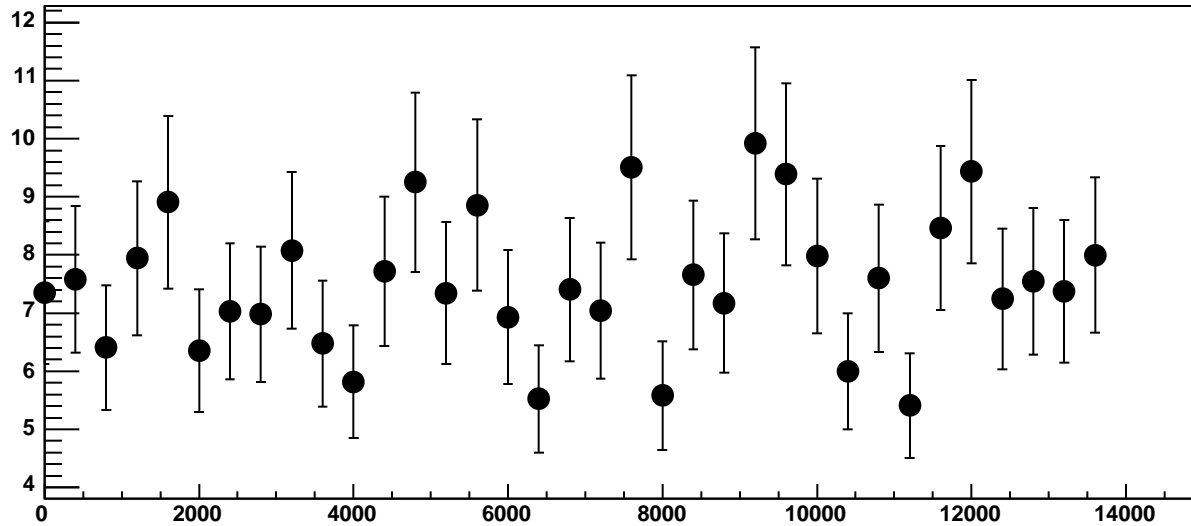
Chip 9, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



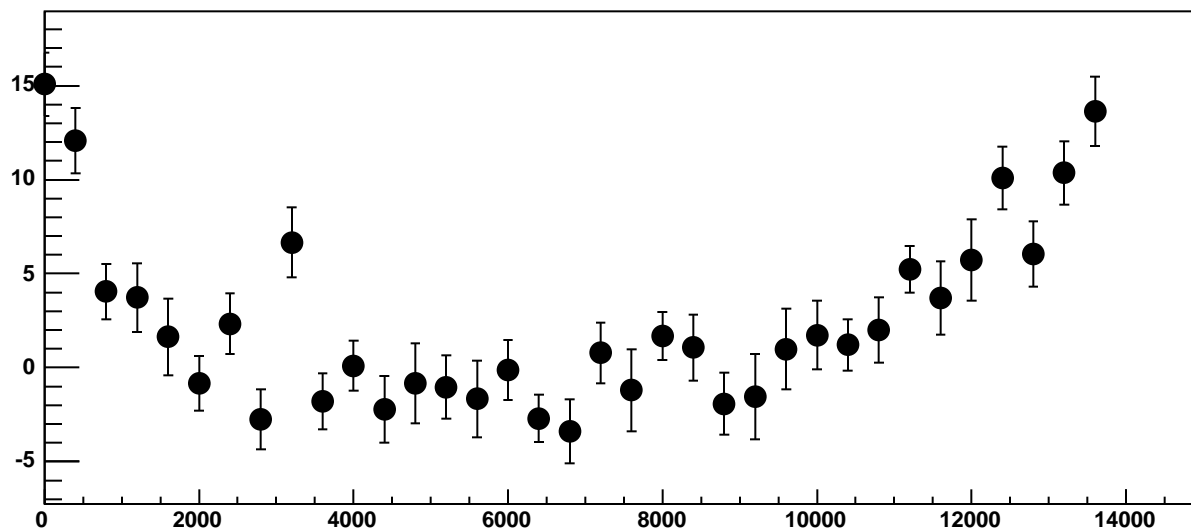
Chip 9, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



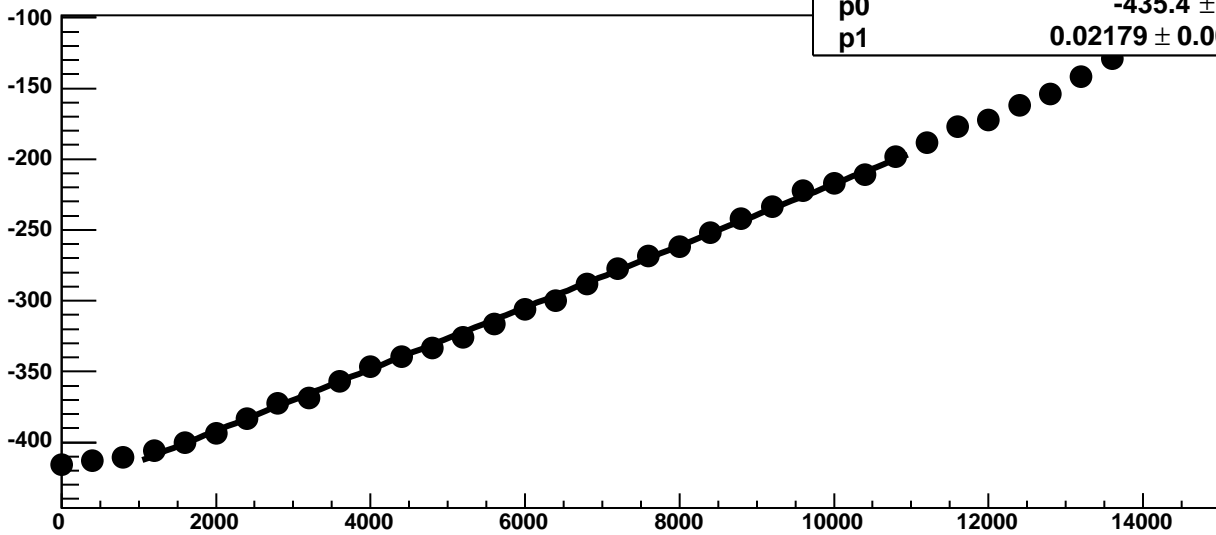
Chip 9, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

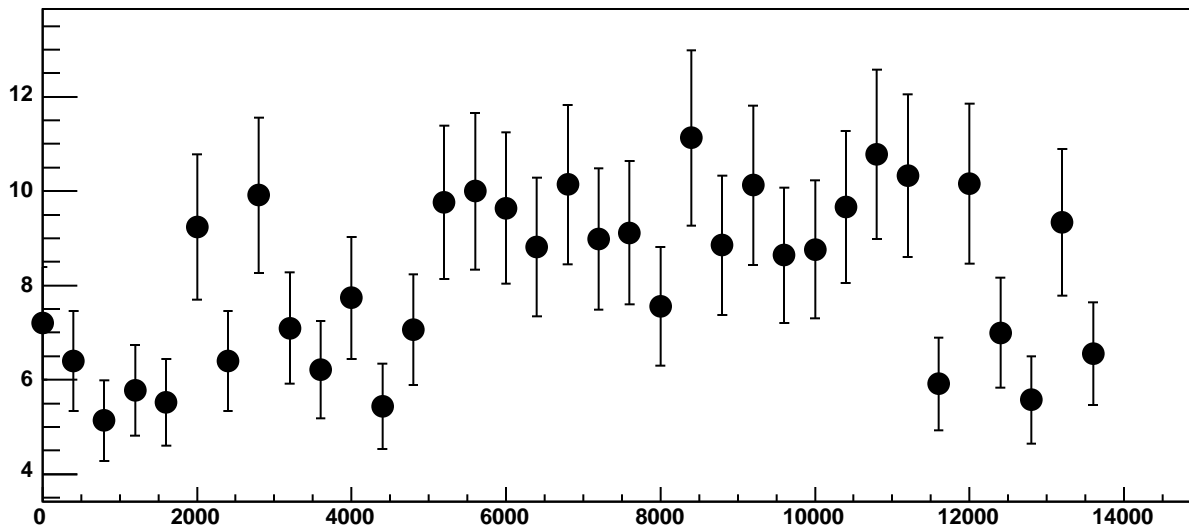


Chip 9, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC

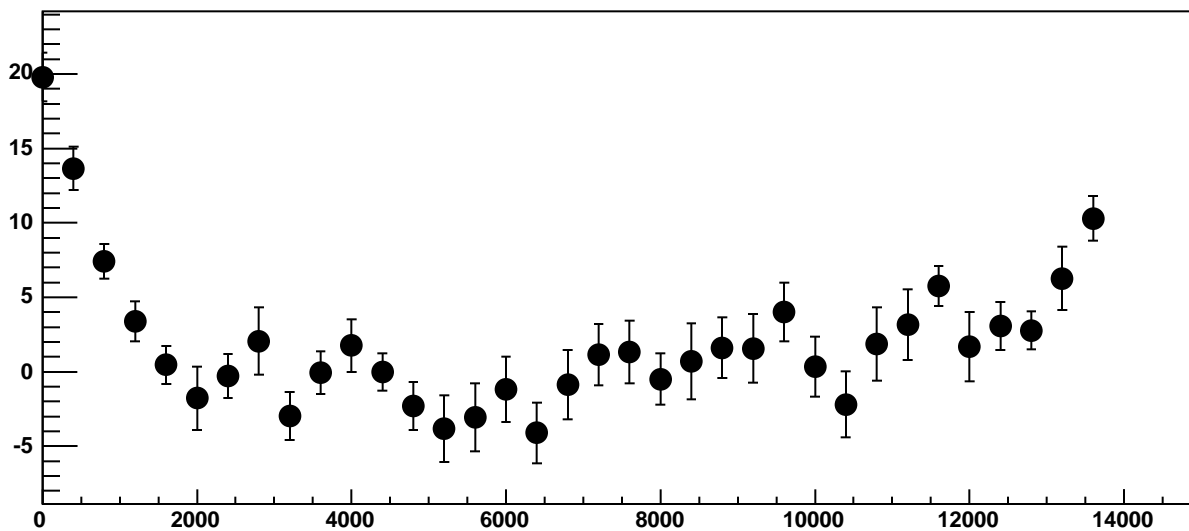


χ^2 / ndf 31.44 / 23
p0 -435.4 ± 0.7445
p1 0.02179 ± 0.0001263

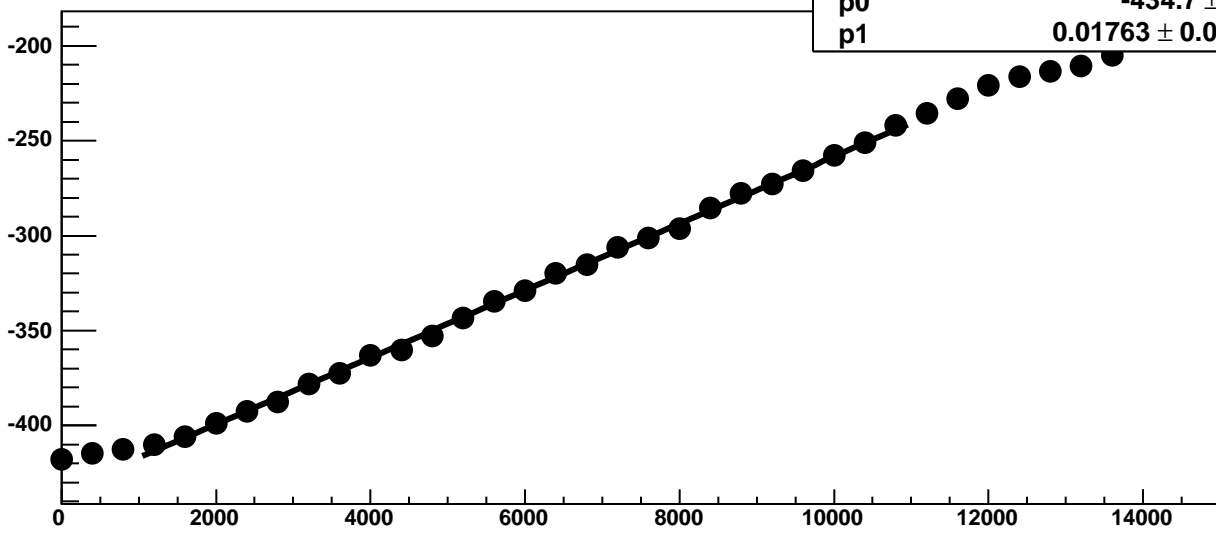
Chip 9, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC

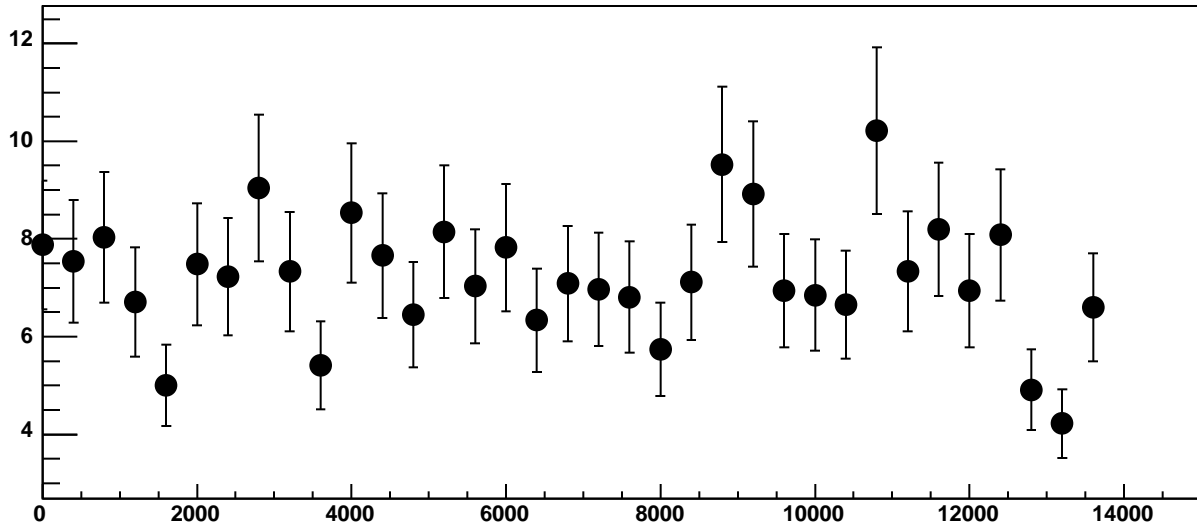


Chip 9, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

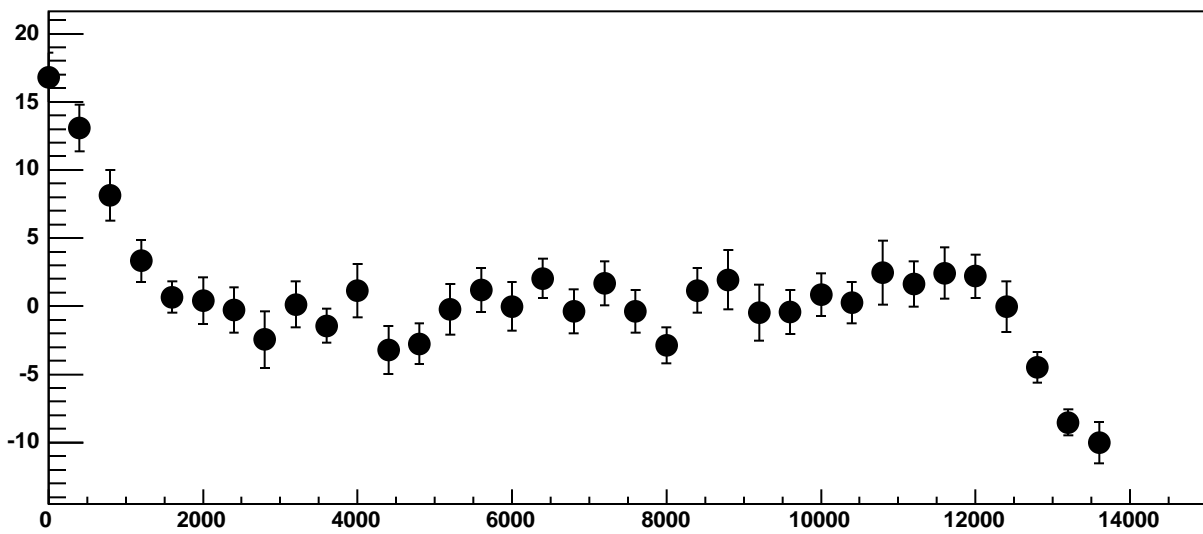


χ^2 / ndf 26.31 / 23
p0 -434.7 ± 0.7181
p1 0.01763 ± 0.0001117

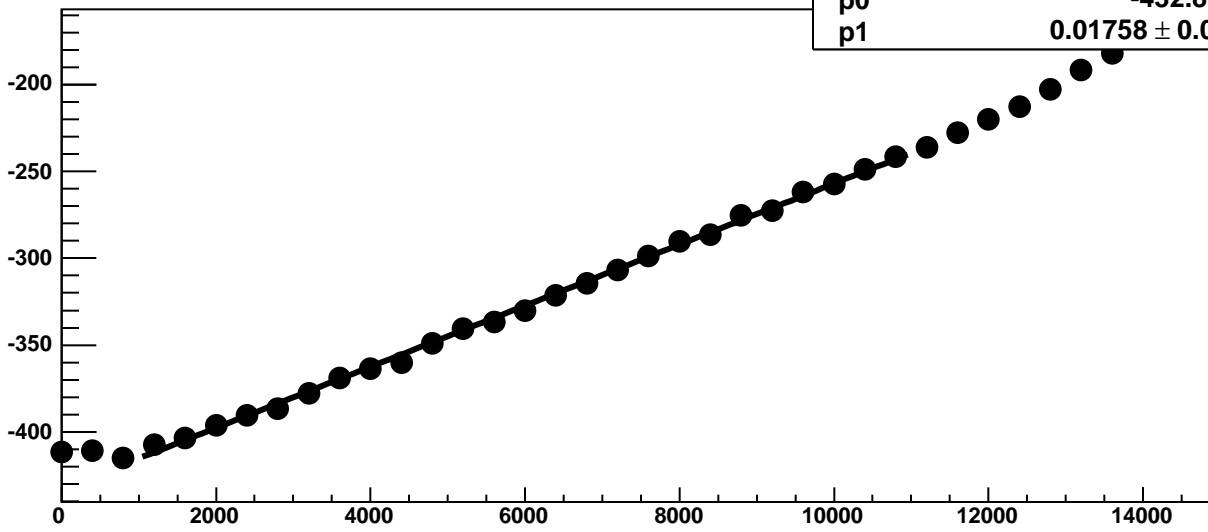
Chip 9, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

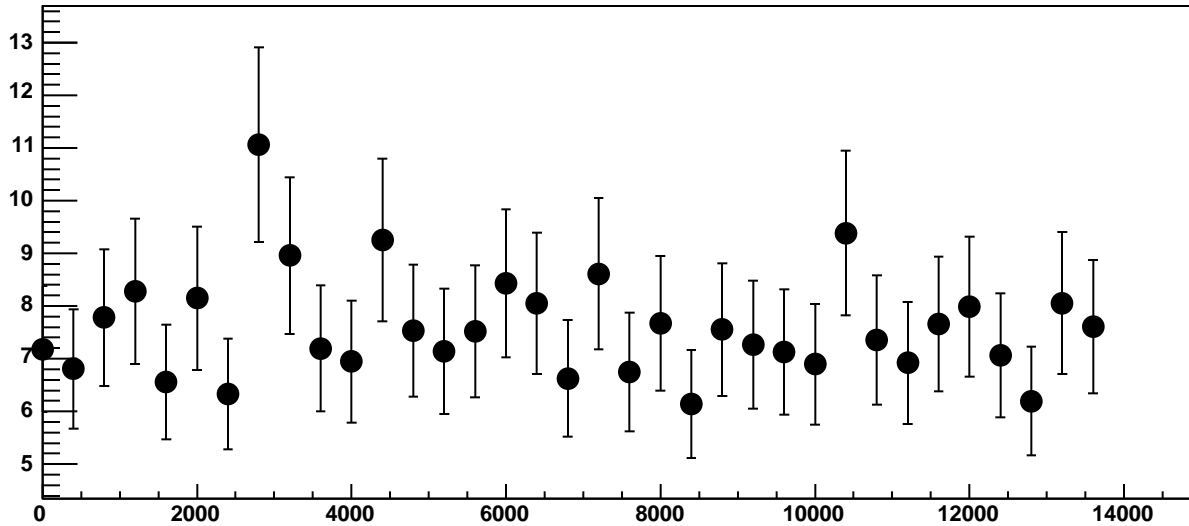


Chip 9, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

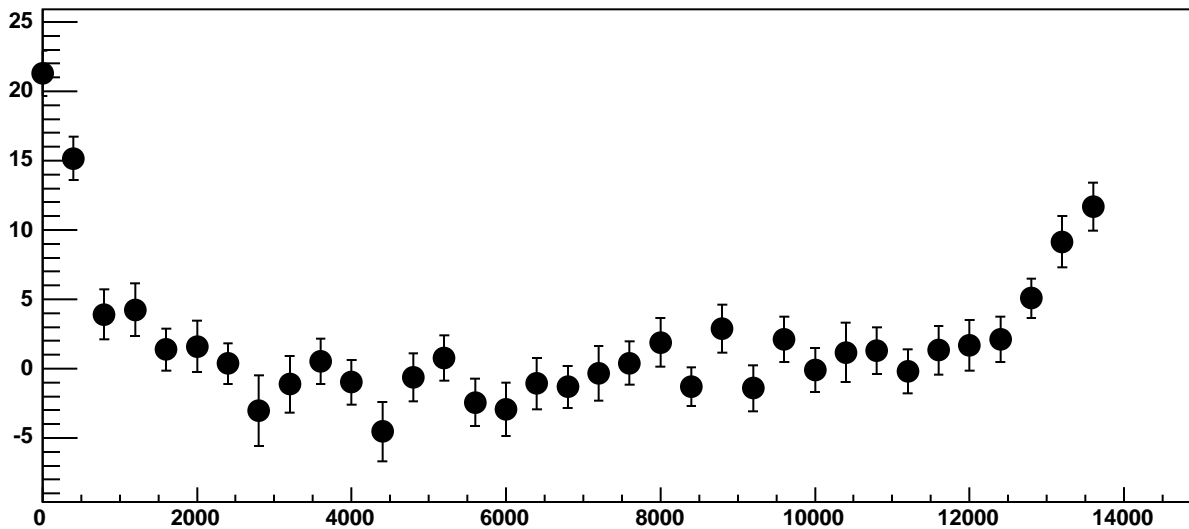


χ^2 / ndf 27.34 / 23
p0 -432.8 ± 0.807
p1 0.01758 ± 0.0001197

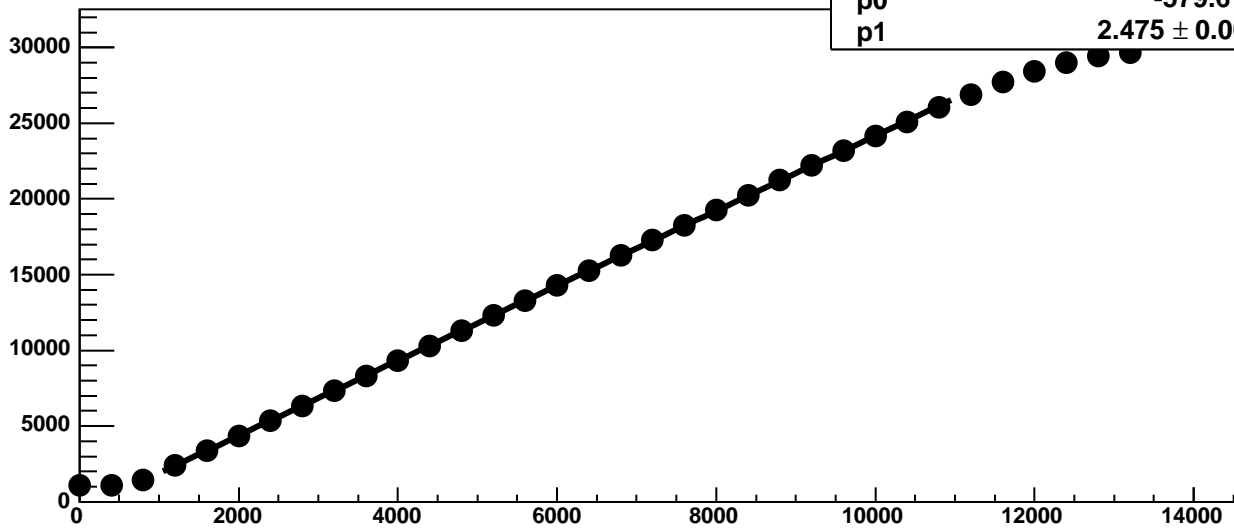
Chip 9, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



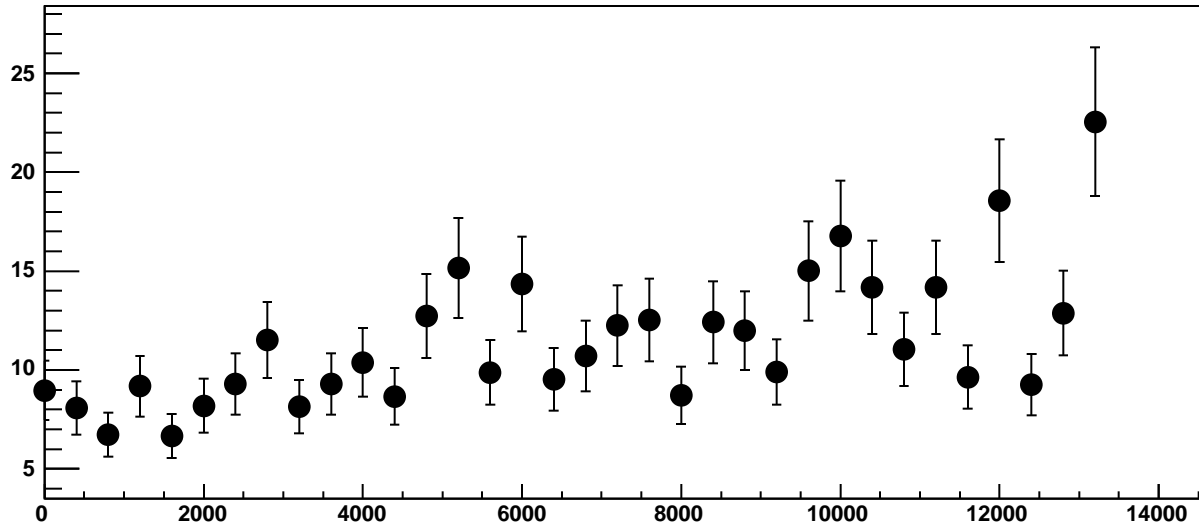
Chip 9, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



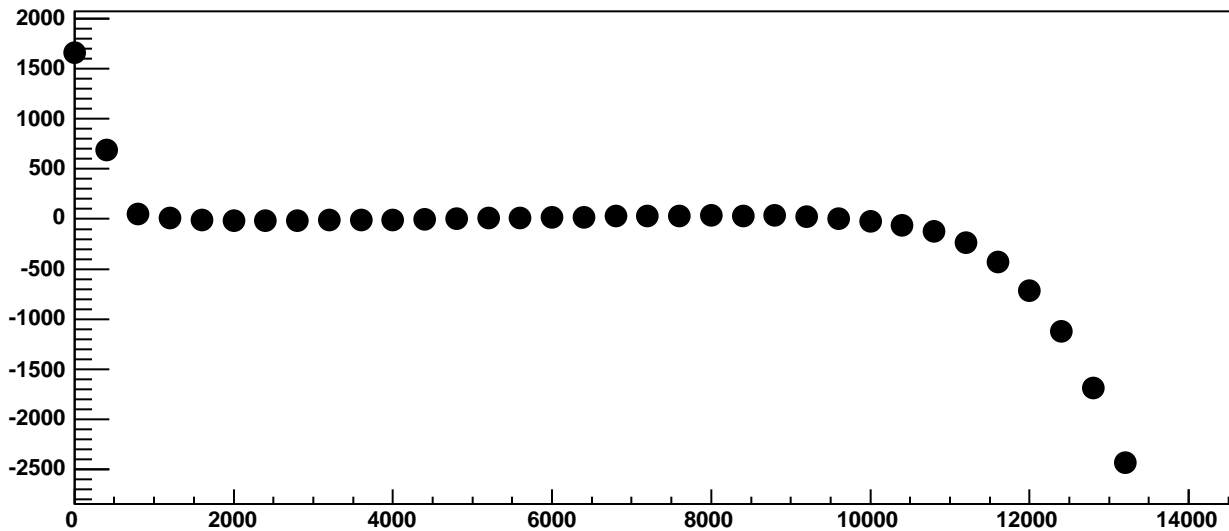
Chip 9, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC



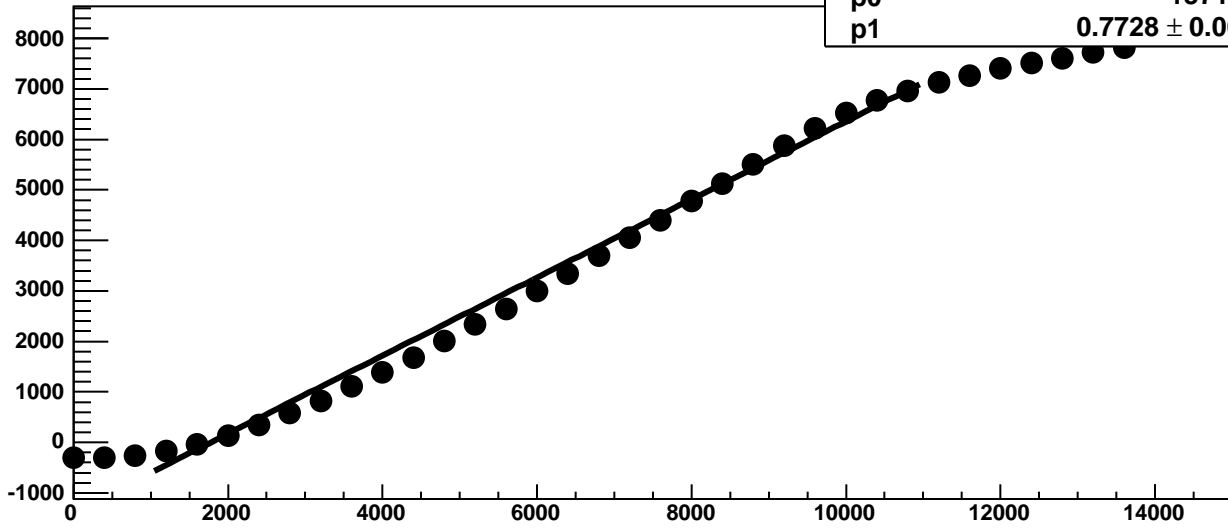
Chip 9, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC



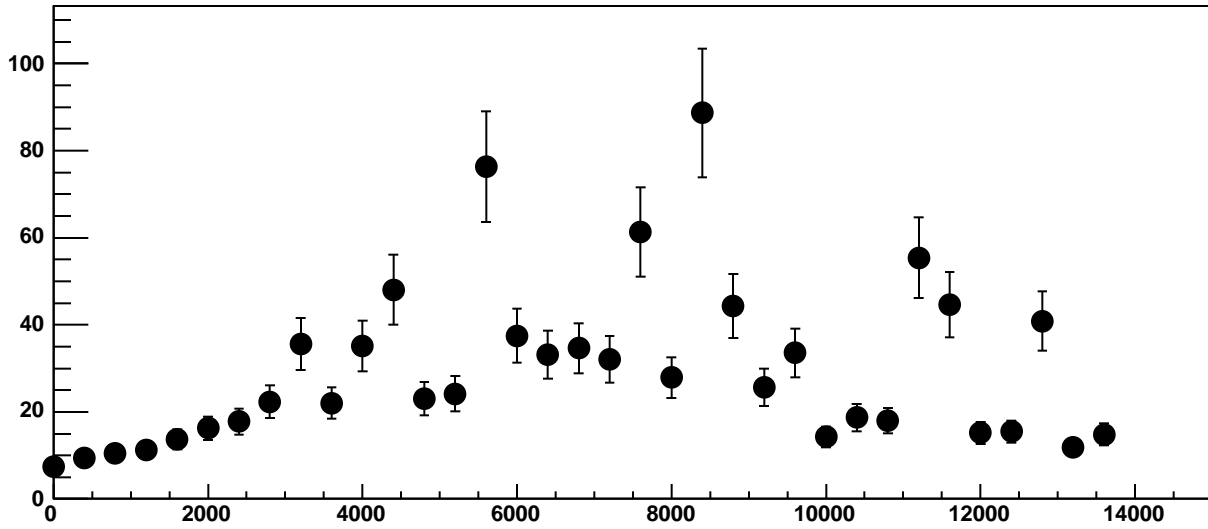
Chip 9, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC



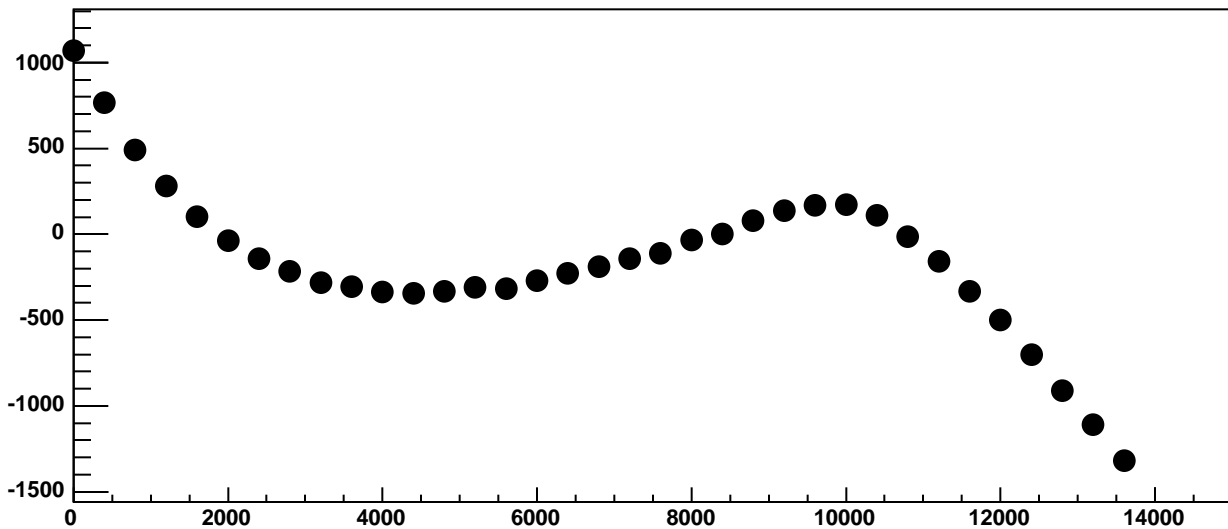
Chip 9, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC



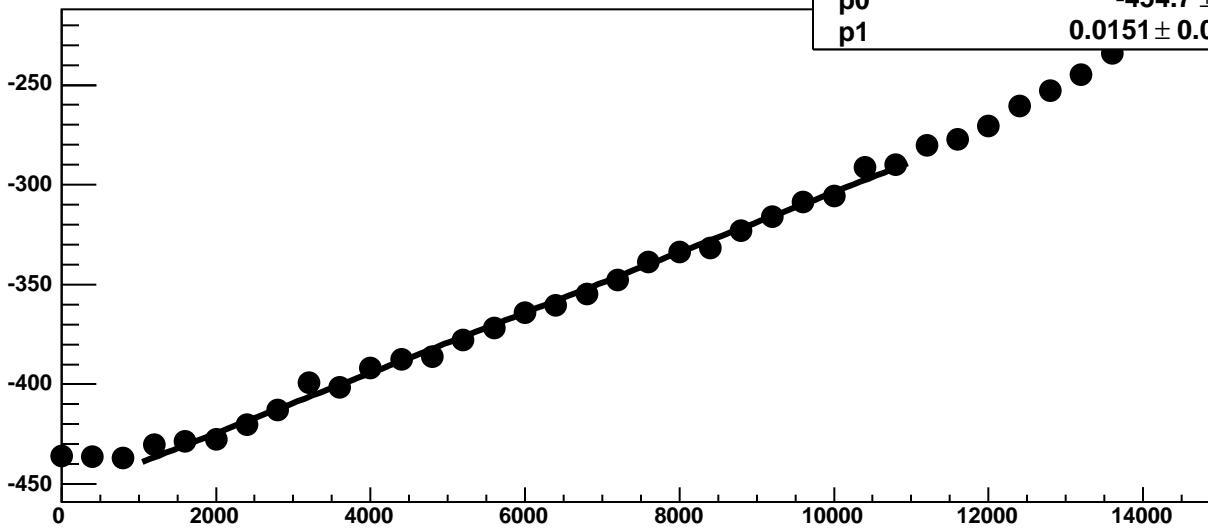
Chip 9, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

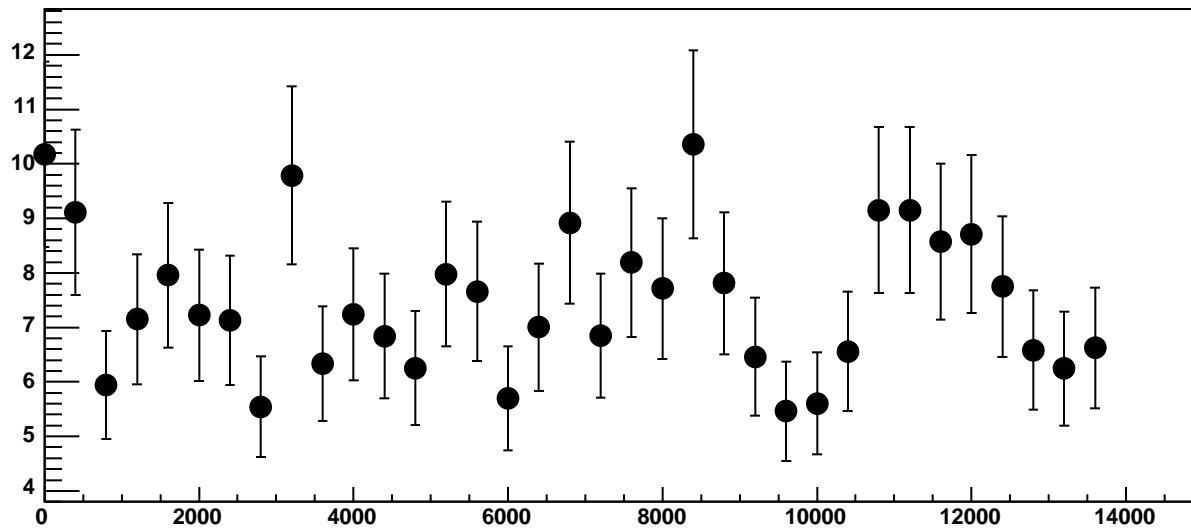


Chip 9, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

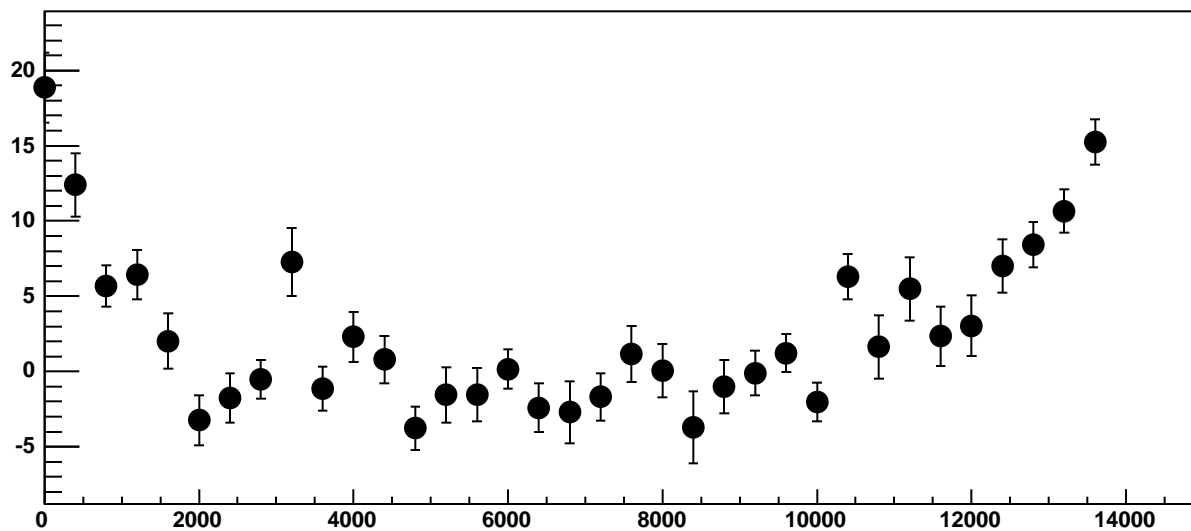


χ^2 / ndf 73.27 / 23
p0 -454.7 ± 0.7432
p1 0.0151 ± 0.0001107

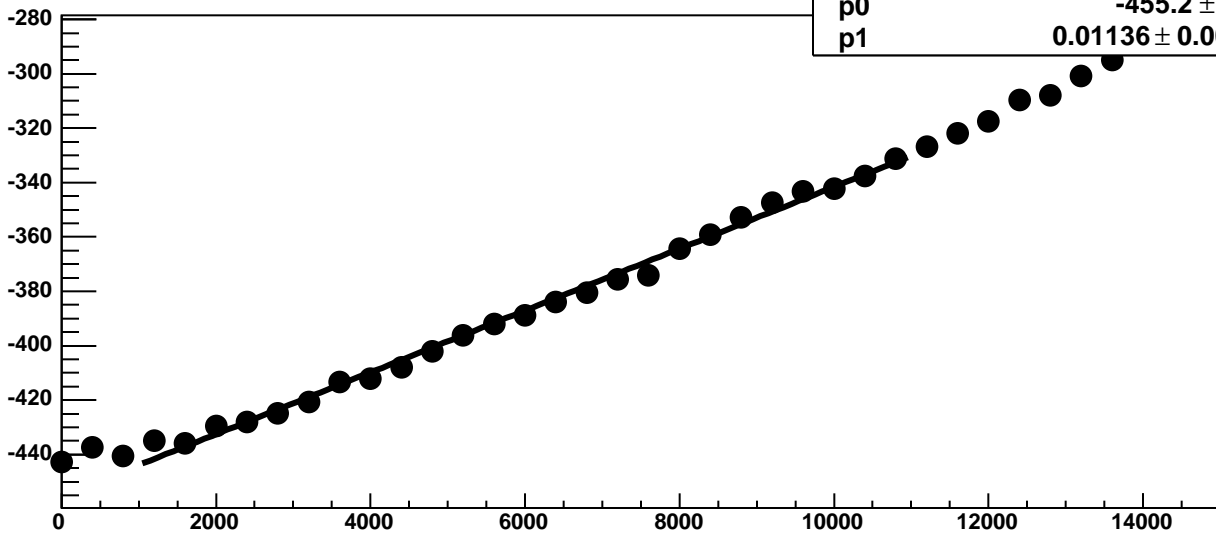
Chip 9, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



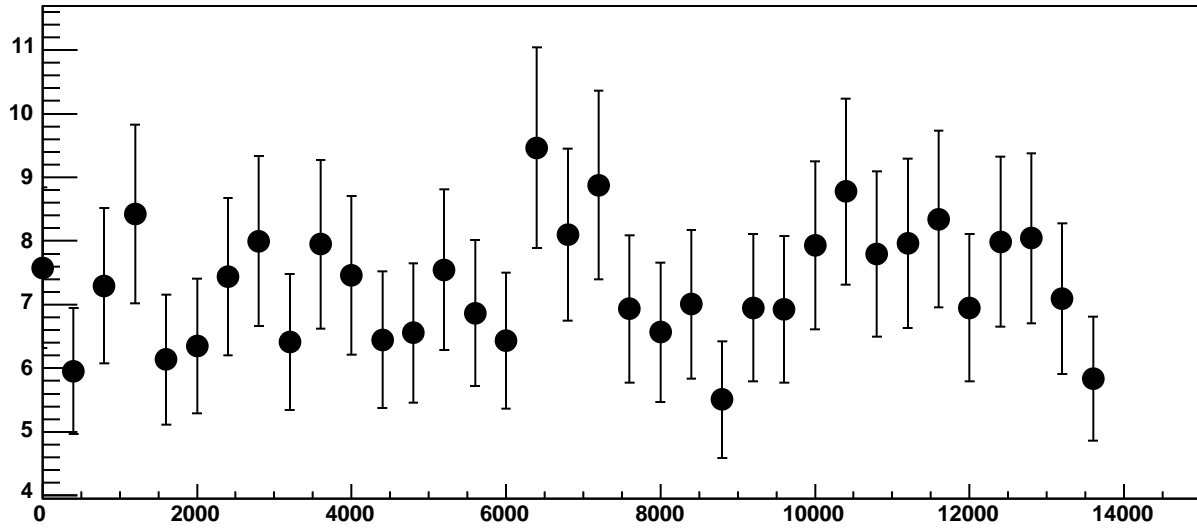
Chip 9, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



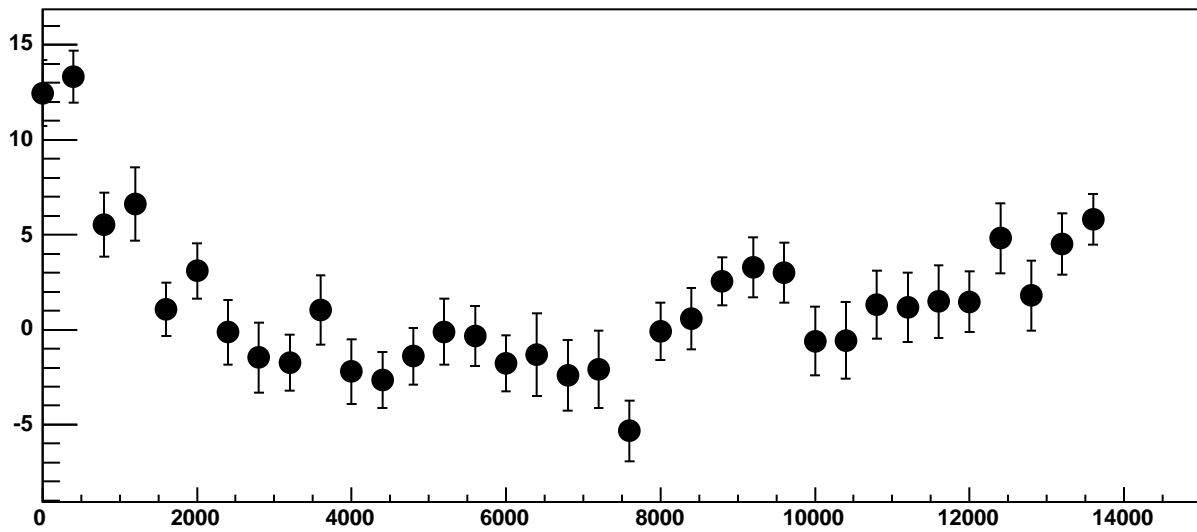
Chip 9, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



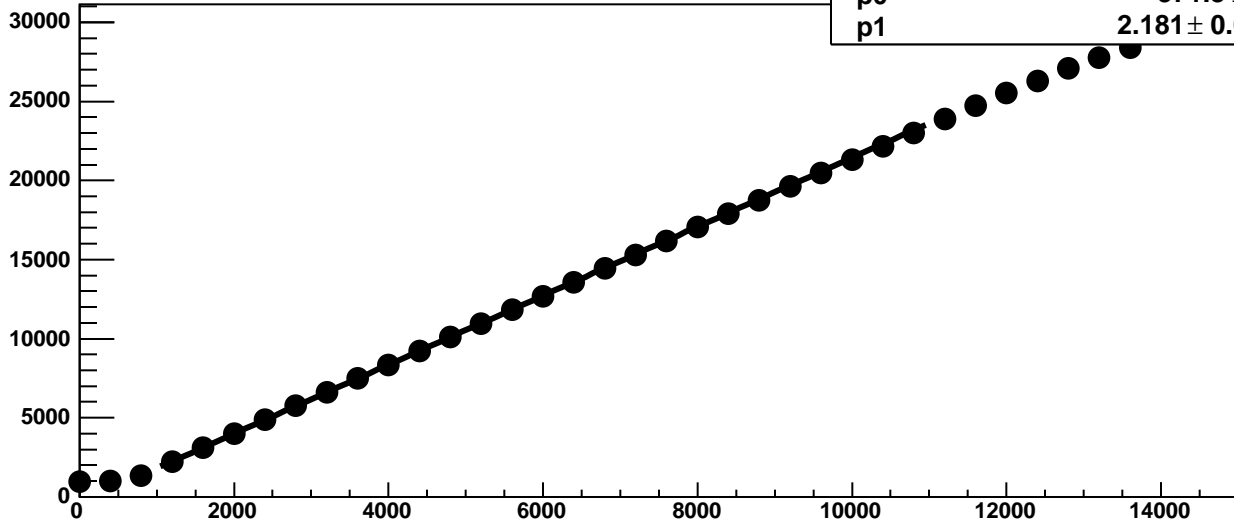
Chip 9, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



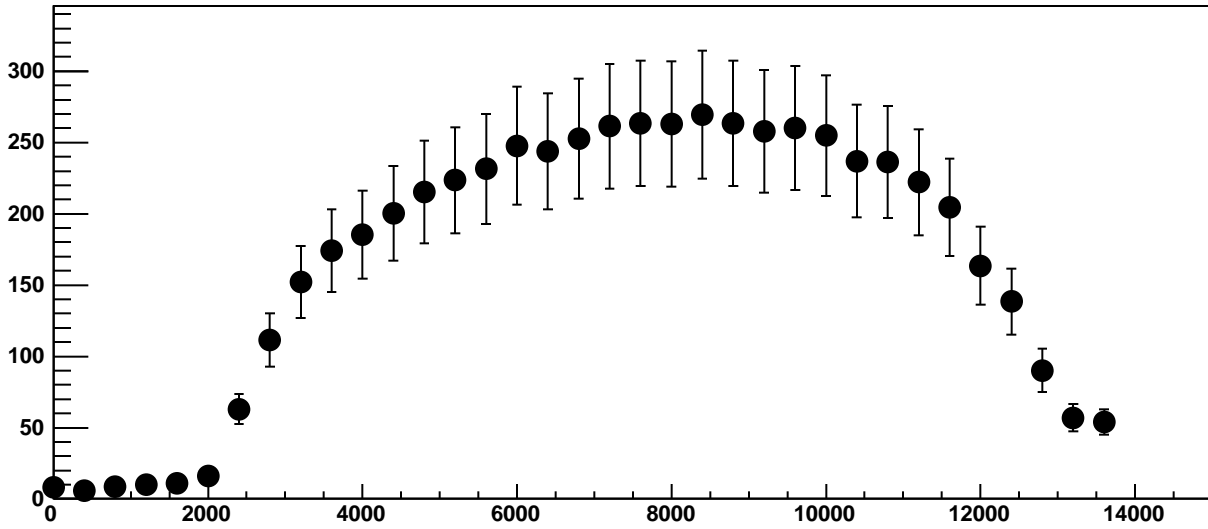
Chip 9, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



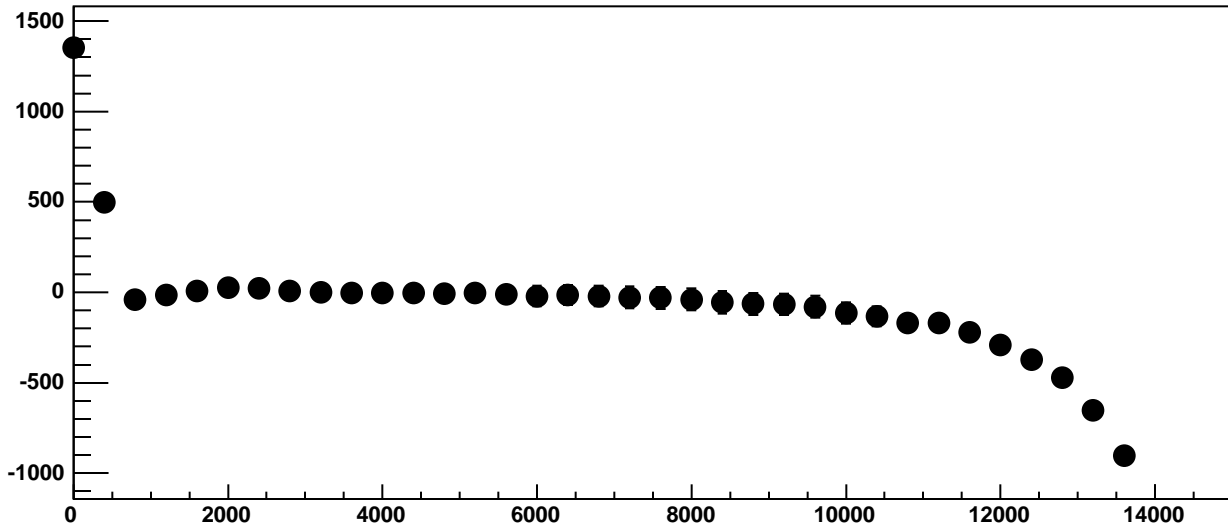
Chip 9, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC



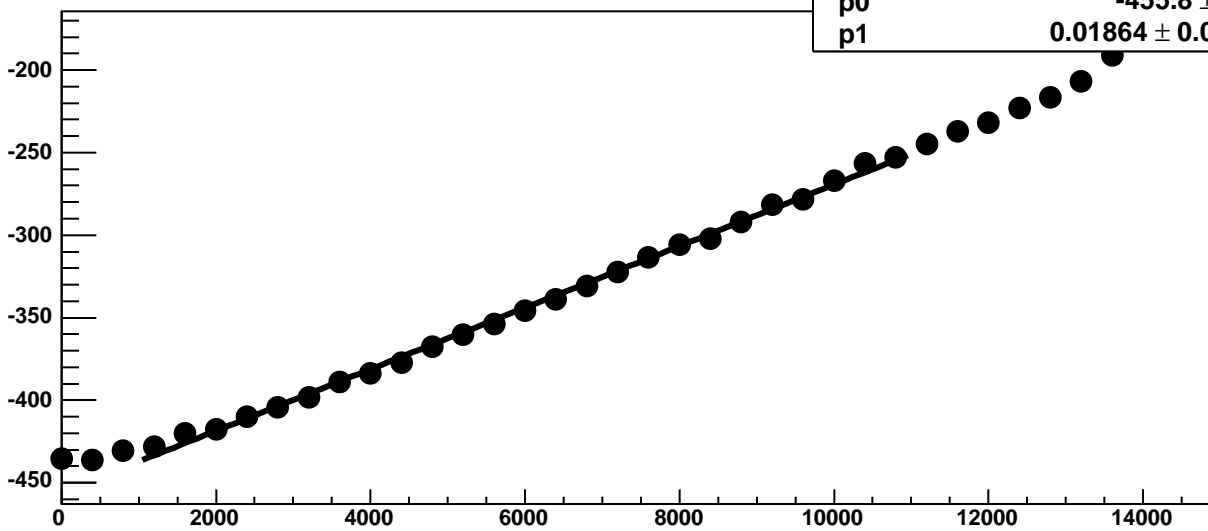
Chip 9, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC

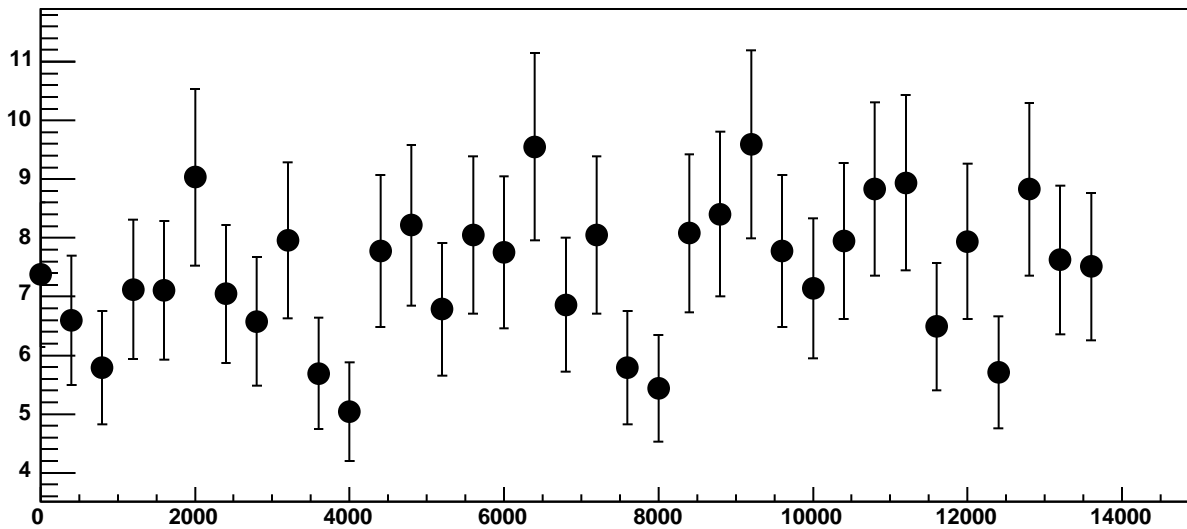


Chip 9, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

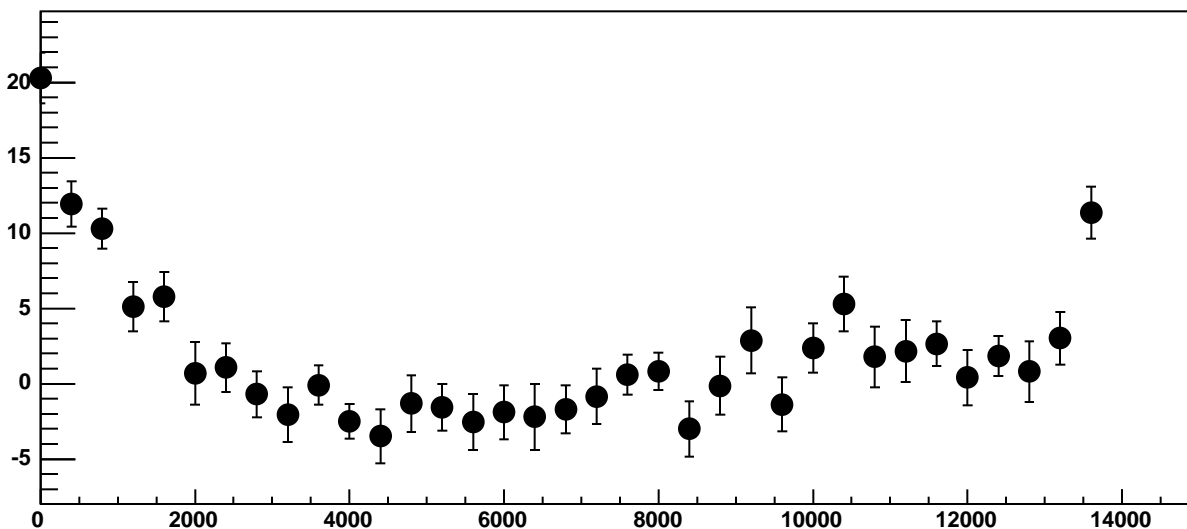


χ^2 / ndf 56.63 / 23
p0 -455.8 ± 0.7612
p1 0.01864 ± 0.0001187

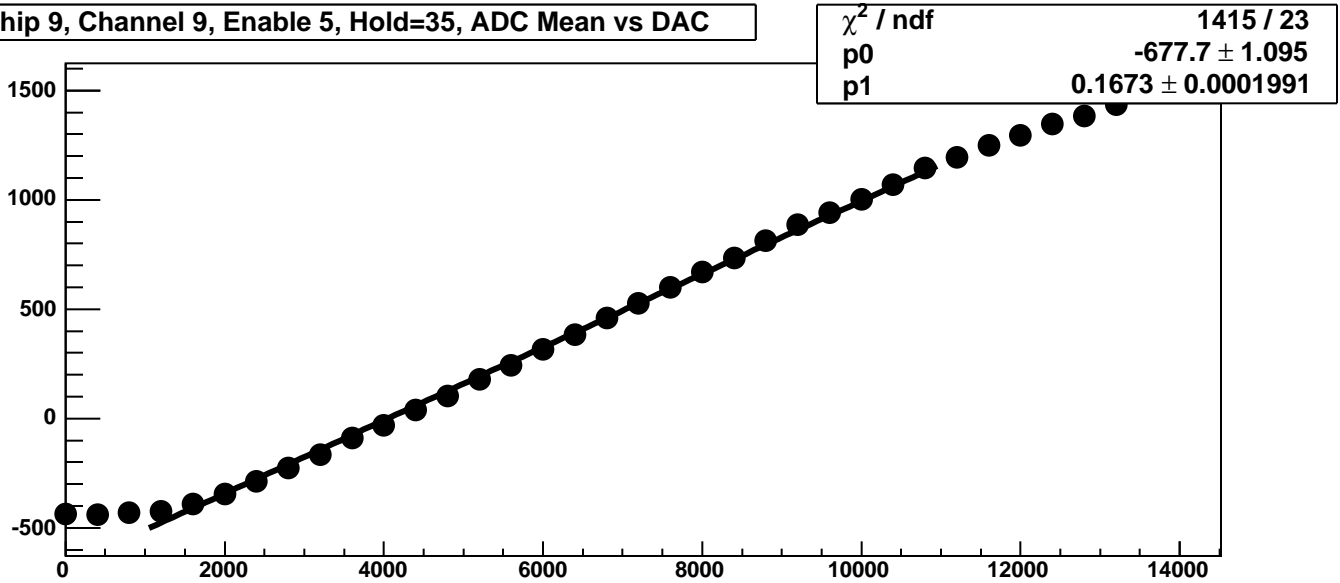
Chip 9, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



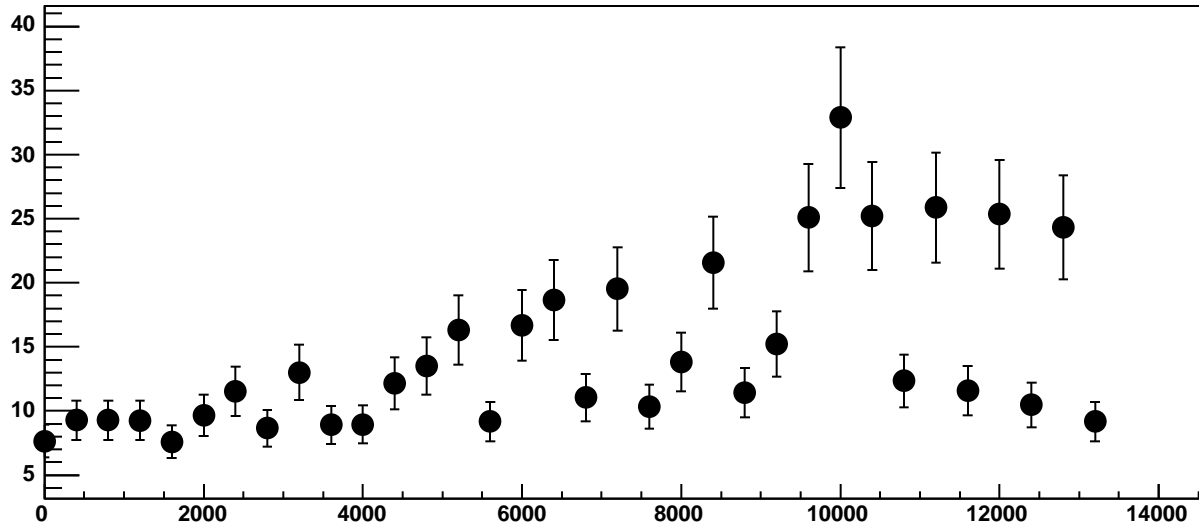
Chip 9, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



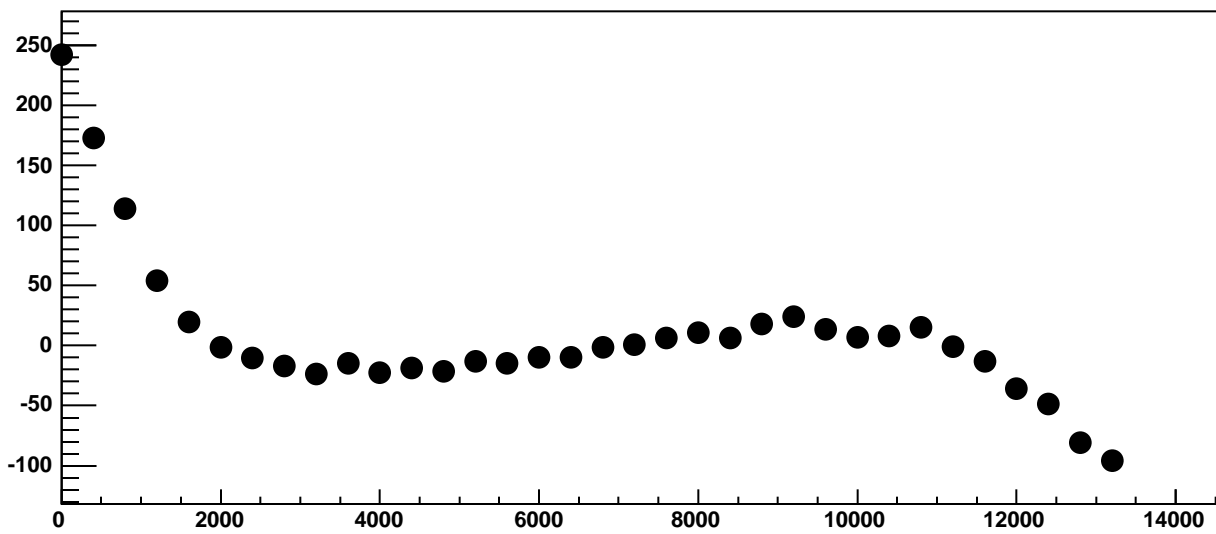
Chip 9, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



Chip 9, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 9, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

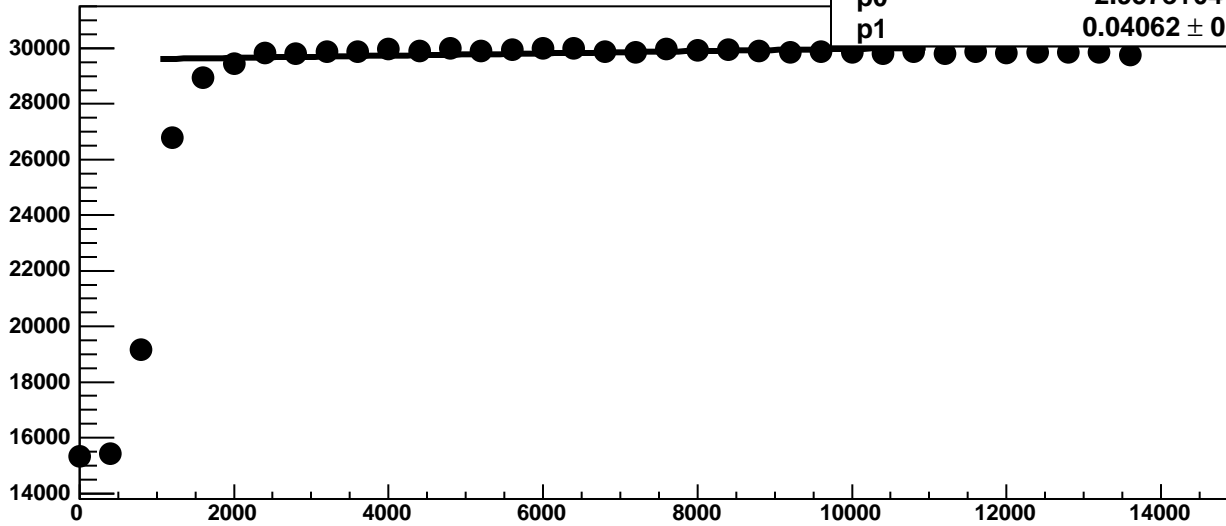
1300 / 23

p0

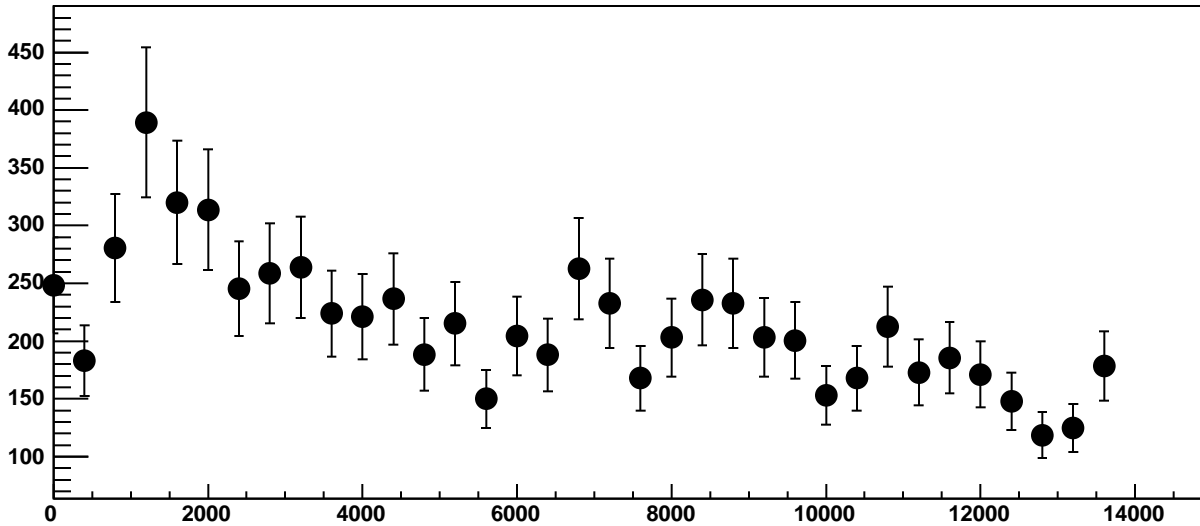
$2.957\text{e}+04 \pm 26.38$

p1

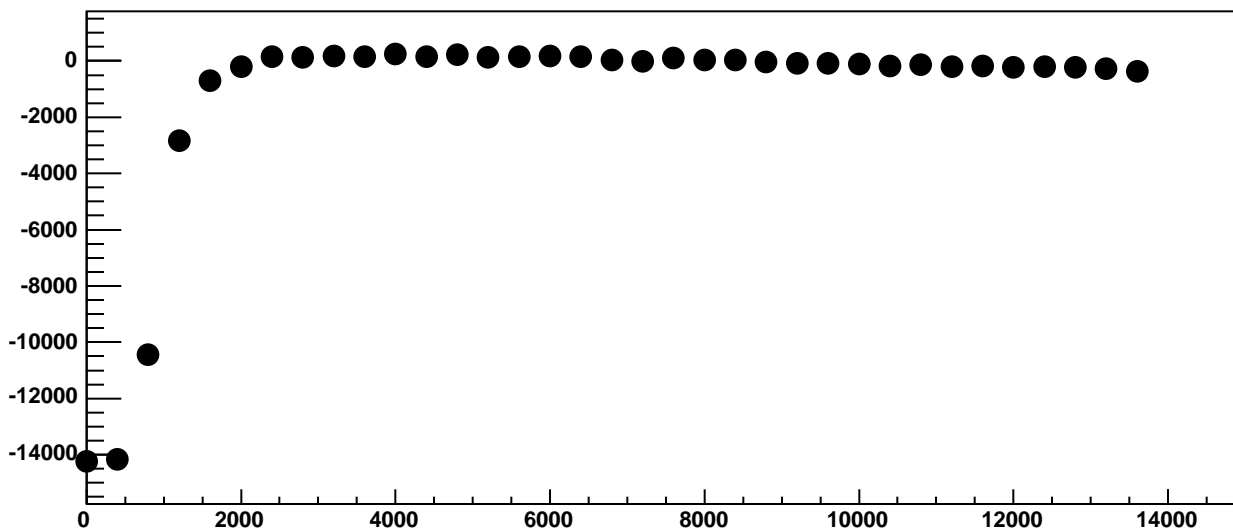
0.04062 ± 0.003649



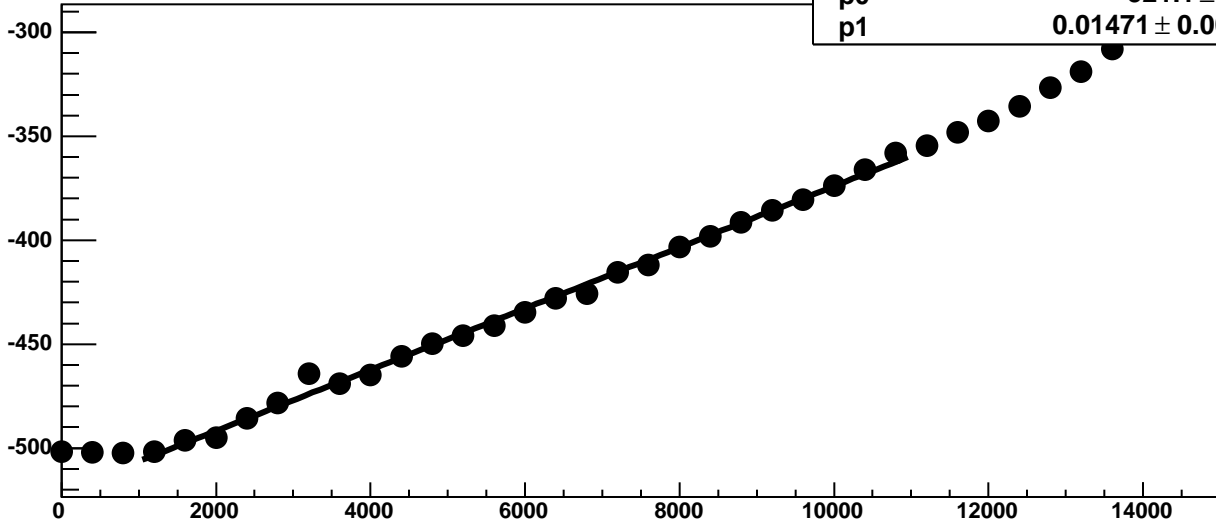
Chip 9, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 9, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

48 / 23

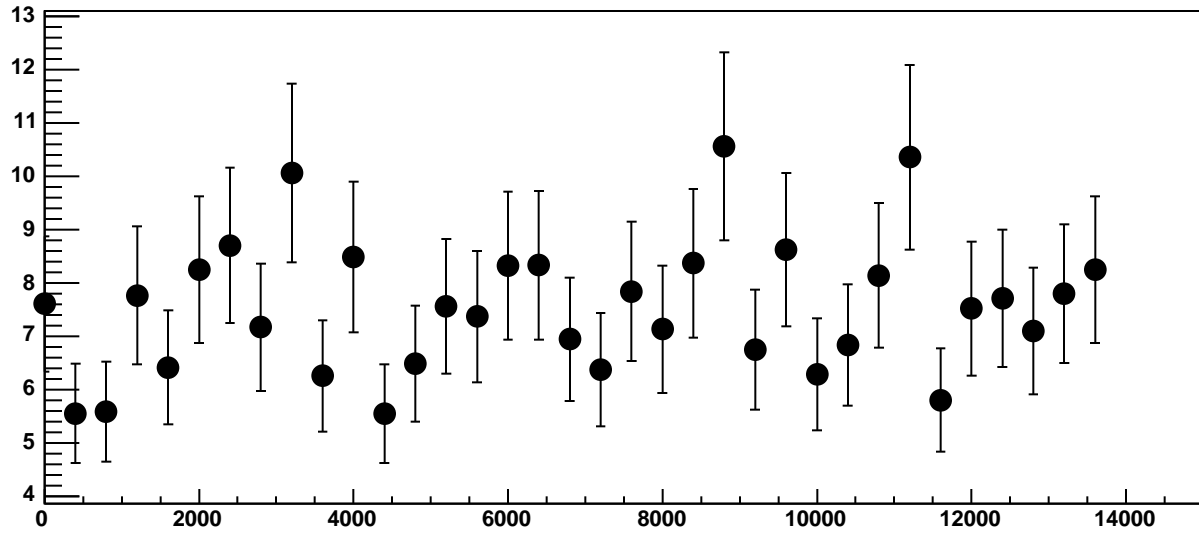
p0

-521.1 ± 0.7843

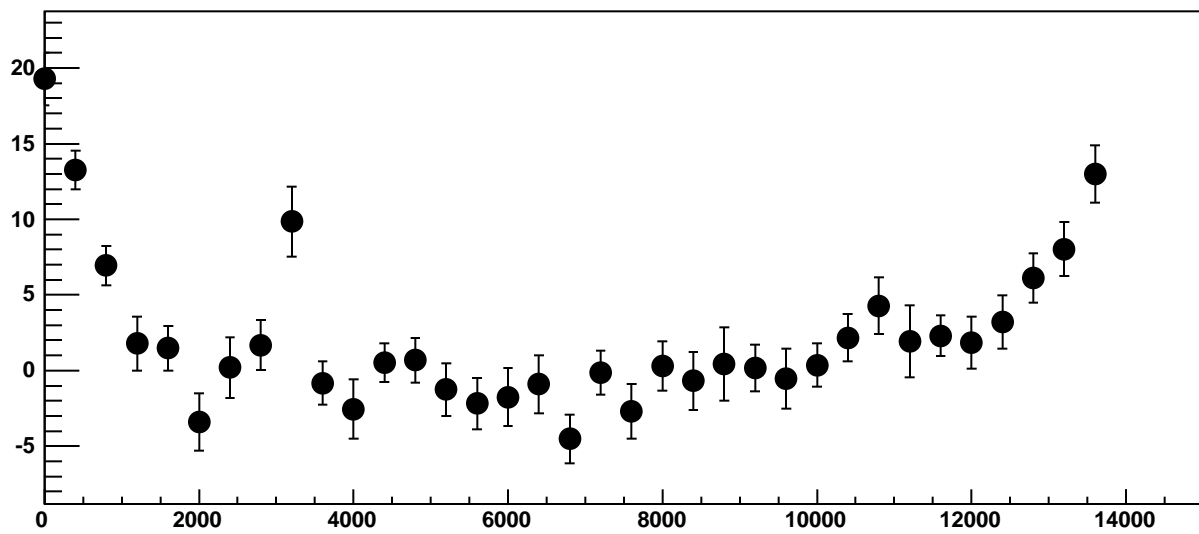
p1

0.01471 ± 0.0001184

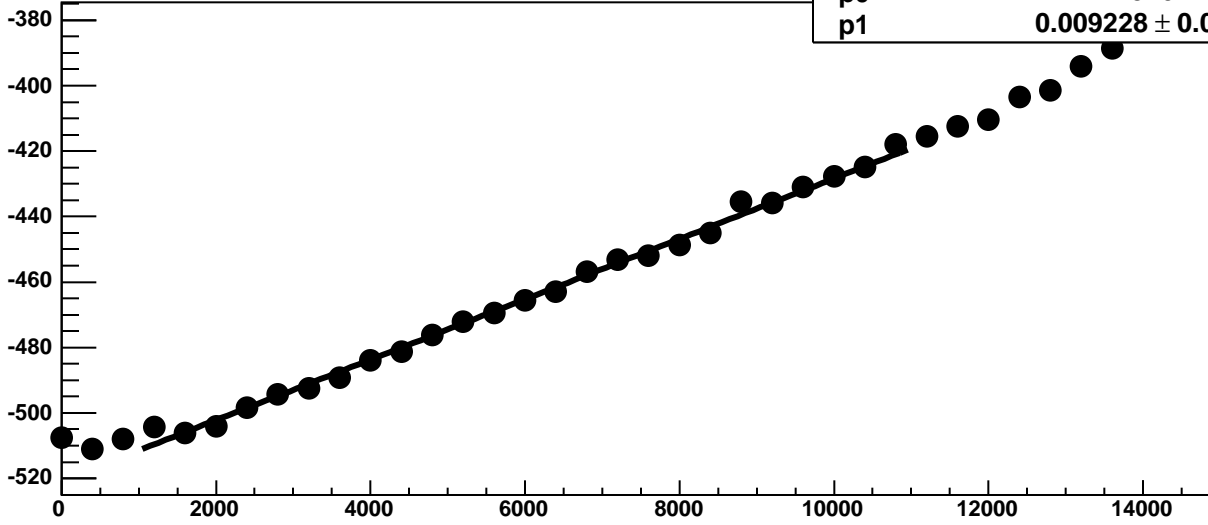
Chip 9, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

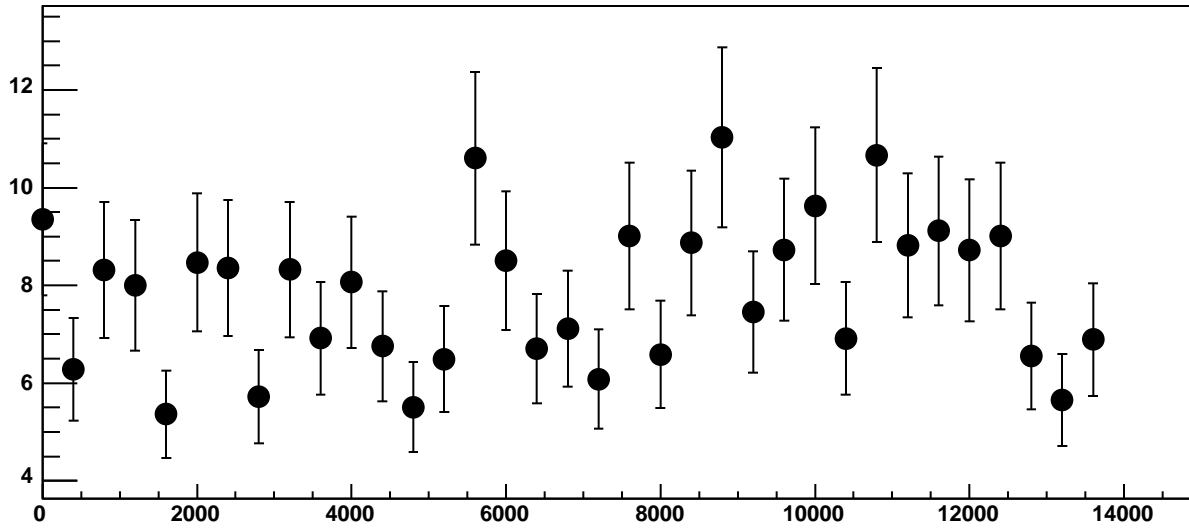


Chip 9, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

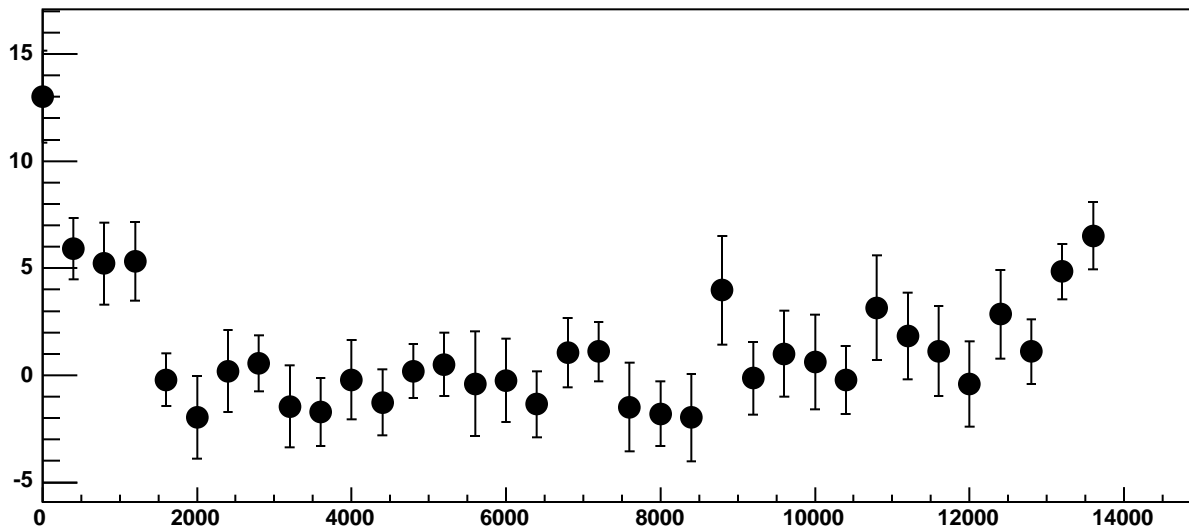


χ^2 / ndf 21.43 / 23
p0 -520.7 ± 0.7601
p1 0.009228 ± 0.0001219

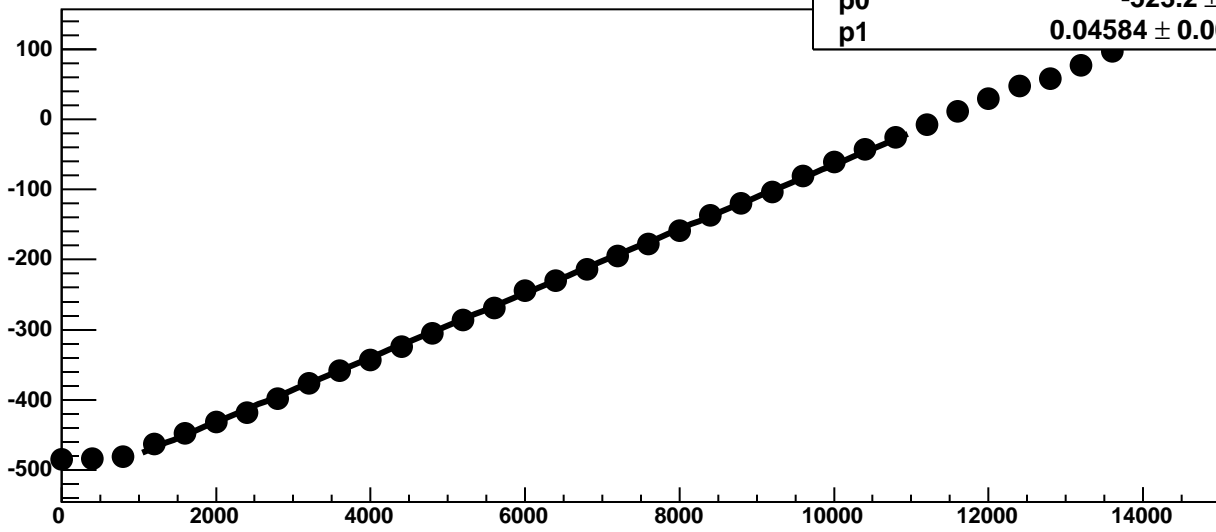
Chip 9, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



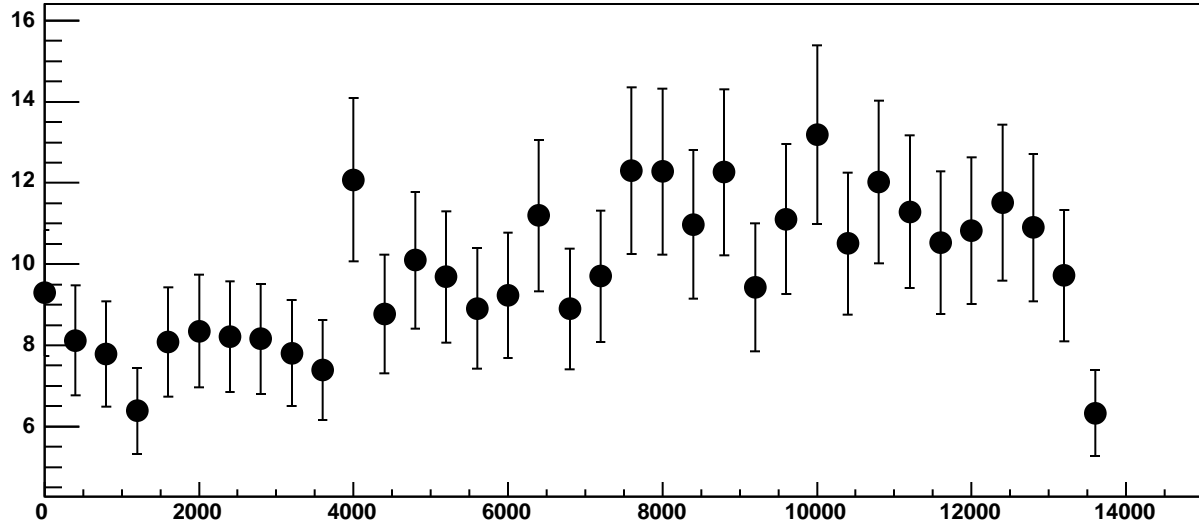
Chip 9, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



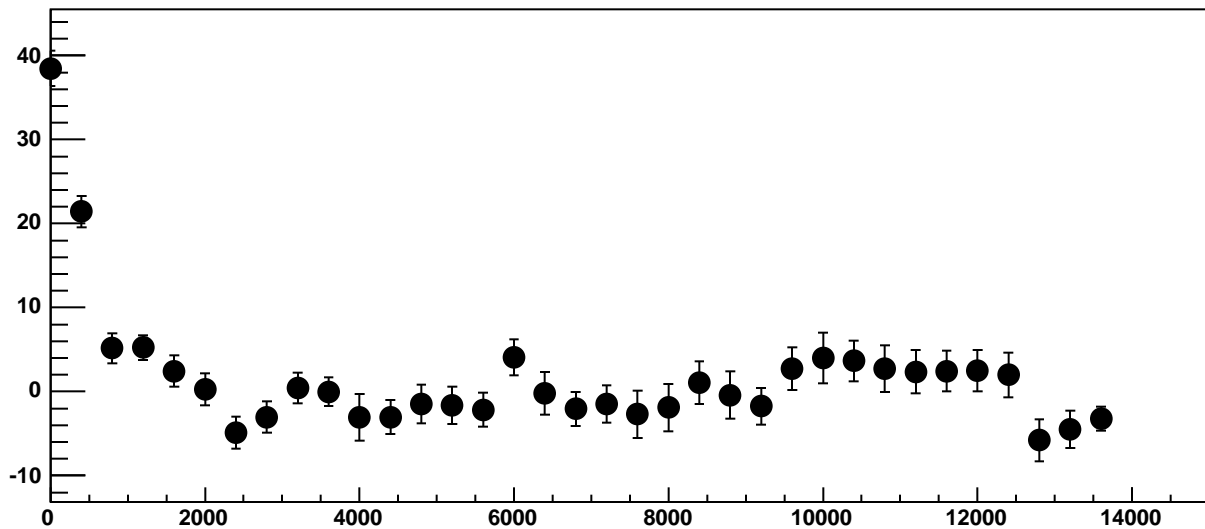
Chip 9, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC



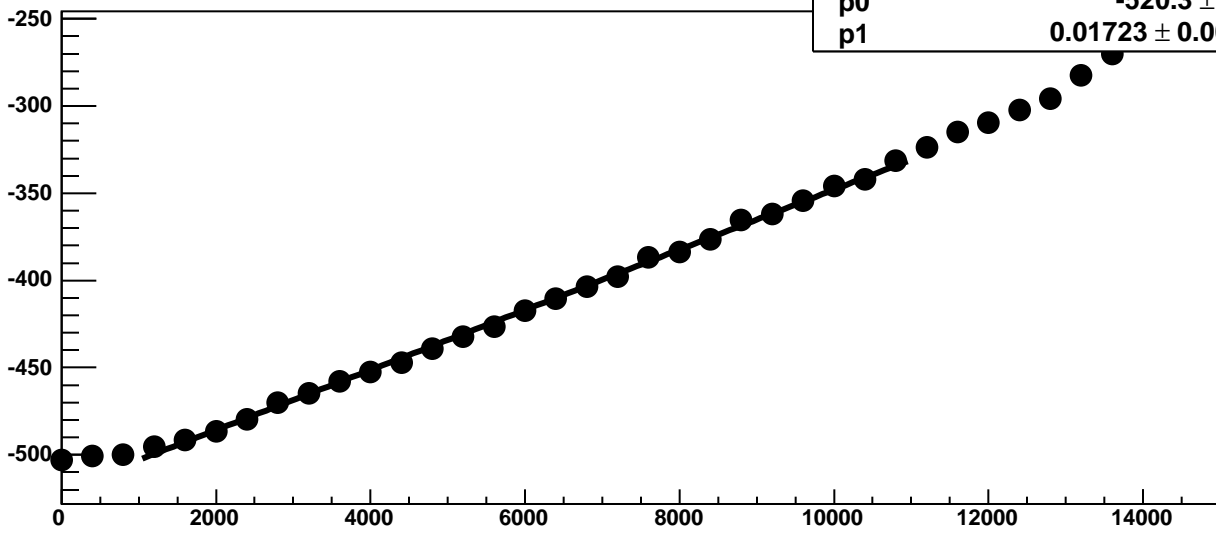
Chip 9, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

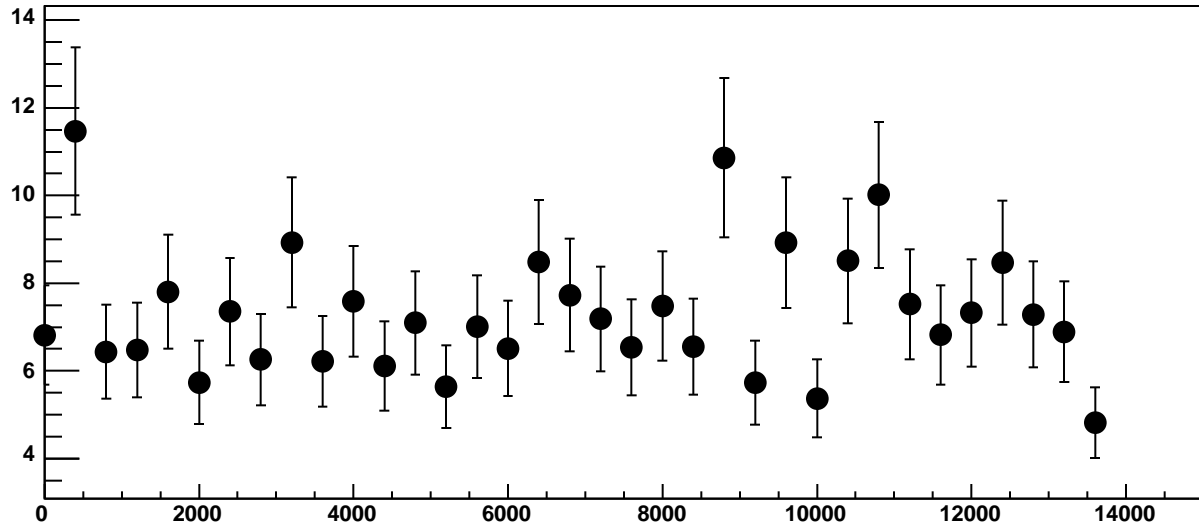


Chip 9, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

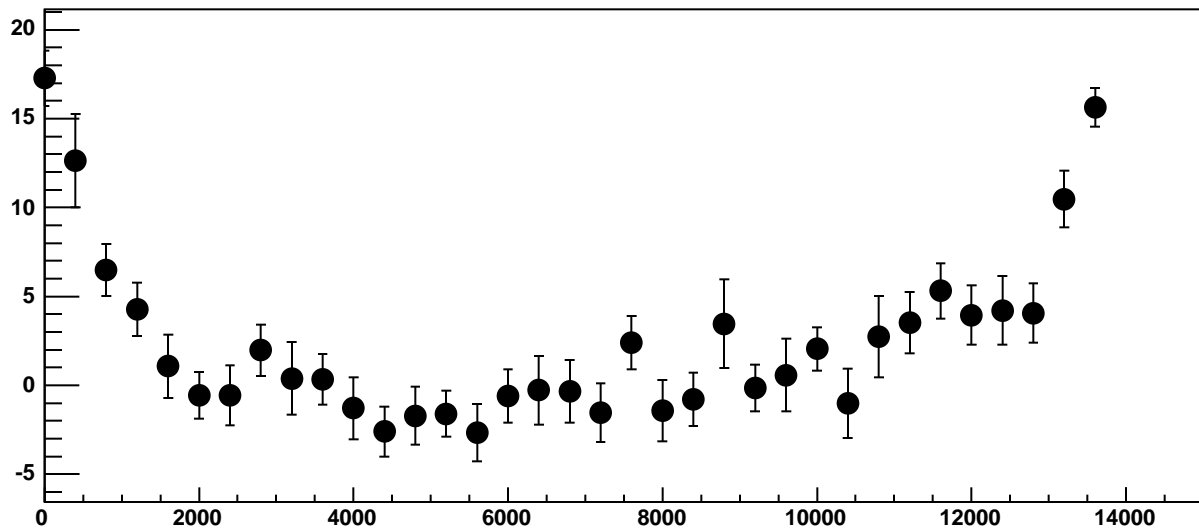


χ^2 / ndf 31.36 / 23
p0 -520.3 ± 0.7256
p1 0.01723 ± 0.0001124

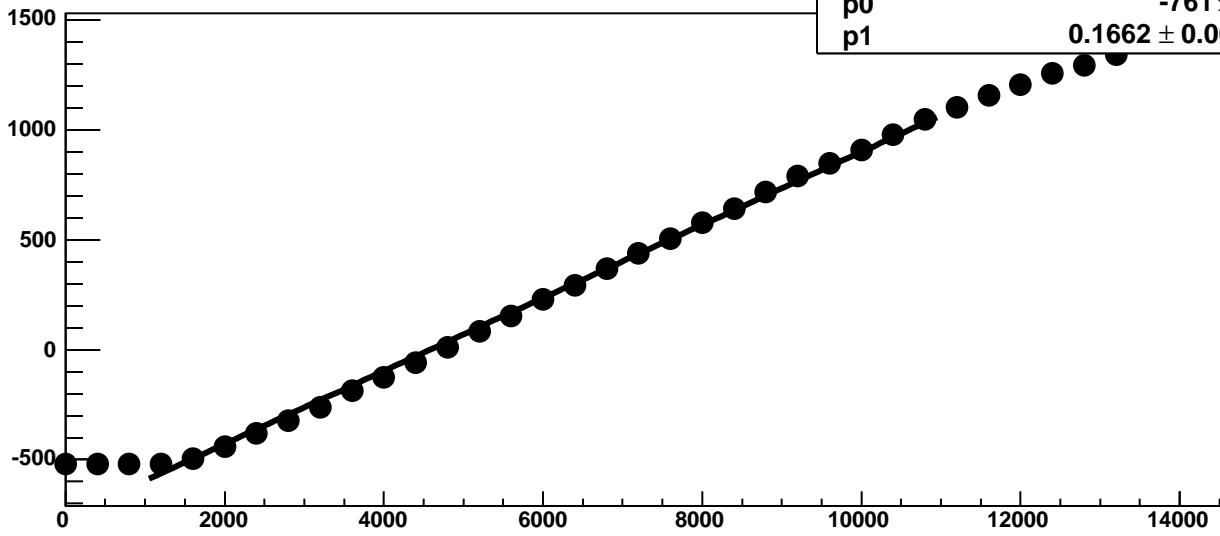
Chip 9, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



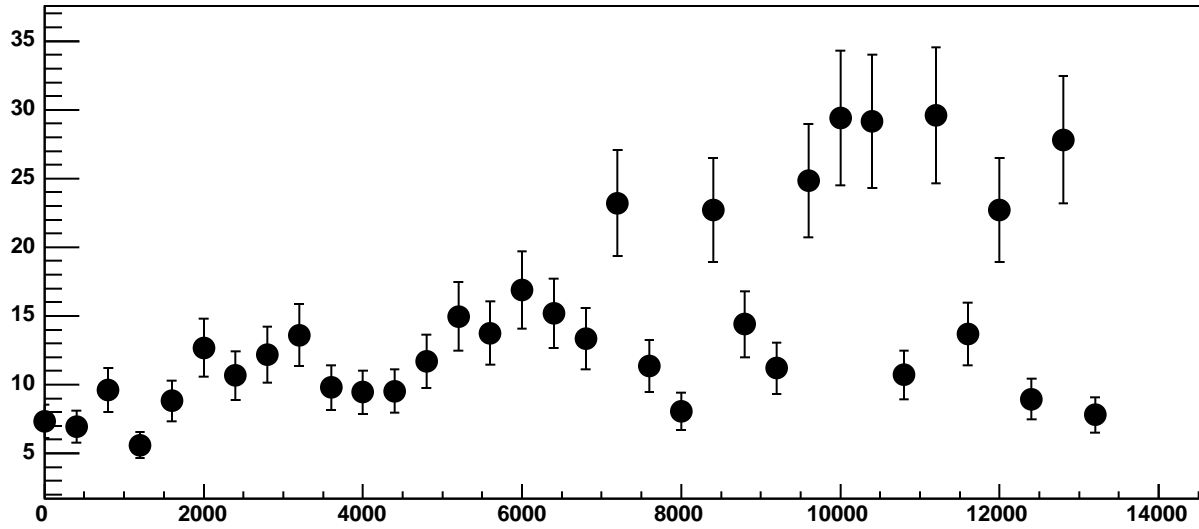
Chip 9, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



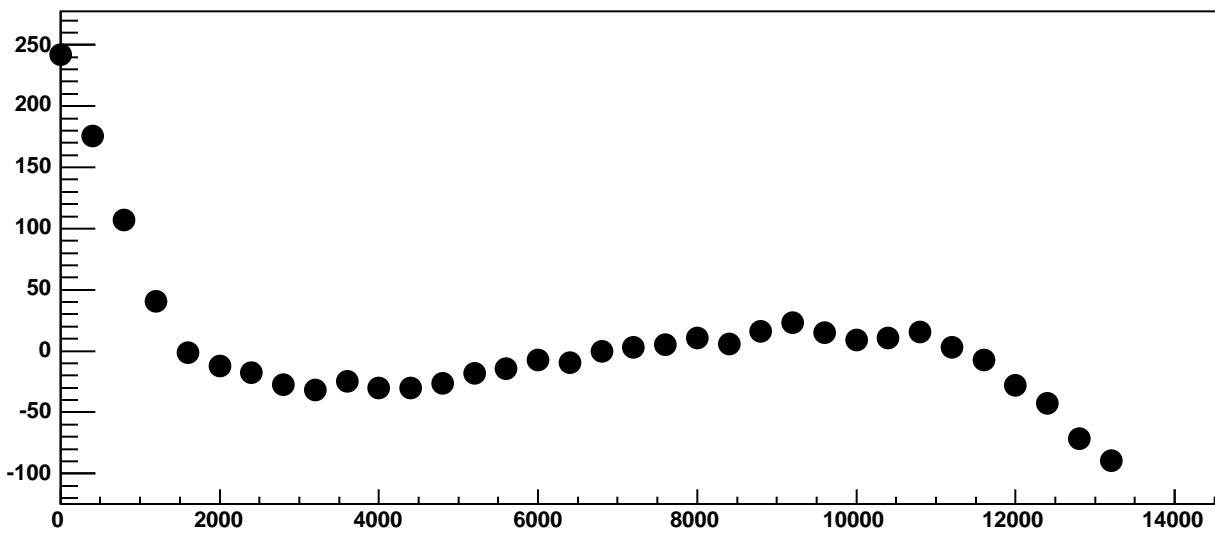
Chip 9, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



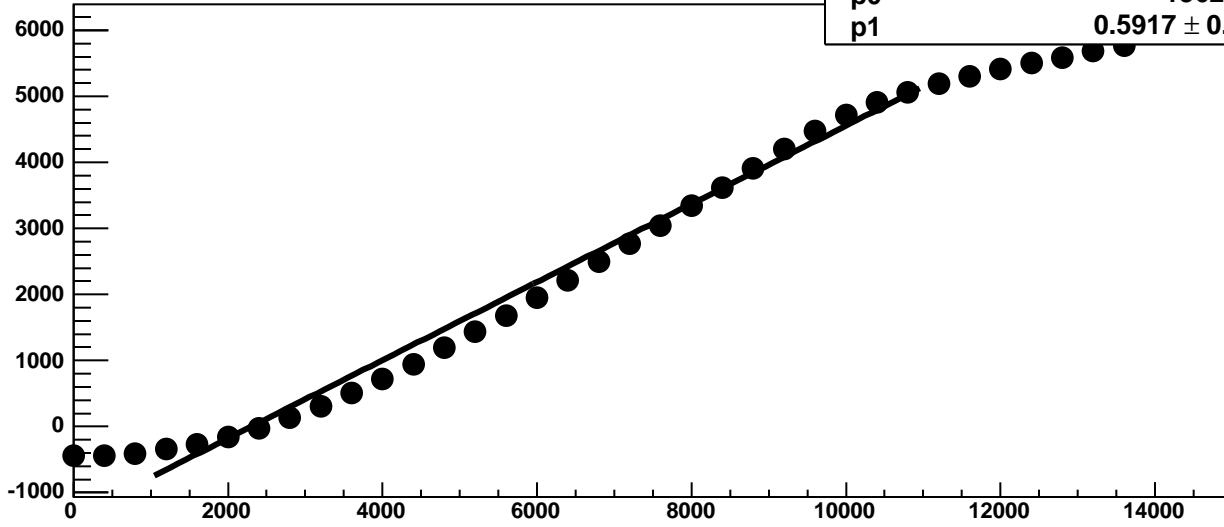
Chip 9, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



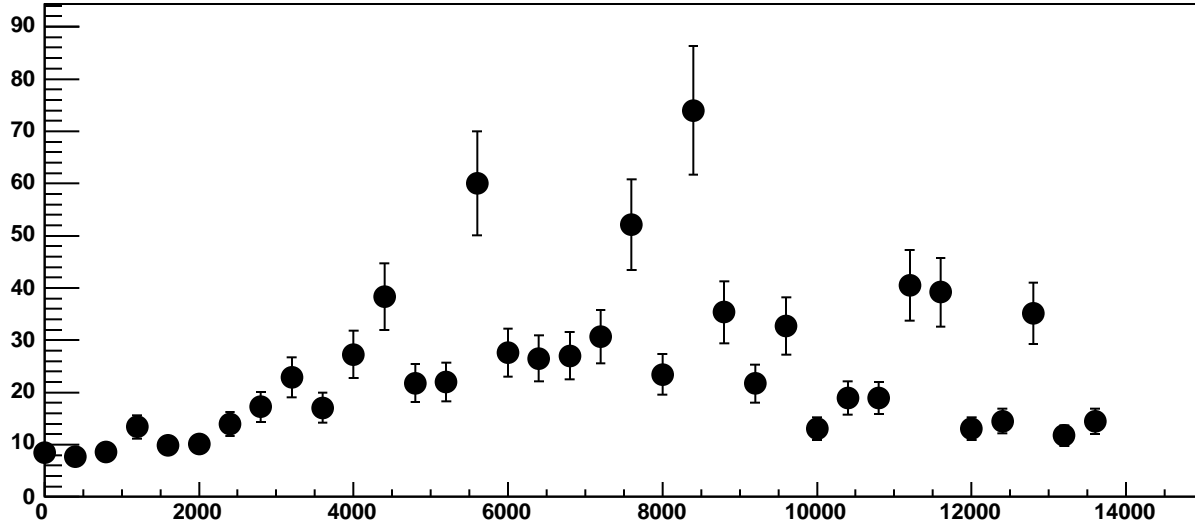
Chip 9, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



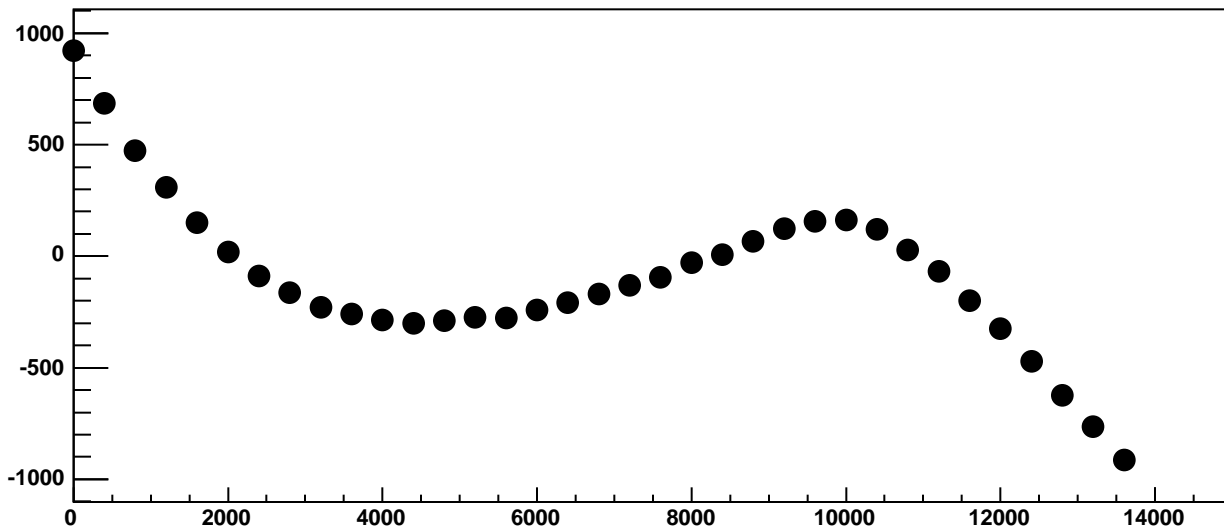
Chip 9, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC



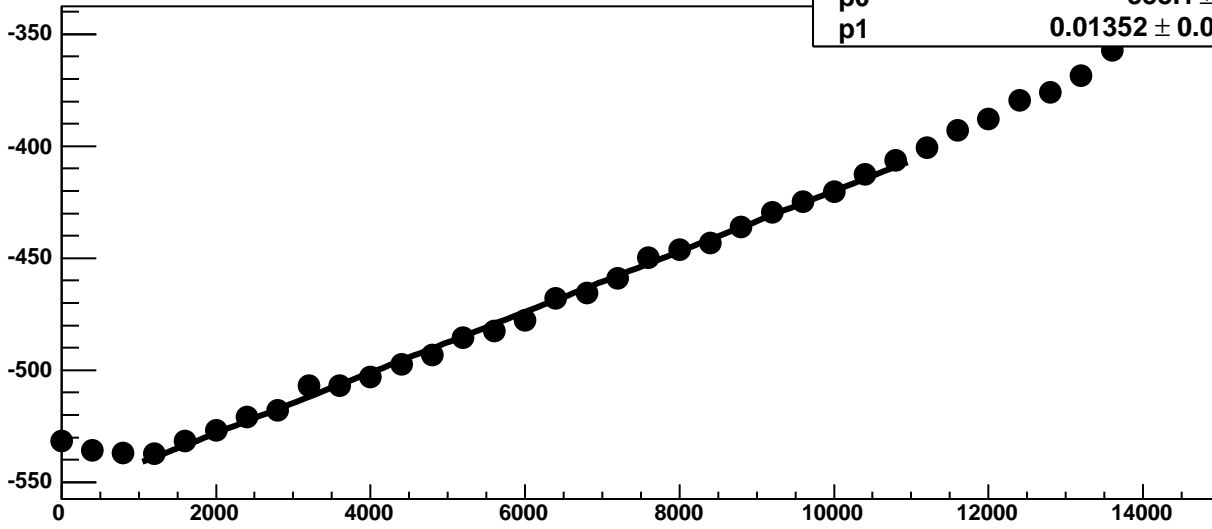
Chip 9, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC

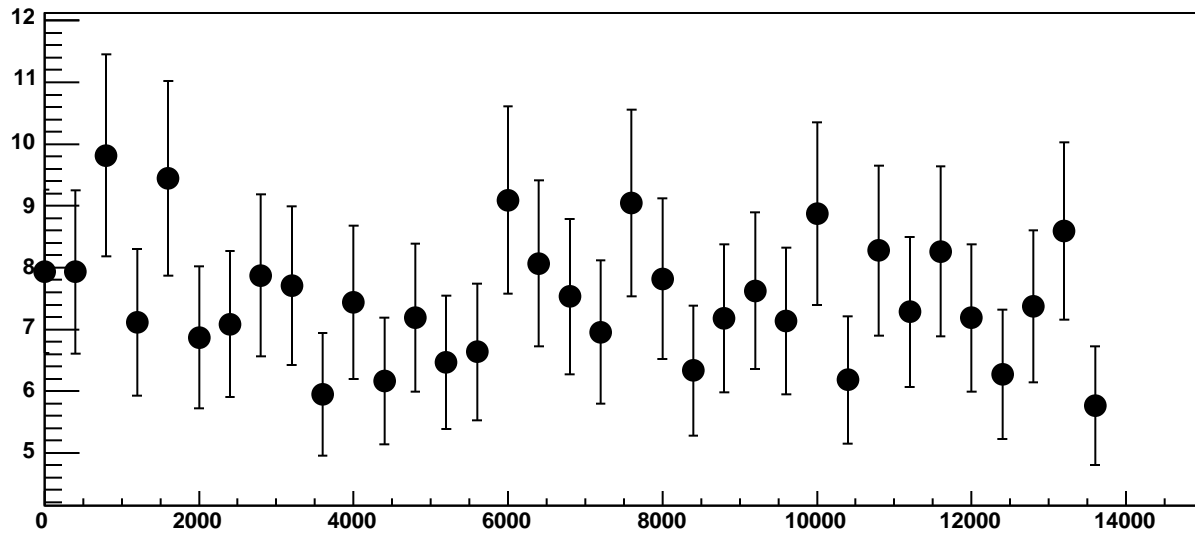


Chip 9, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

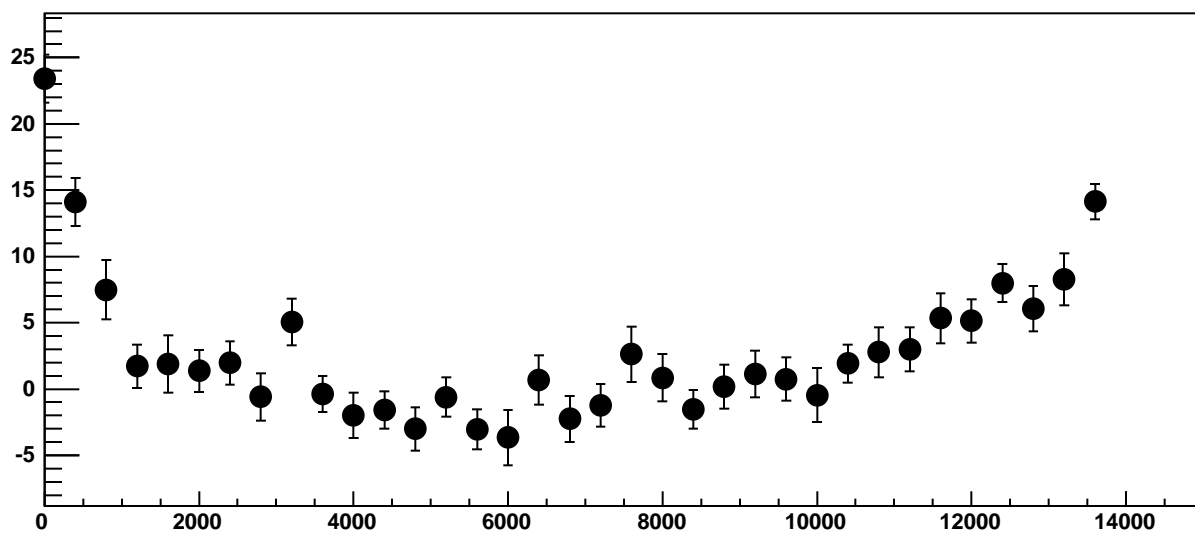


χ^2 / ndf 35.56 / 23
p0 -555.1 ± 0.7734
p1 0.01352 ± 0.0001173

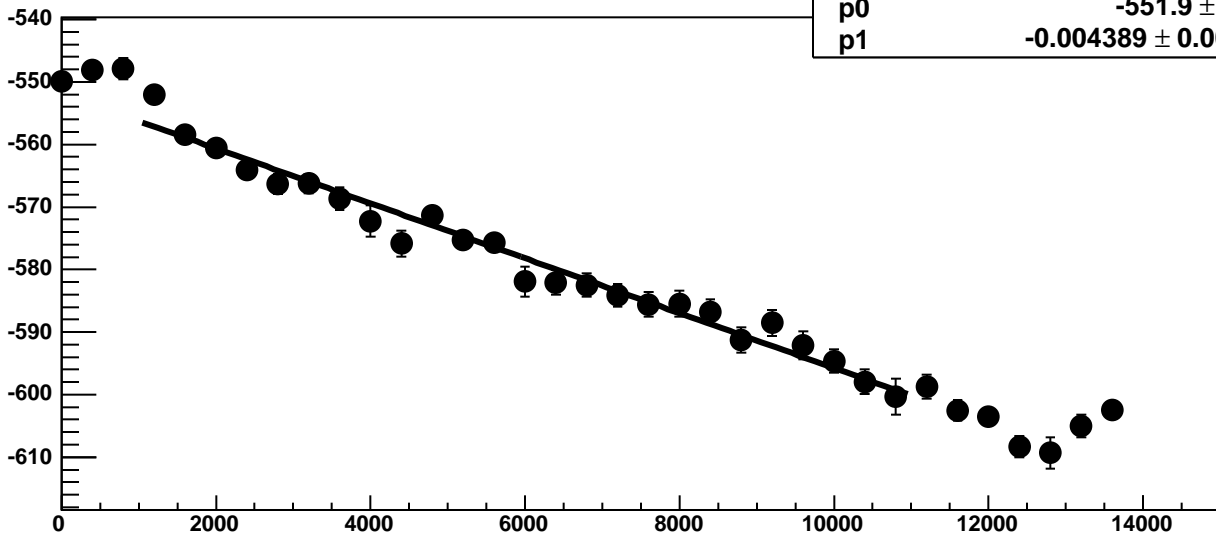
Chip 9, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 9, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

35.99 / 23

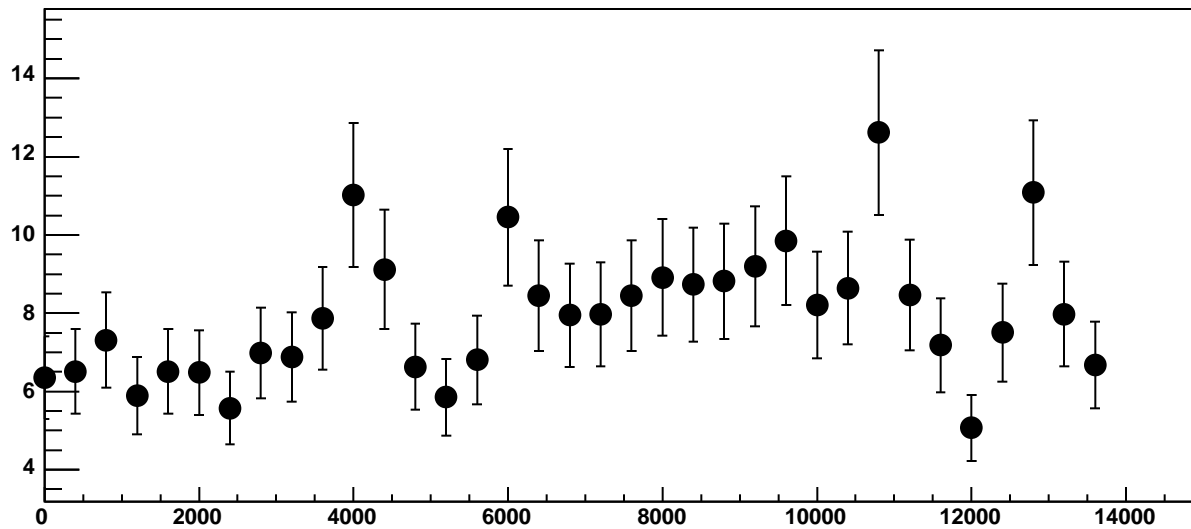
p0

-551.9 ± 0.7348

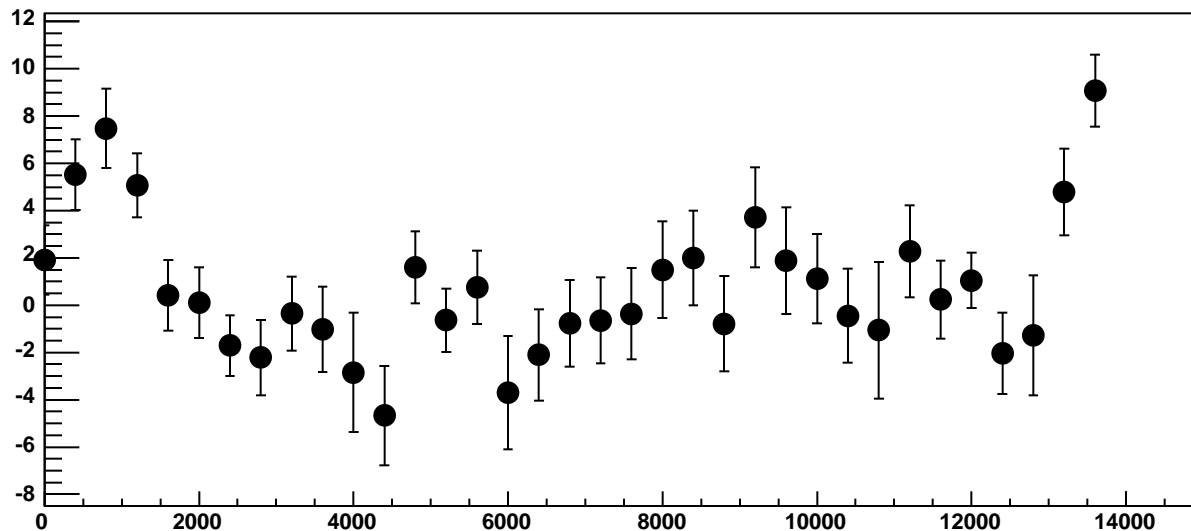
p1

-0.004389 ± 0.0001238

Chip 9, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

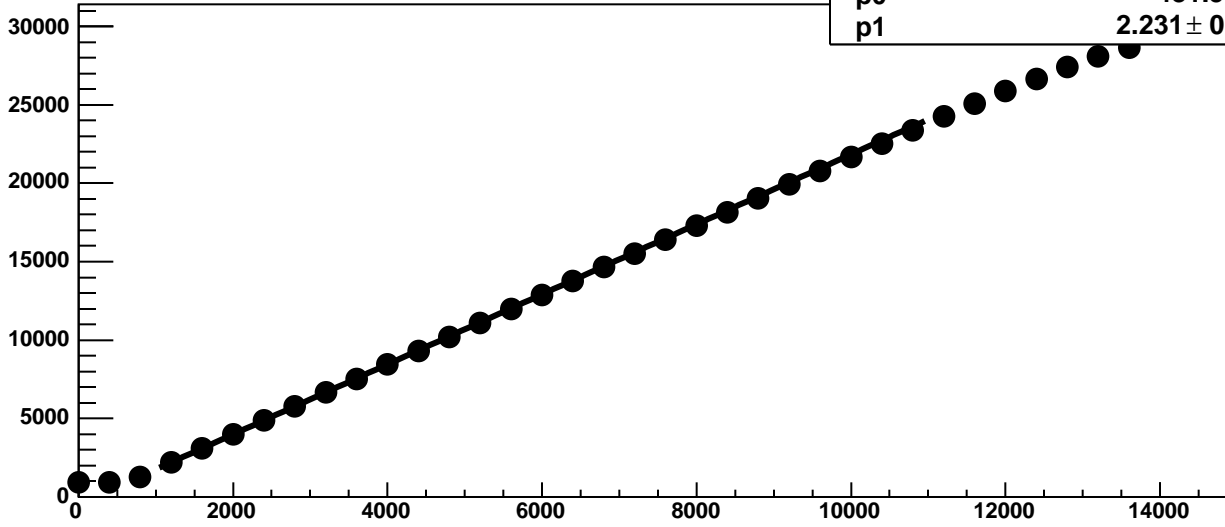


Chip 9, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

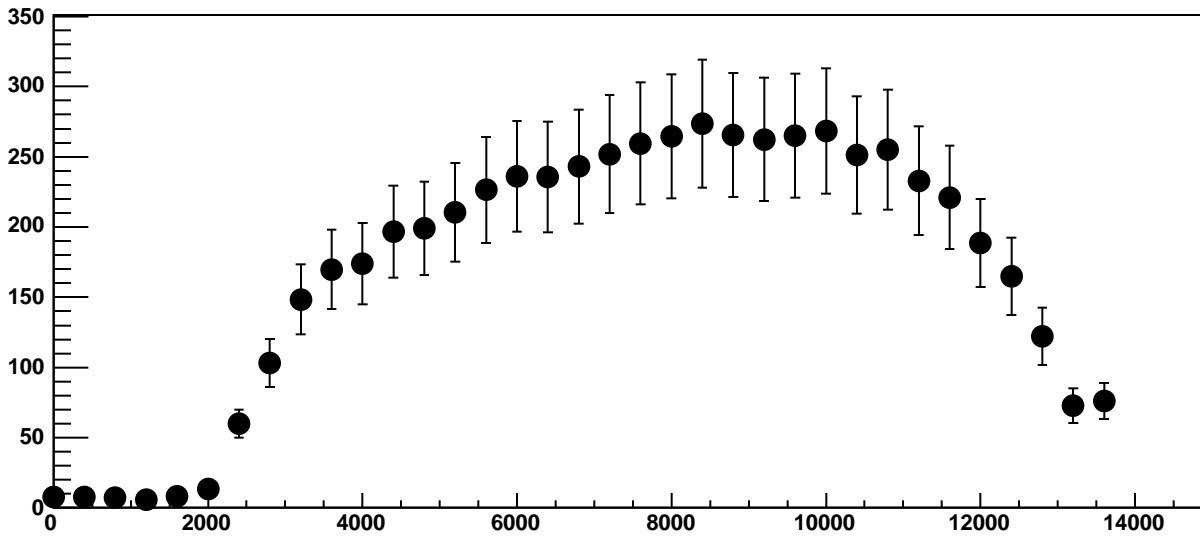


Chip 9, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC

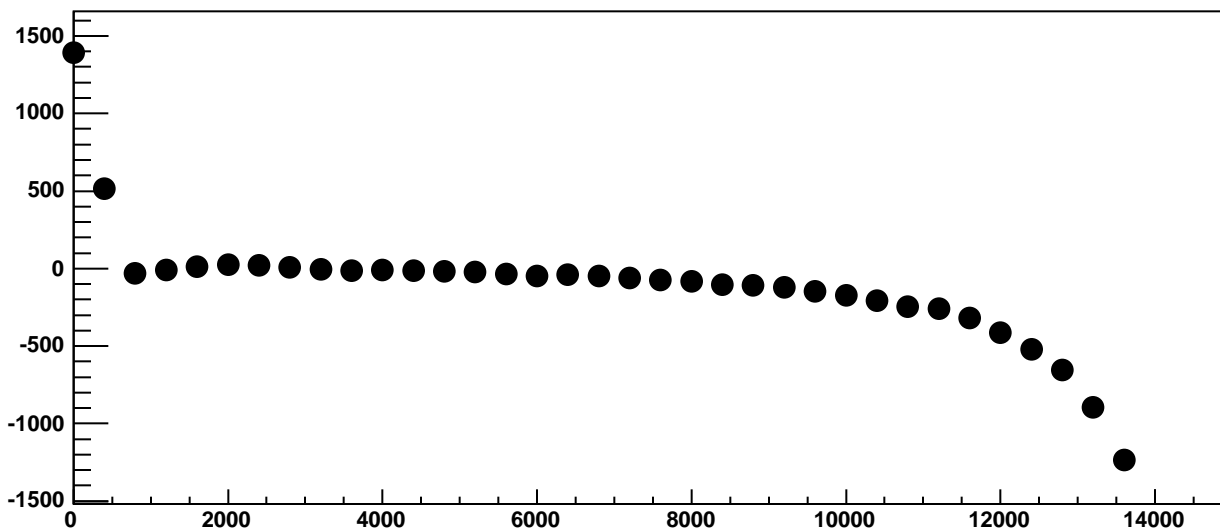
χ^2 / ndf 218.4 / 23
p0 -481.5 ± 2.835
p1 2.231 ± 0.001828



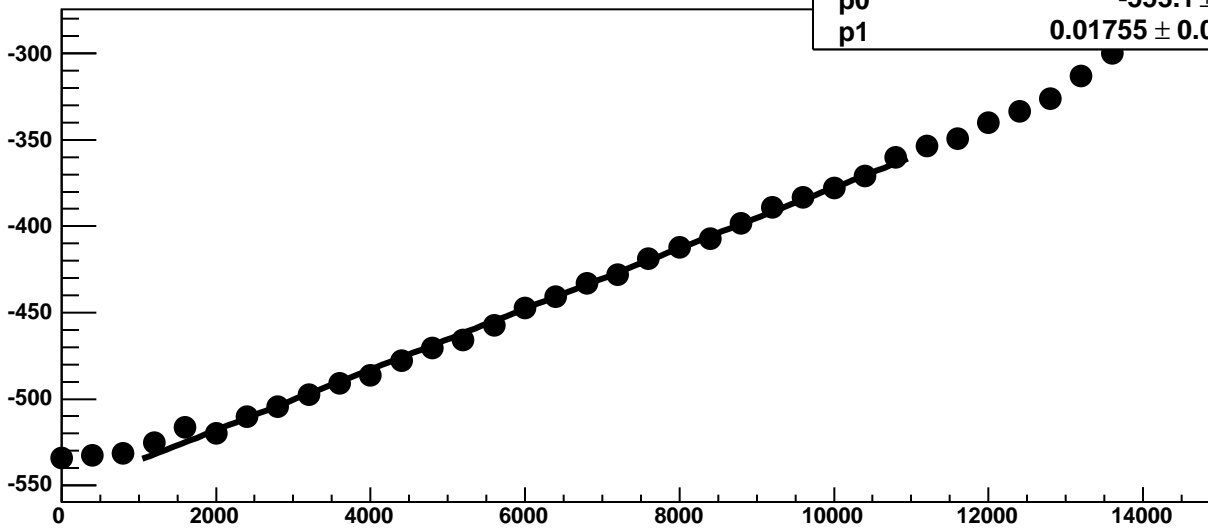
Chip 9, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC

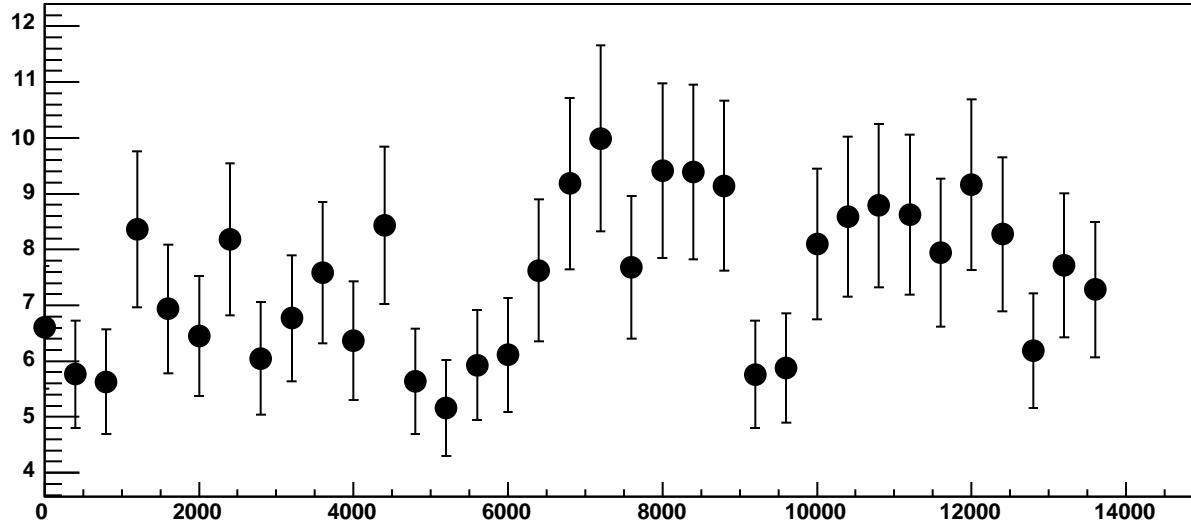


Chip 9, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

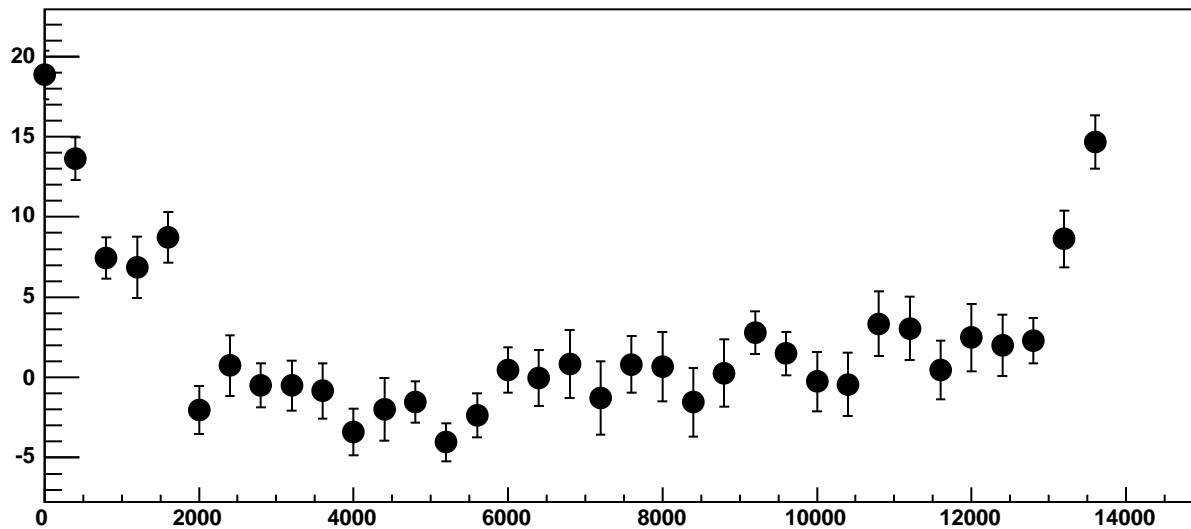


χ^2 / ndf 77.62 / 23
p0 -553.1 ± 0.7504
p1 0.01755 ± 0.0001177

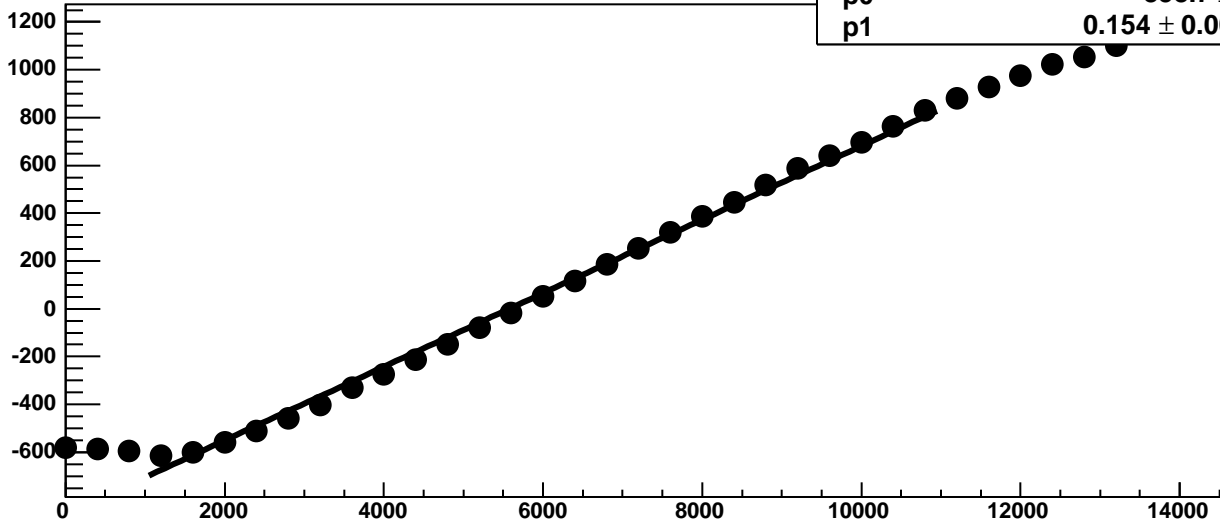
Chip 9, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



Chip 9, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

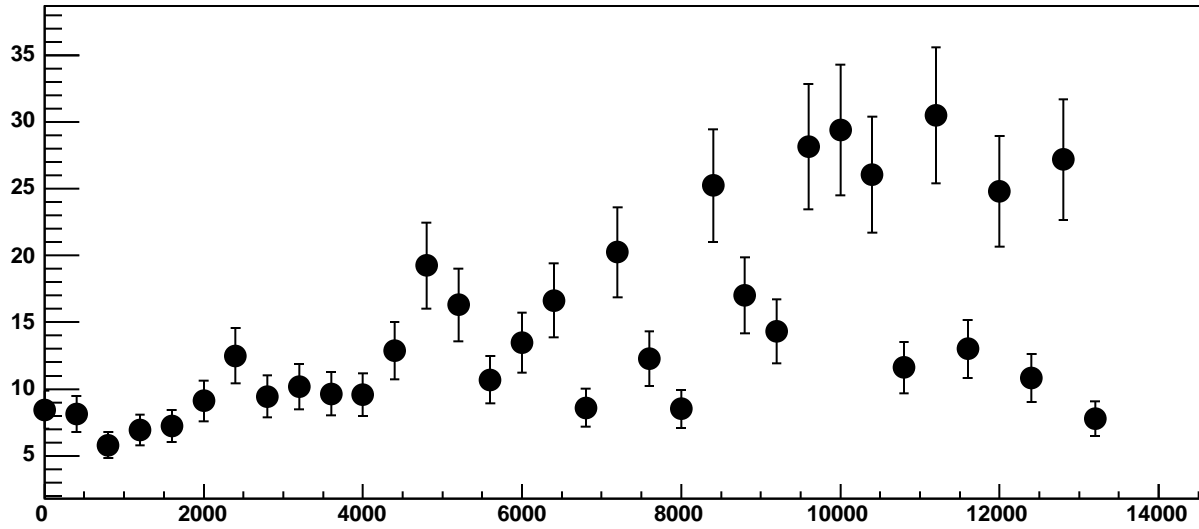


Chip 9, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

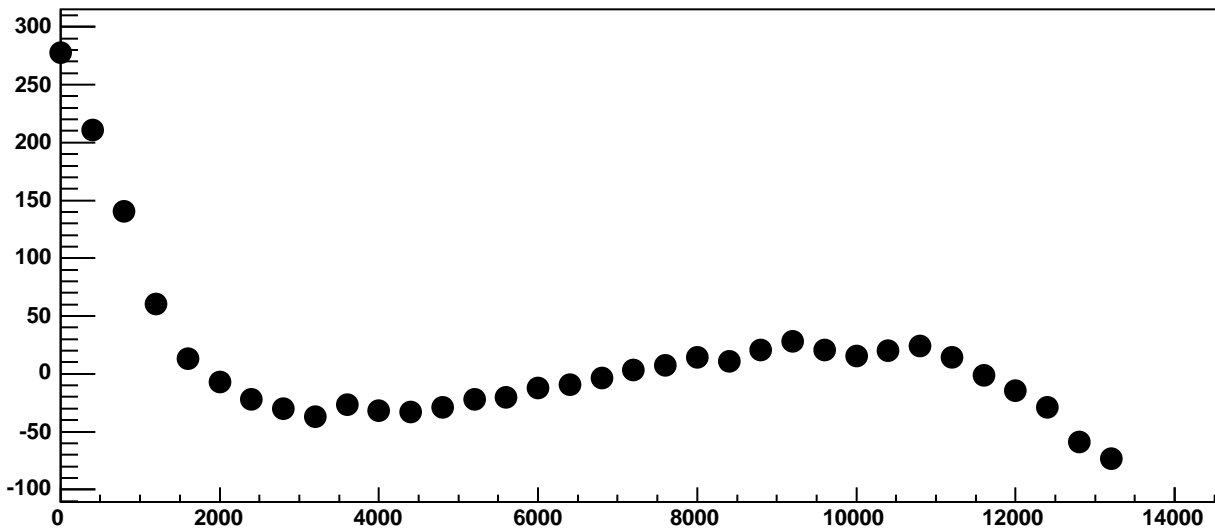


χ^2 / ndf 2951 / 23
p0 -858.7 ± 1.018
p1 0.154 ± 0.0001864

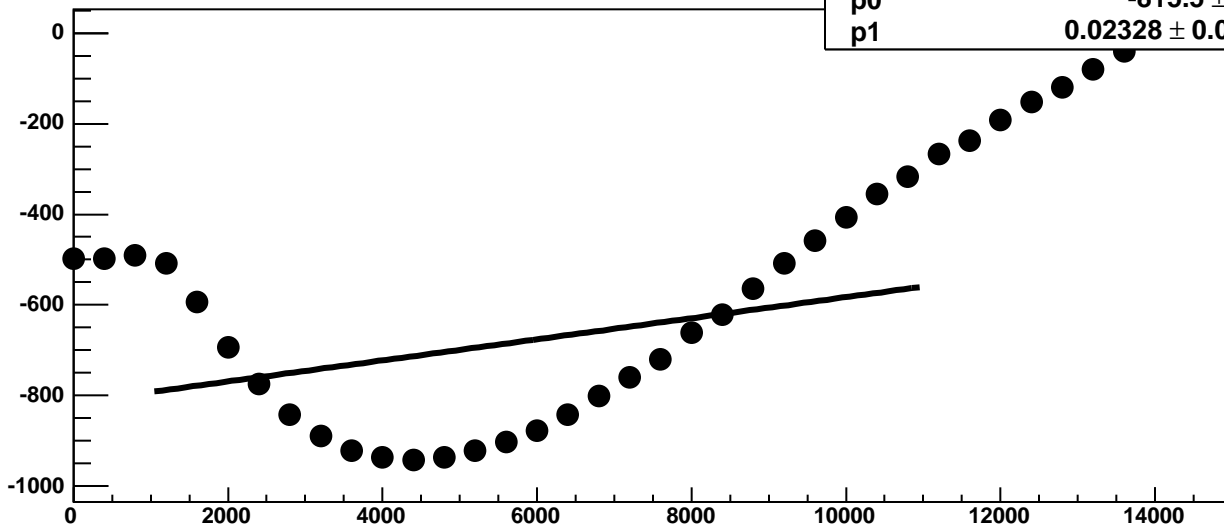
Chip 9, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

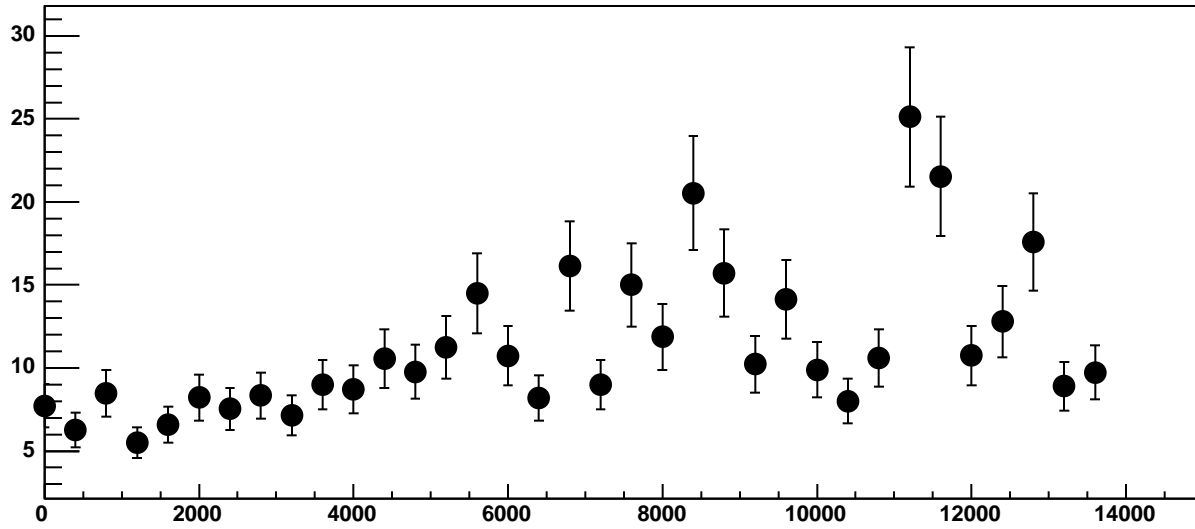


Chip 9, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

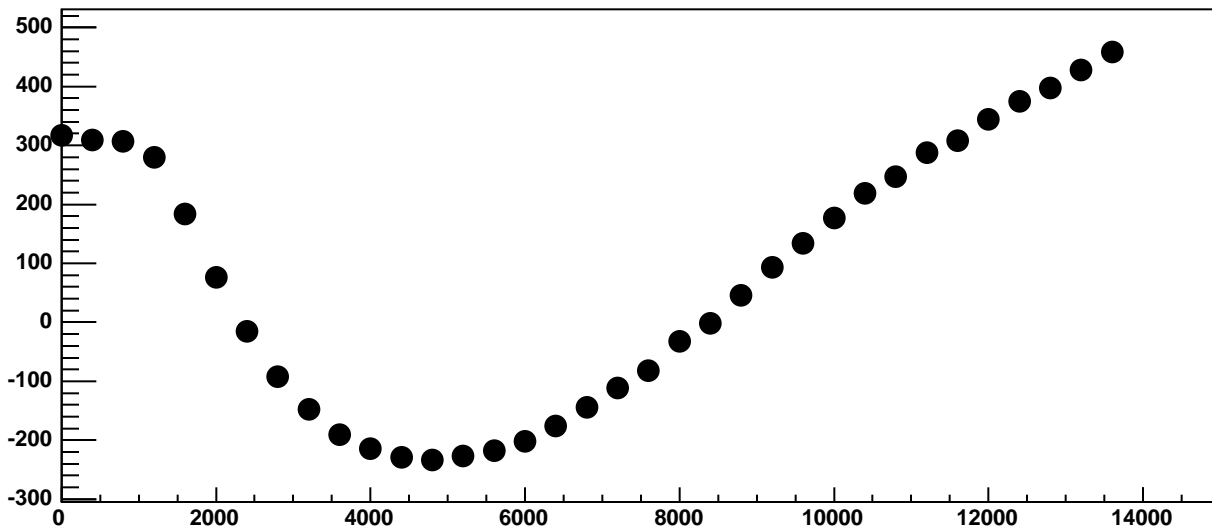


χ^2 / ndf 1.827e+05 / 23
p0 -815.5 ± 0.8035
p1 0.02328 ± 0.0001381

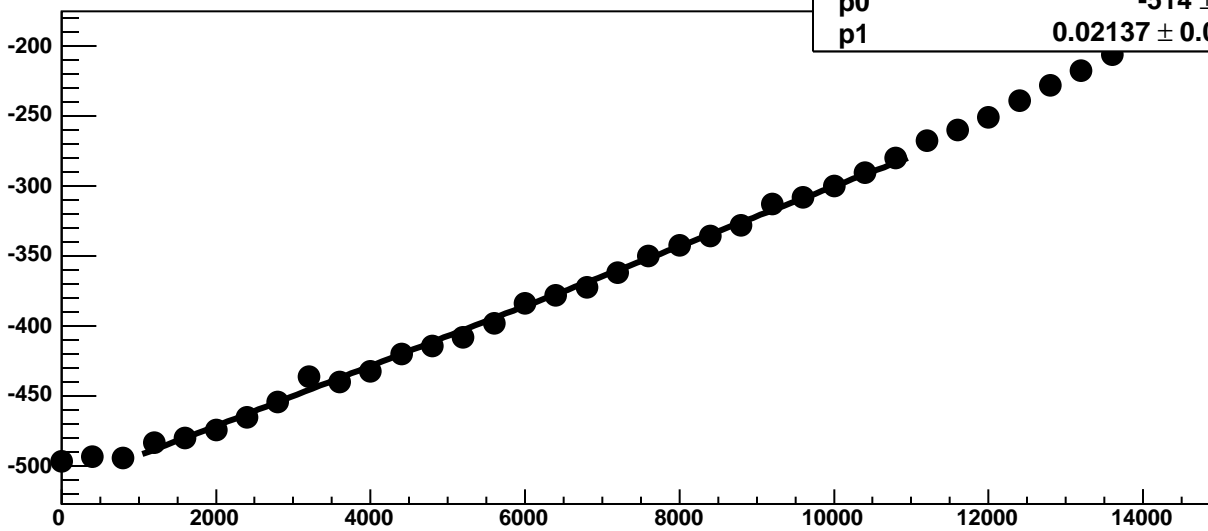
Chip 9, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

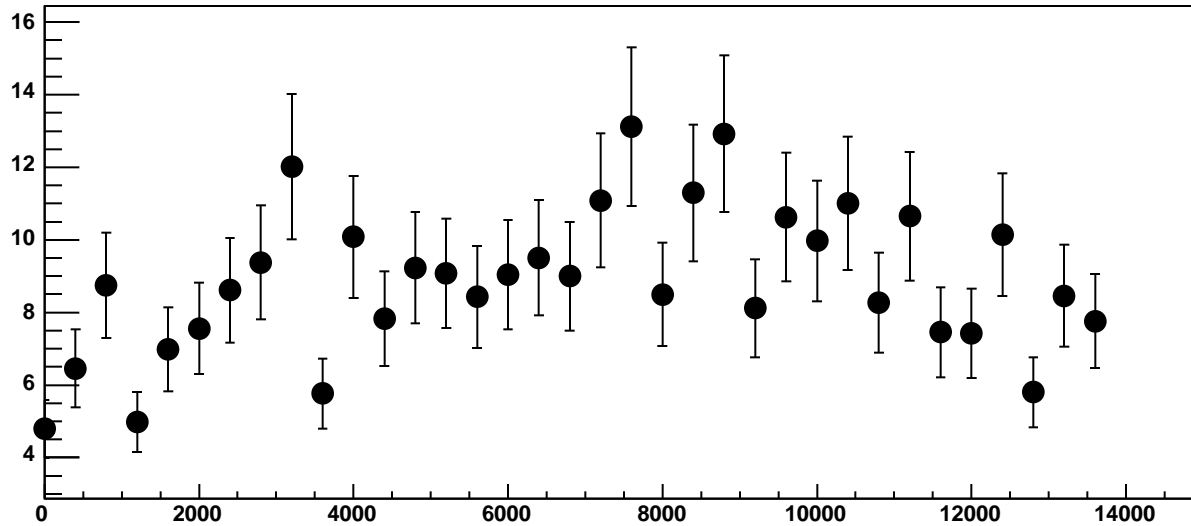


Chip 9, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

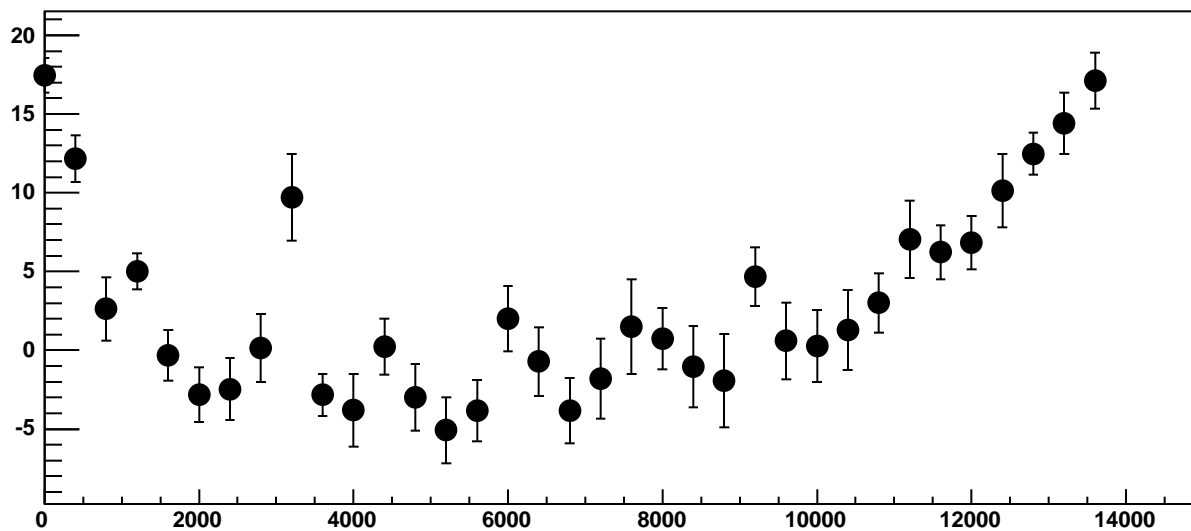


χ^2 / ndf 70.27 / 23
p0 -514 ± 0.7793
p1 0.02137 ± 0.0001301

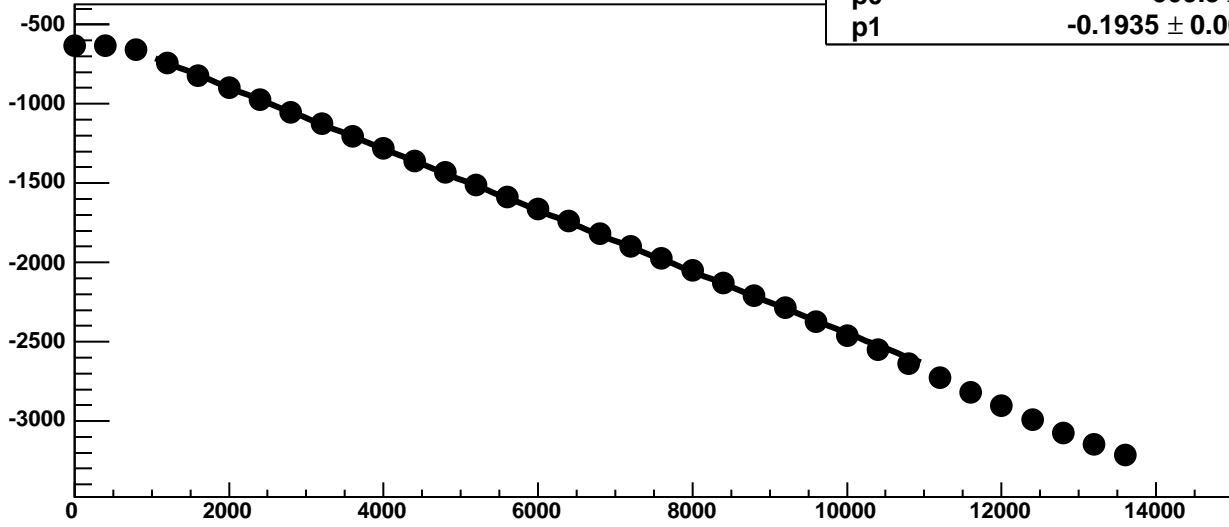
Chip 9, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC

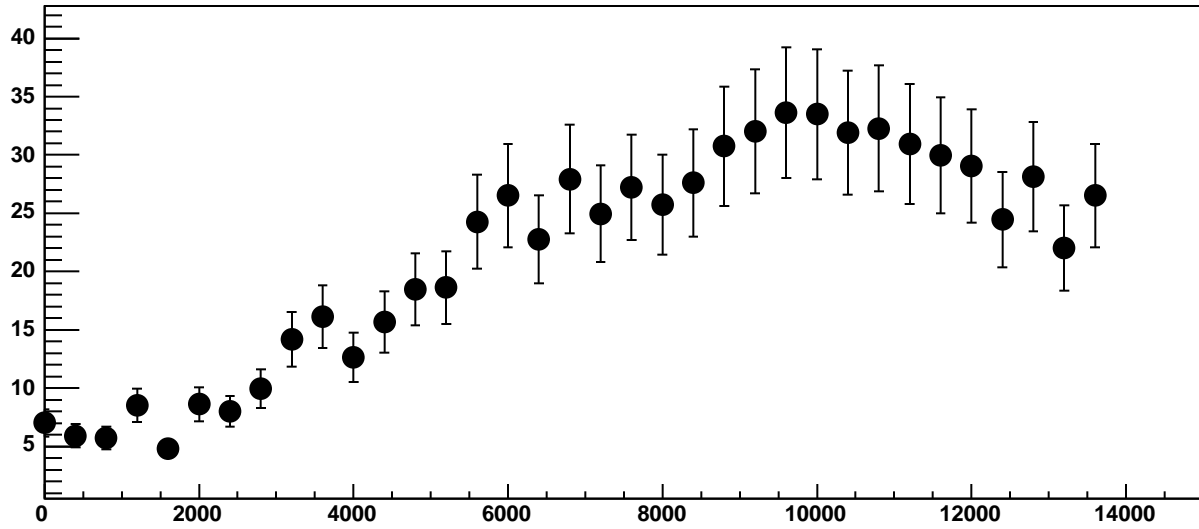


Chip 9, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

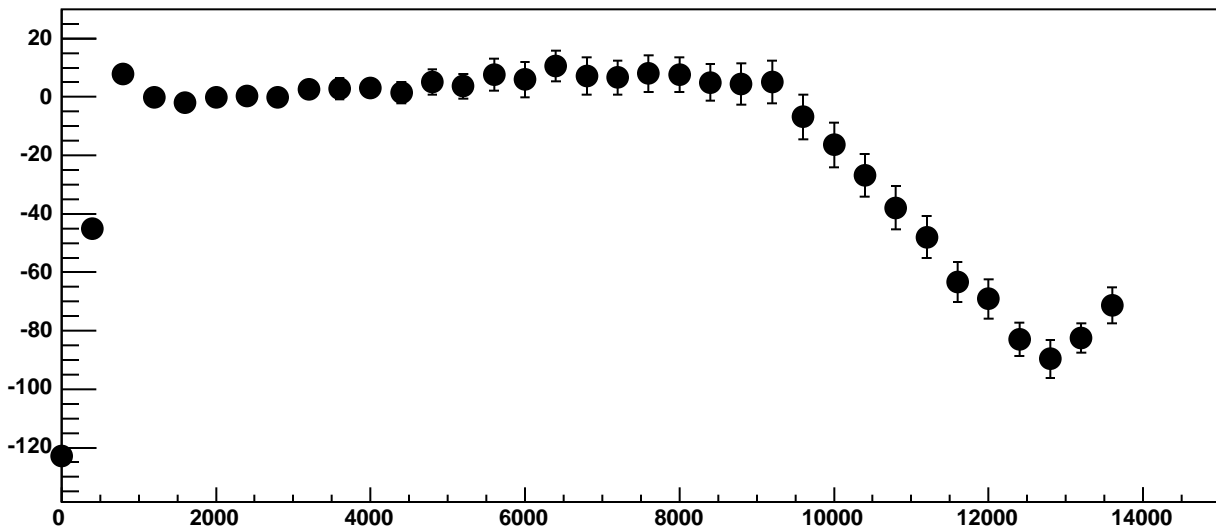


χ^2 / ndf 67.32 / 23
p0 -509.5 ± 1.057
p1 -0.1935 ± 0.0002839

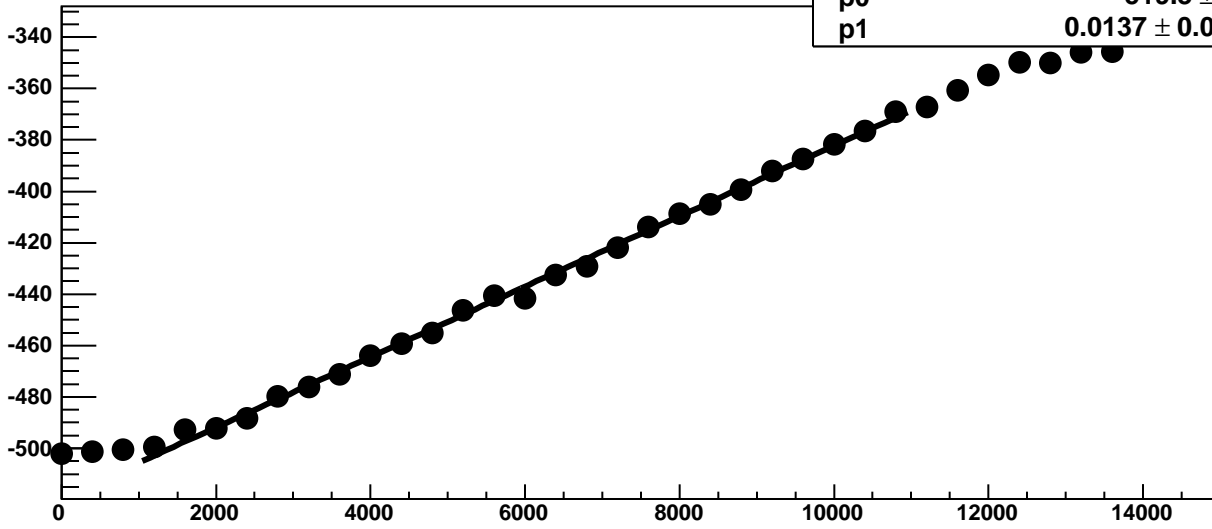
Chip 9, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



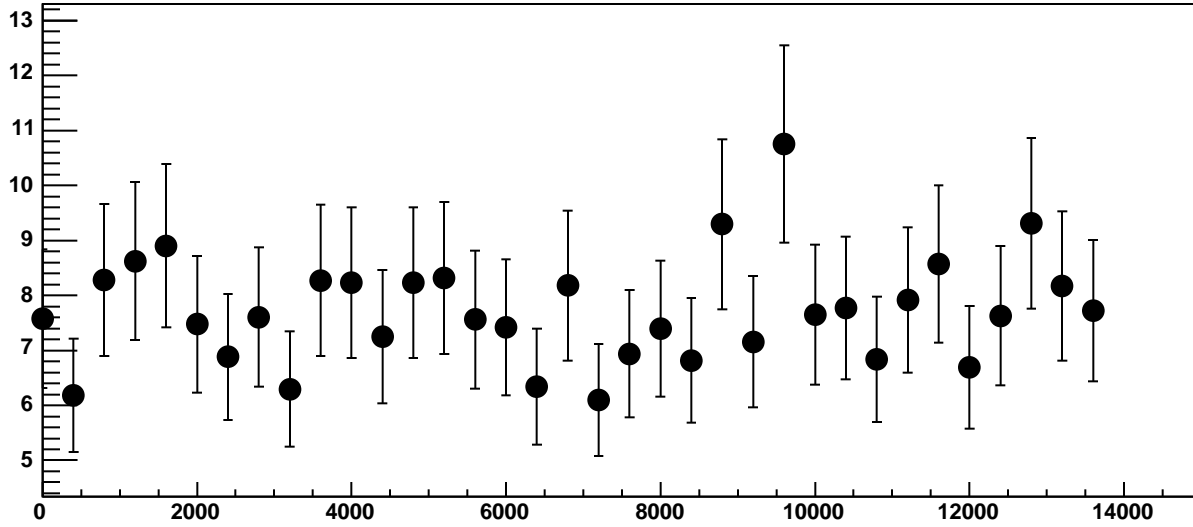
Chip 9, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



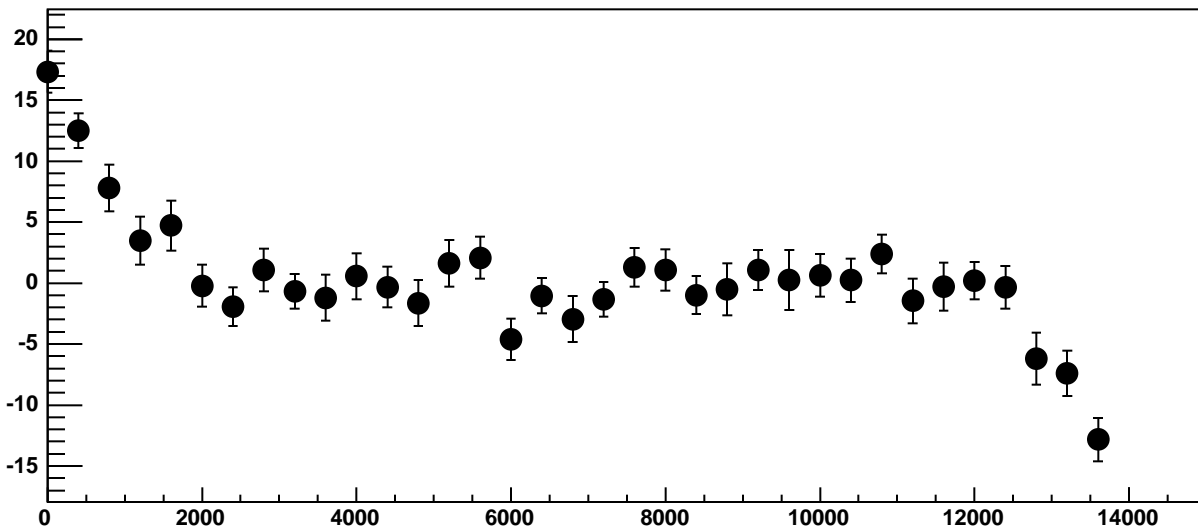
Chip 9, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



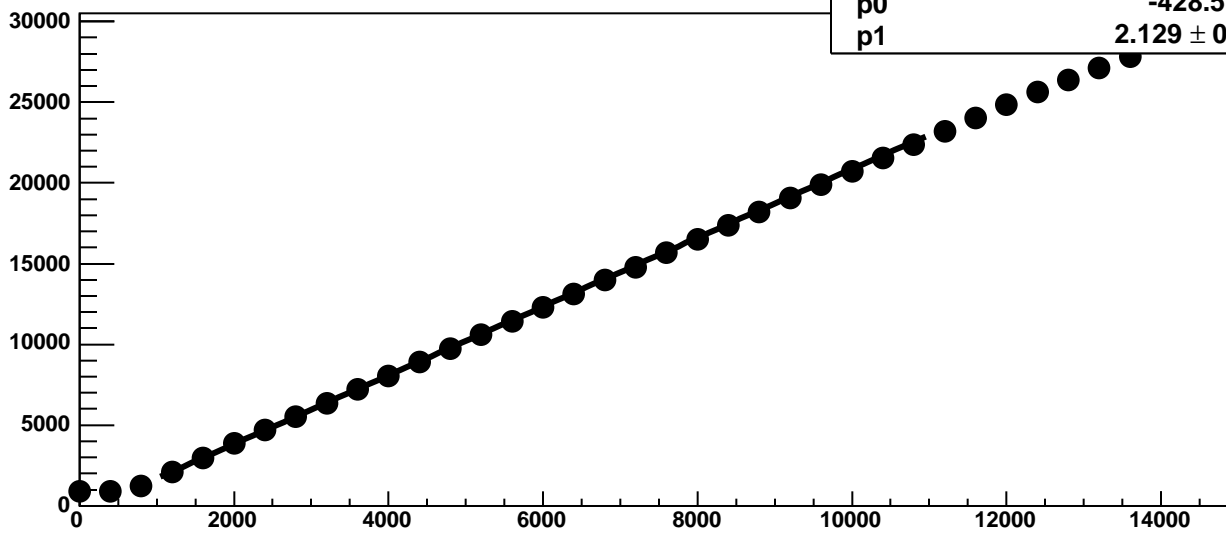
Chip 9, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



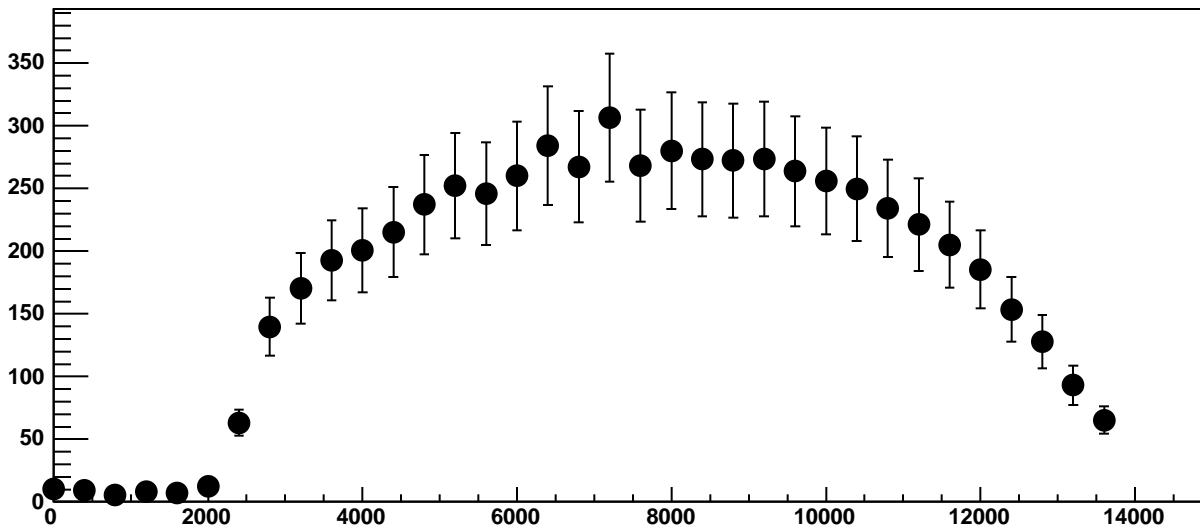
Chip 9, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



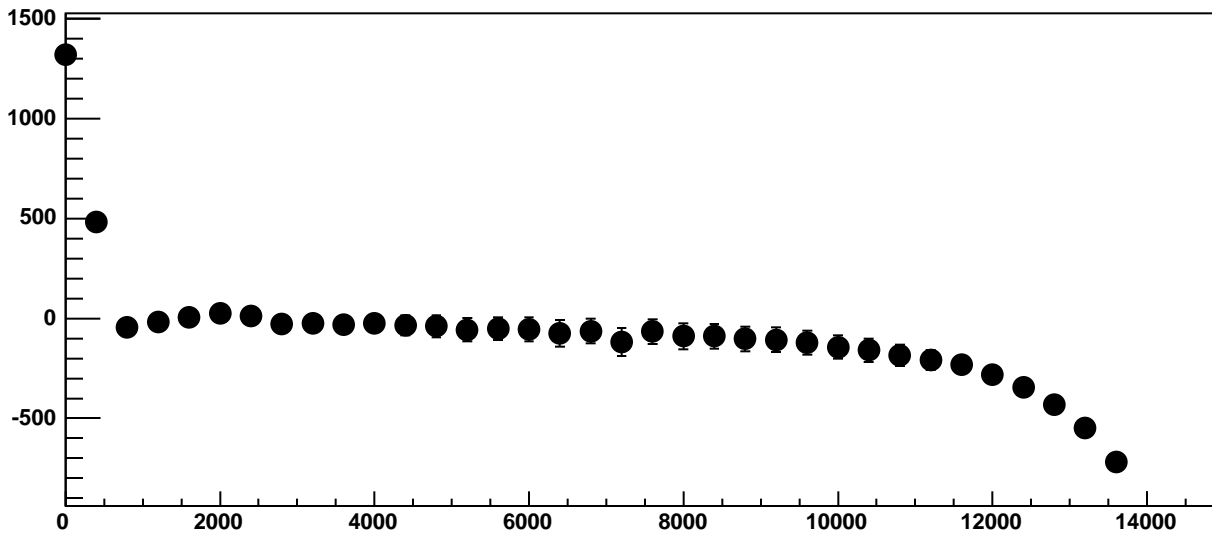
Chip 9, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC



Chip 9, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 9, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

χ^2 / ndf

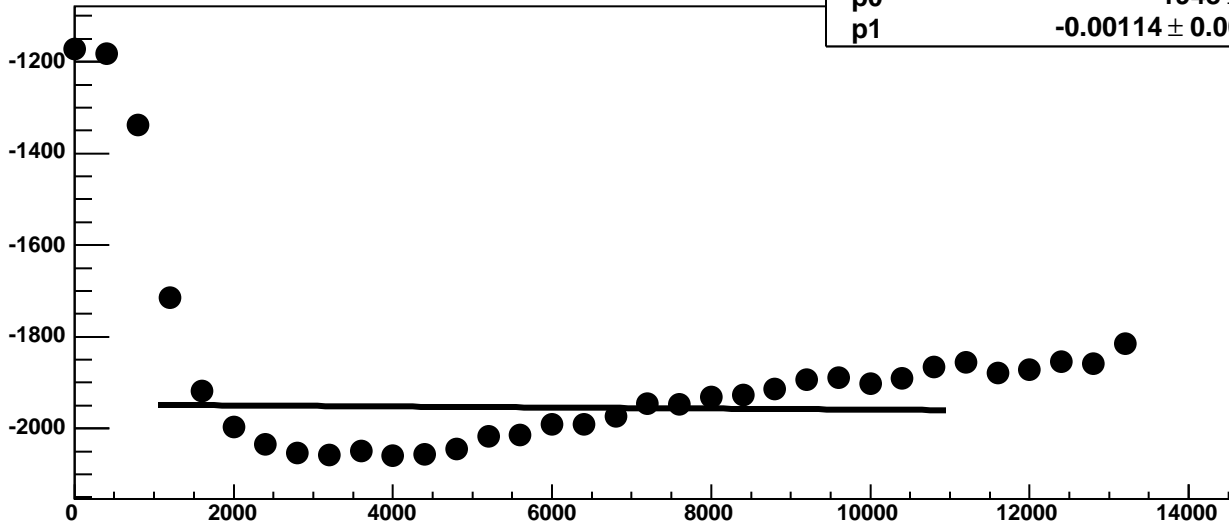
5980 / 23

p0

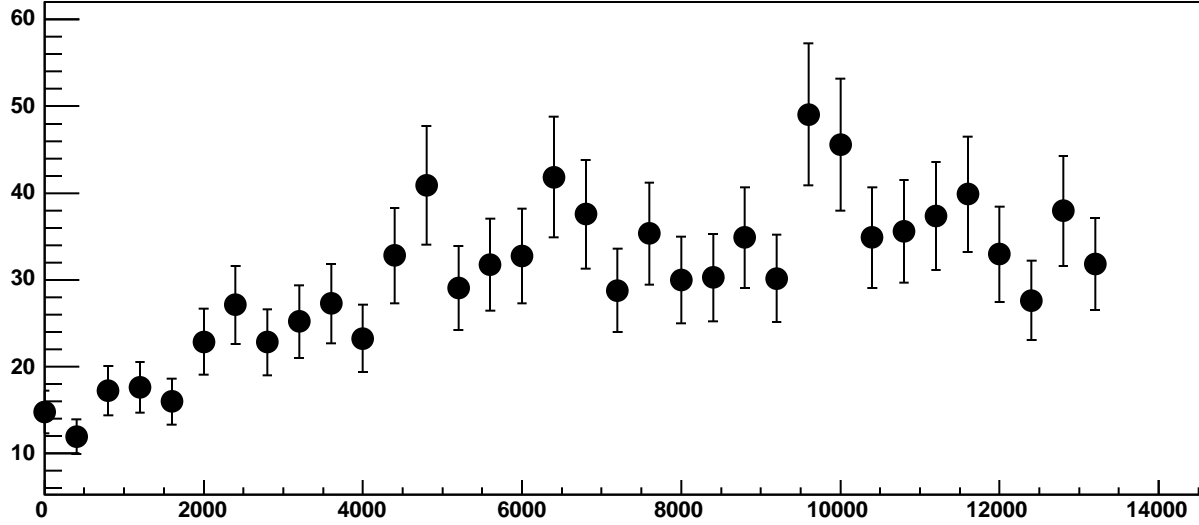
-1948 ± 2.423

p1

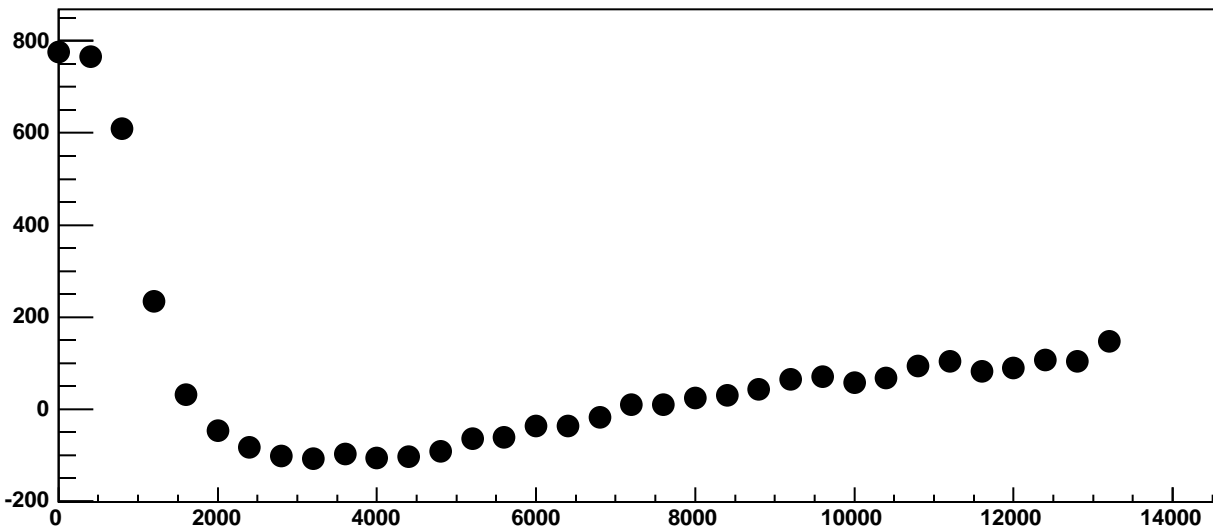
-0.00114 ± 0.0004368



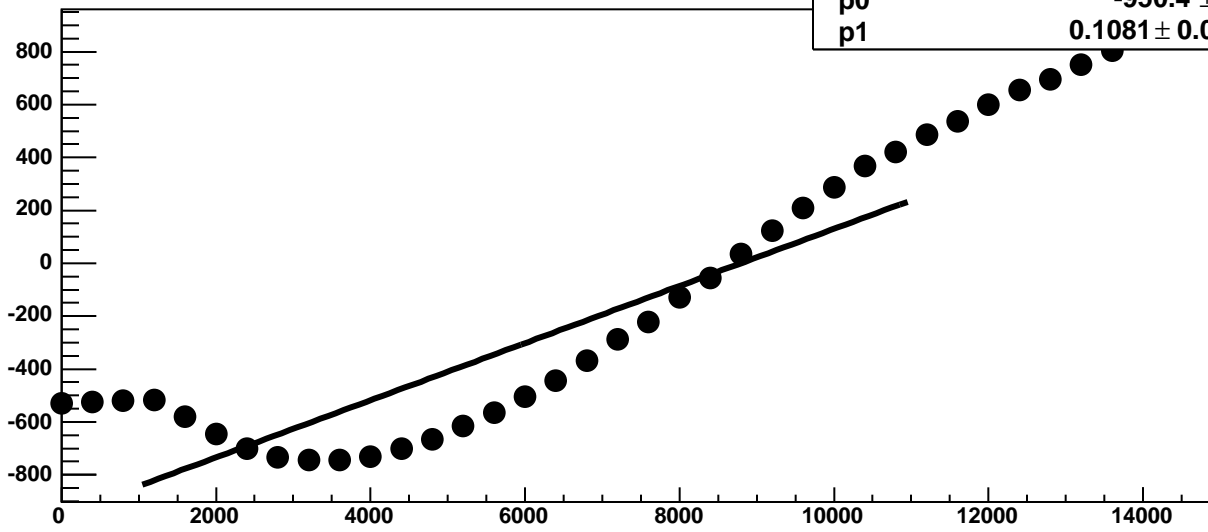
Chip 9, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



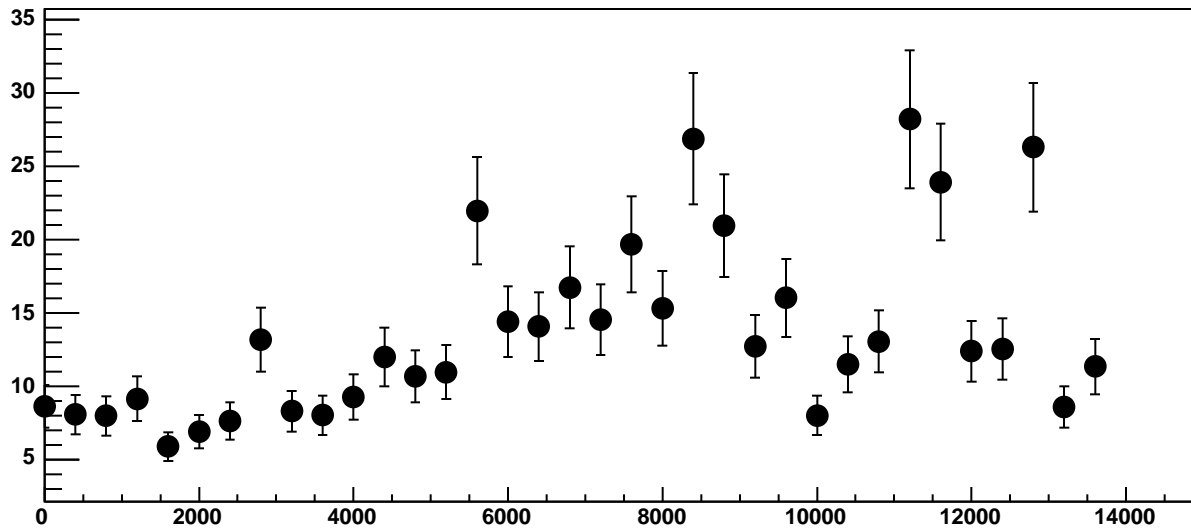
Chip 9, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC



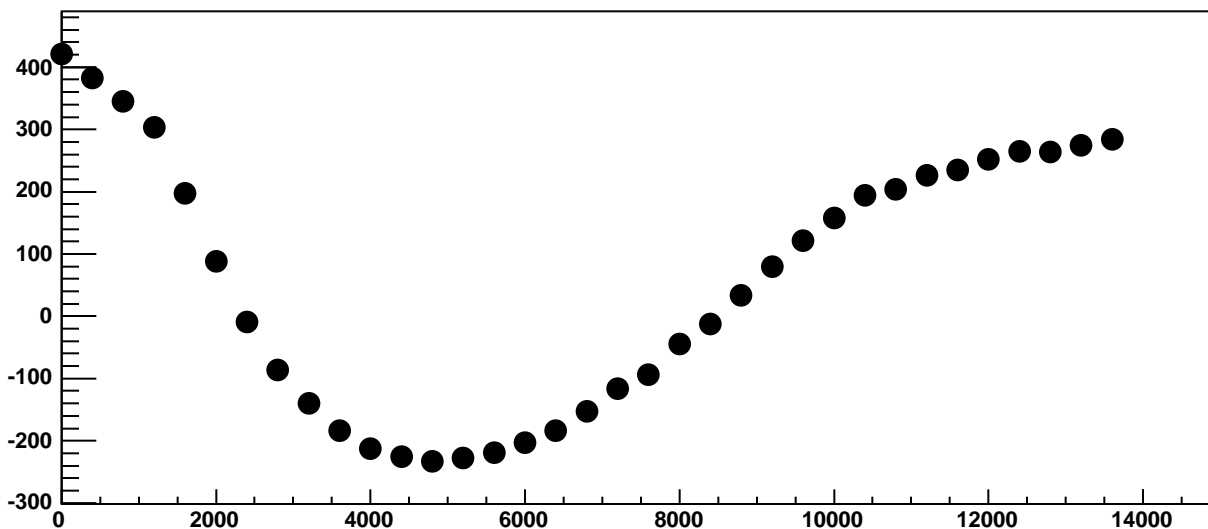
Chip 9, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



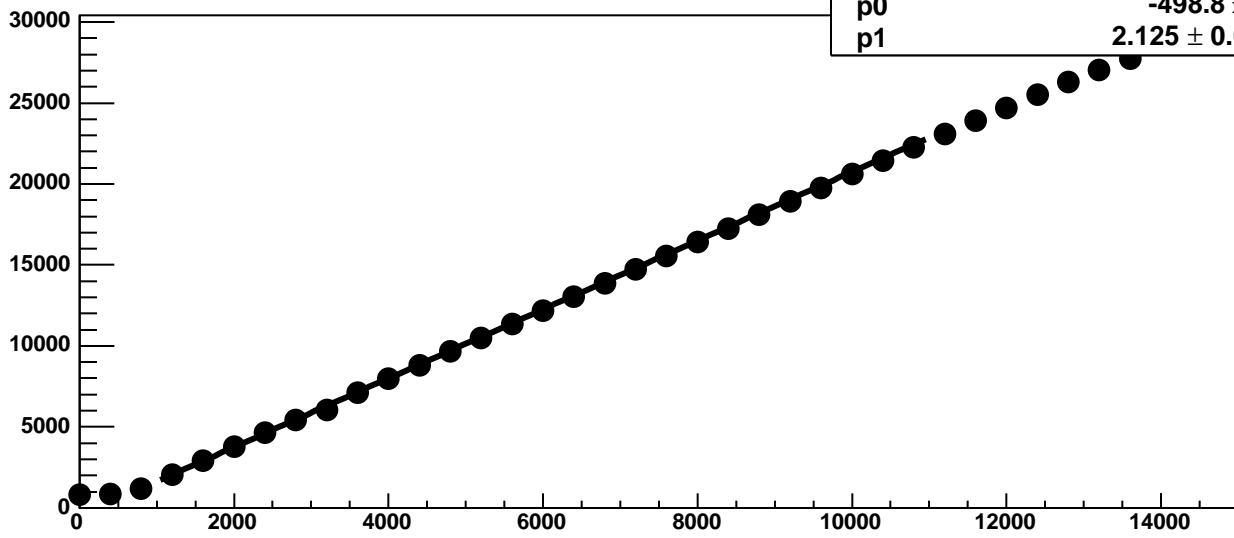
Chip 9, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

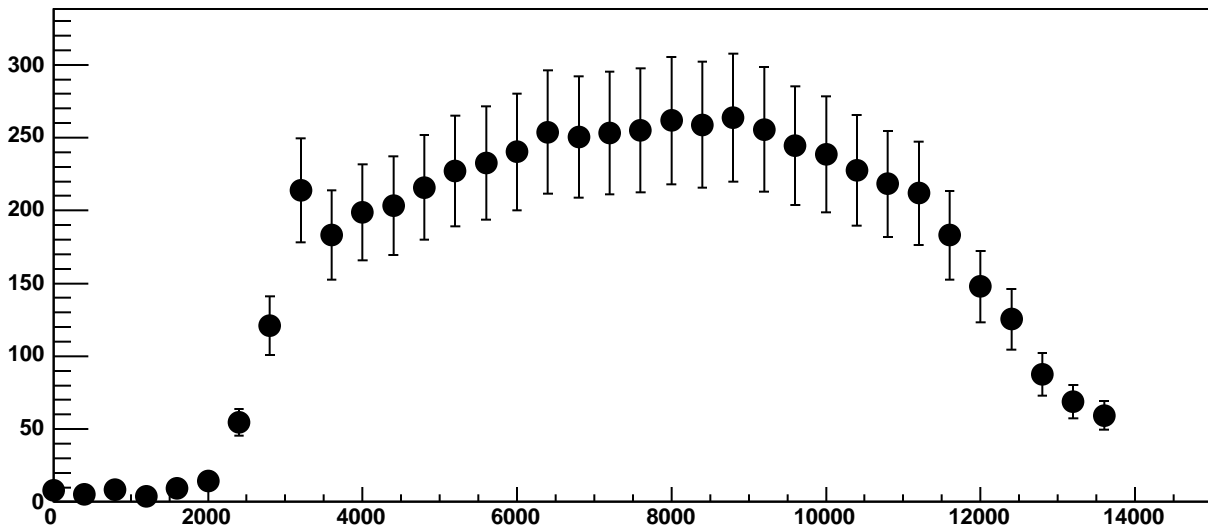


Chip 9, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC

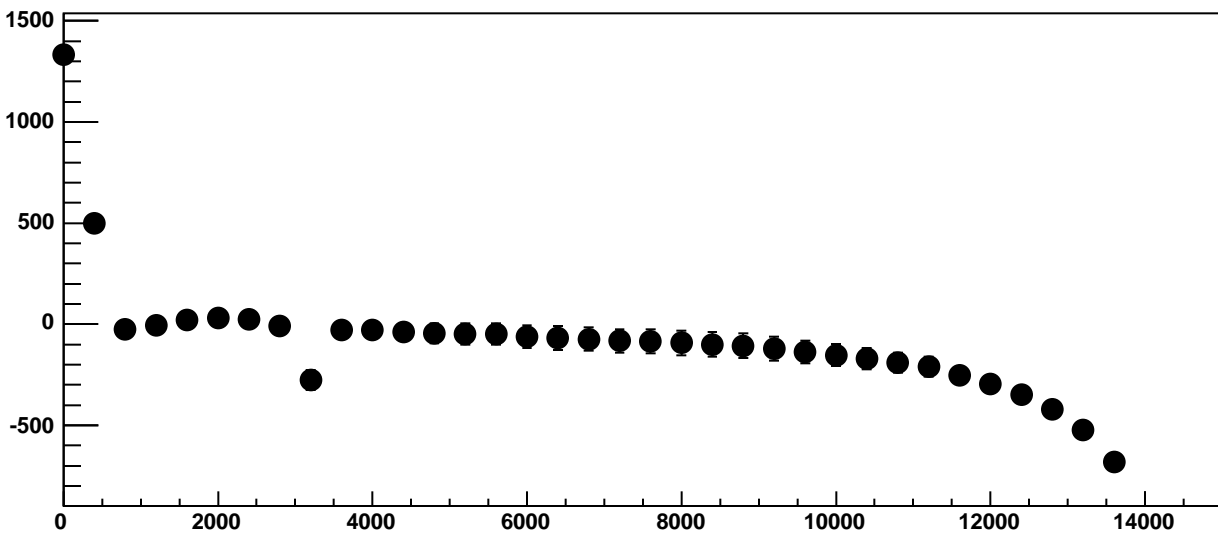


χ^2 / ndf 311.2 / 23
p0 -498.8 ± 2.447
p1 2.125 ± 0.001729

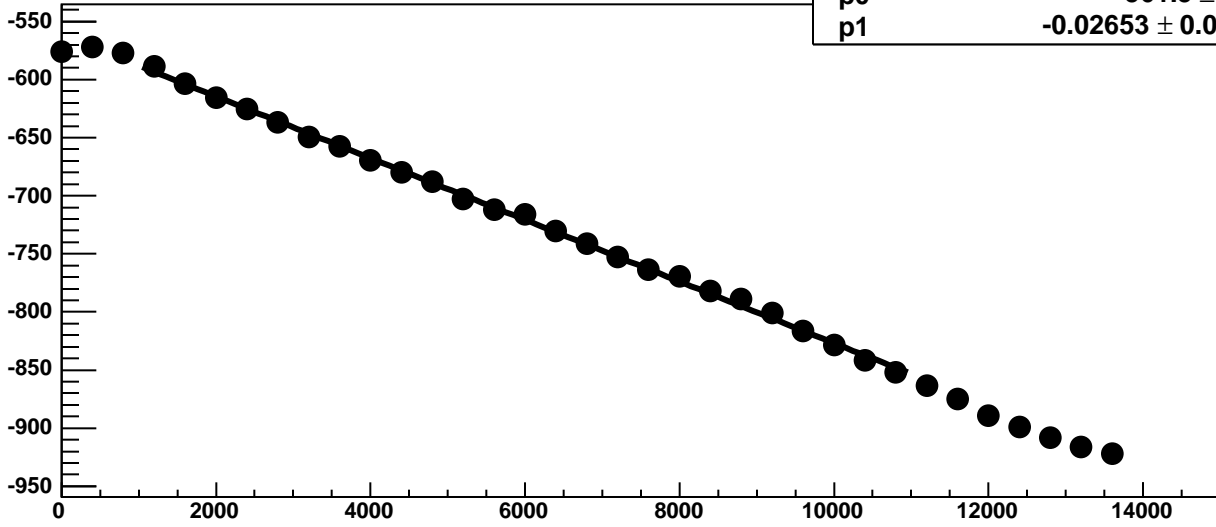
Chip 9, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 9, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

48.93 / 23

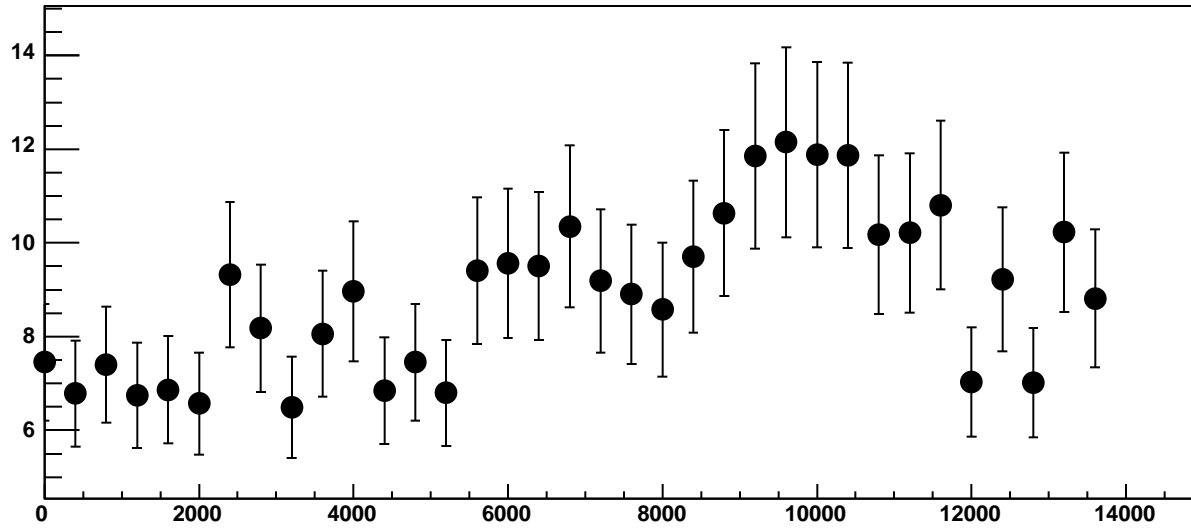
p0

-561.5 ± 0.8234

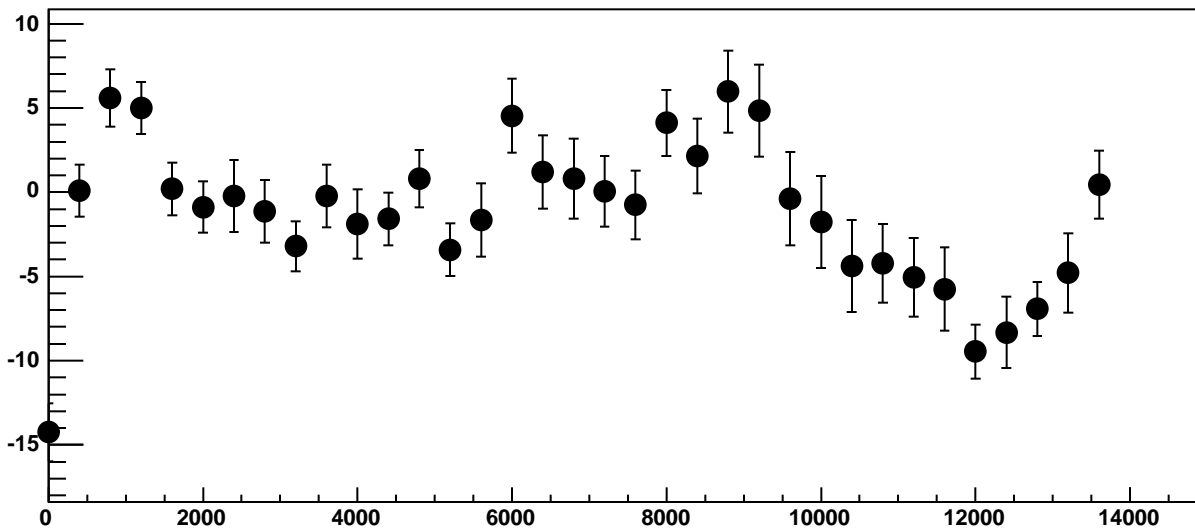
p1

-0.02653 ± 0.0001421

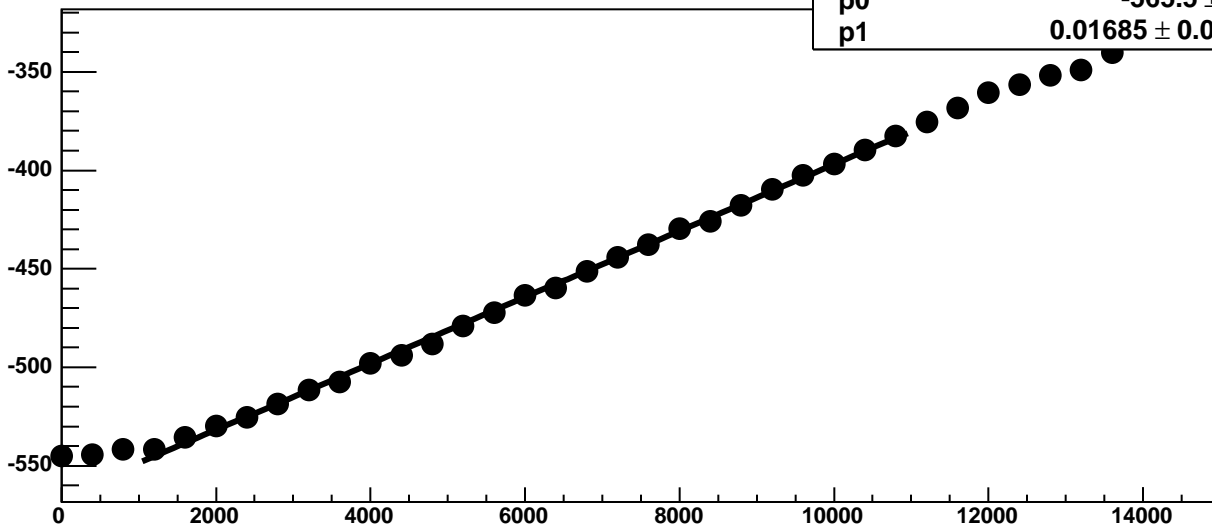
Chip 9, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

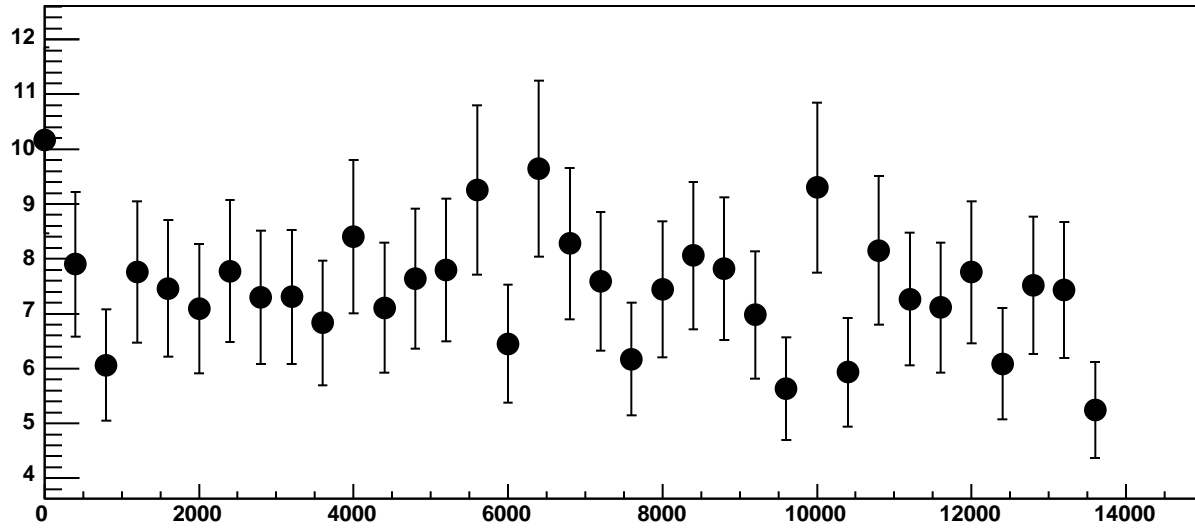


Chip 9, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

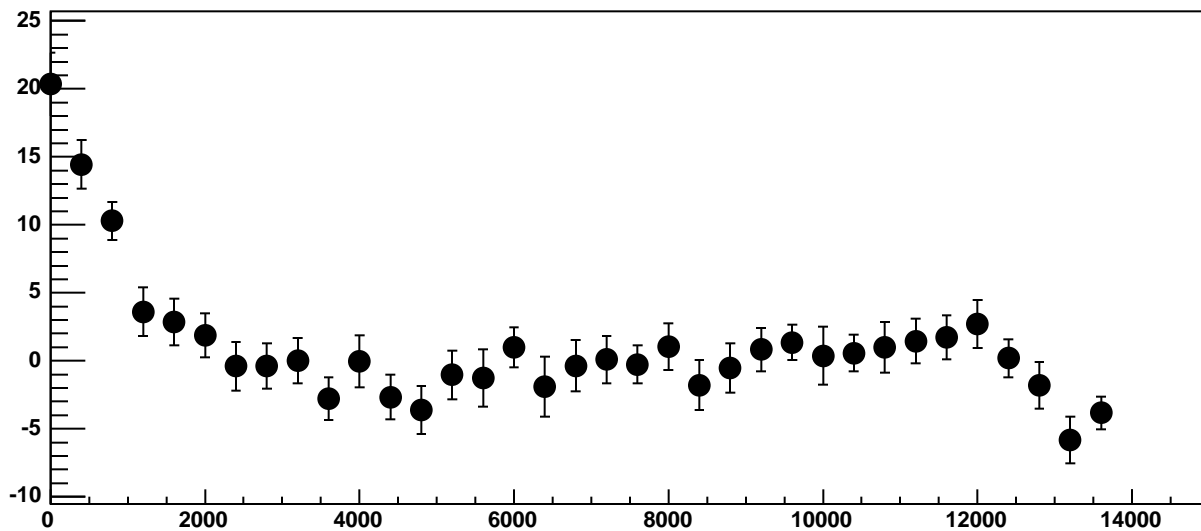


χ^2 / ndf 23.64 / 23
p0 -565.5 ± 0.7841
p1 0.01685 ± 0.0001154

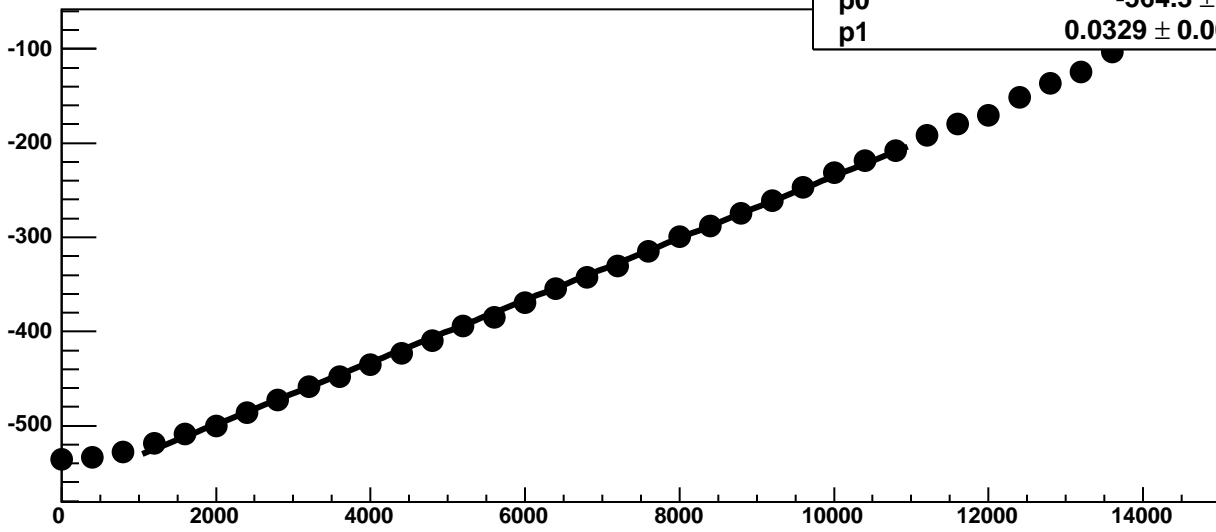
Chip 9, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

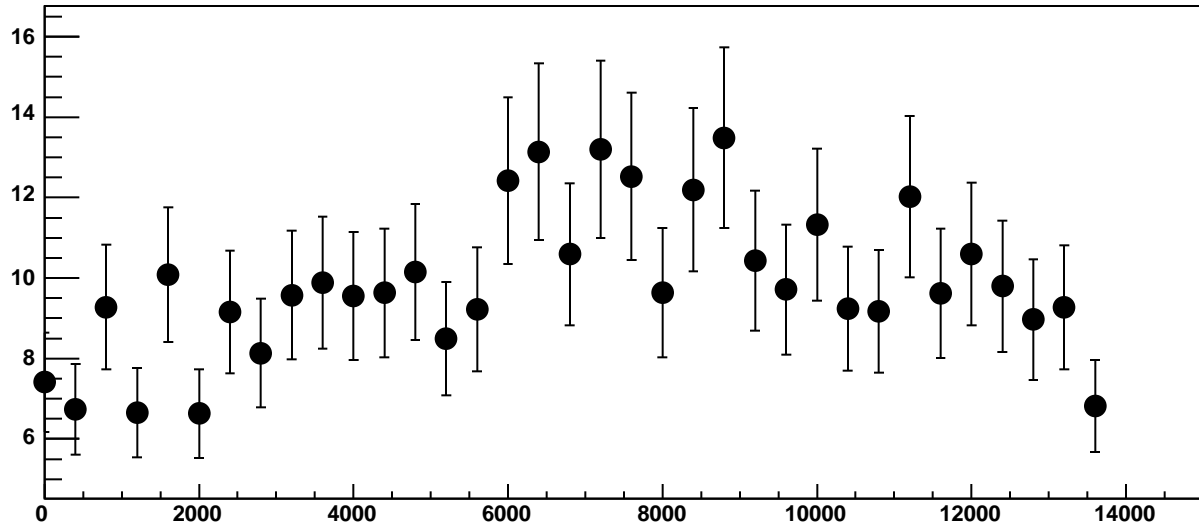


Chip 9, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC

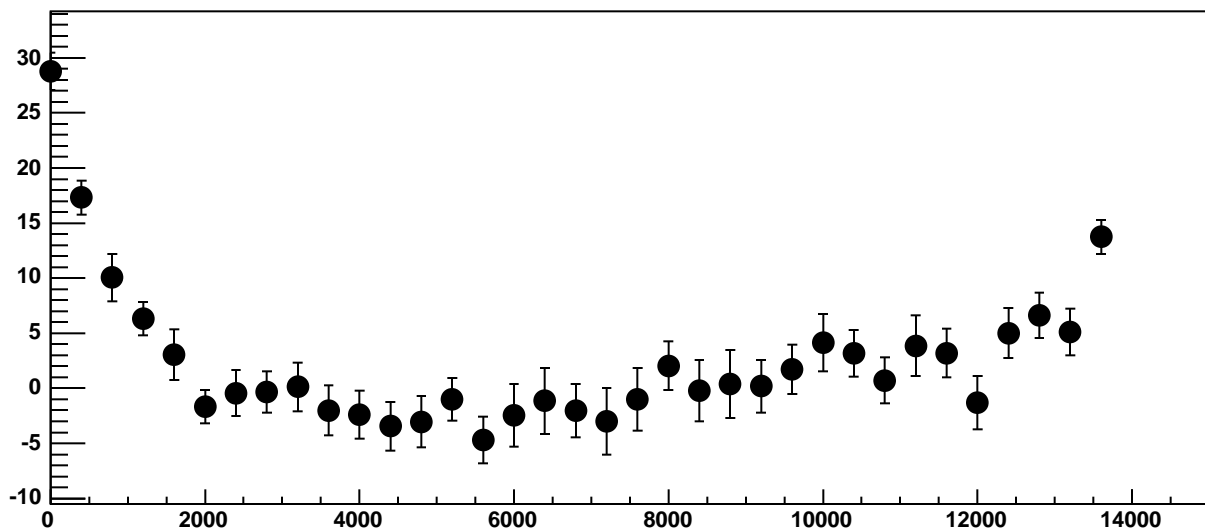


χ^2 / ndf 40.43 / 23
p0 -564.3 ± 0.8922
p1 0.0329 ± 0.0001442

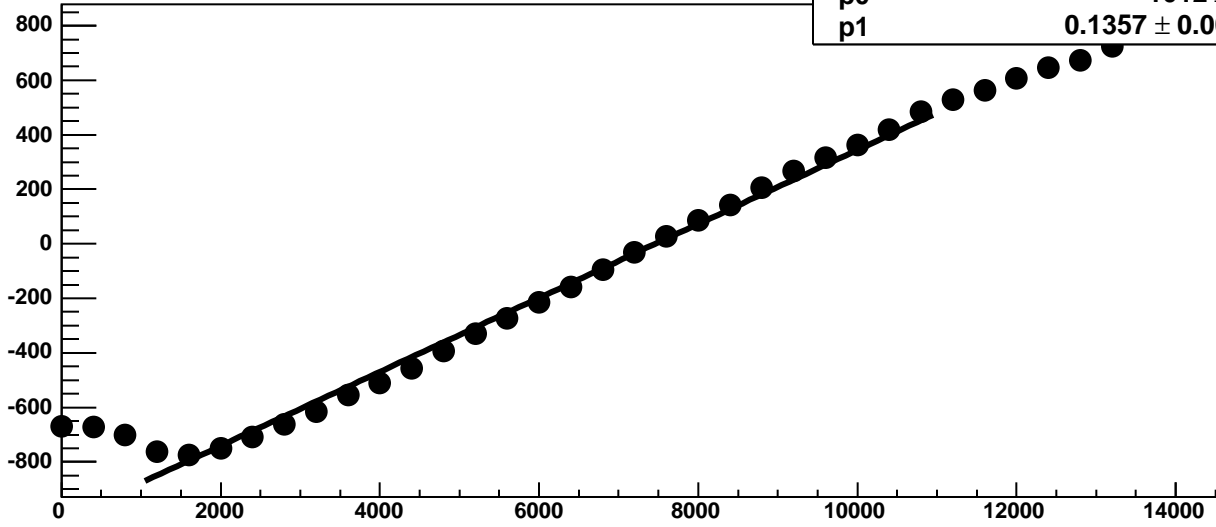
Chip 9, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



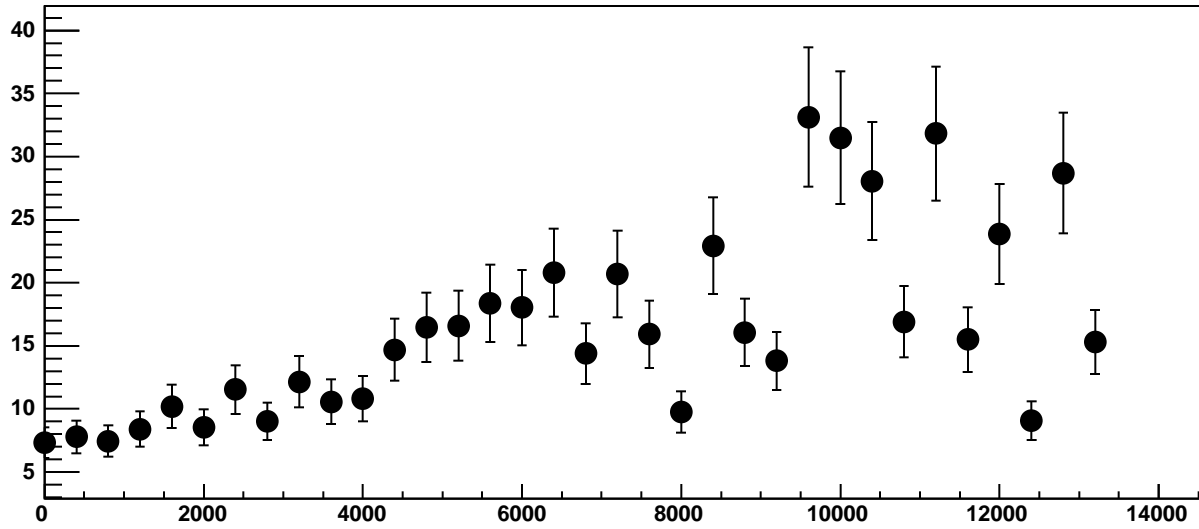
Chip 9, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC



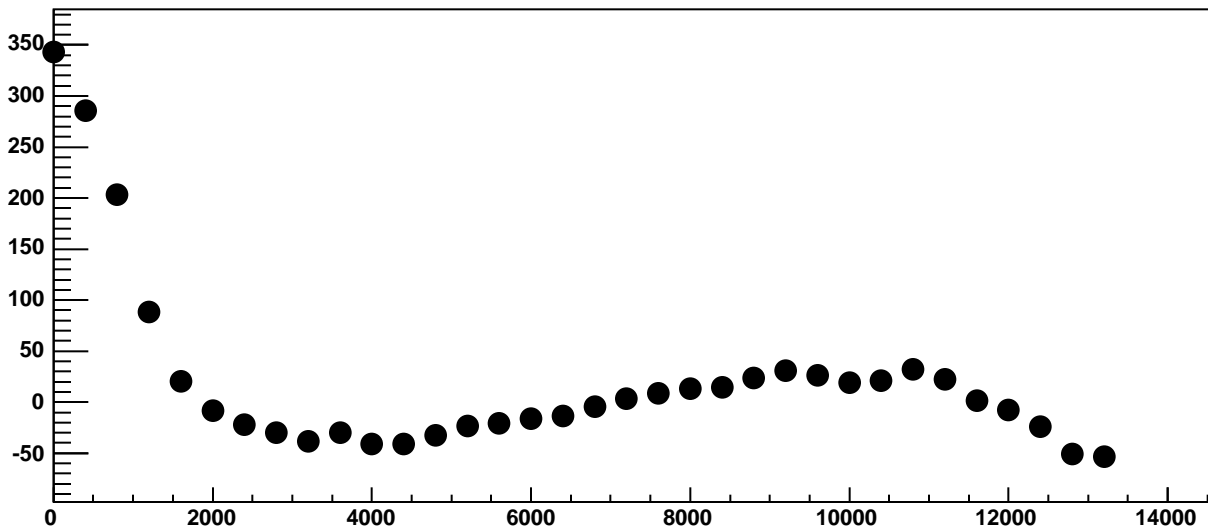
Chip 9, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



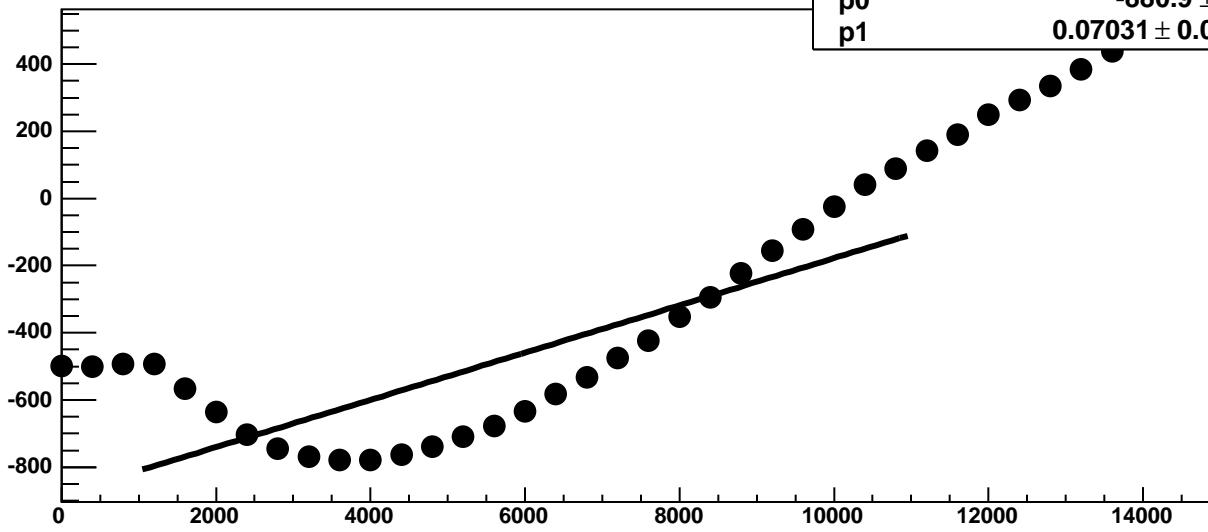
Chip 9, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

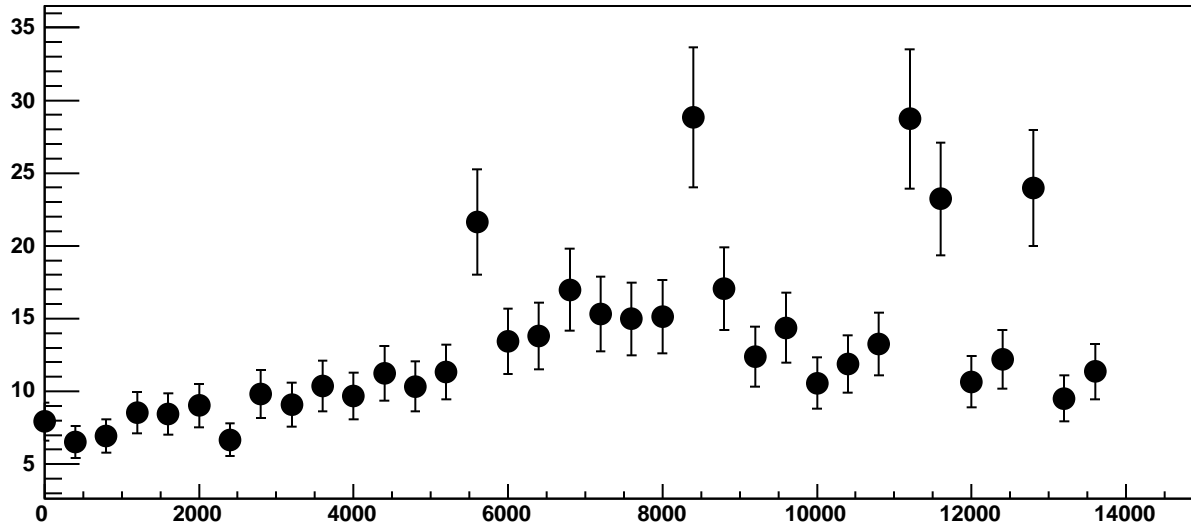


Chip 9, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

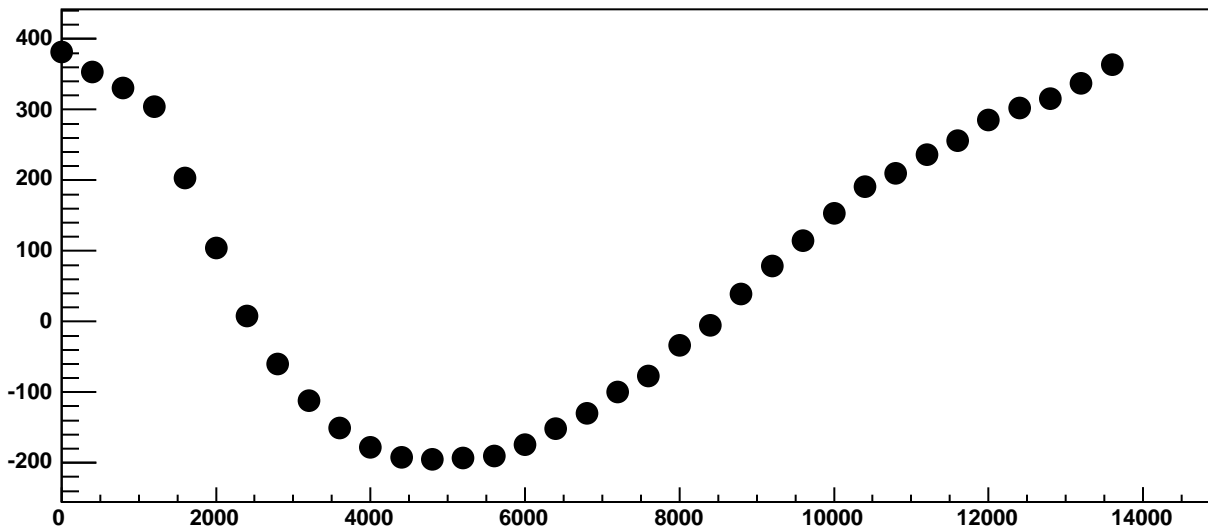


χ^2 / ndf 9.508e+04 / 23
p0 -880.9 ± 0.9828
p1 0.07031 ± 0.0001724

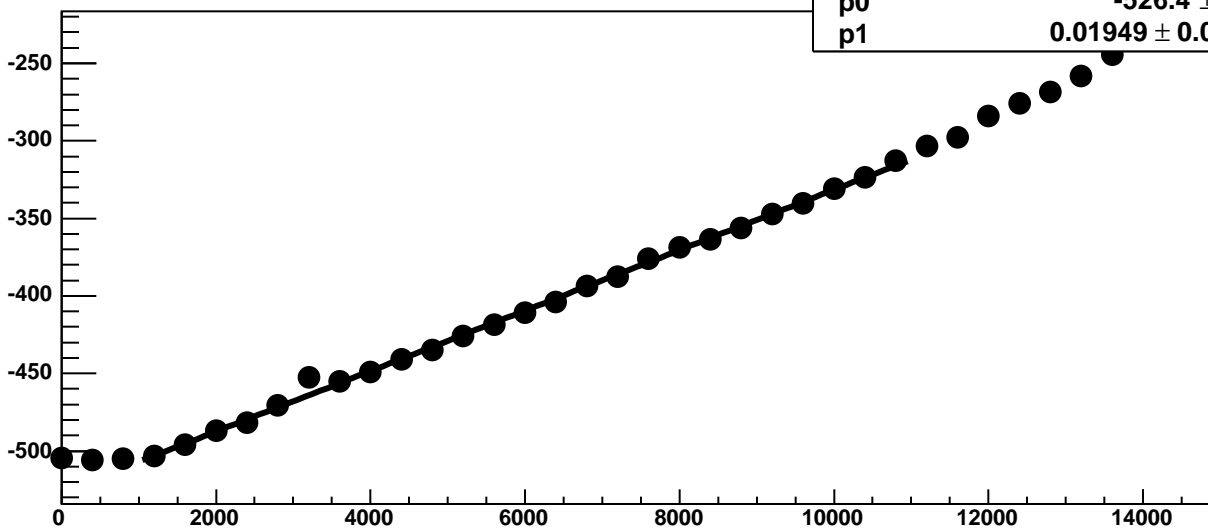
Chip 9, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

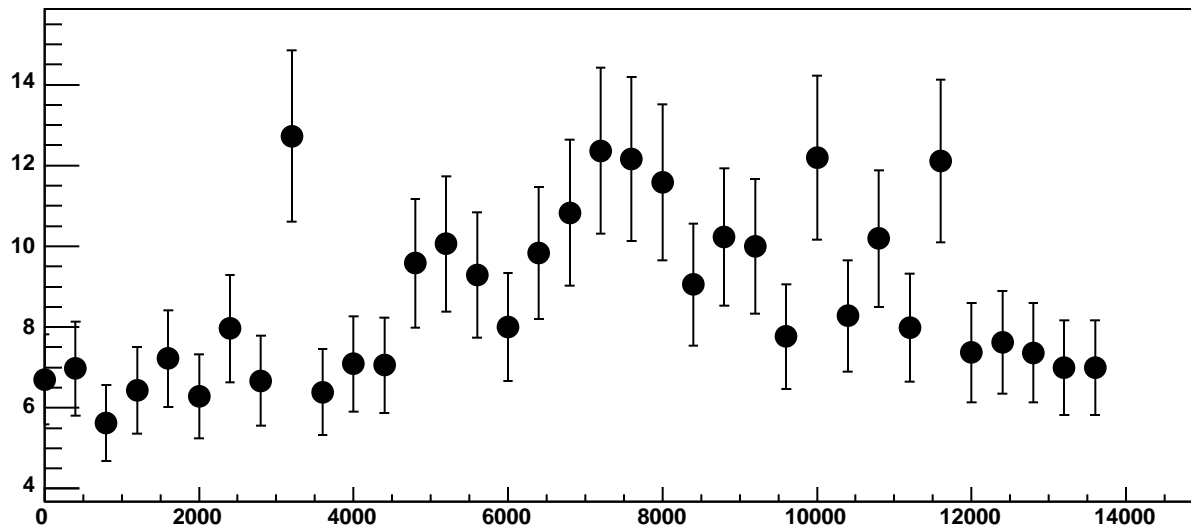


Chip 9, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC

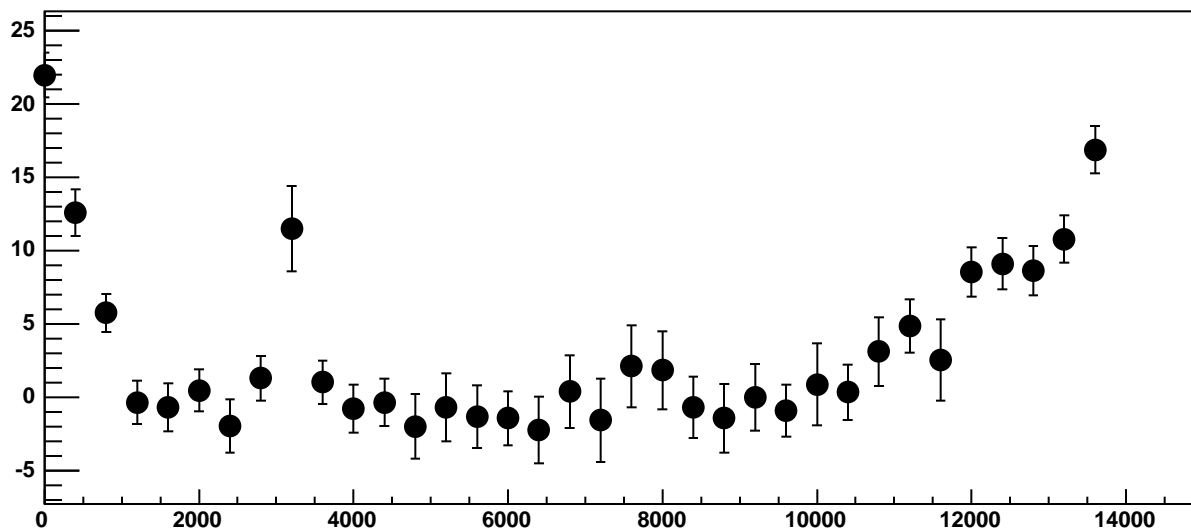


χ^2 / ndf 25.41 / 23
p0 -526.4 ± 0.7893
p1 0.01949 ± 0.0001318

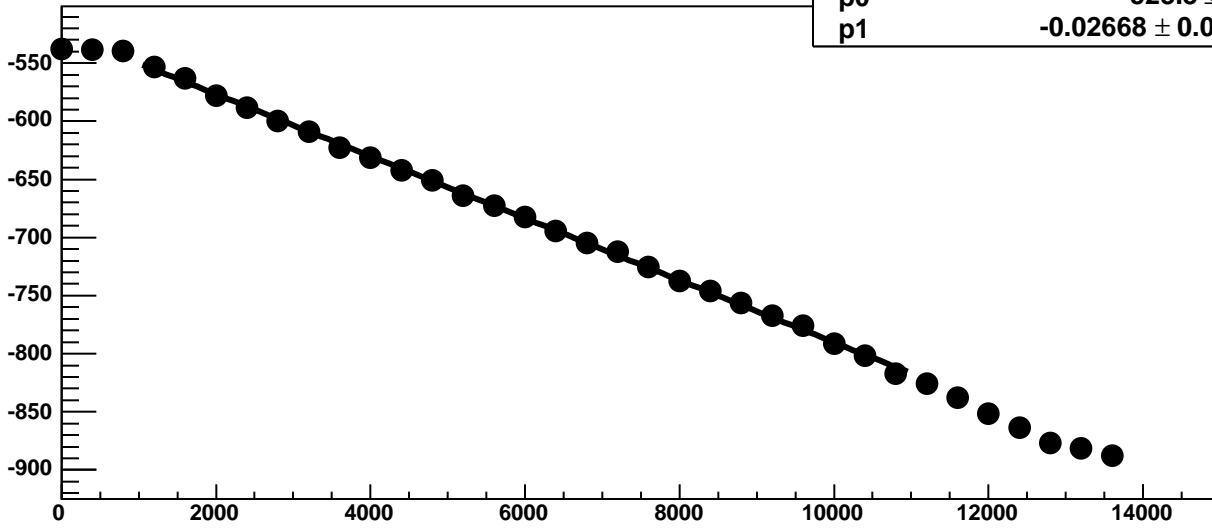
Chip 9, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC

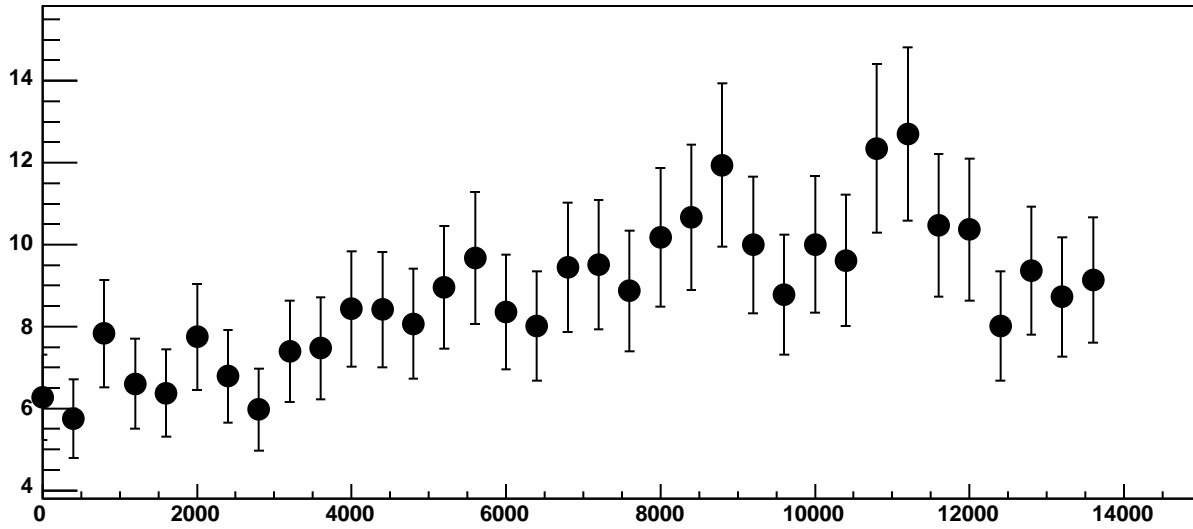


Chip 9, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

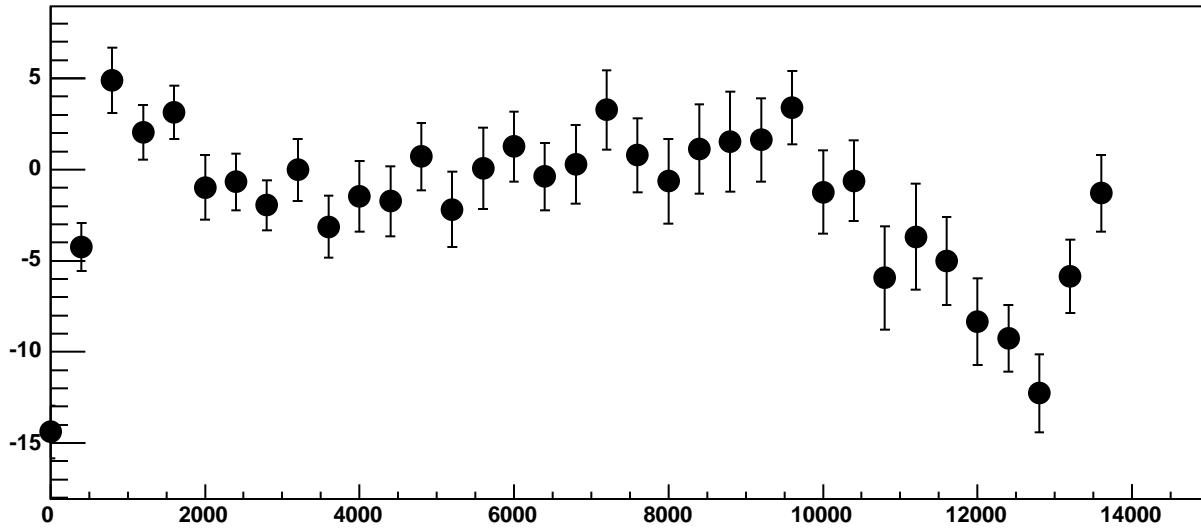


χ^2 / ndf 26.62 / 23
p0 -523.3 ± 0.7851
p1 -0.02668 ± 0.0001345

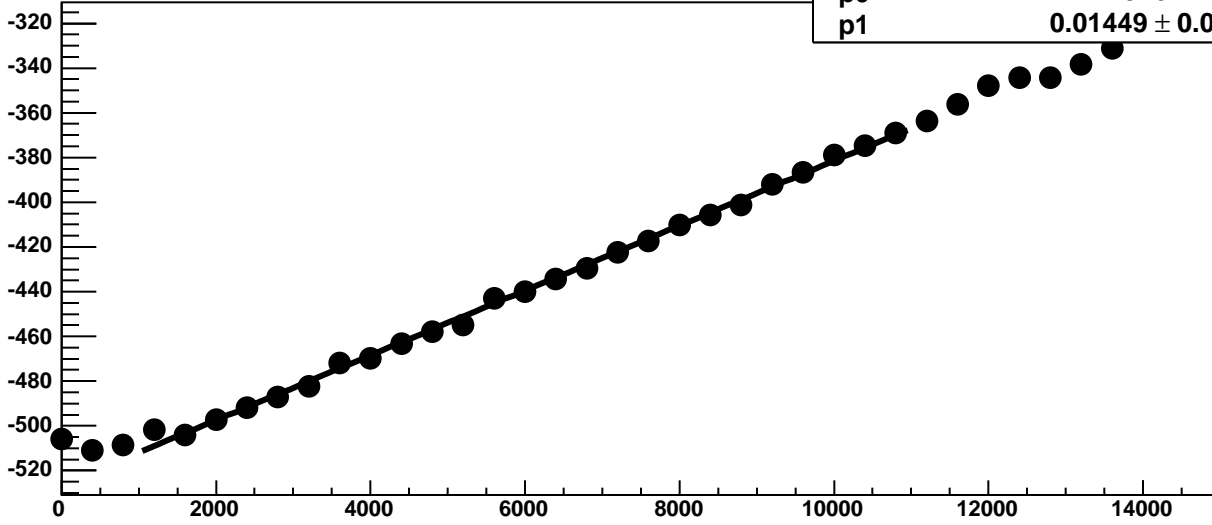
Chip 9, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

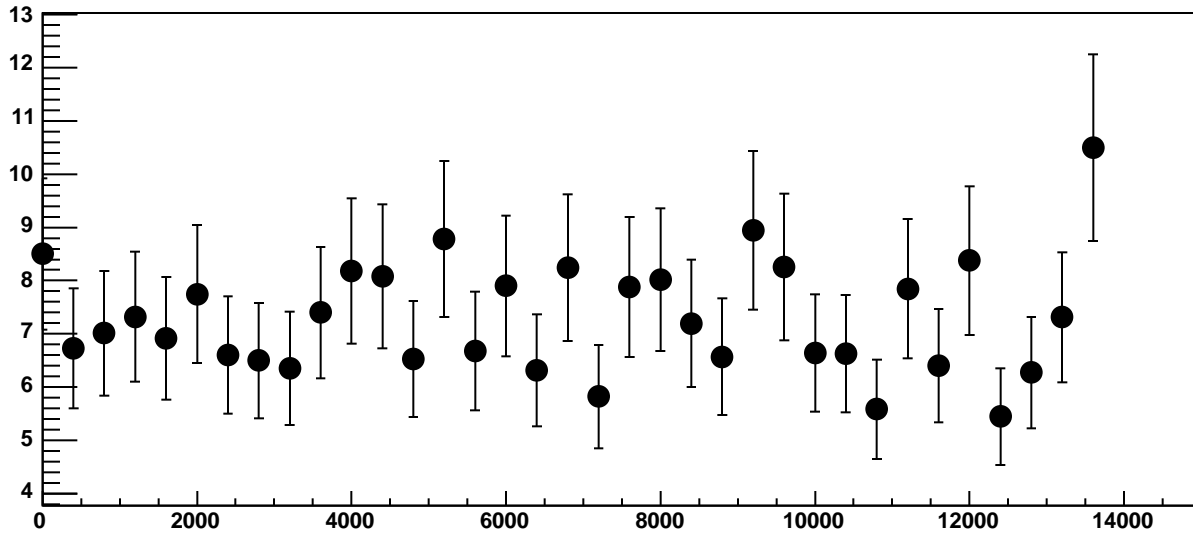


Chip 9, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

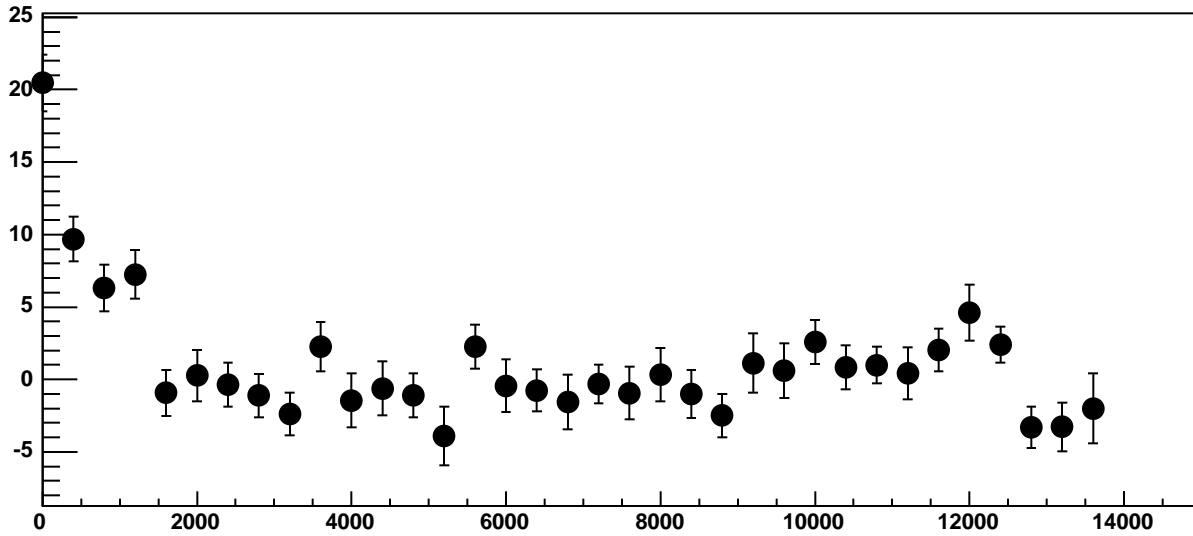


χ^2 / ndf 39.78 / 23
p0 -526.4 ± 0.7407
p1 0.01449 ± 0.0001094

Chip 9, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC

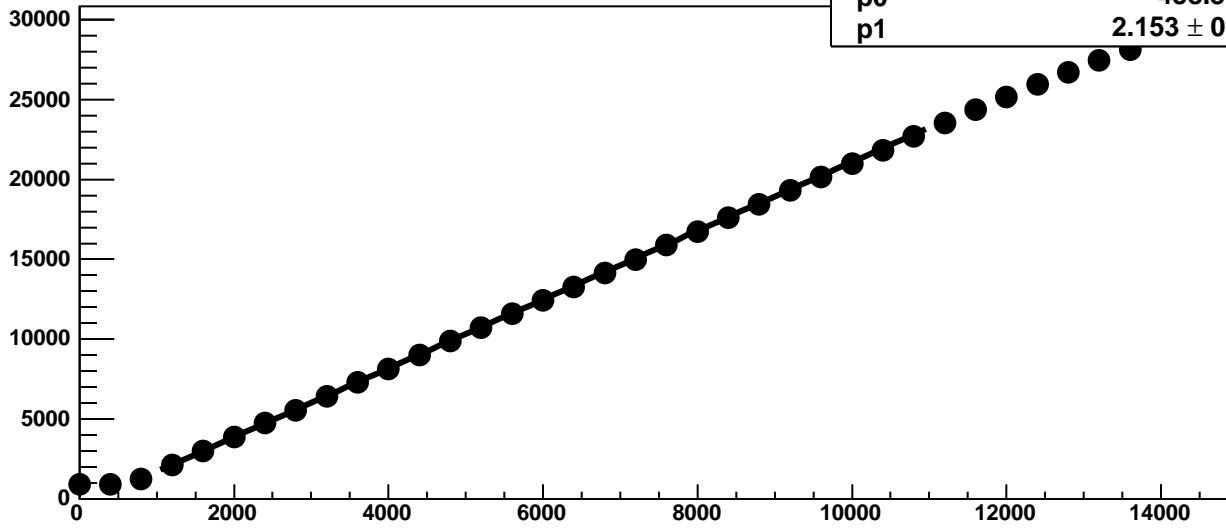


Chip 9, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

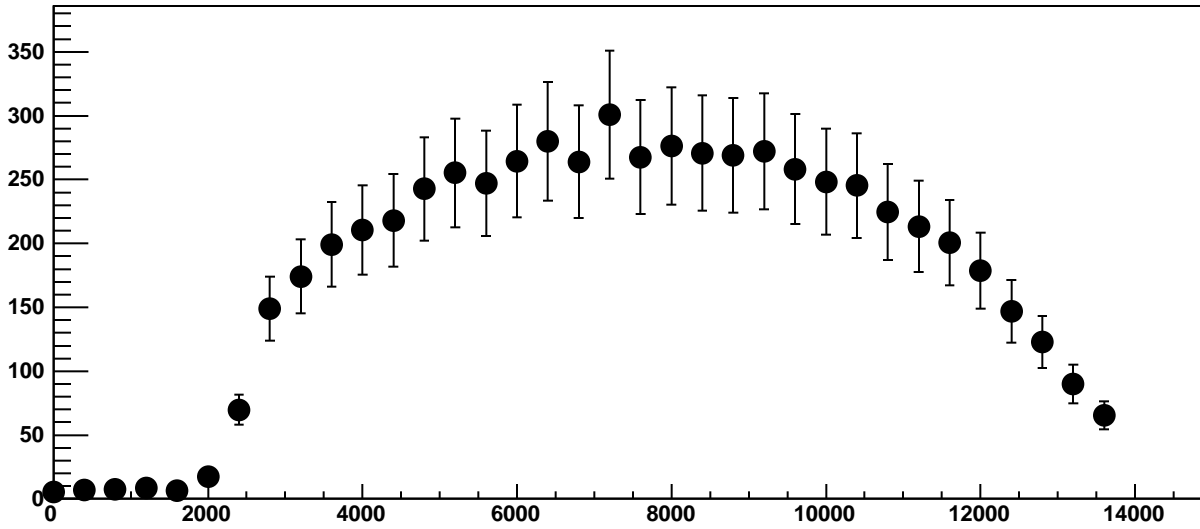


Chip 9, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC

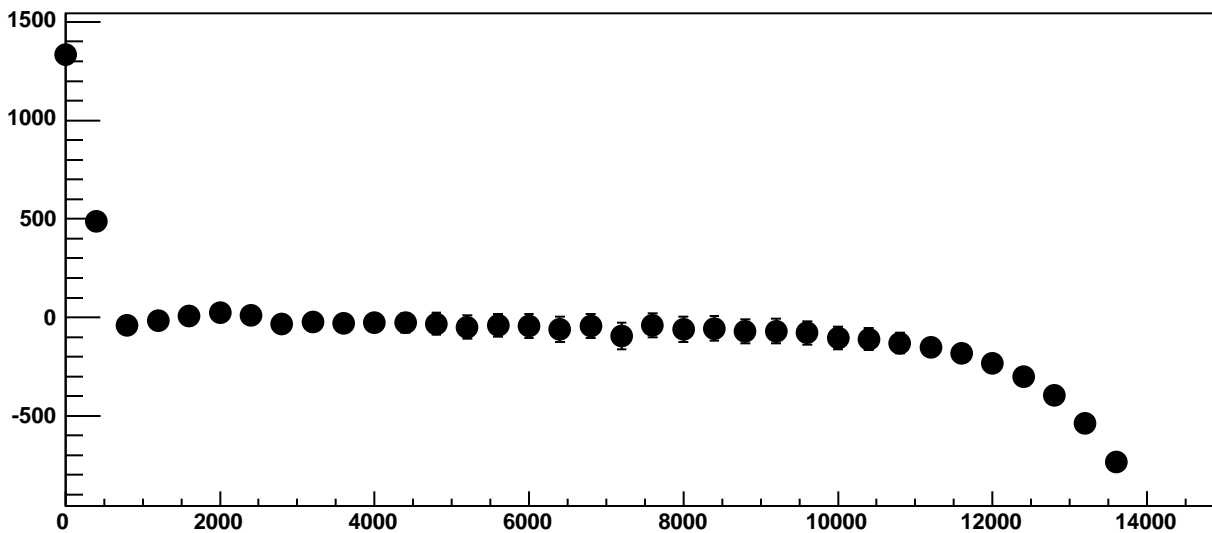
χ^2 / ndf 155.7 / 23
p0 -438.3 ± 3.277
p1 2.153 ± 0.001983



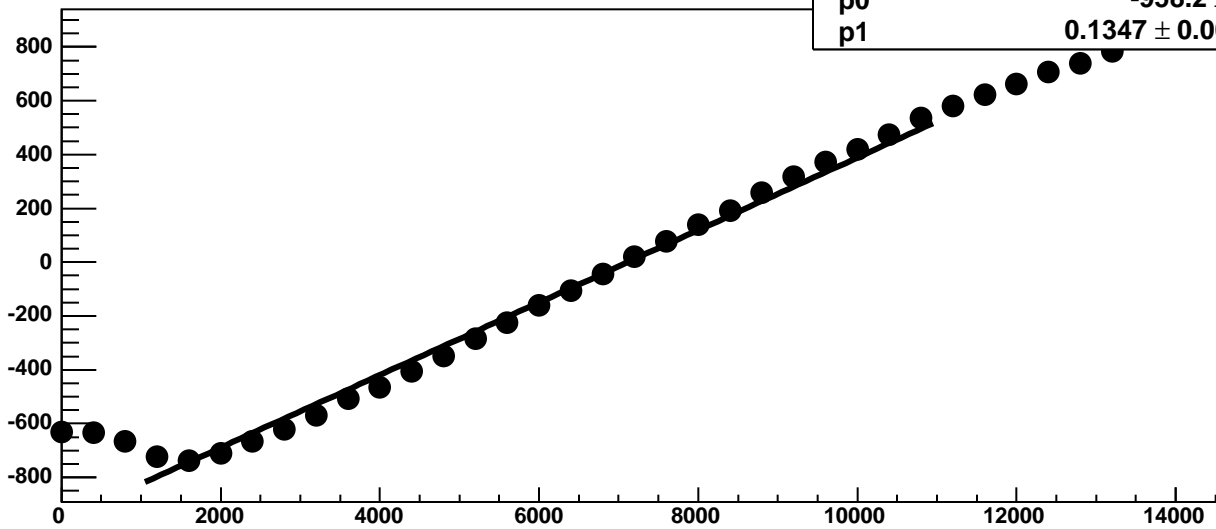
Chip 9, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



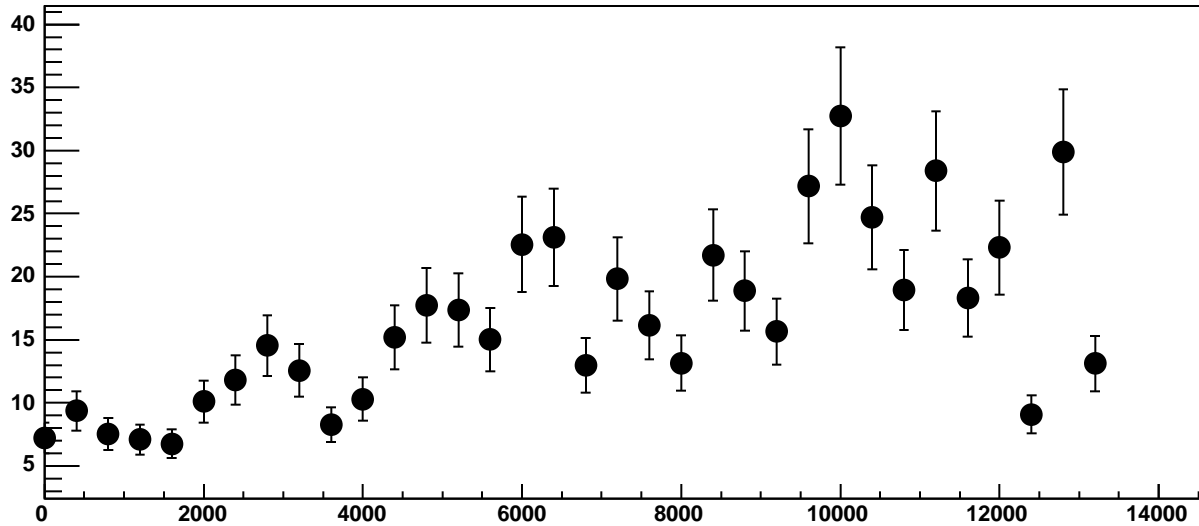
Chip 9, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC



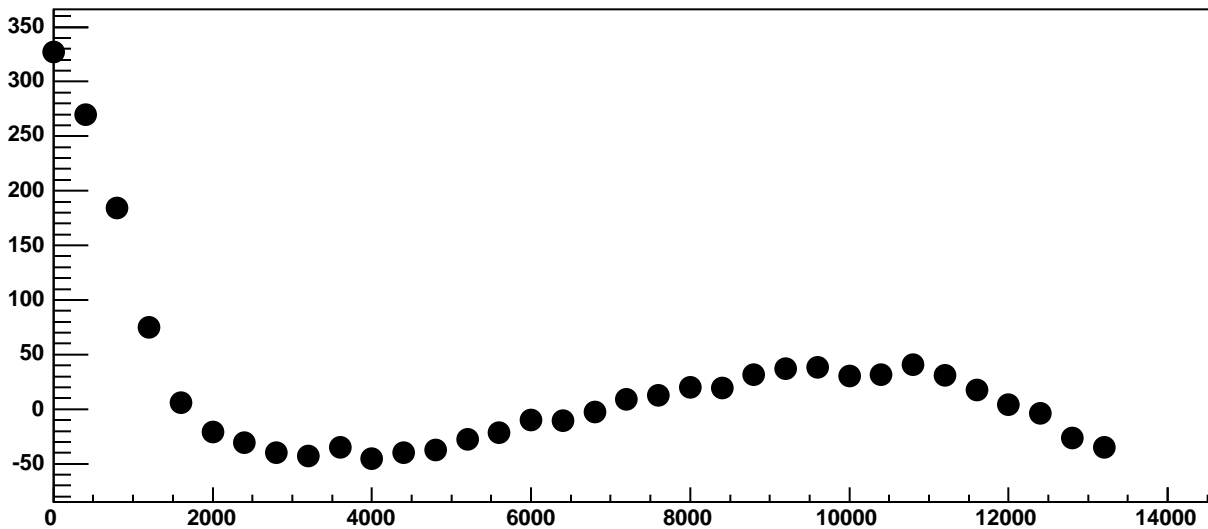
Chip 9, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



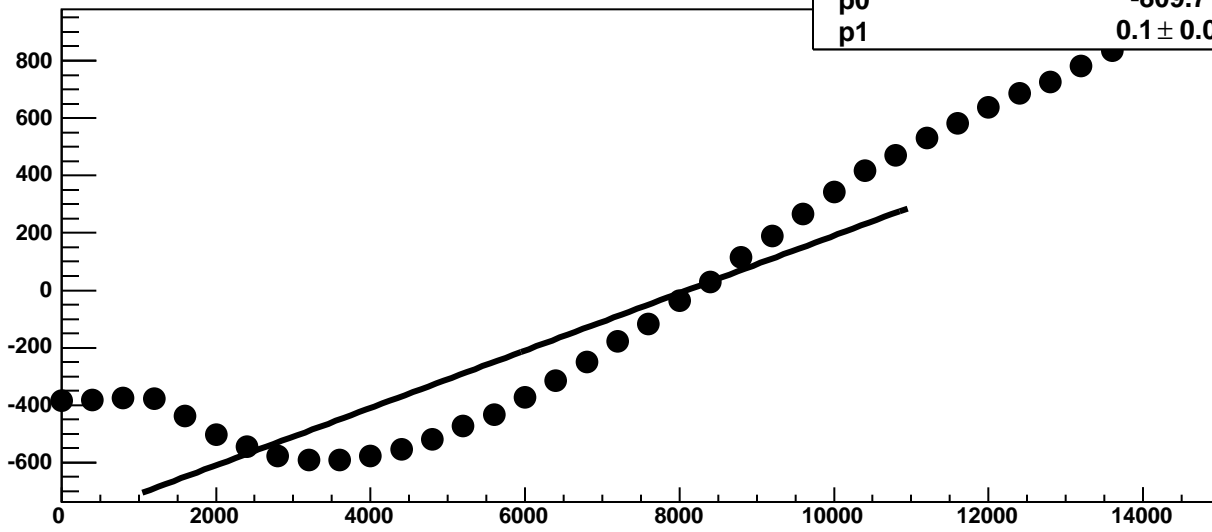
Chip 9, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 9, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1.048e+05 / 23

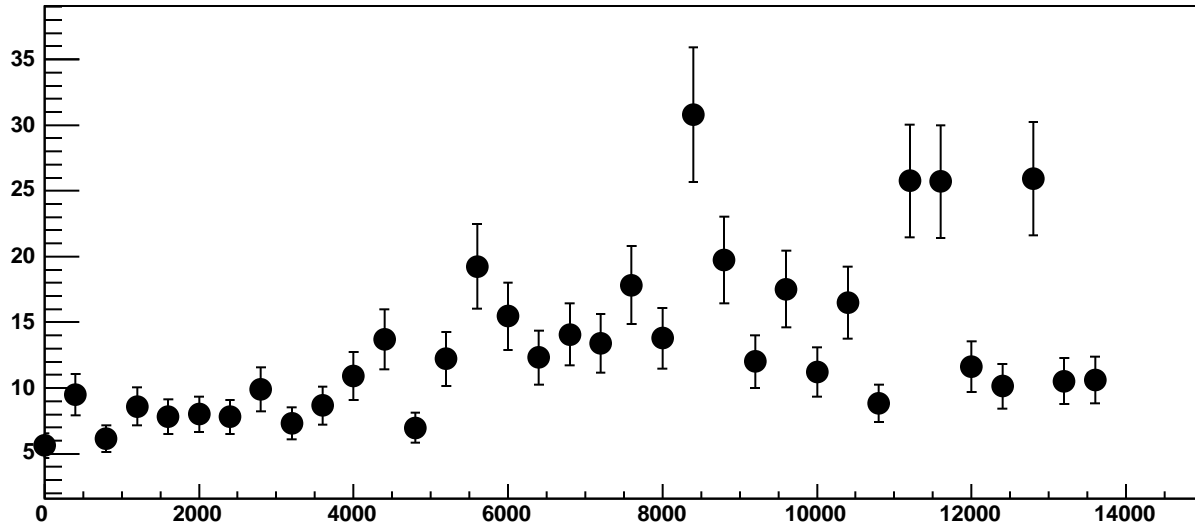
p0

-809.7 ± 0.946

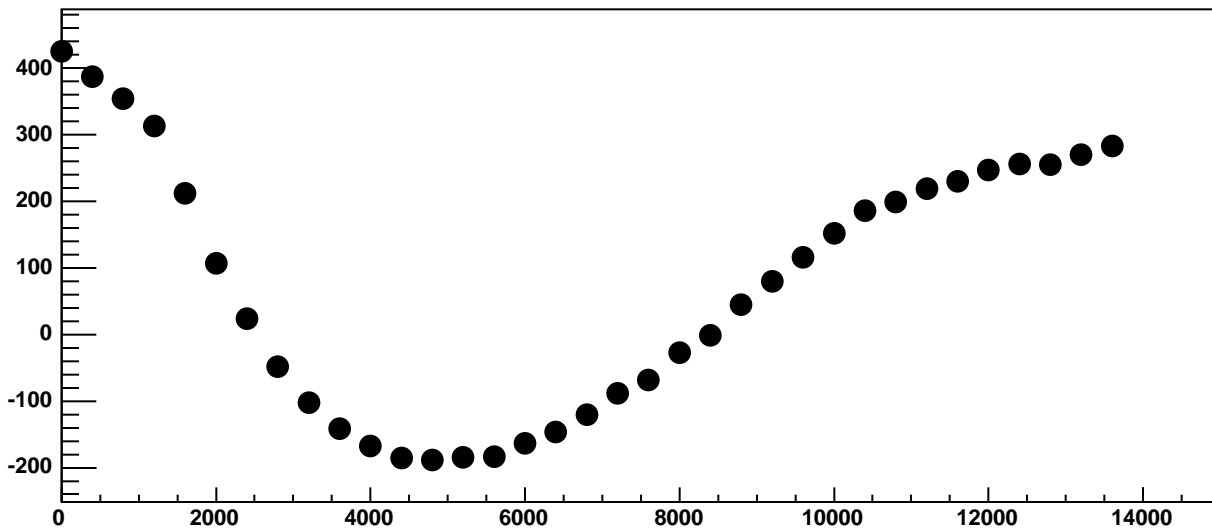
p1

0.1 ± 0.0001661

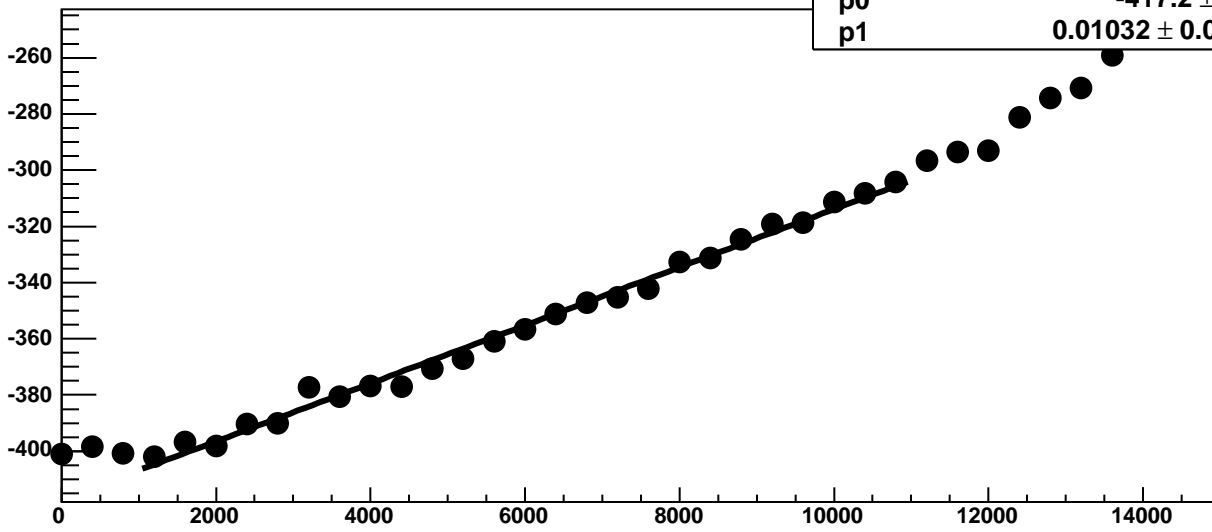
Chip 9, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



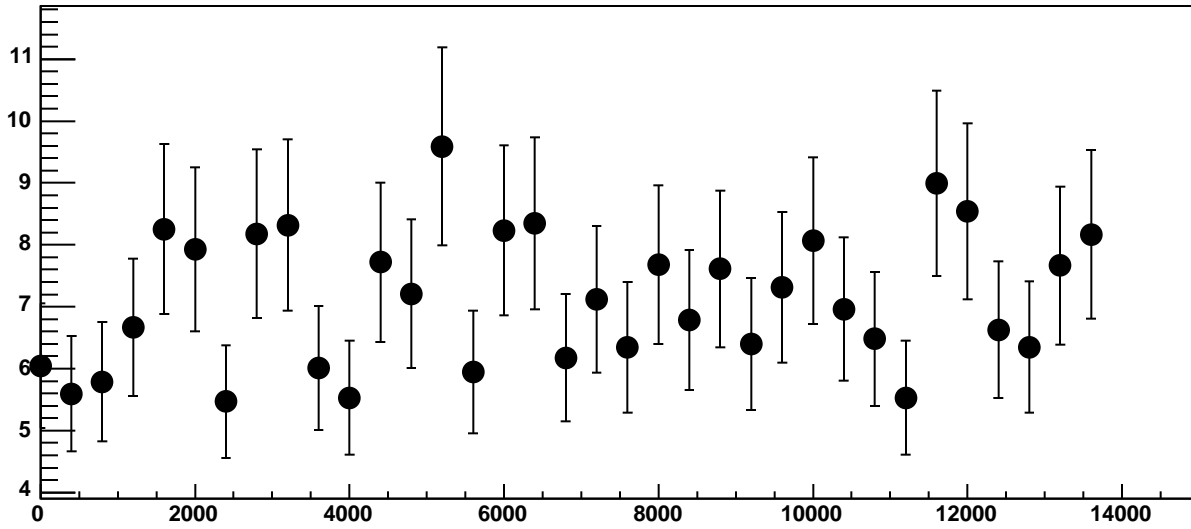
Chip 9, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



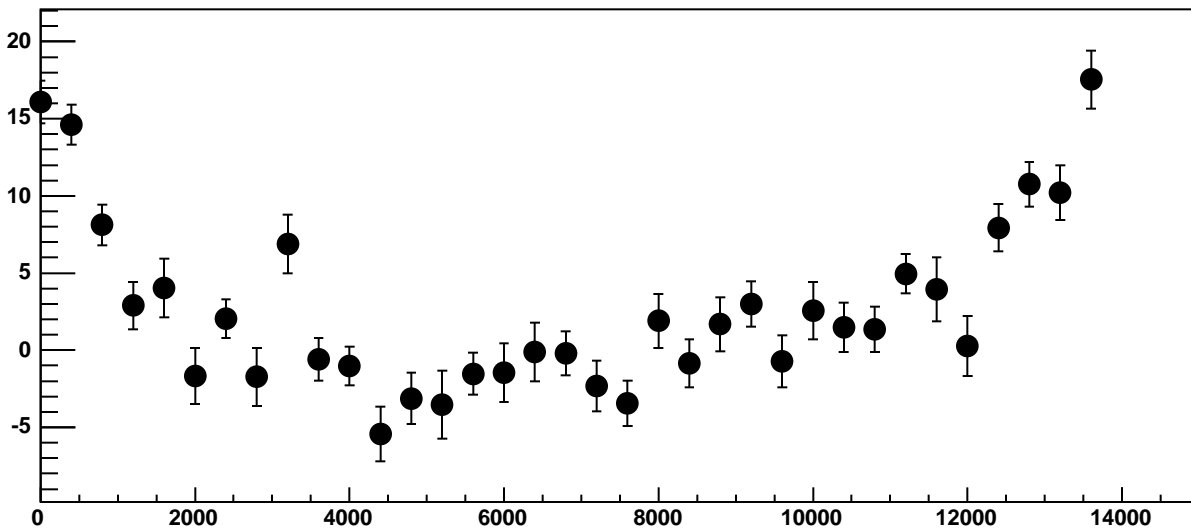
Chip 9, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



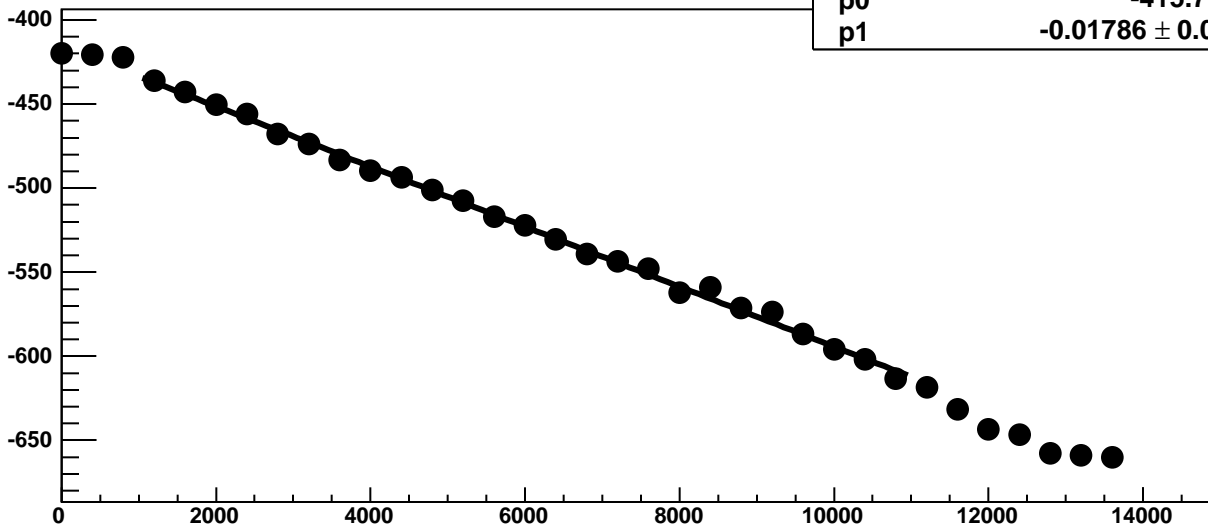
Chip 9, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

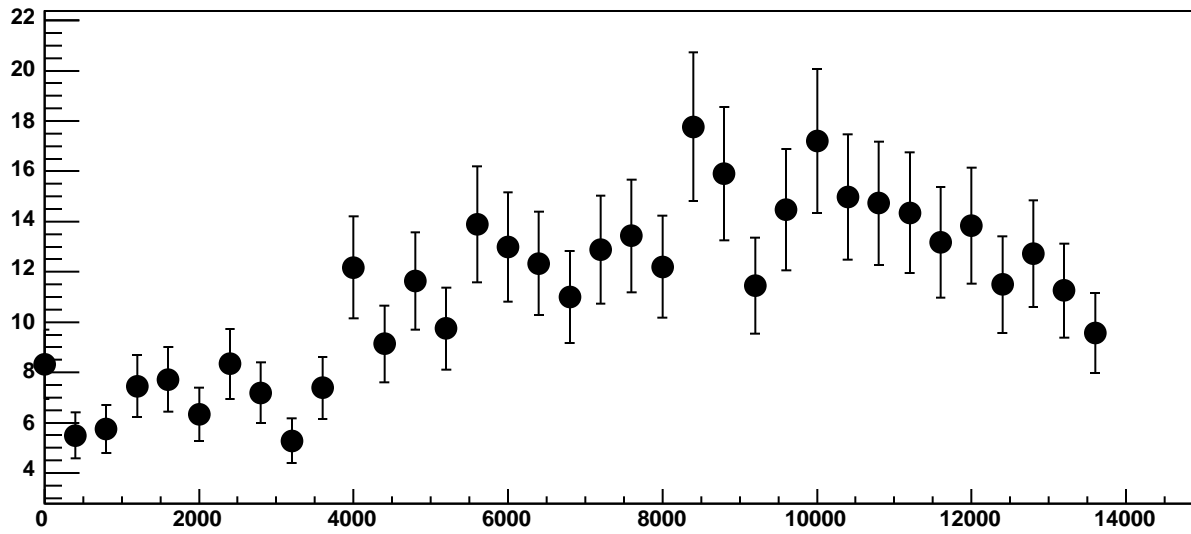


Chip 9, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

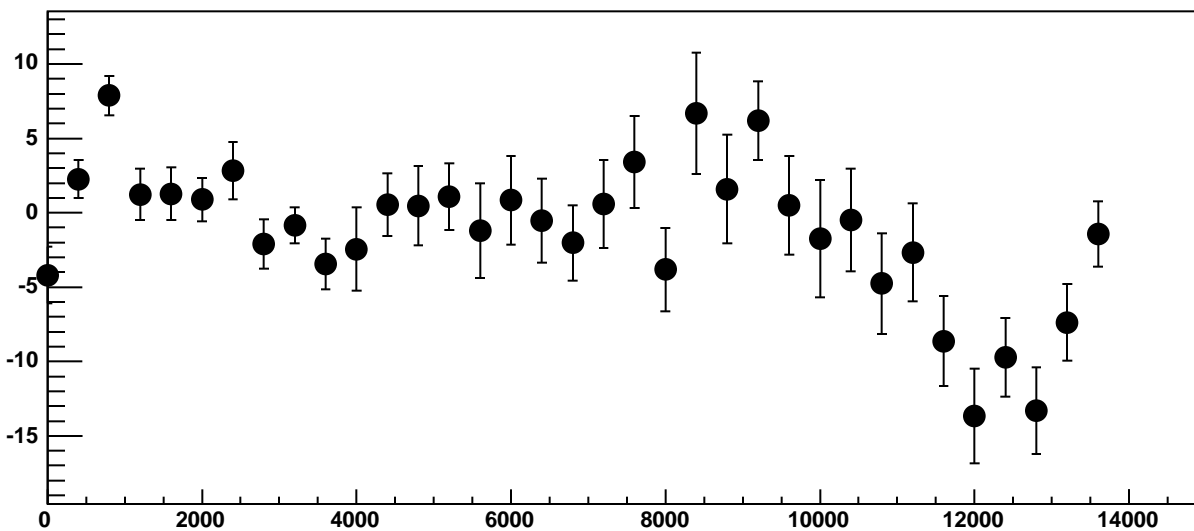


χ^2 / ndf 25.57 / 23
p0 -415.7 ± 0.875
p1 -0.01786 ± 0.0001704

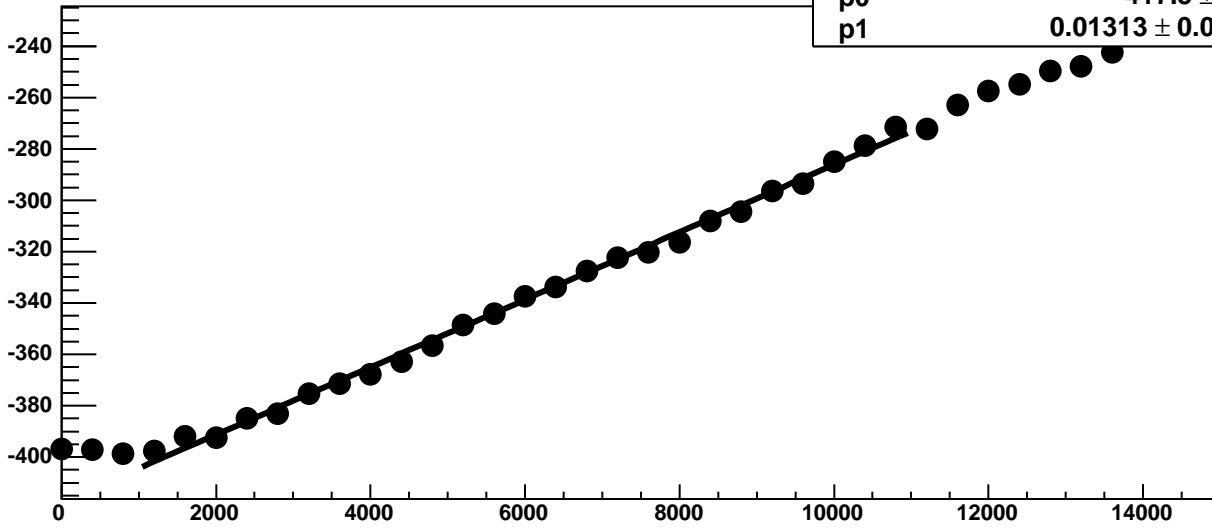
Chip 9, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



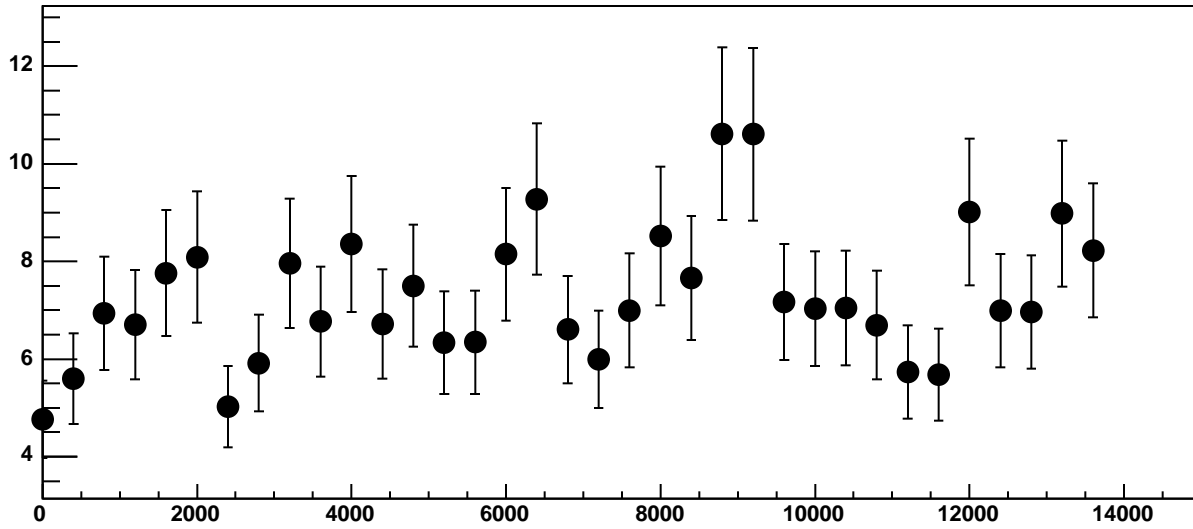
Chip 9, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC



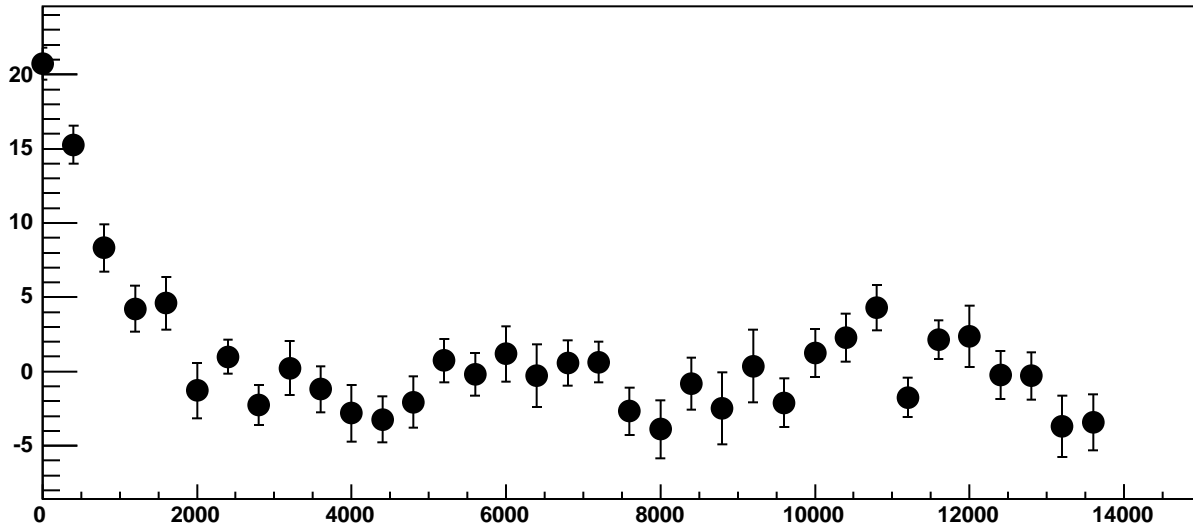
Chip 9, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



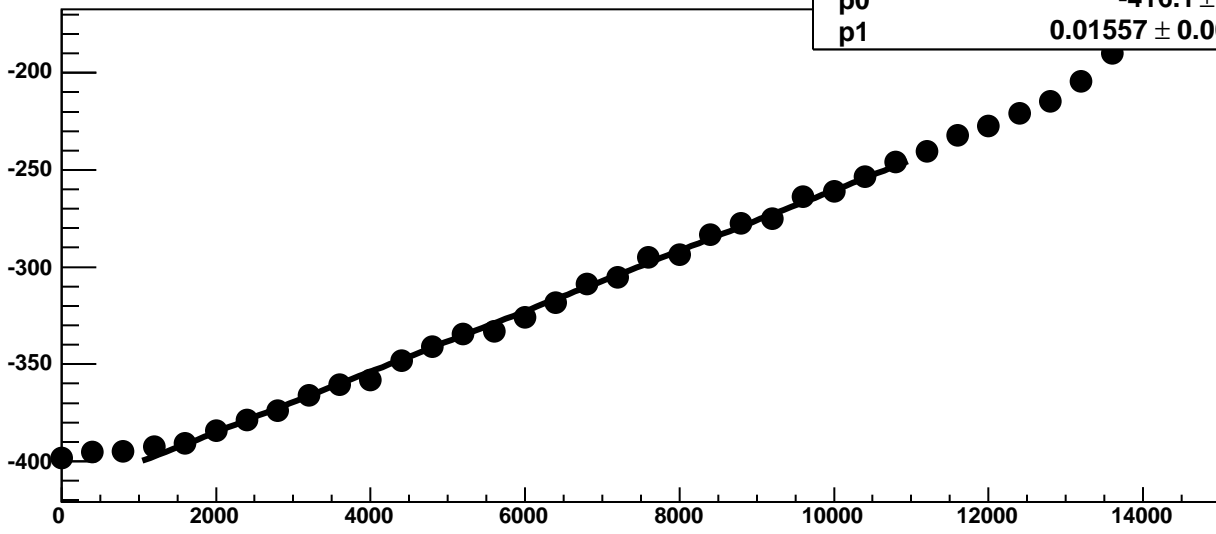
Chip 9, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

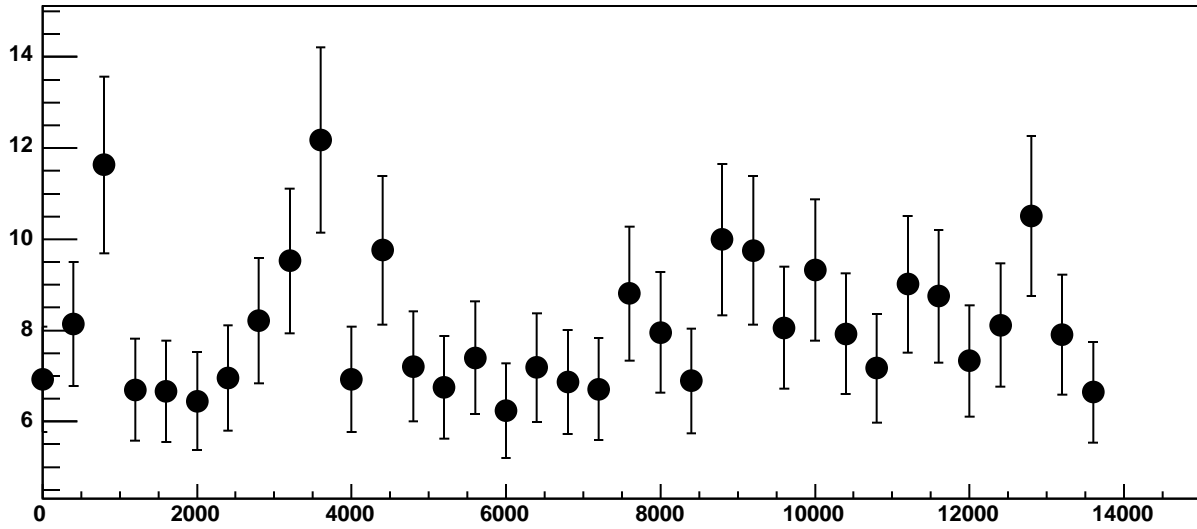


Chip 9, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

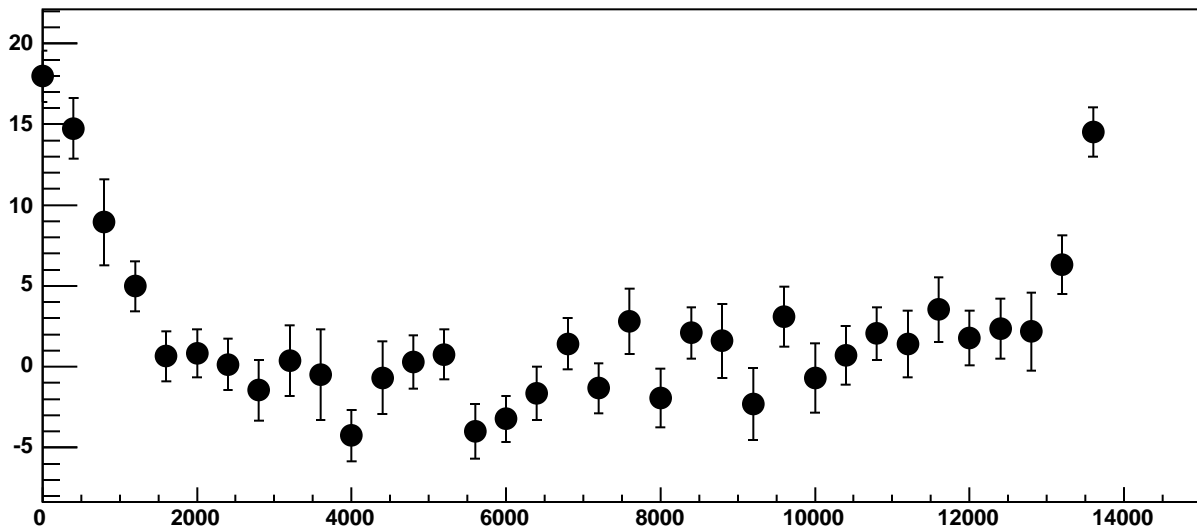


χ^2 / ndf 43.36 / 23
p0 -416.1 ± 0.7799
p1 0.01557 ± 0.0001208

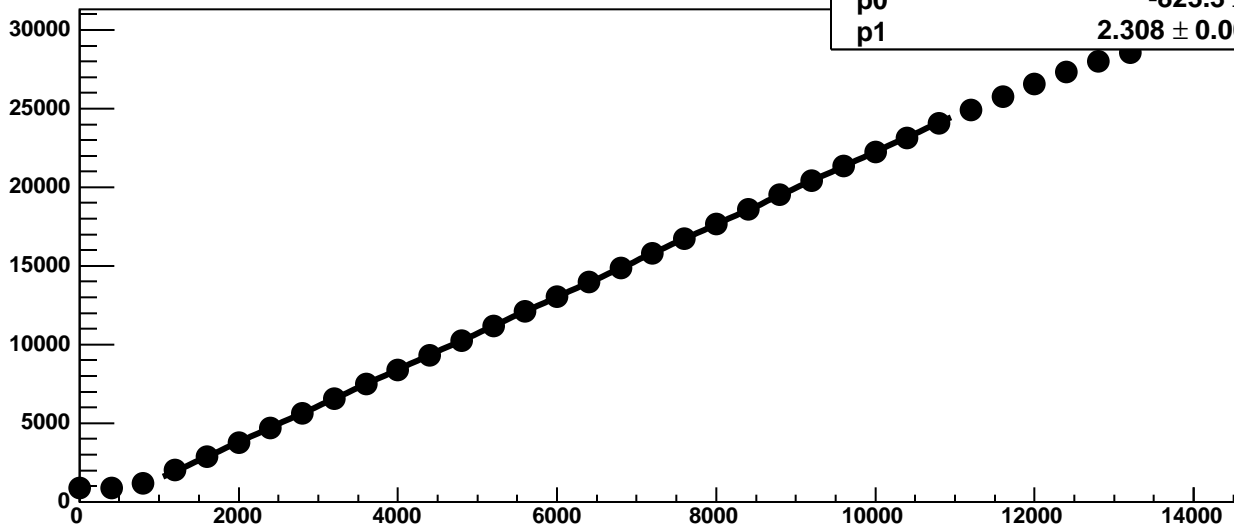
Chip 9, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



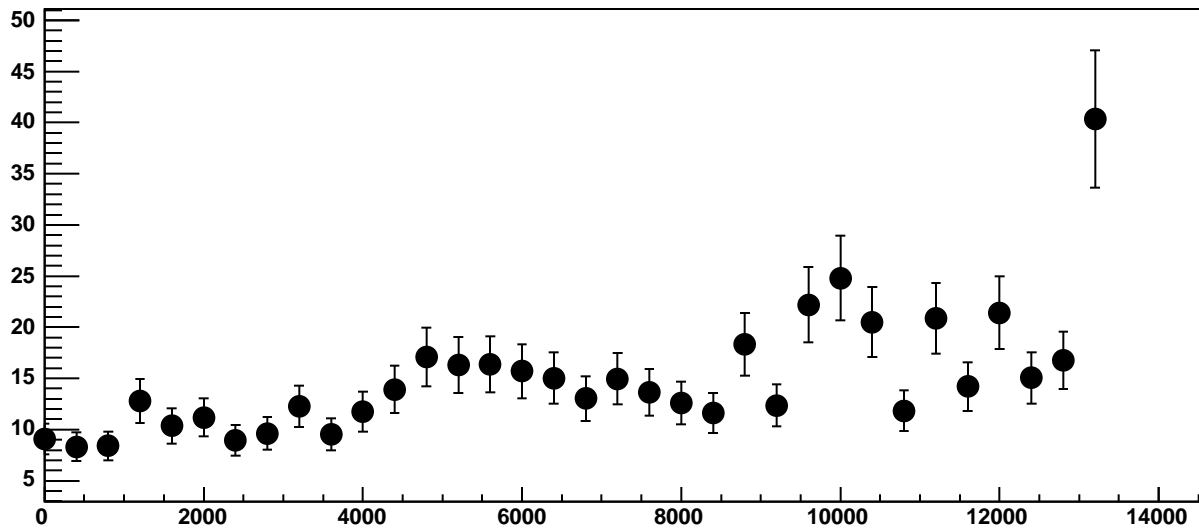
Chip 9, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



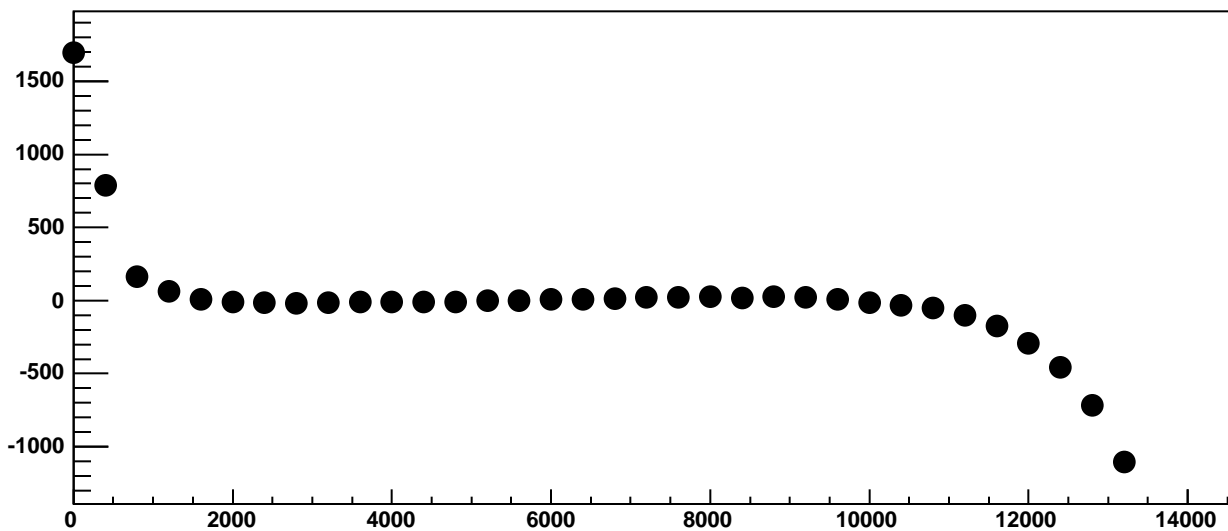
Chip 9, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC



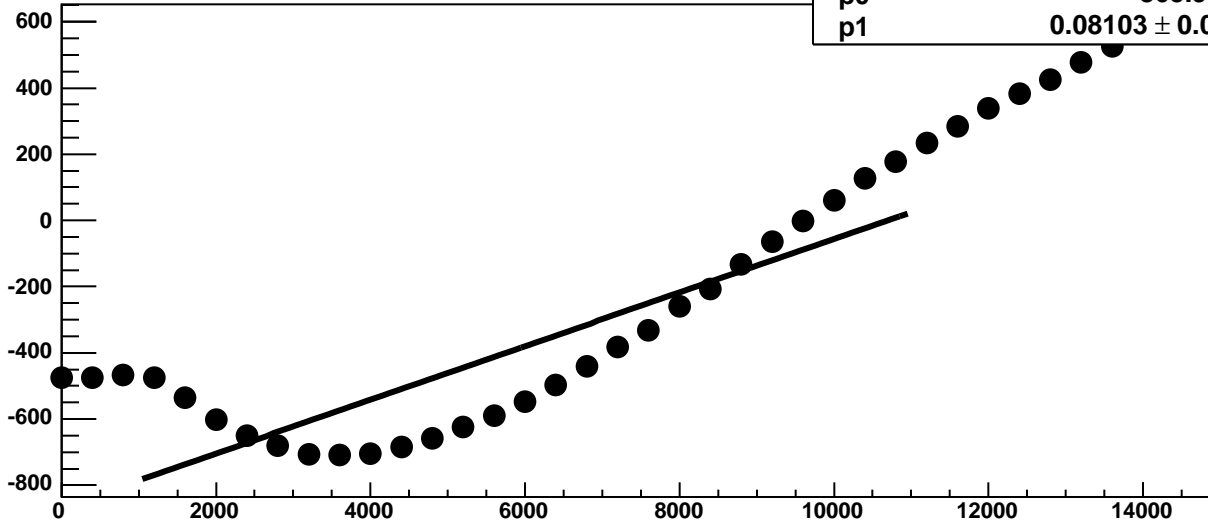
Chip 9, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC



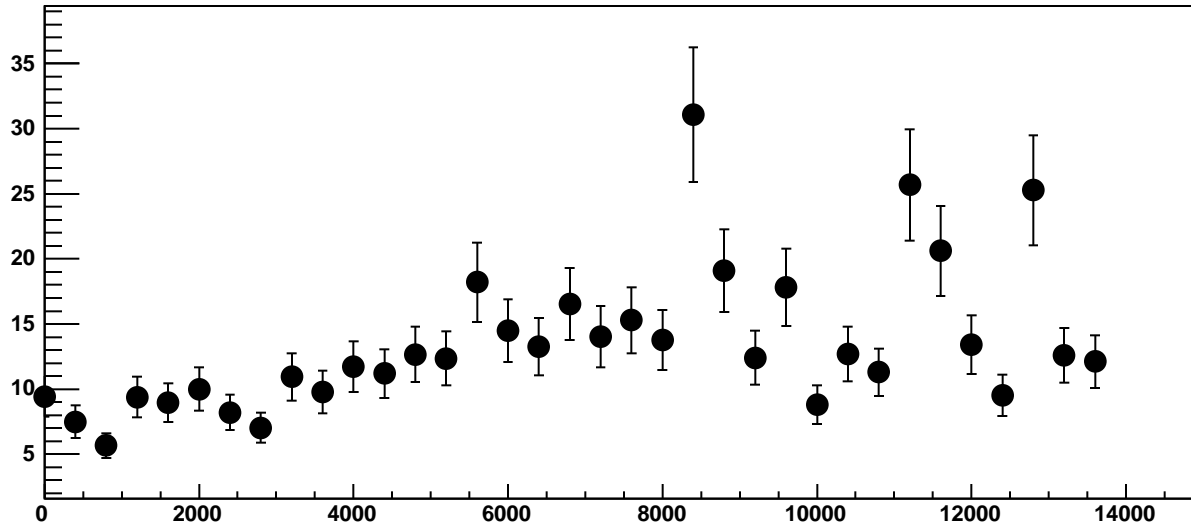
Chip 9, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC



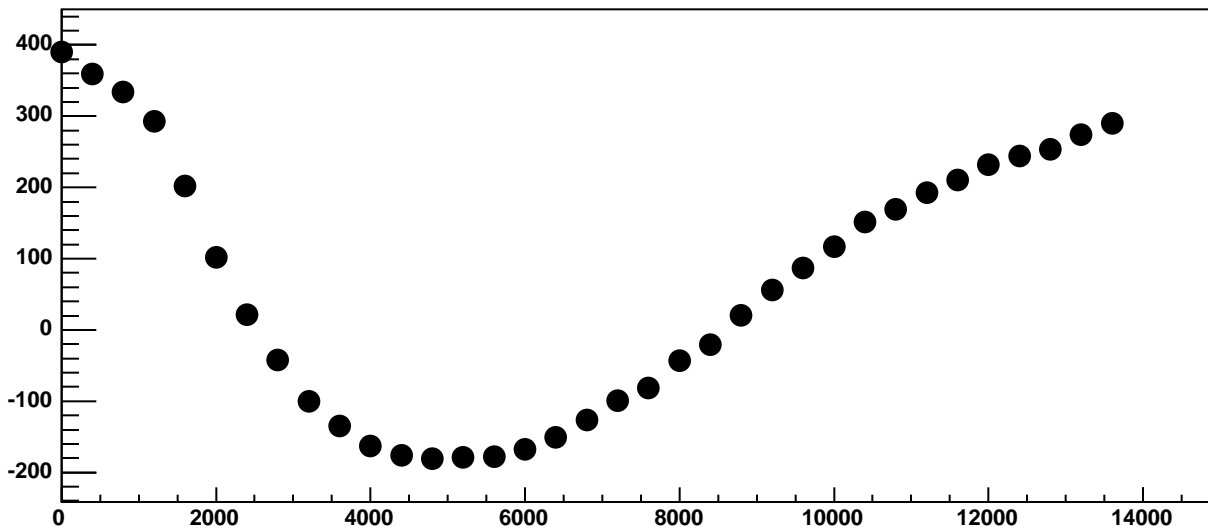
Chip 9, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



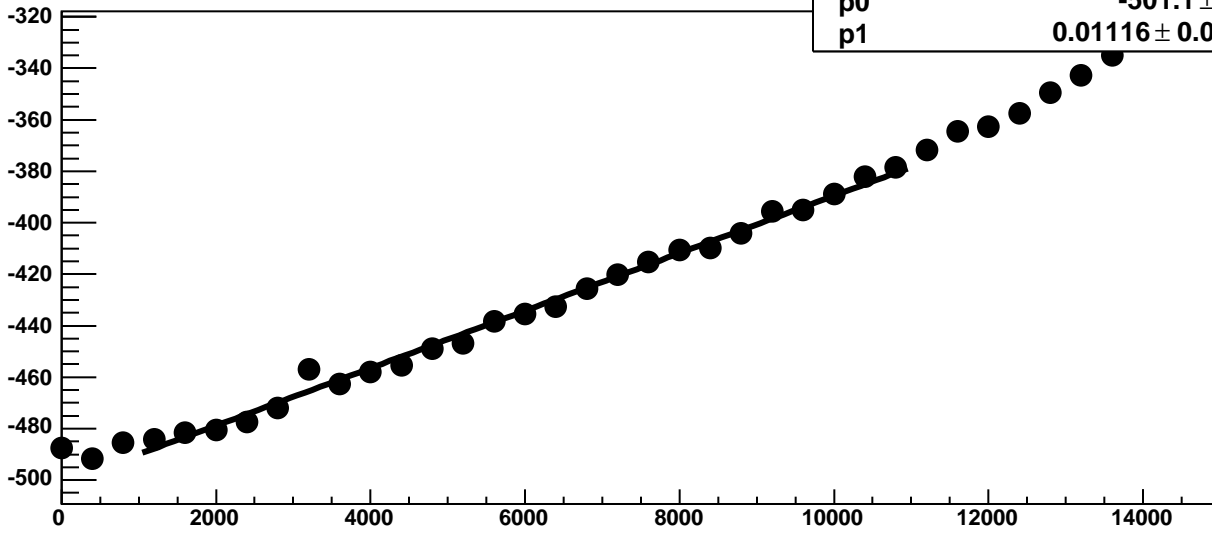
Chip 9, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



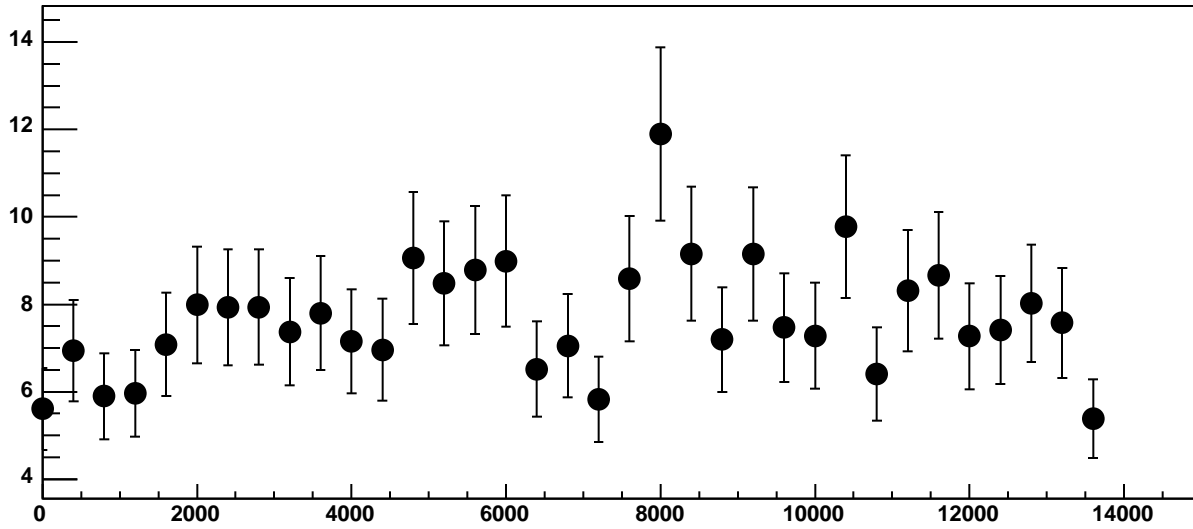
Chip 9, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



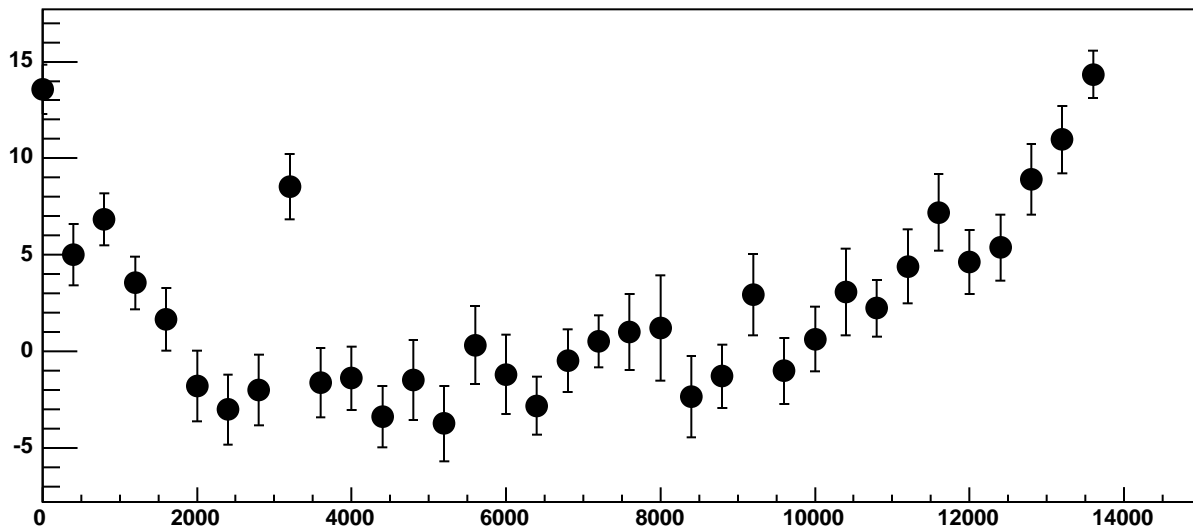
Chip 9, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



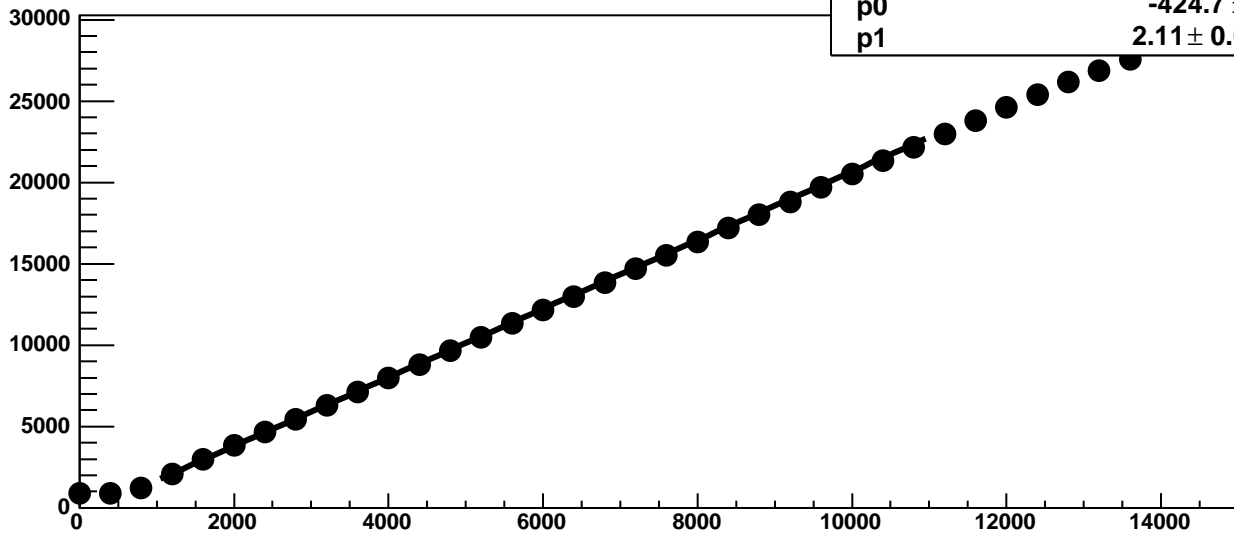
Chip 9, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

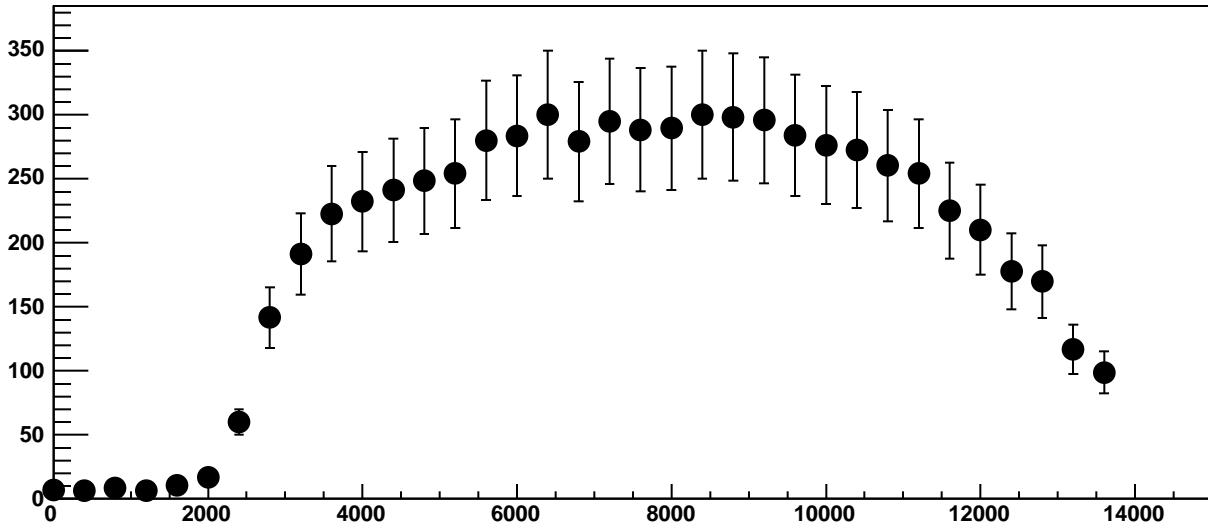


Chip 9, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC

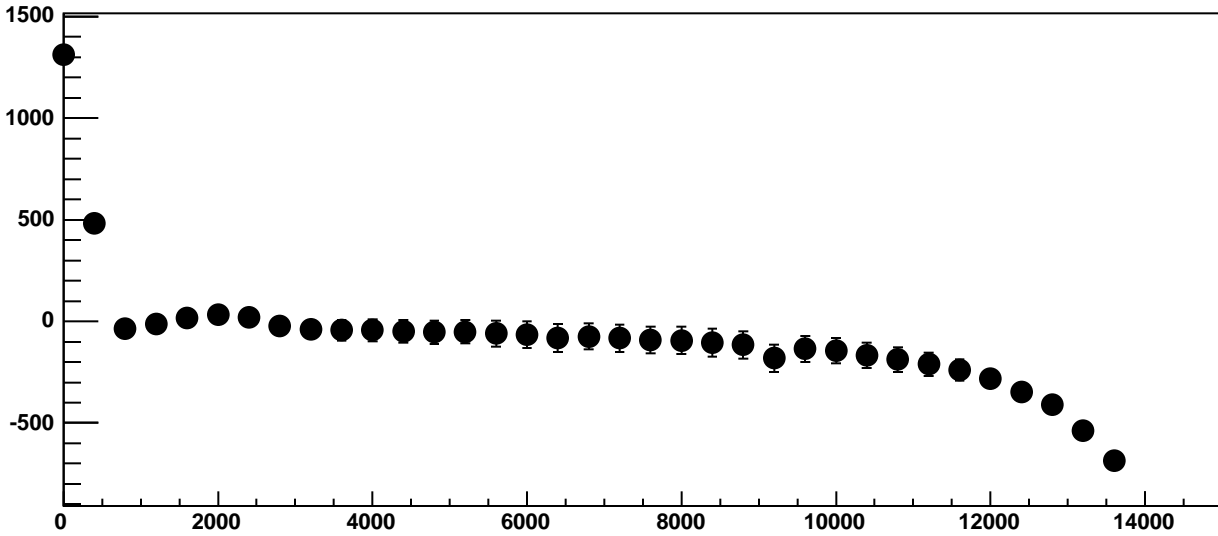


χ^2 / ndf 246.5 / 23
p0 -424.7 ± 3.207
p1 2.11 ± 0.002068

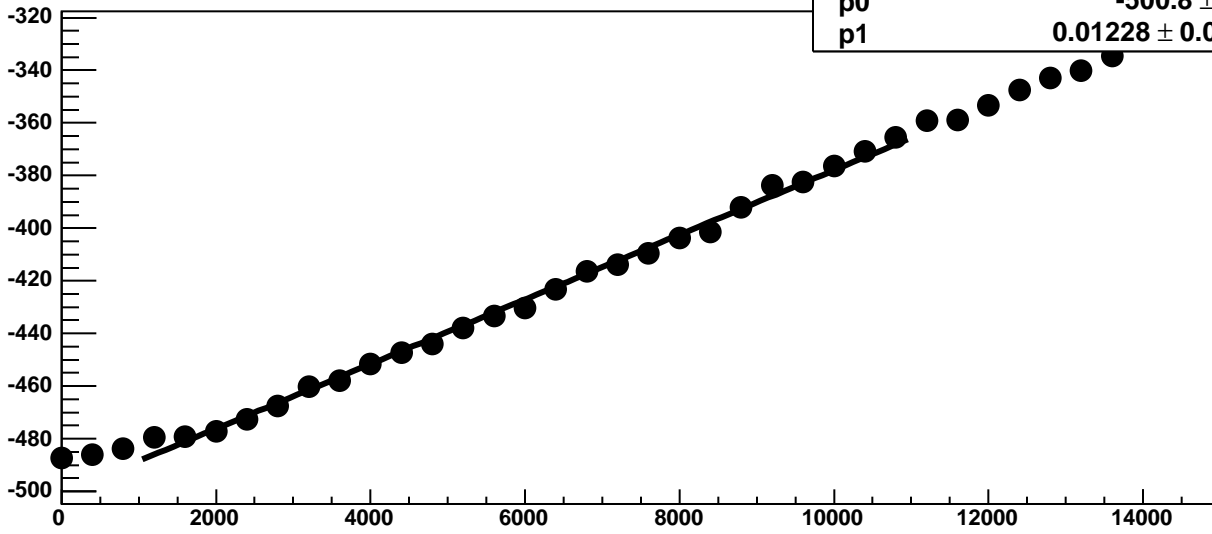
Chip 9, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC



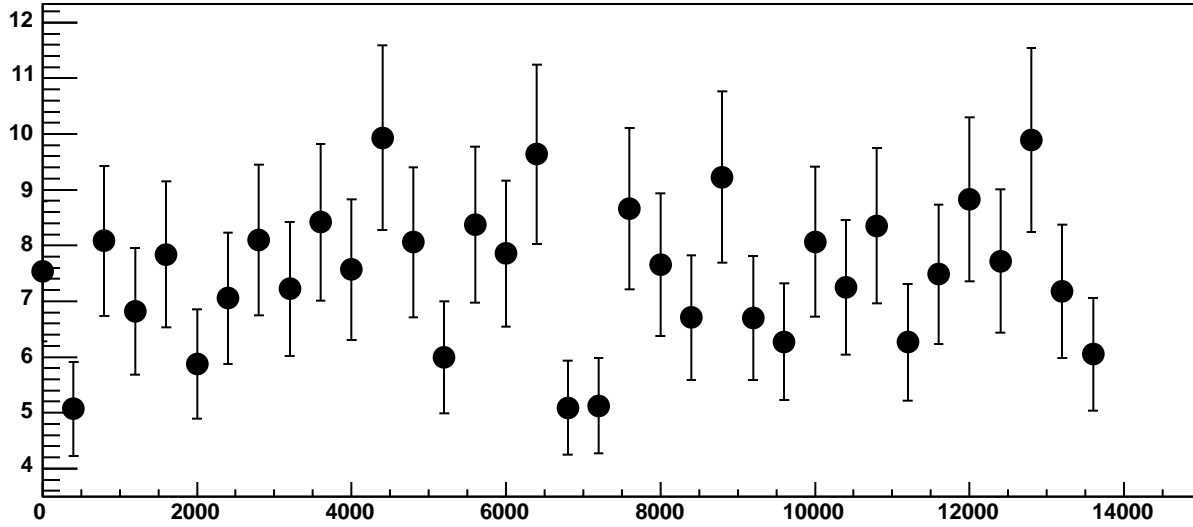
Chip 9, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC



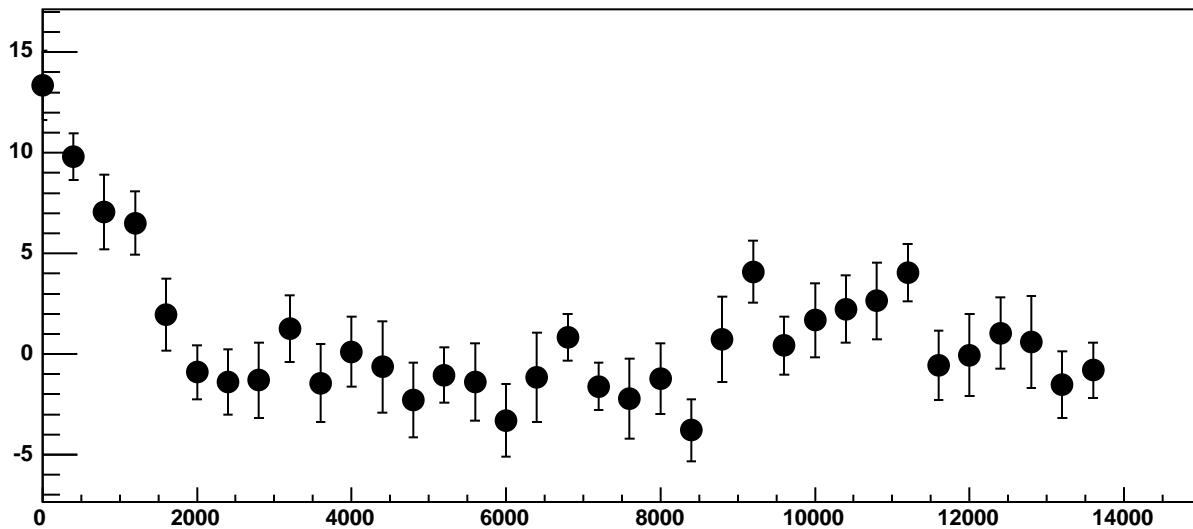
Chip 9, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



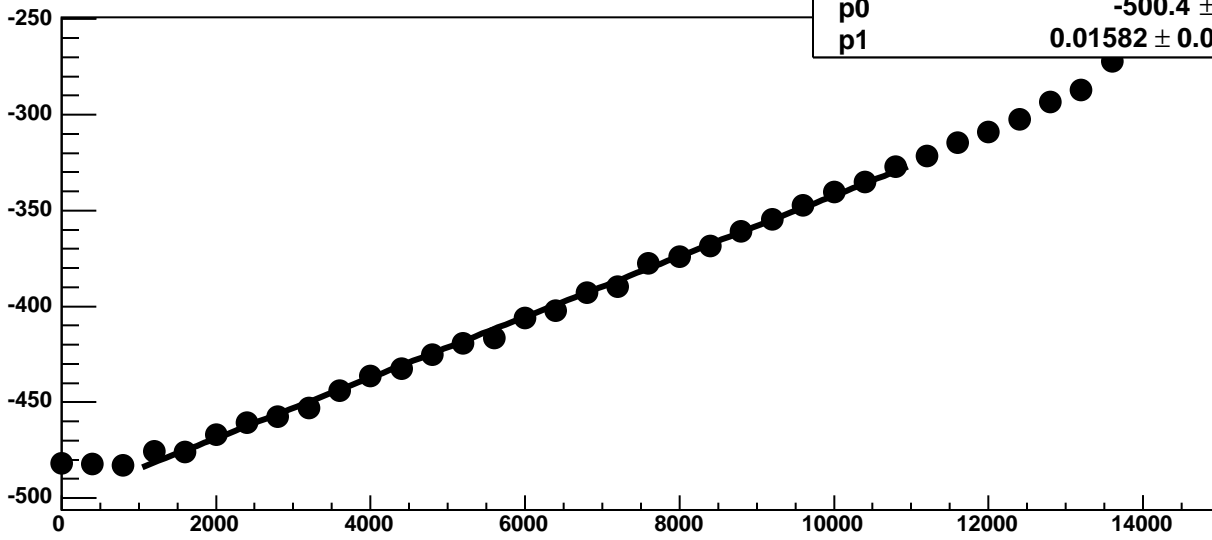
Chip 9, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

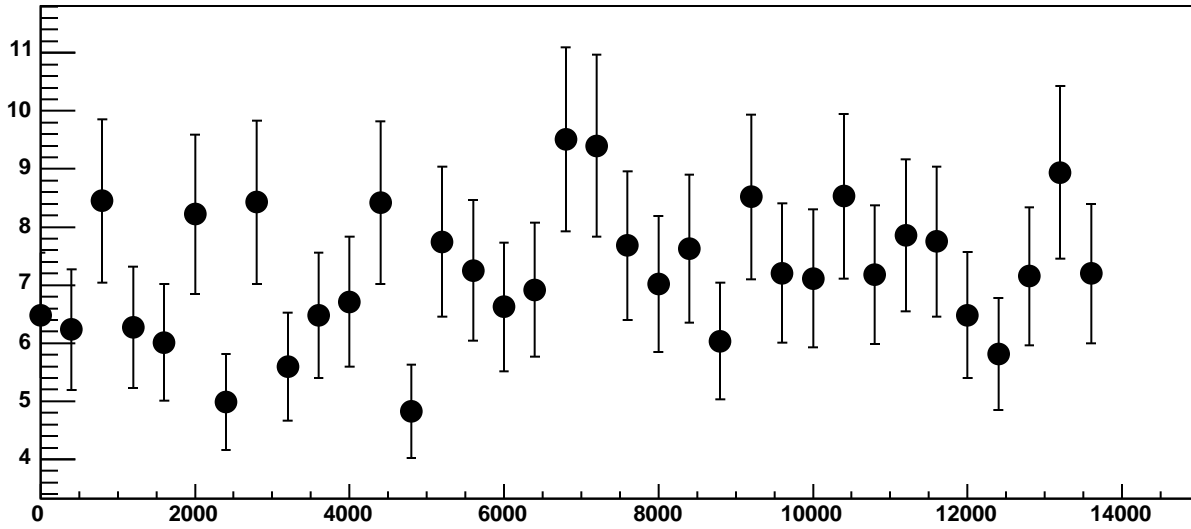


Chip 9, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

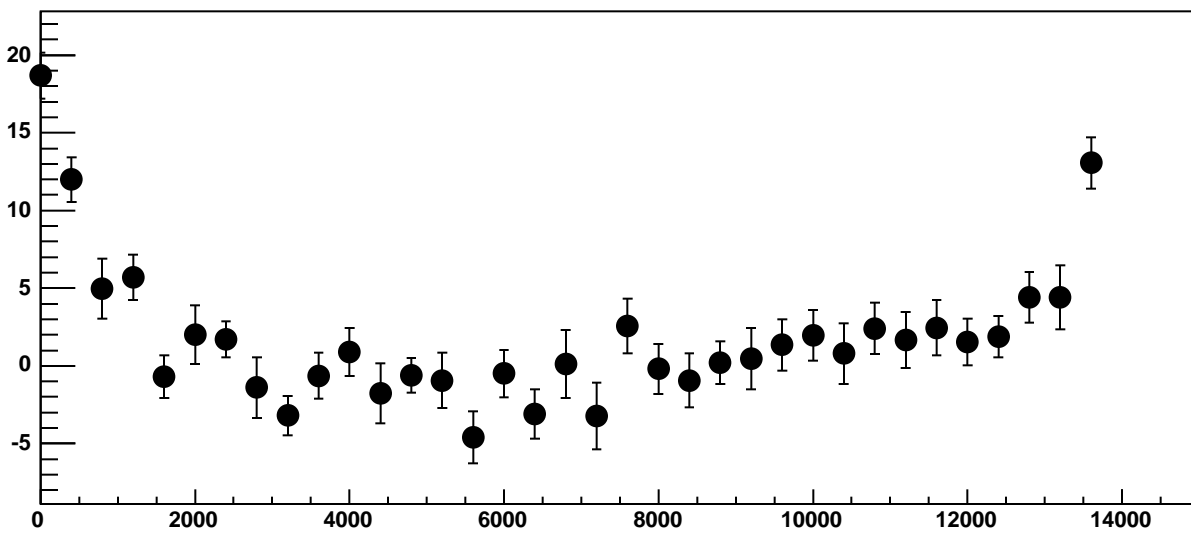


χ^2 / ndf 48.62 / 23
p0 -500.4 ± 0.6879
p1 0.01582 ± 0.0001093

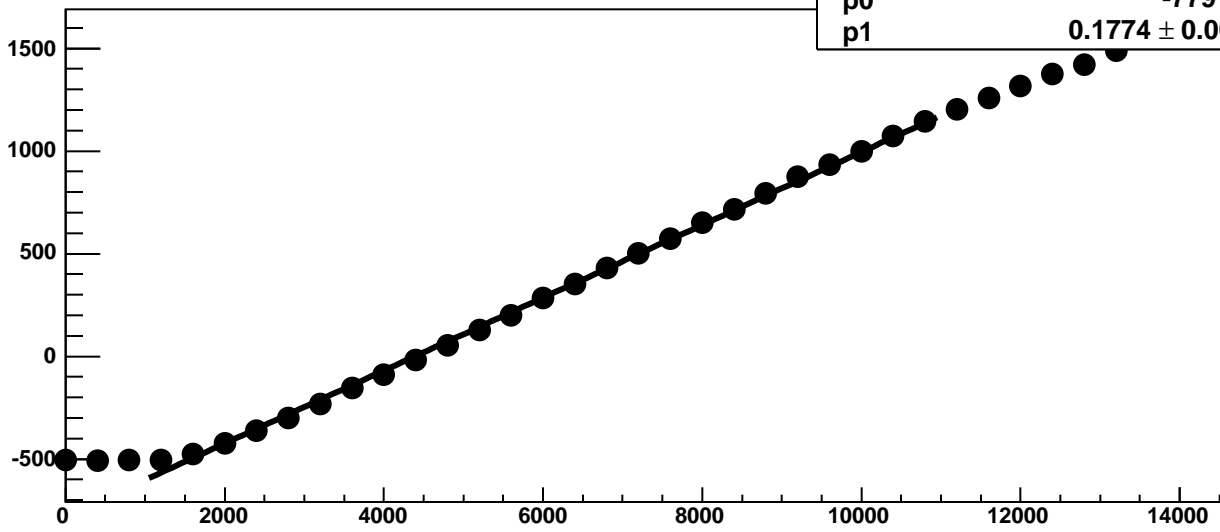
Chip 9, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



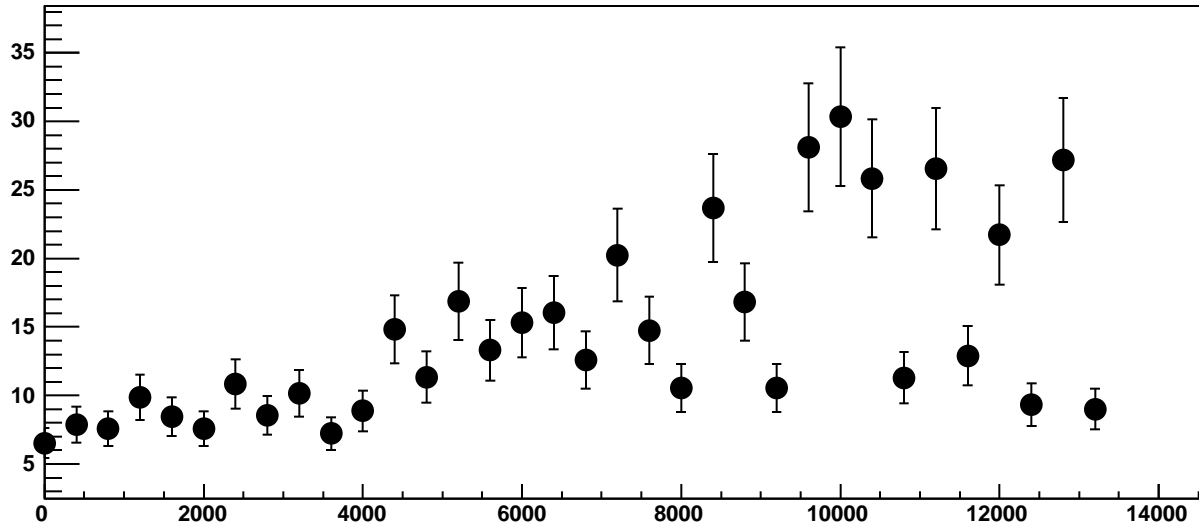
Chip 9, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



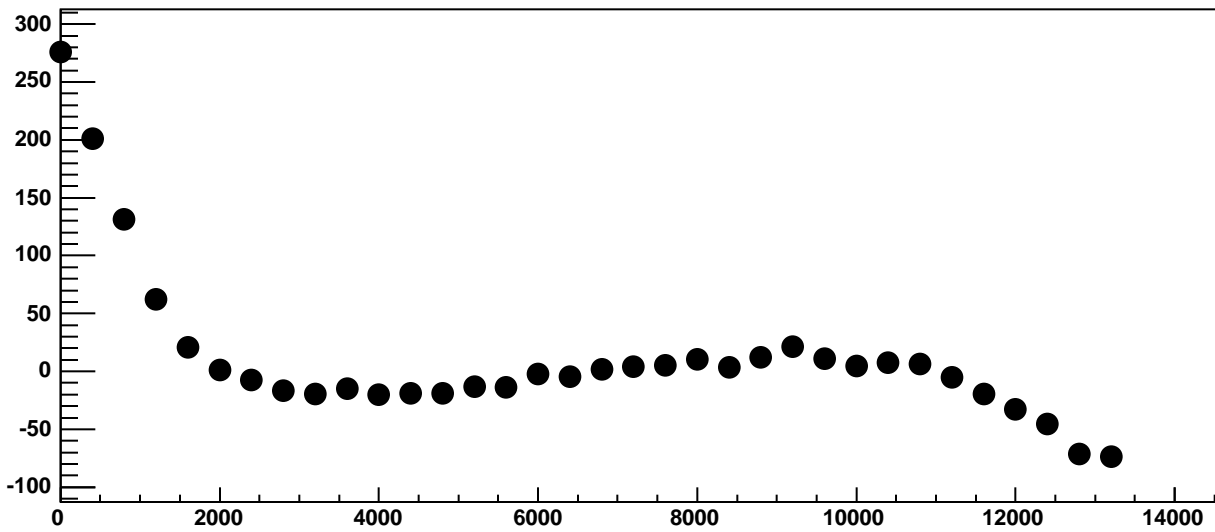
Chip 9, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC



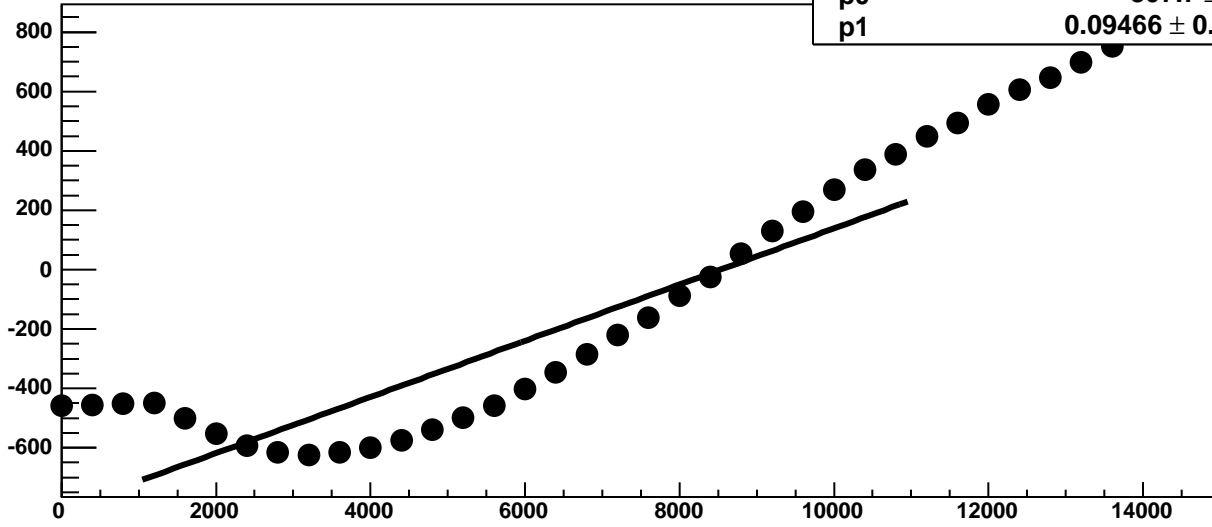
Chip 9, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC

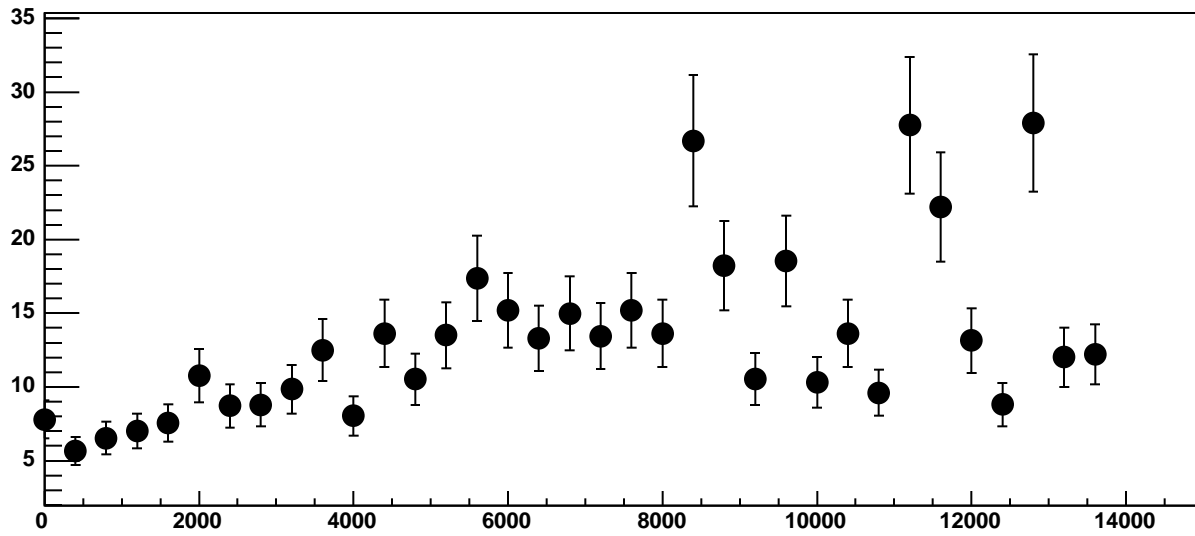


Chip 9, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

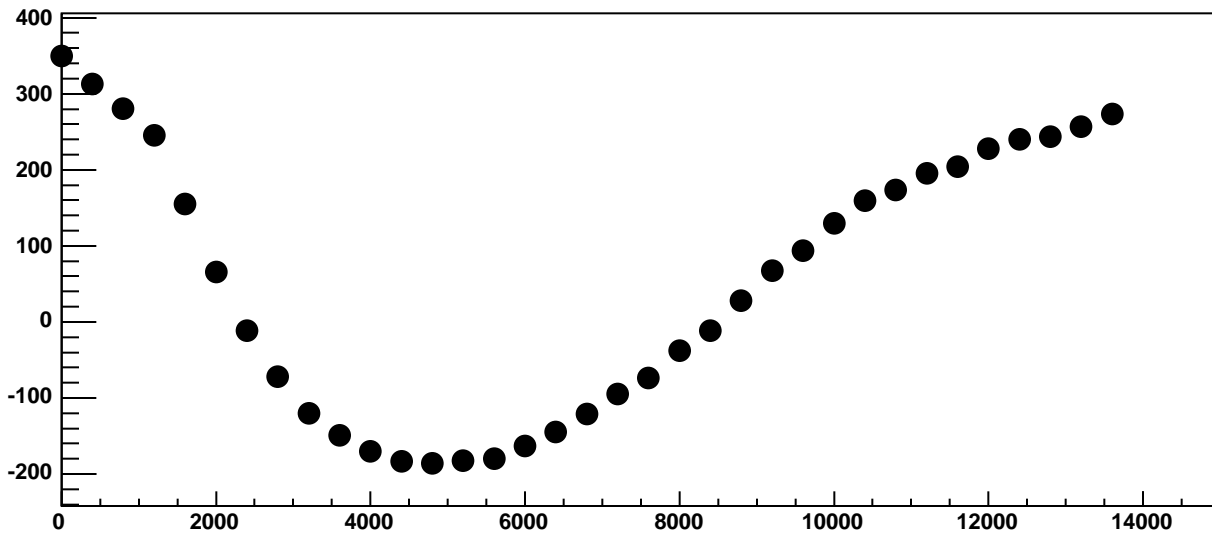


χ^2 / ndf 8.286e+04 / 23
p0 -807.7 ± 0.9621
p1 0.09466 ± 0.000163

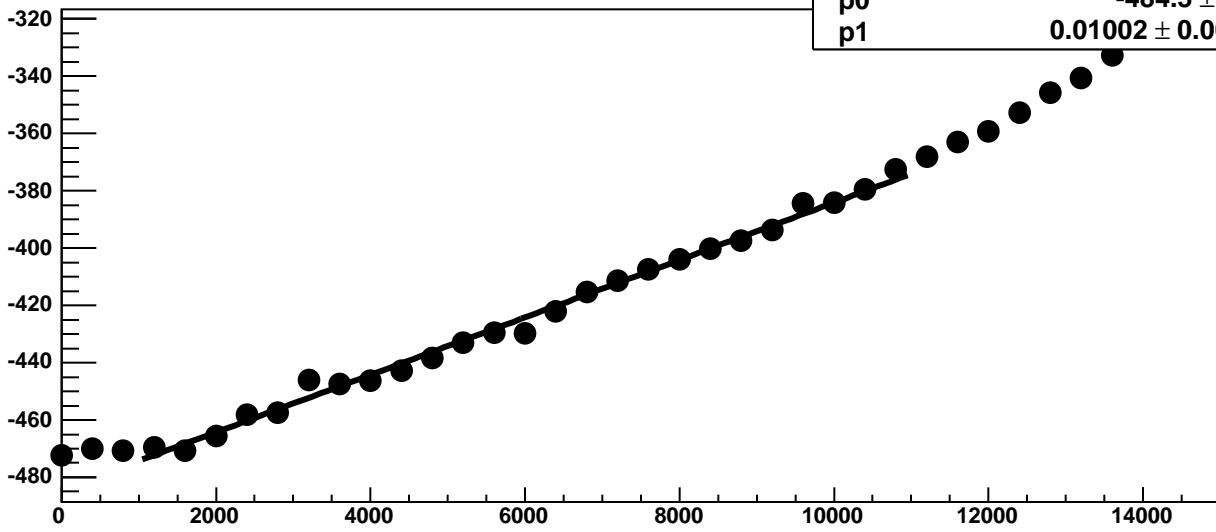
Chip 9, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



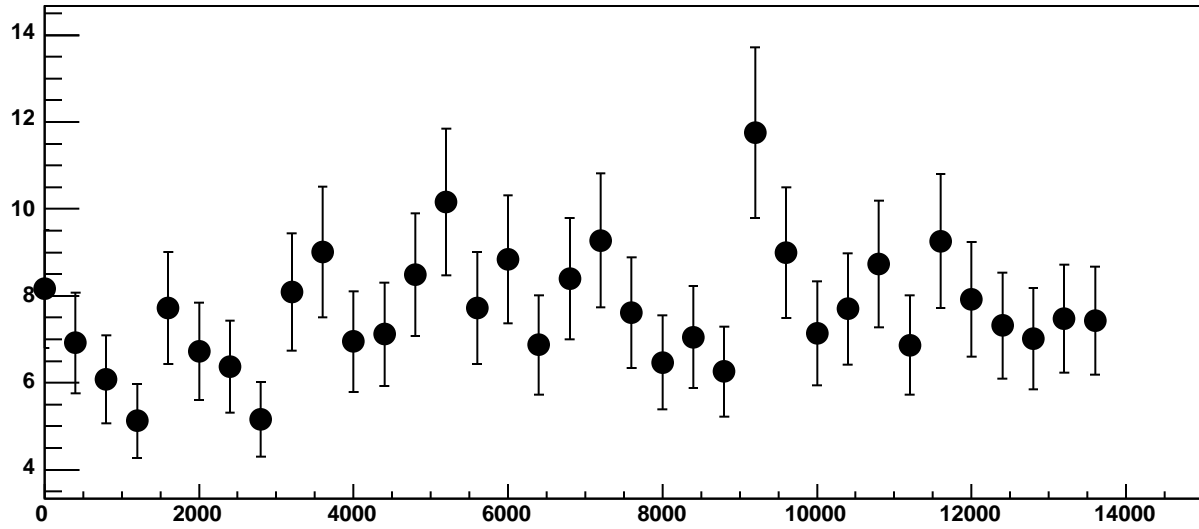
Chip 9, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



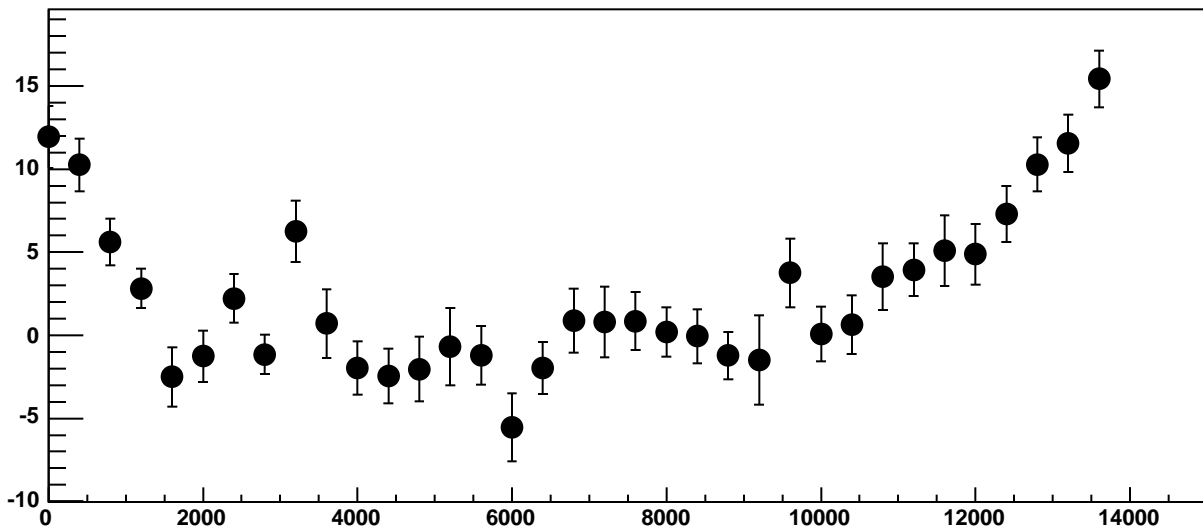
Chip 9, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



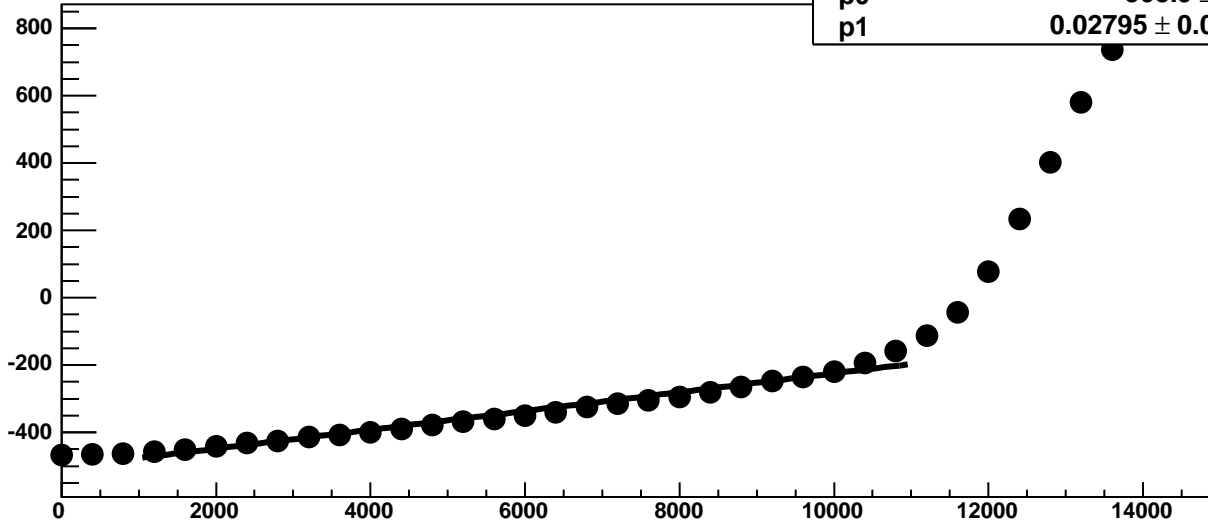
Chip 9, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



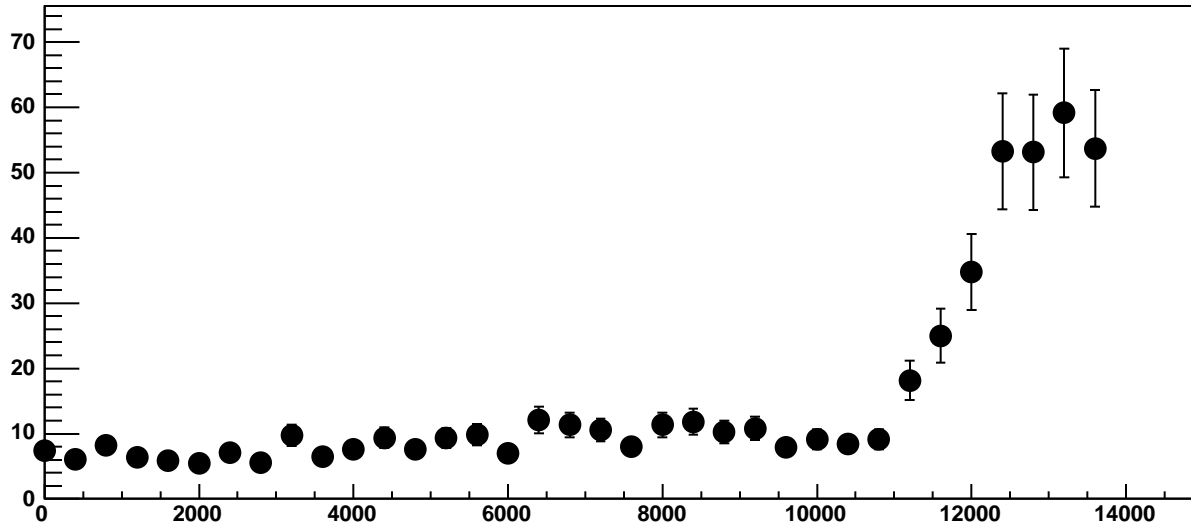
Chip 9, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



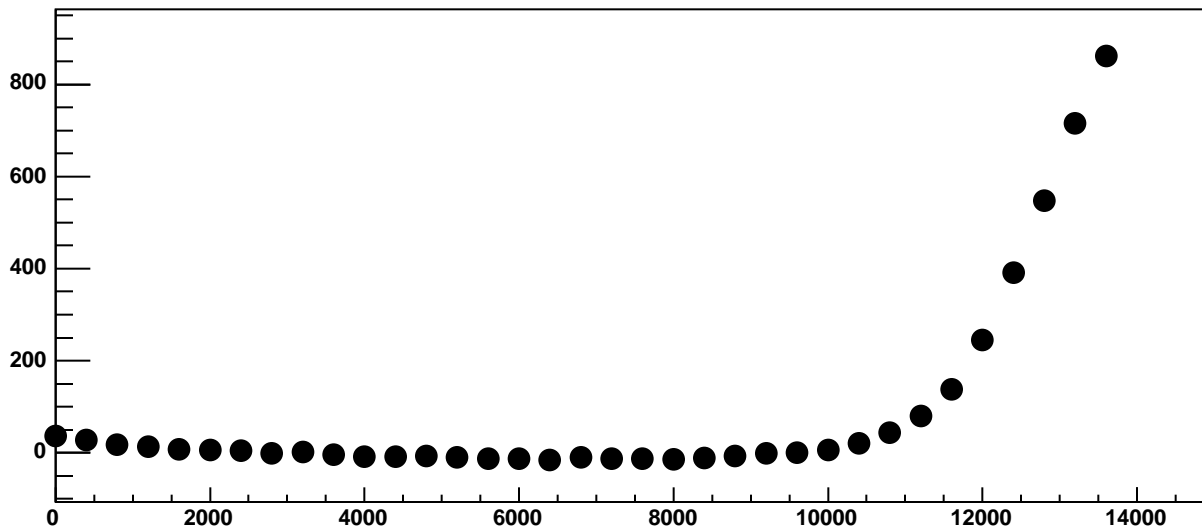
Chip 9, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC



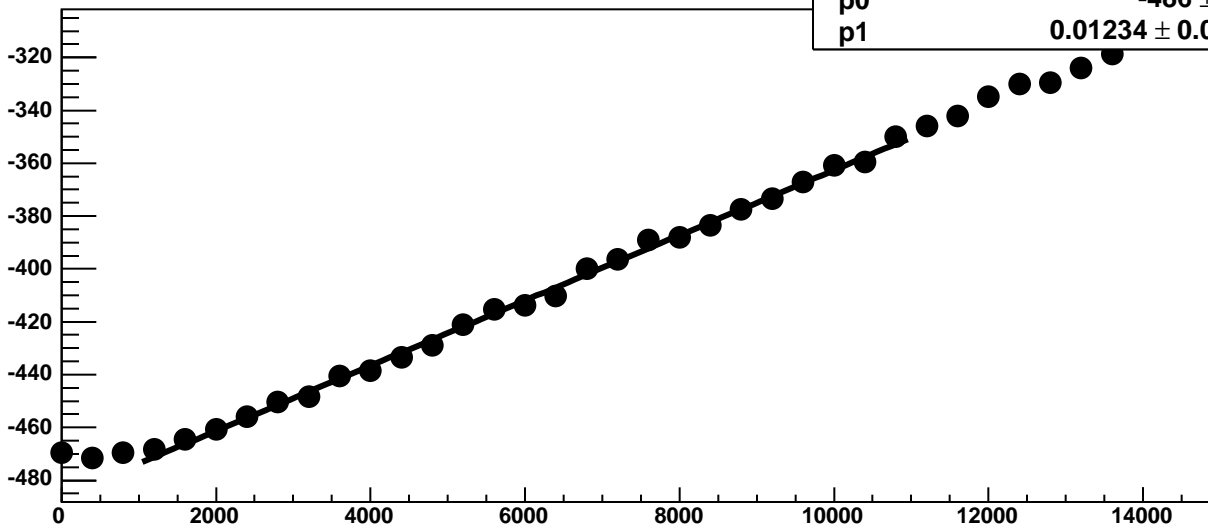
Chip 9, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



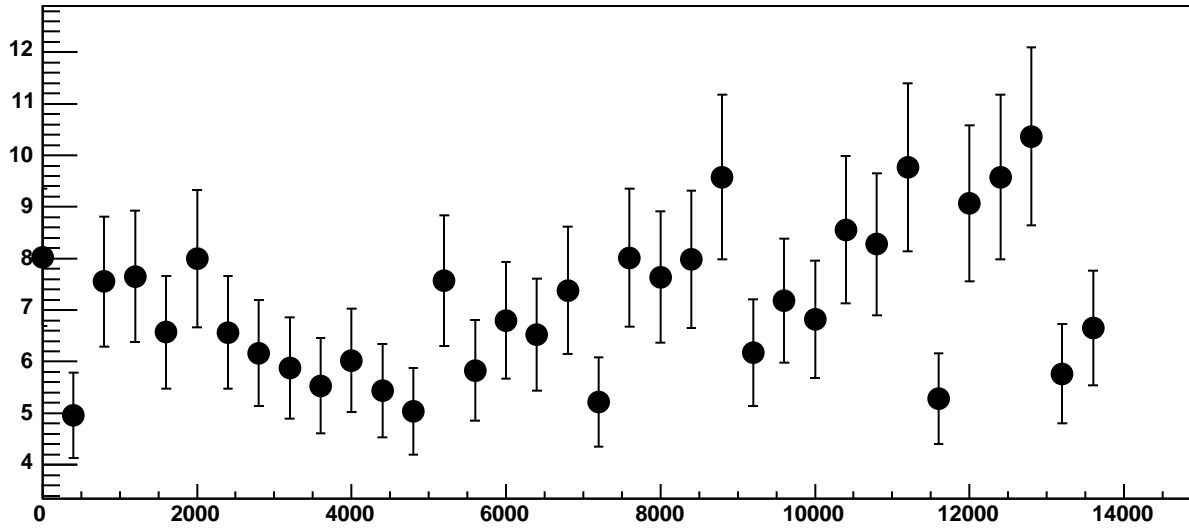
Chip 9, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC



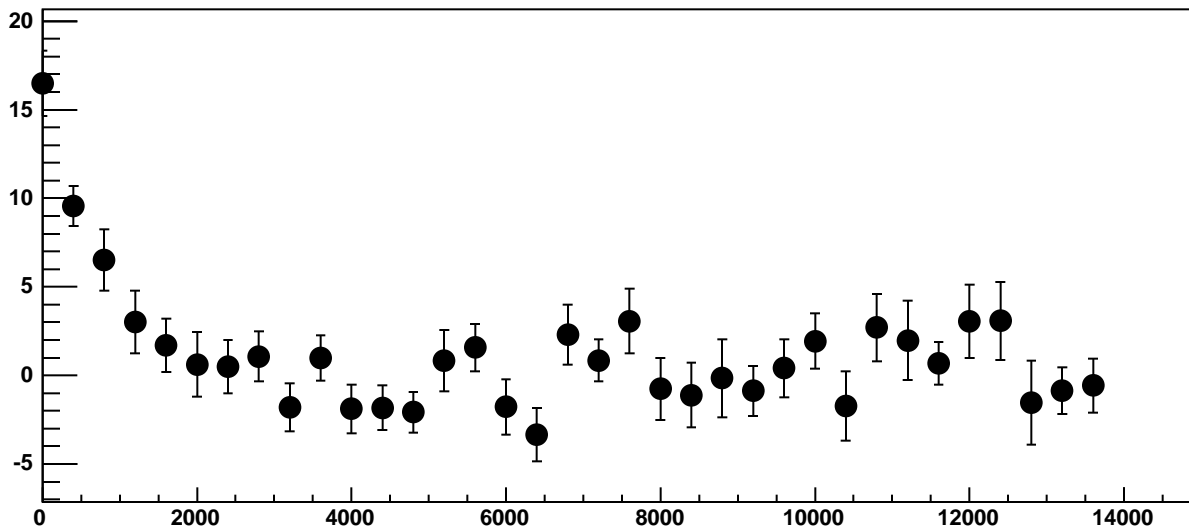
Chip 9, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



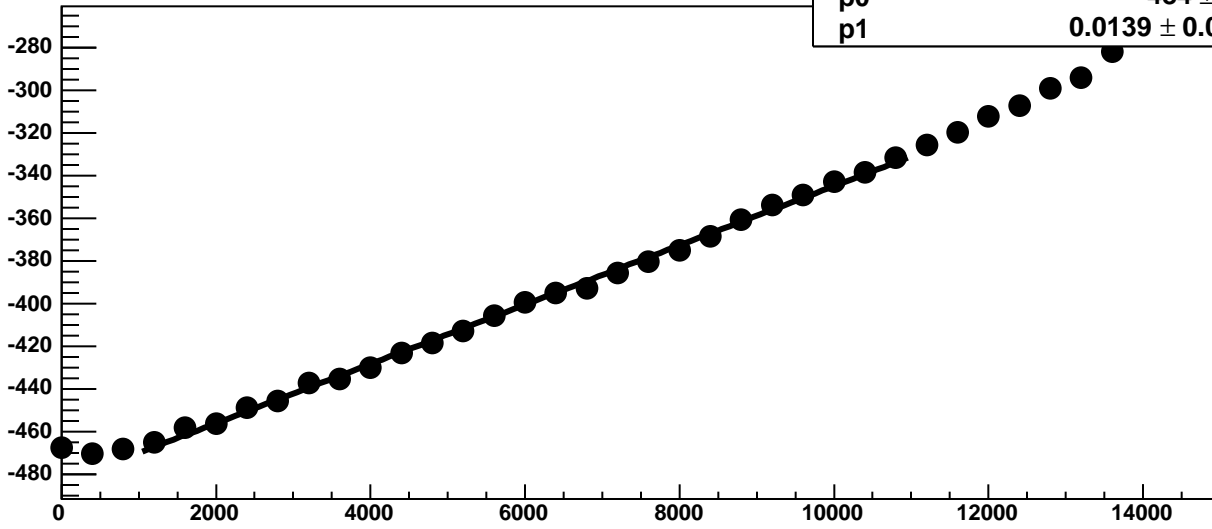
Chip 9, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



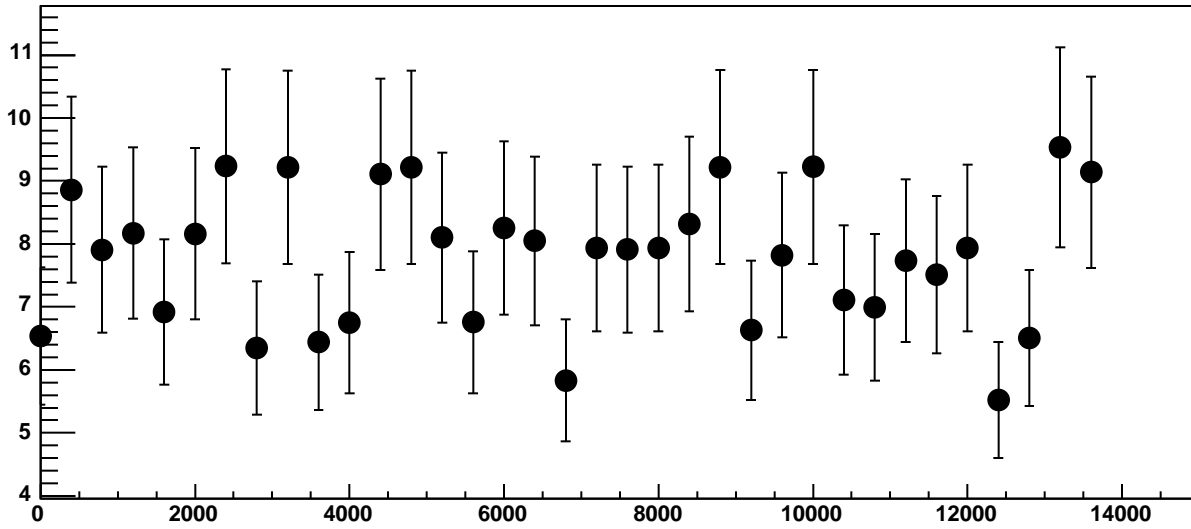
Chip 9, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



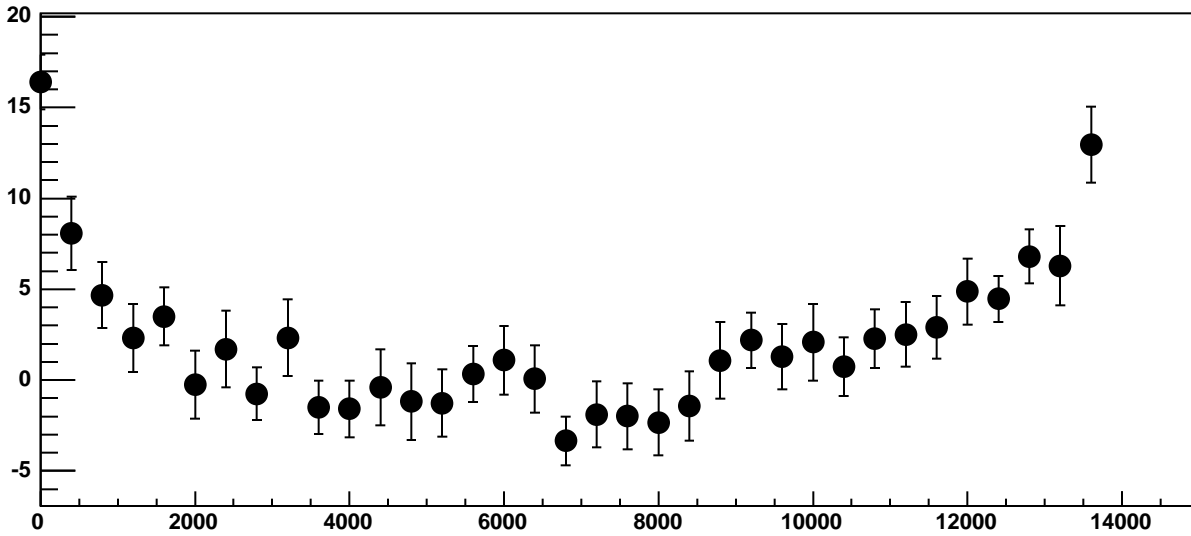
Chip 9, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



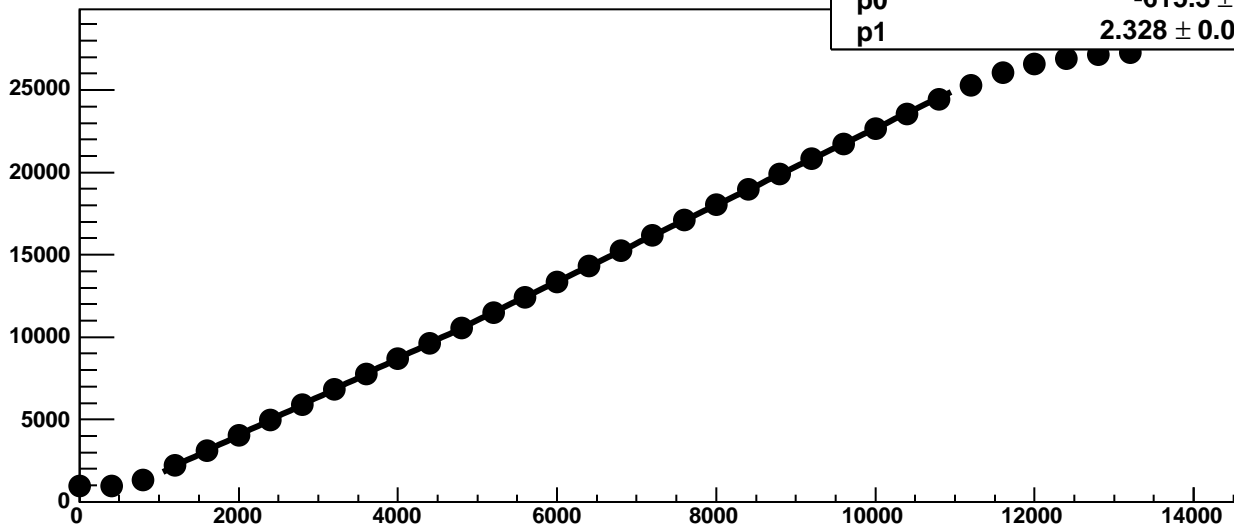
Chip 9, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



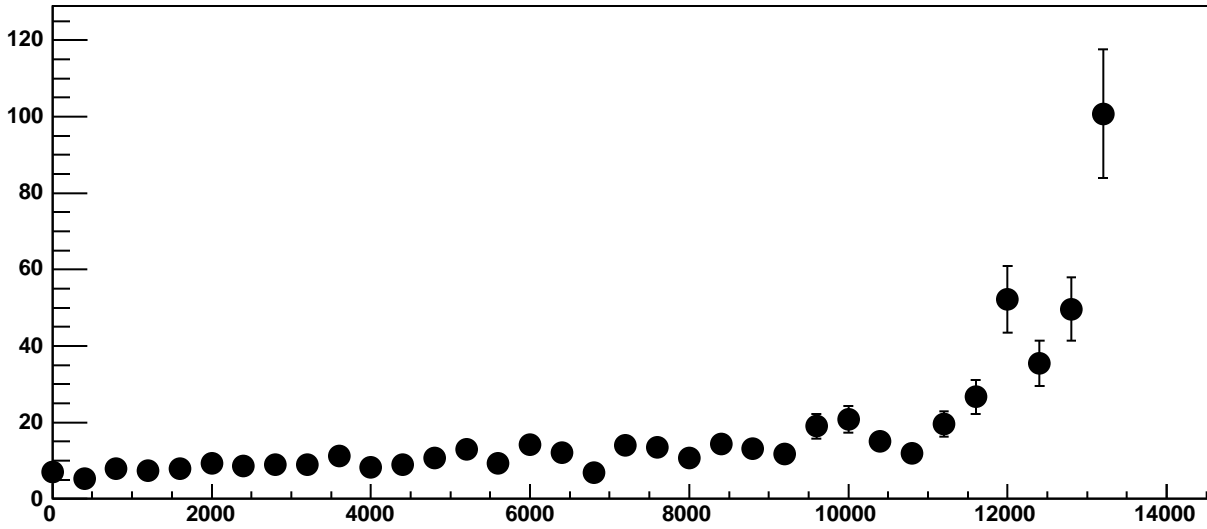
Chip 9, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



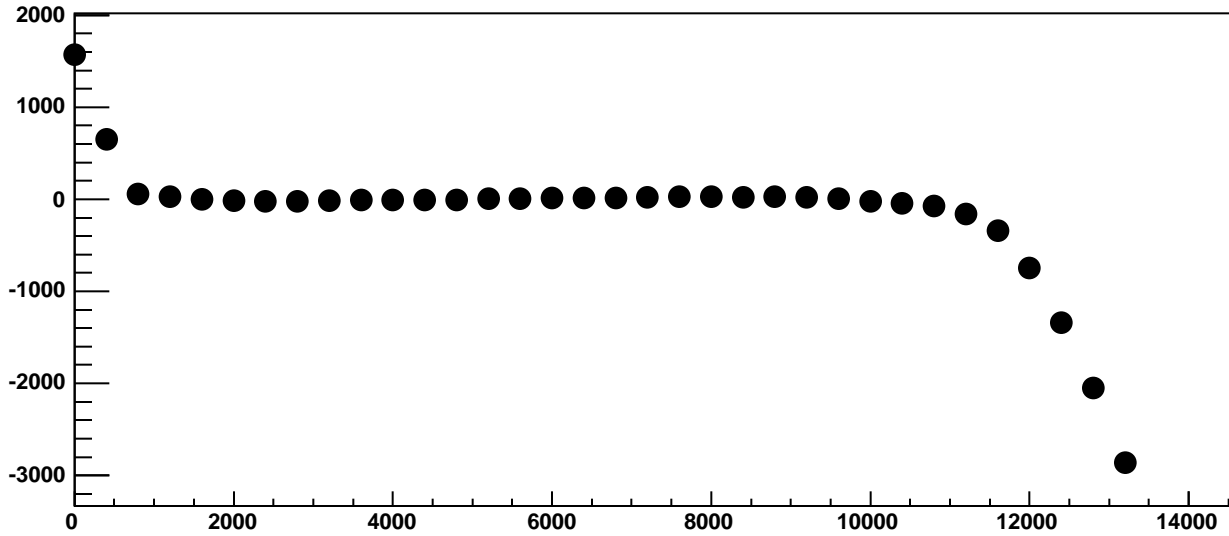
Chip 9, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC



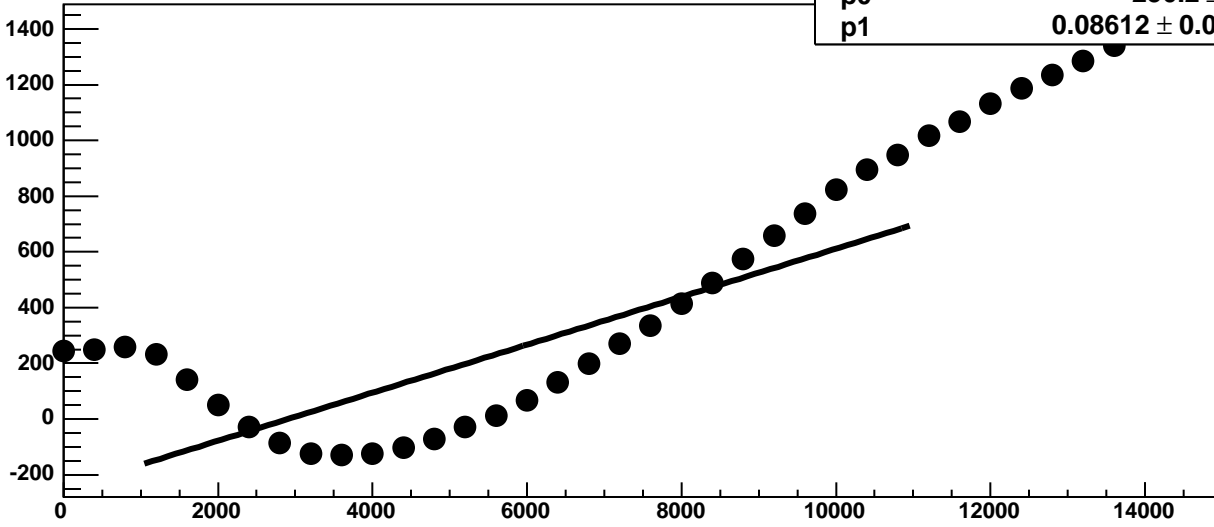
Chip 9, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC



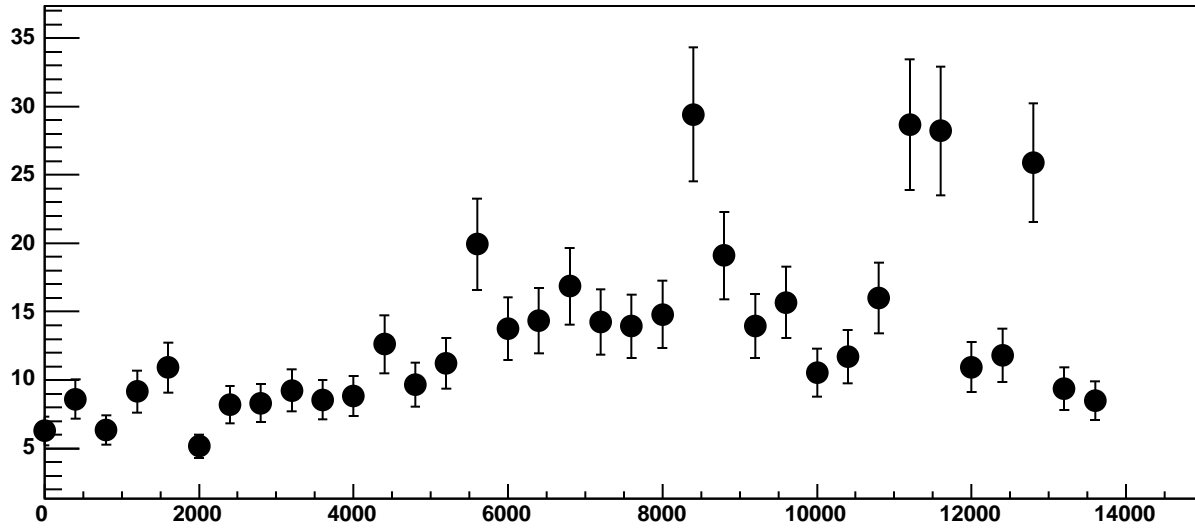
Chip 9, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC



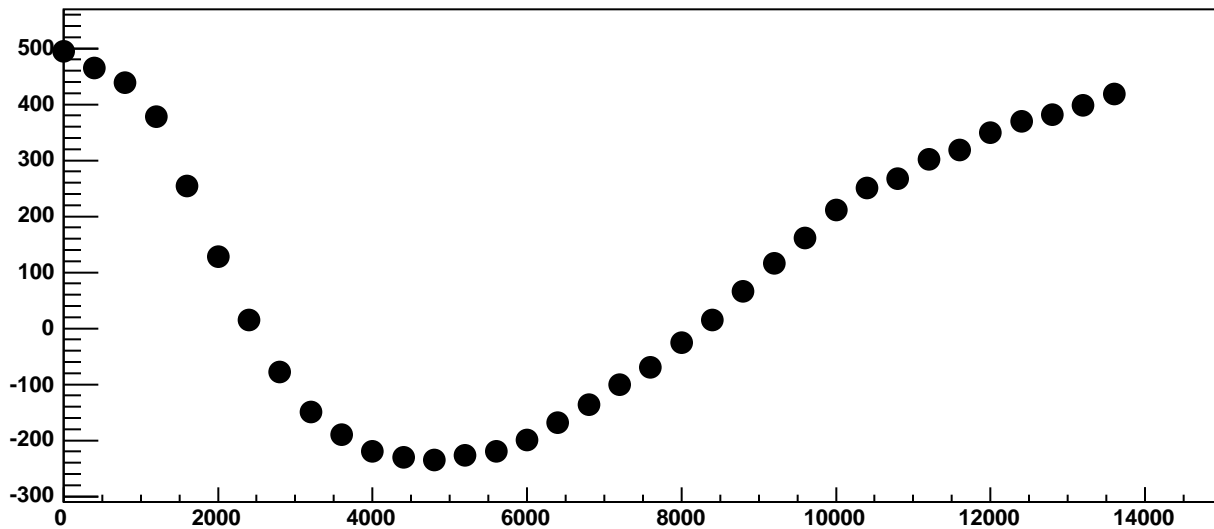
Chip 10, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



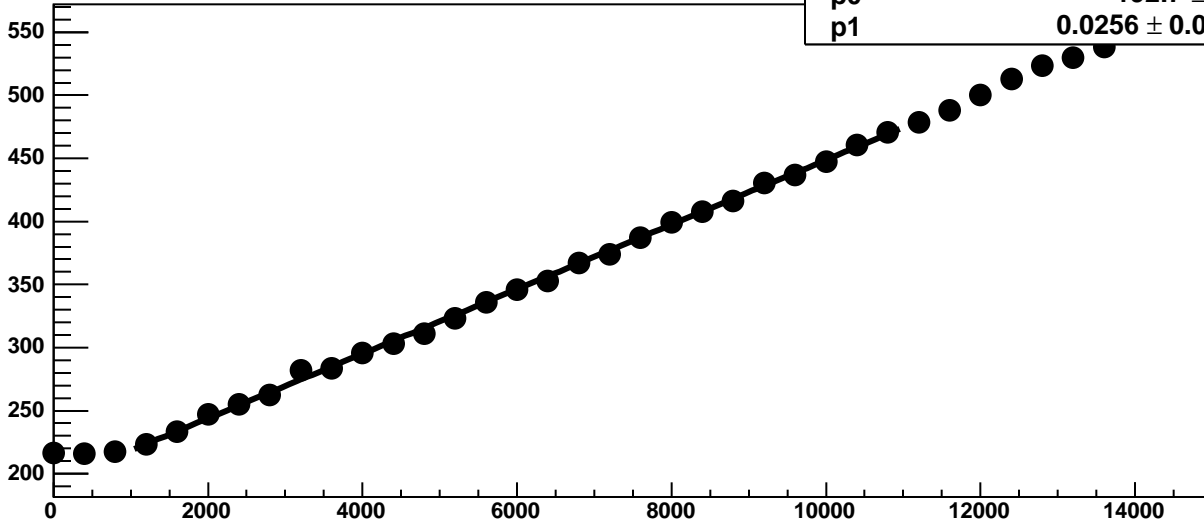
Chip 10, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC

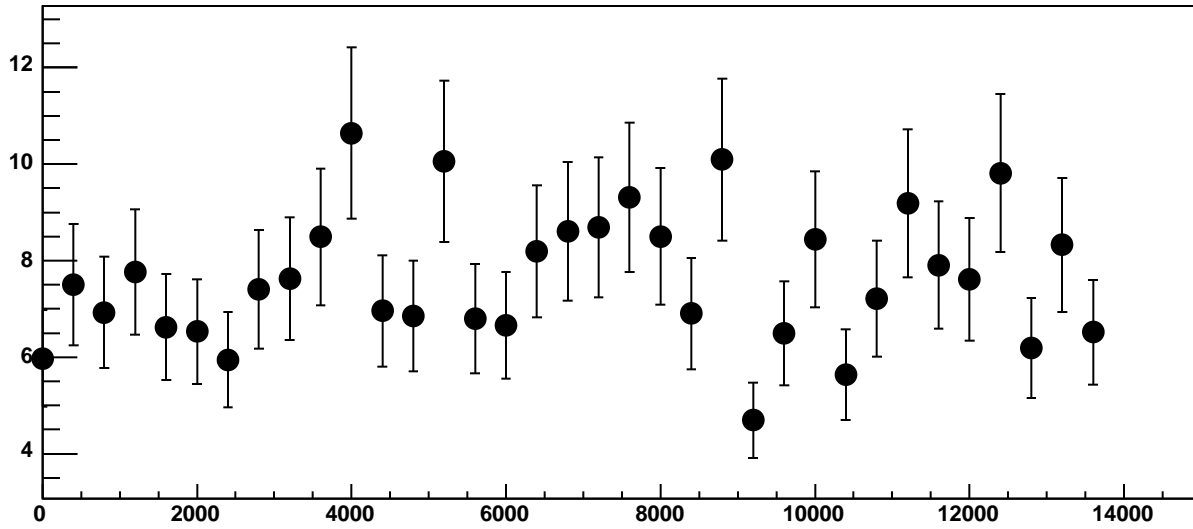


Chip 10, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

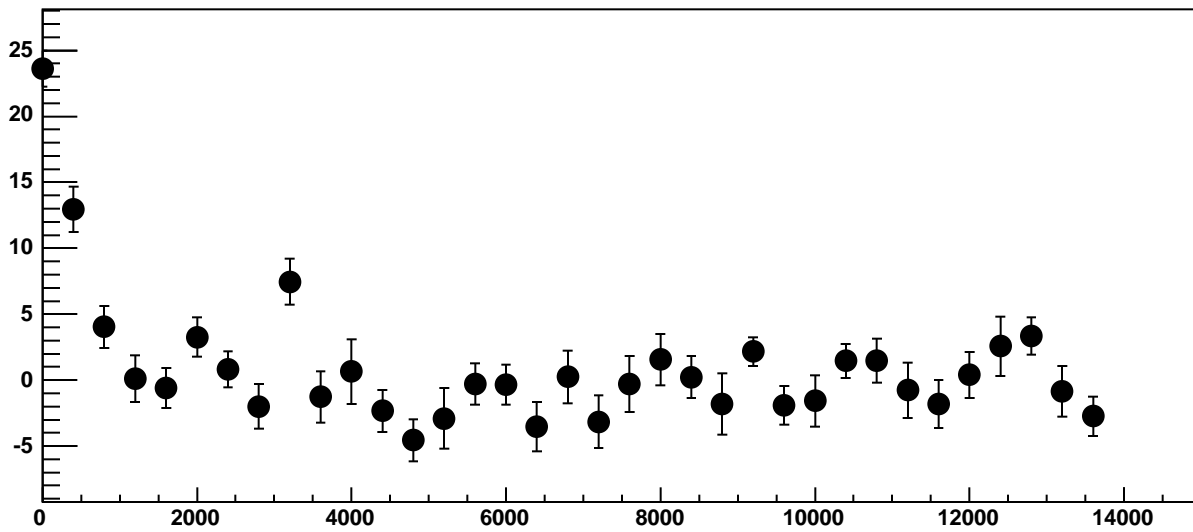


χ^2 / ndf 53.28 / 23
p0 192.7 ± 0.7457
p1 0.0256 ± 0.0001088

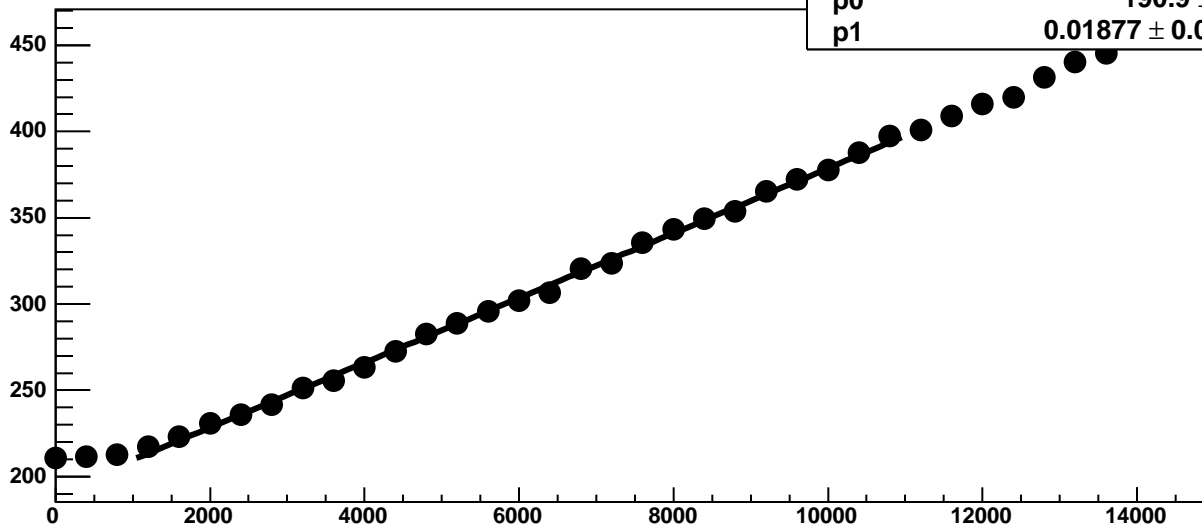
Chip 10, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

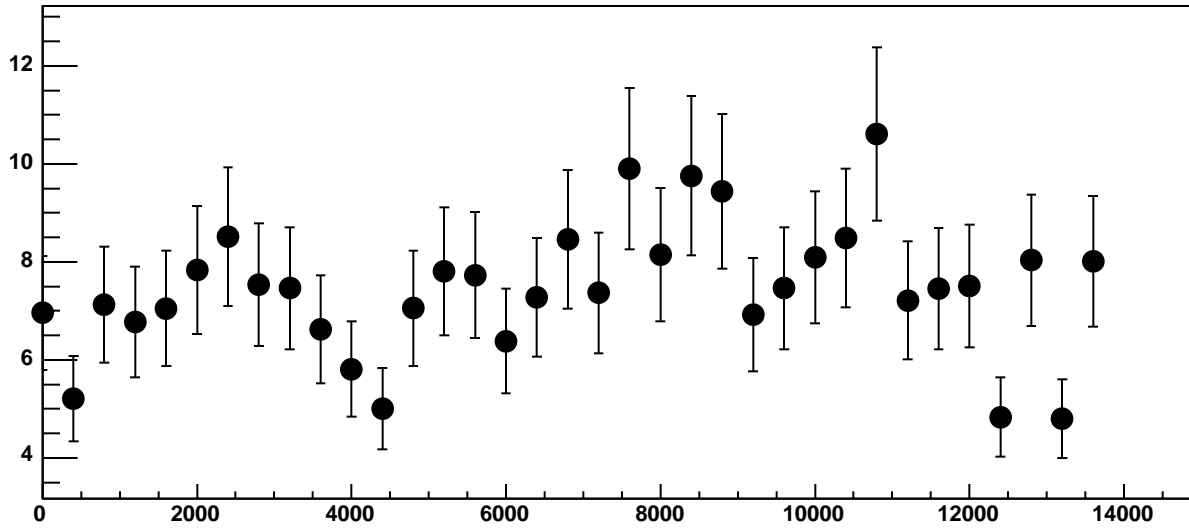


Chip 10, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

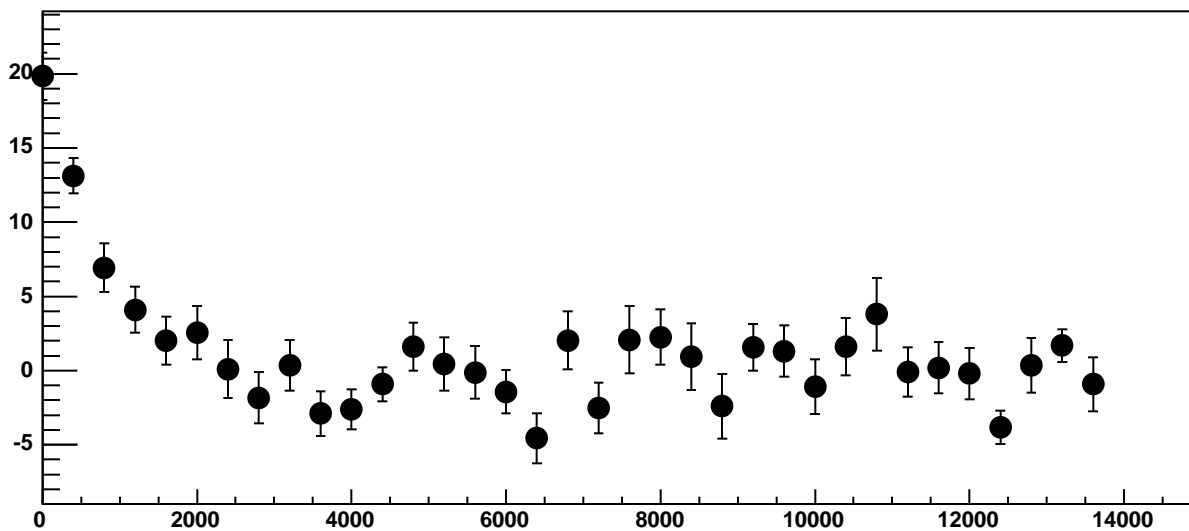


χ^2 / ndf 41.35 / 23
p0 190.9 ± 0.7721
p1 0.01877 ± 0.0001245

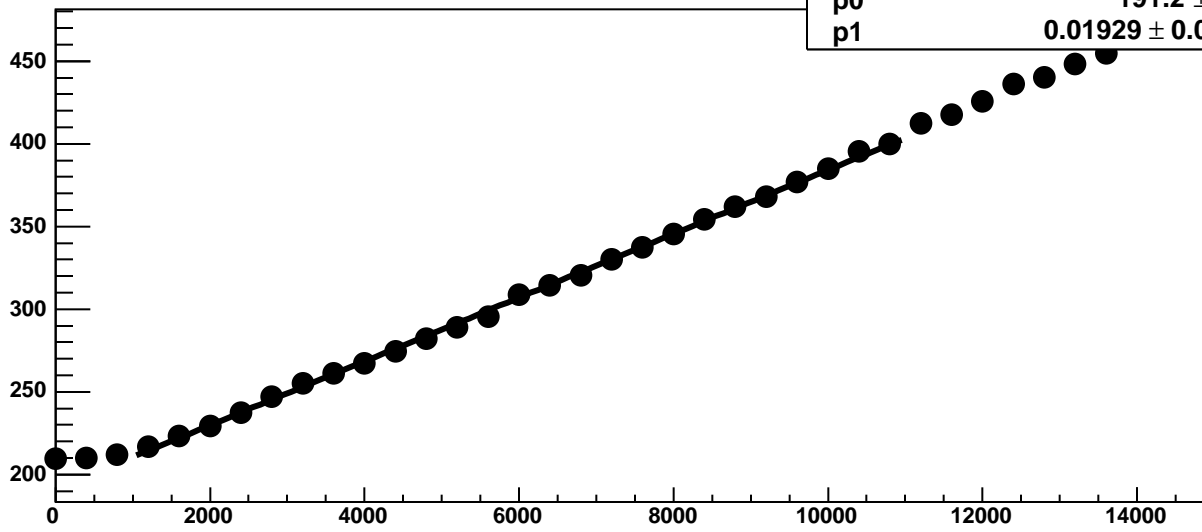
Chip 10, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

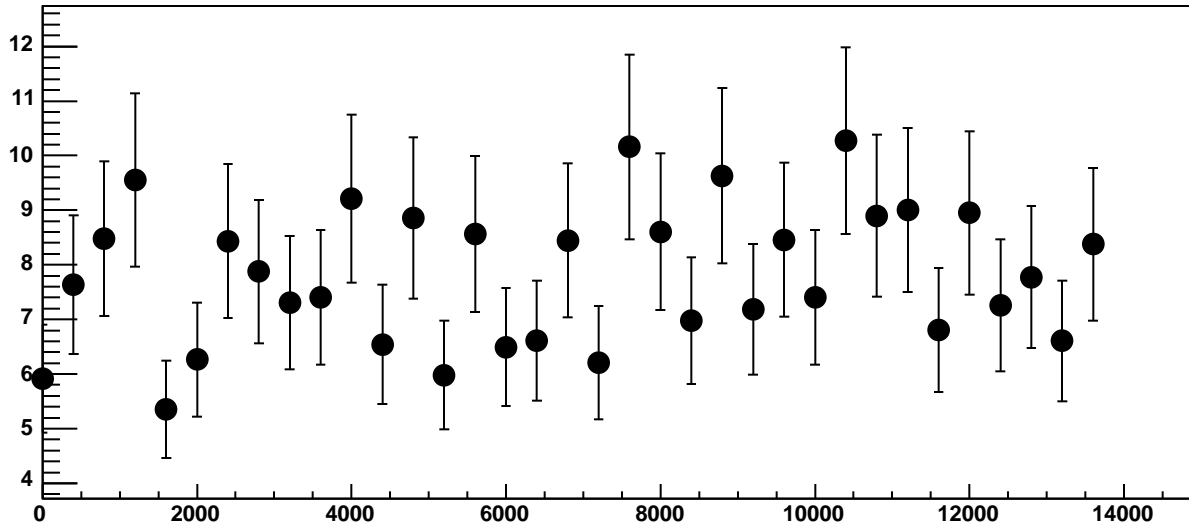


Chip 10, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

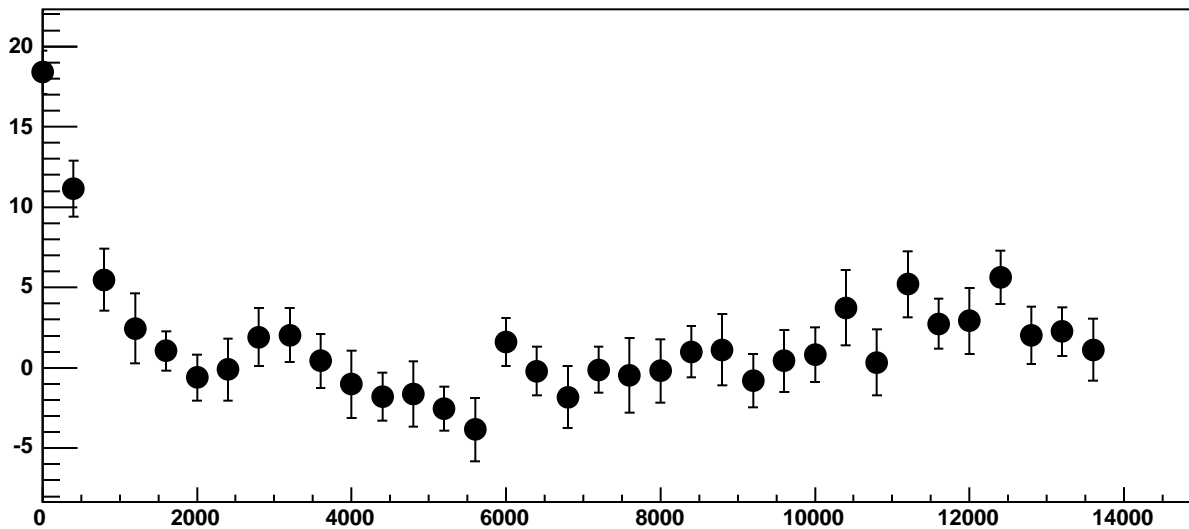


χ^2 / ndf 20.2 / 23
p0 191.2 ± 0.7748
p1 0.01929 ± 0.0001225

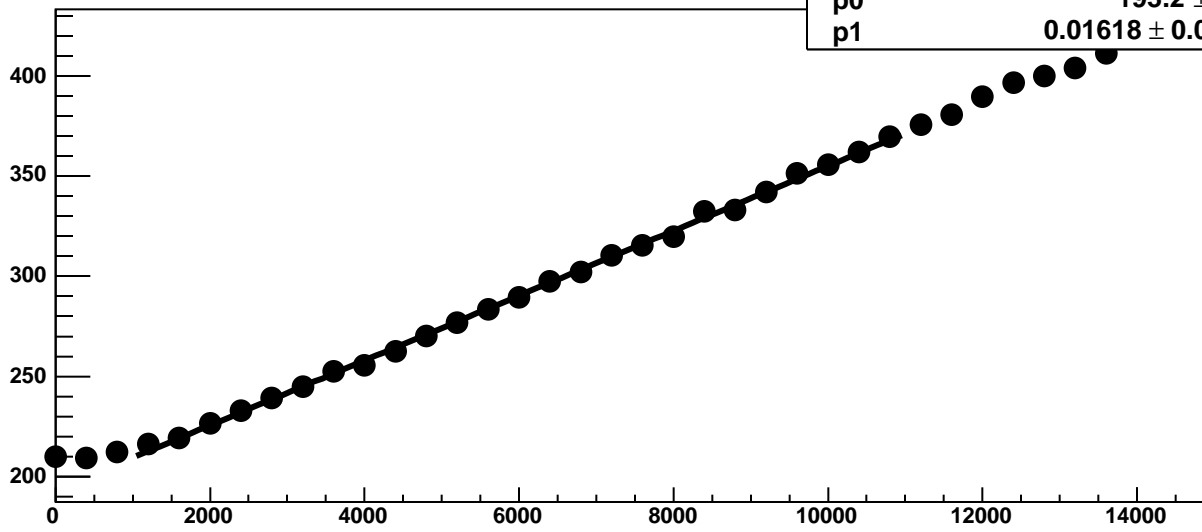
Chip 10, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC

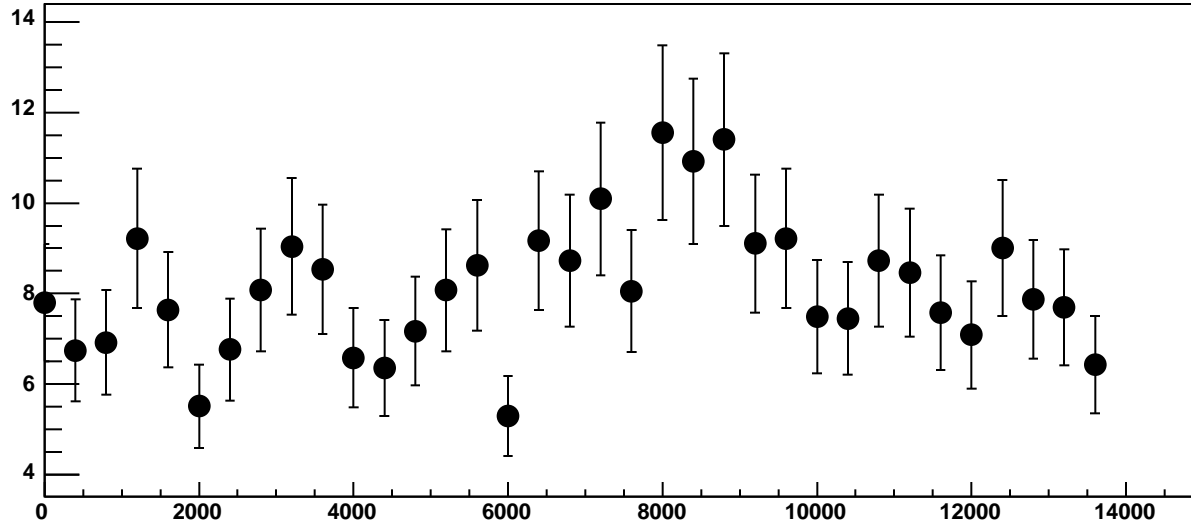


Chip 10, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

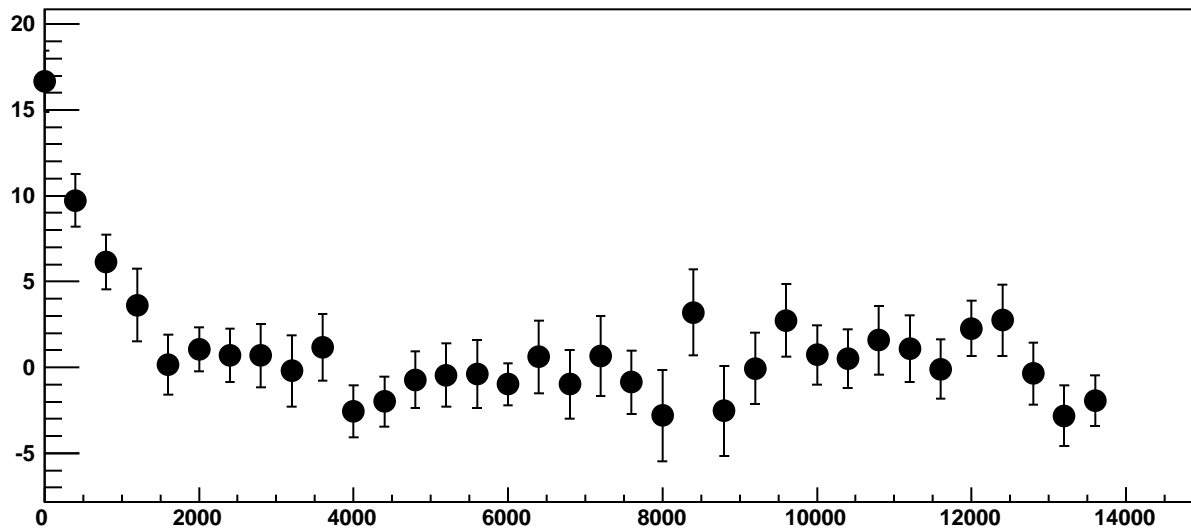


χ^2 / ndf 16.88 / 23
p0 193.2 ± 0.7913
p1 0.01618 ± 0.0001269

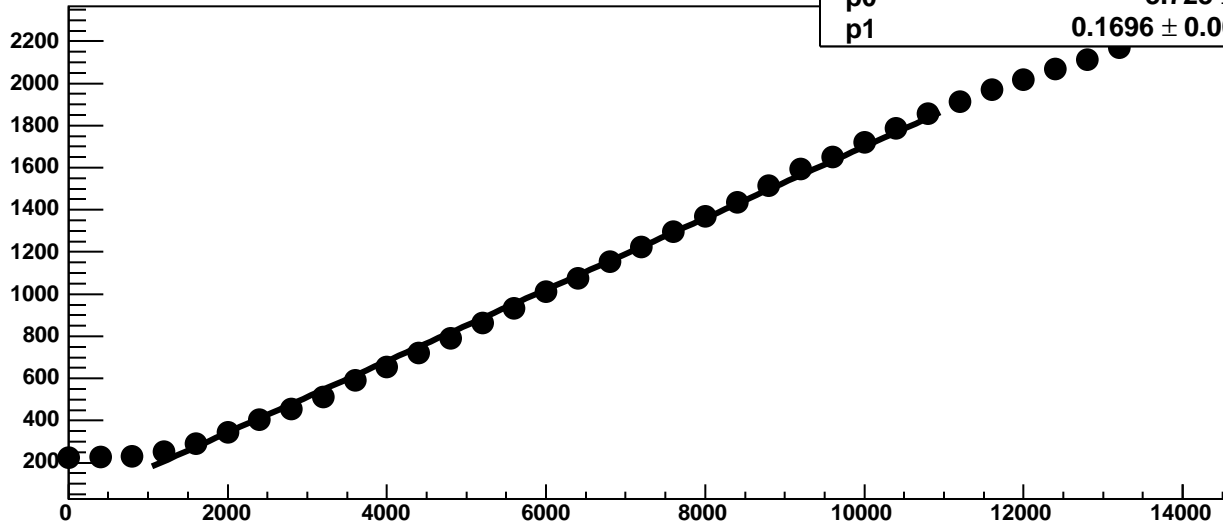
Chip 10, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 10, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

2163 / 23

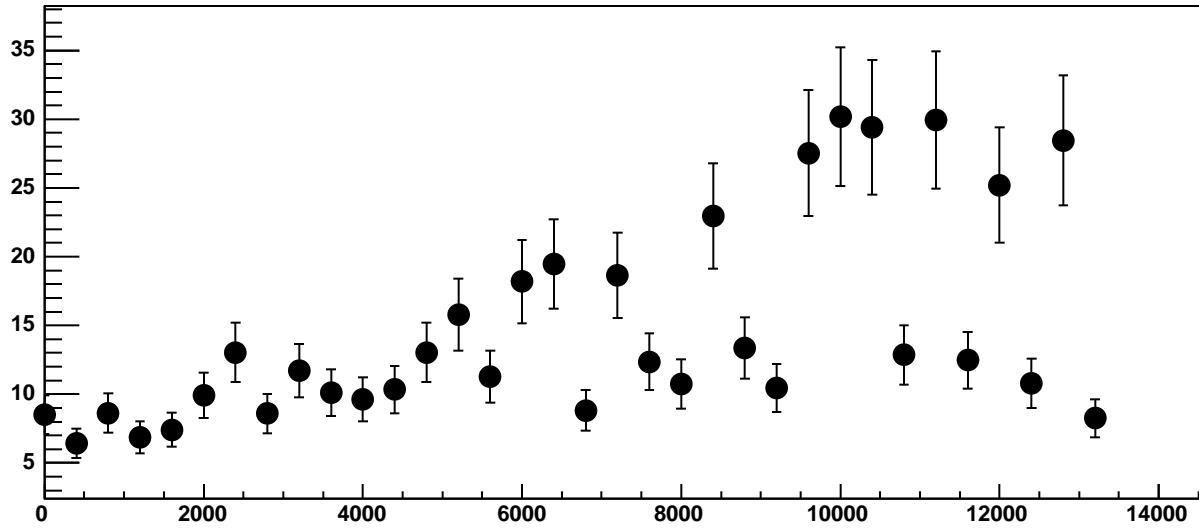
p0

3.723 ± 1.027

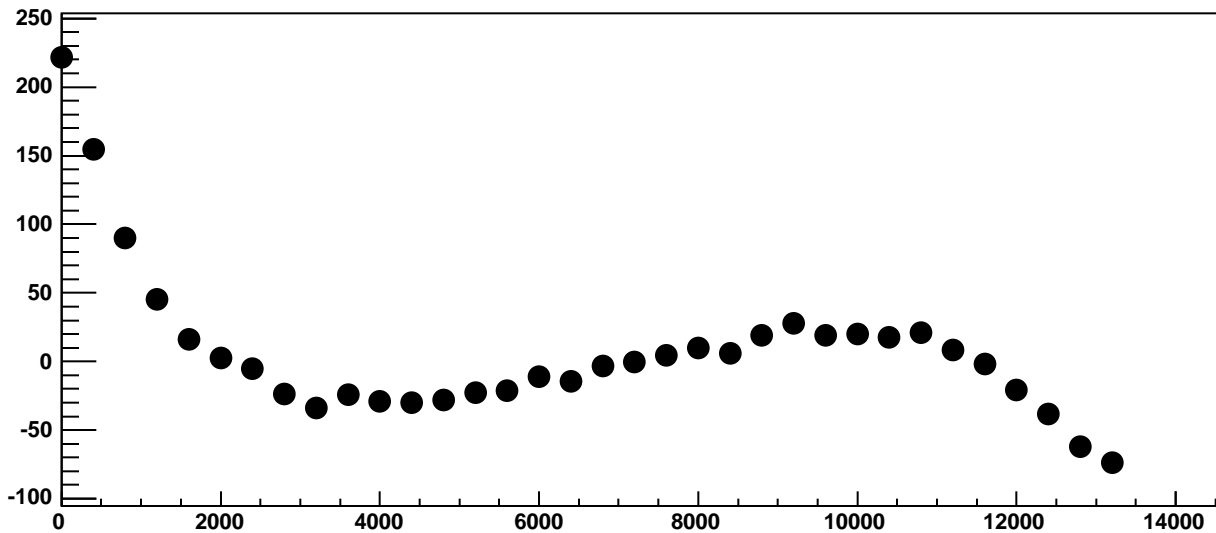
p1

0.1696 ± 0.0001875

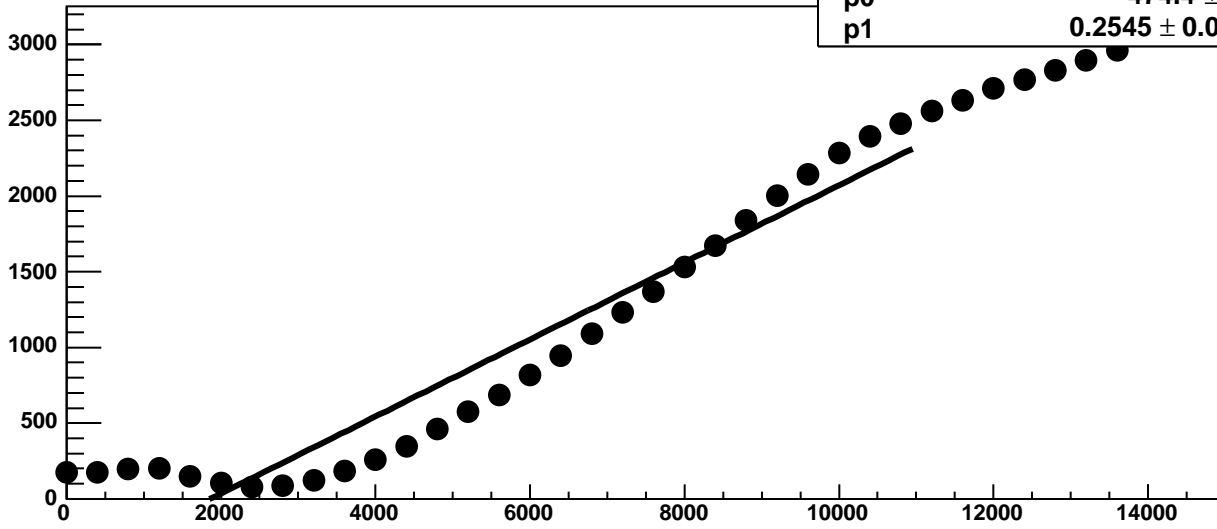
Chip 10, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

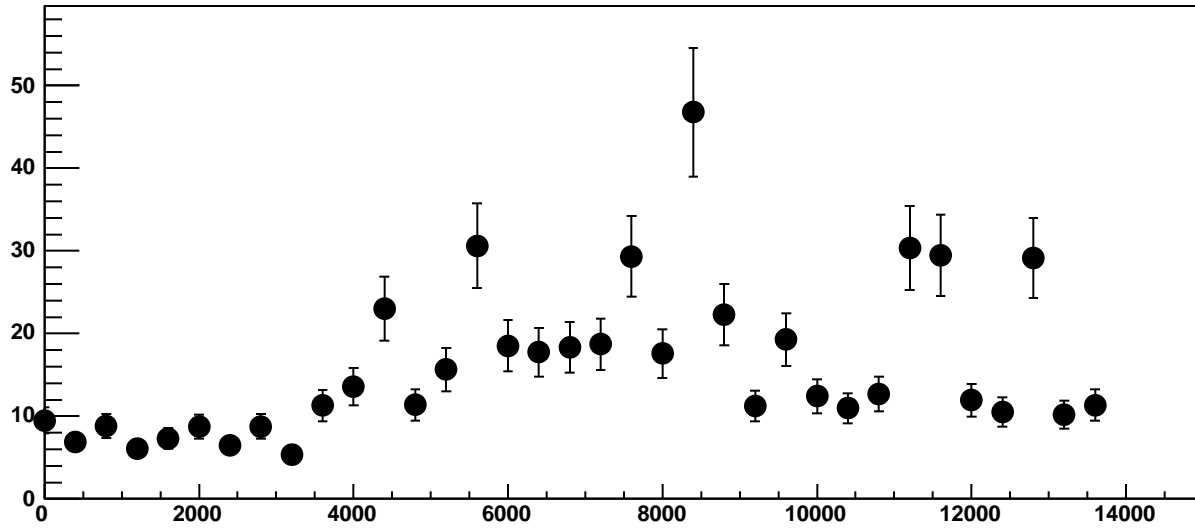


Chip 10, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC

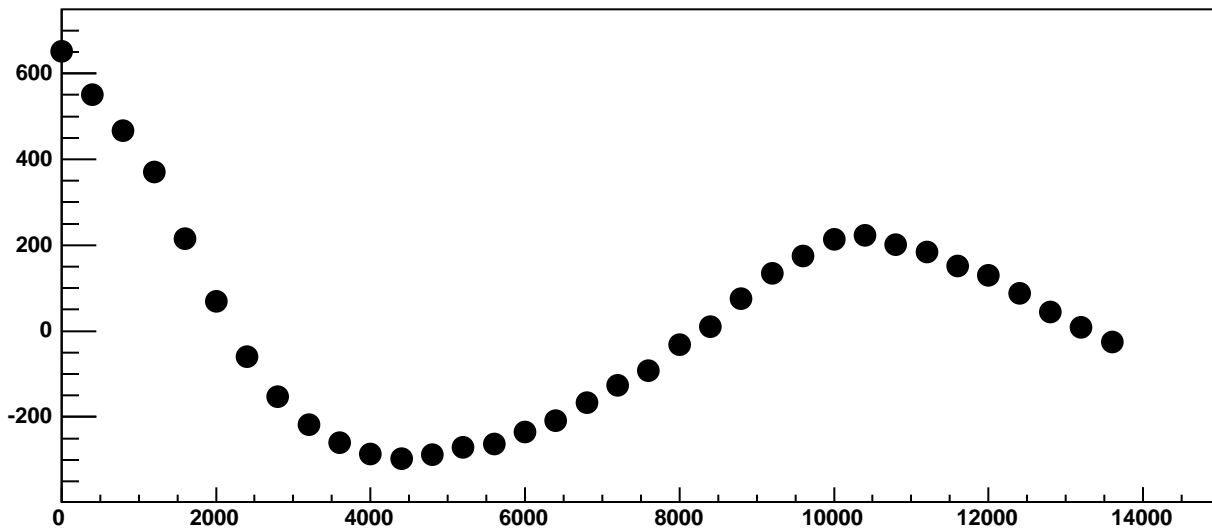


χ^2 / ndf 1.991e+05 / 23
p0 -474.4 ± 0.8614
p1 0.2545 ± 0.0001682

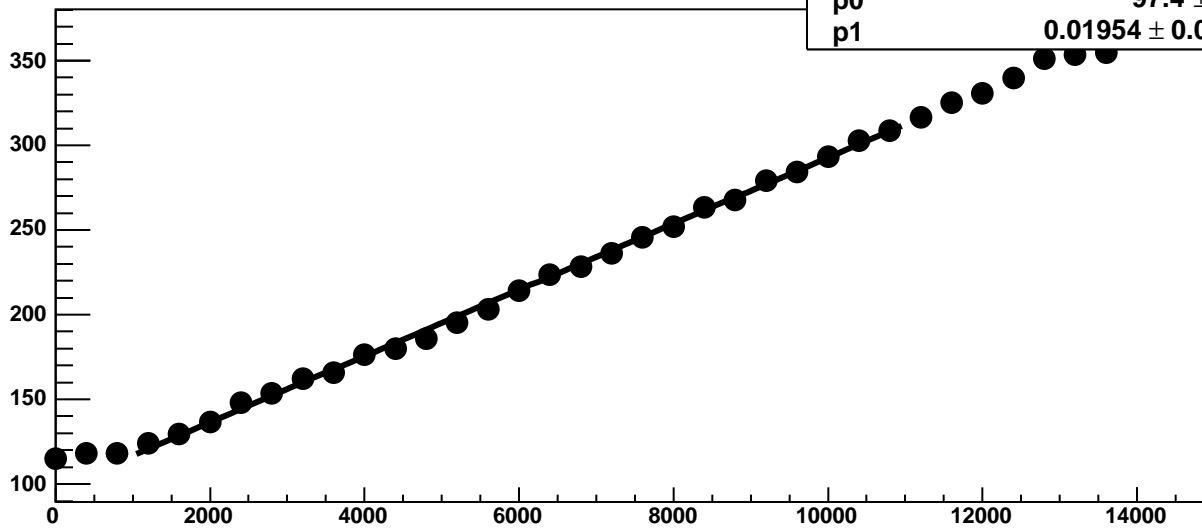
Chip 10, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC

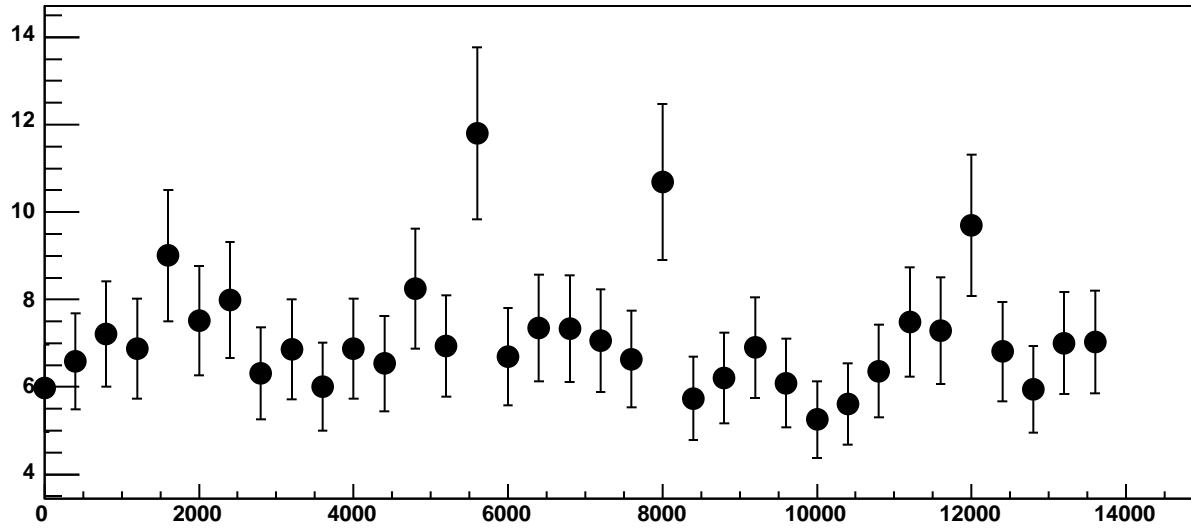


Chip 10, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

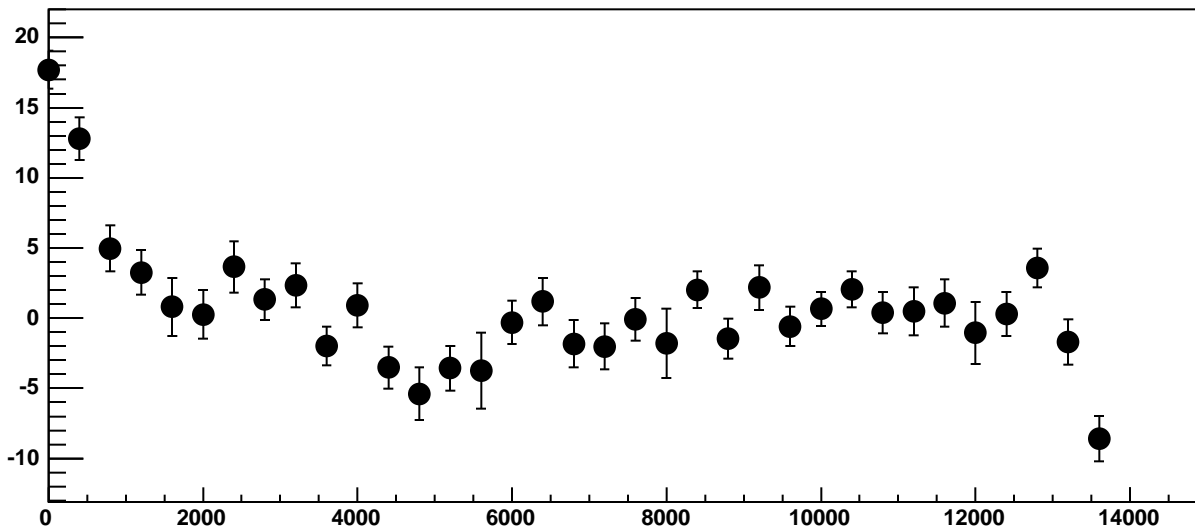


χ^2 / ndf 46.71 / 23
p0 97.4 ± 0.7426
p1 0.01954 ± 0.0001054

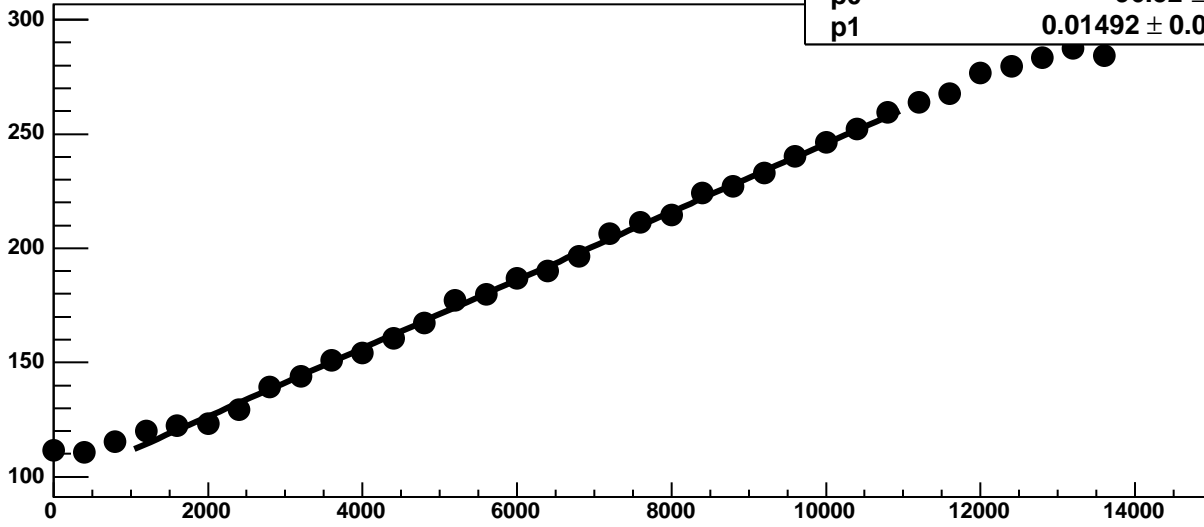
Chip 10, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

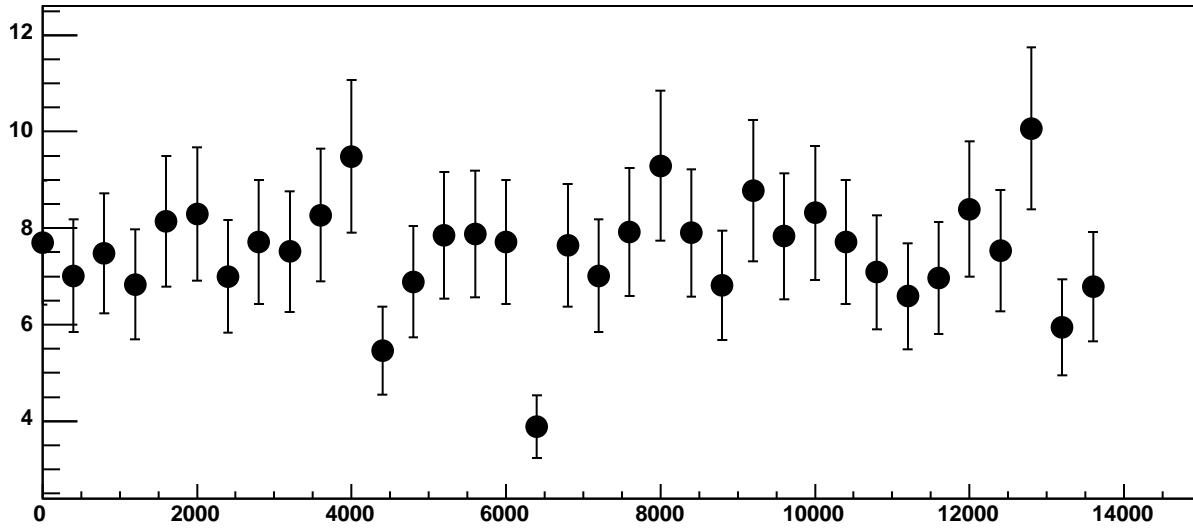


Chip 10, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

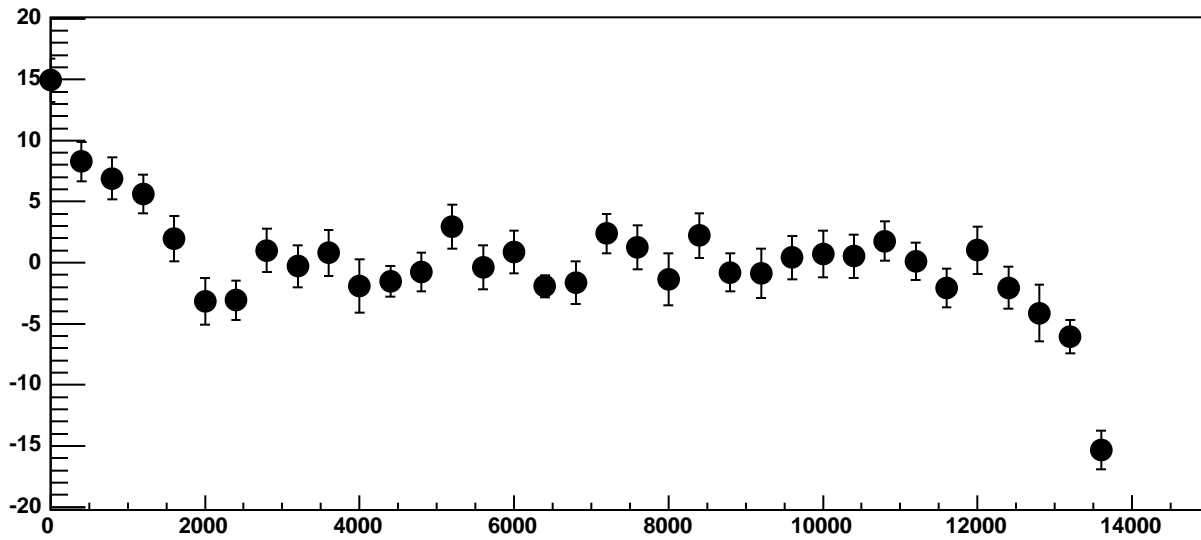


χ^2 / ndf 38.45 / 23
p0 96.52 ± 0.7913
p1 0.01492 ± 0.0001207

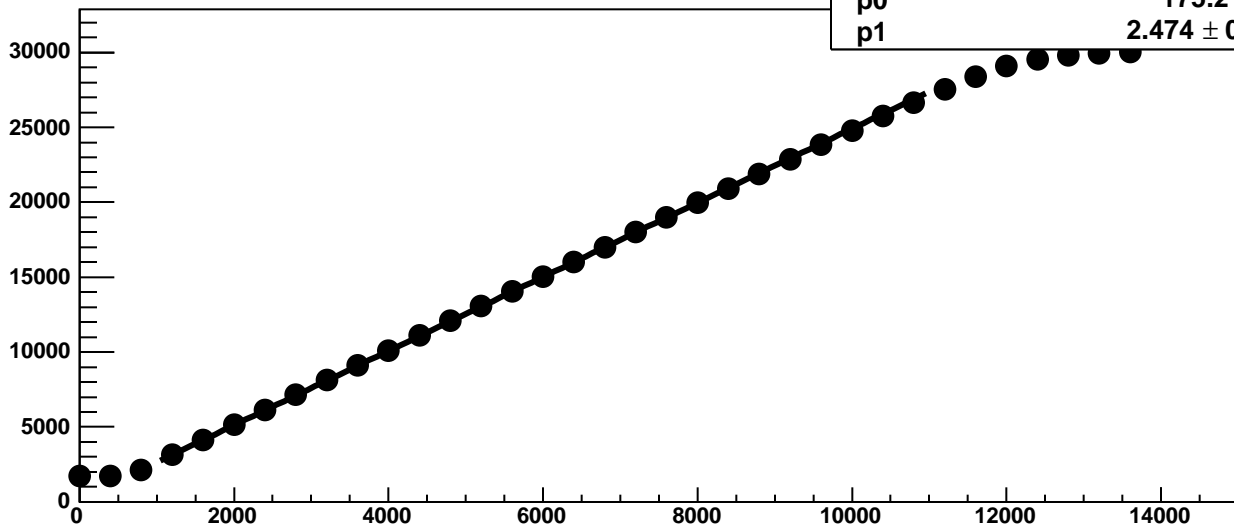
Chip 10, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



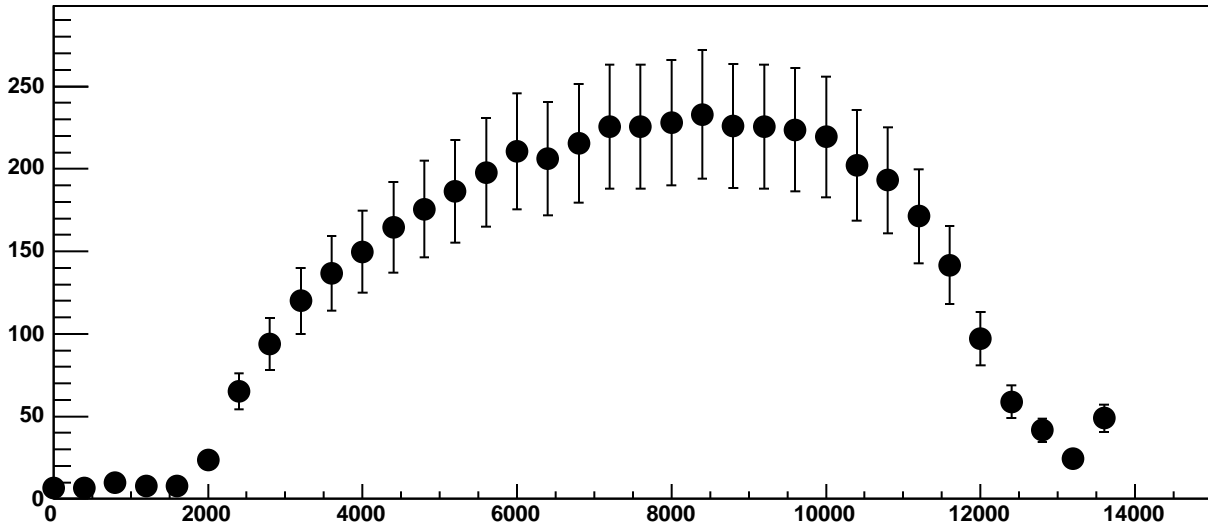
Chip 10, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



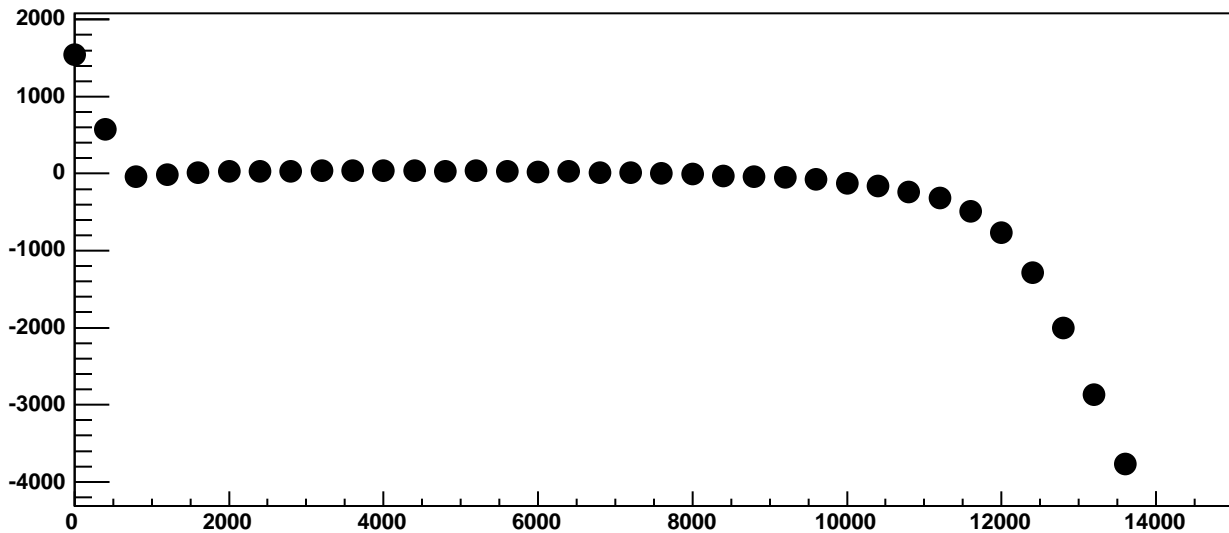
Chip 10, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC



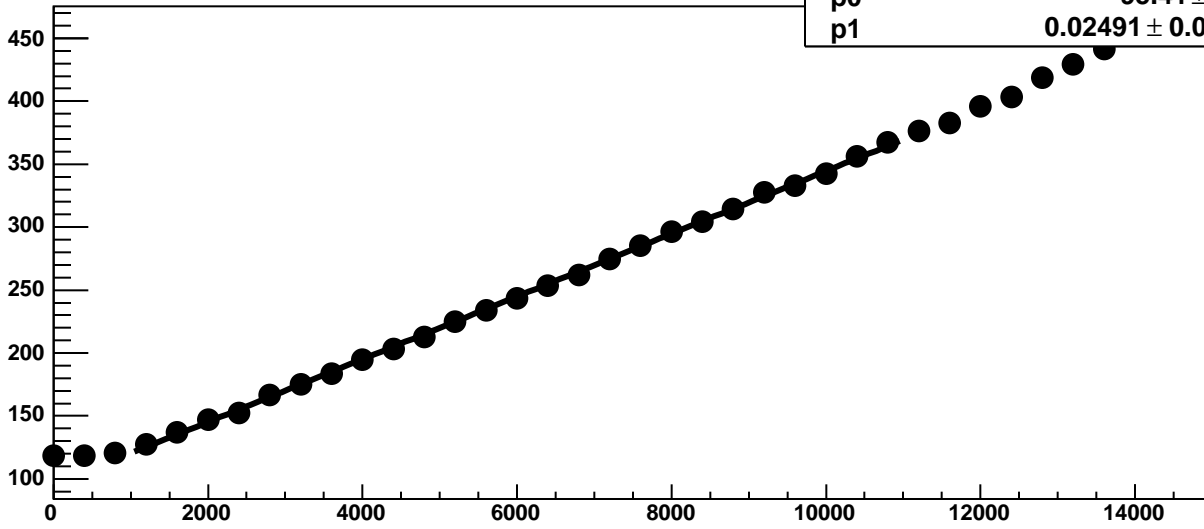
Chip 10, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC

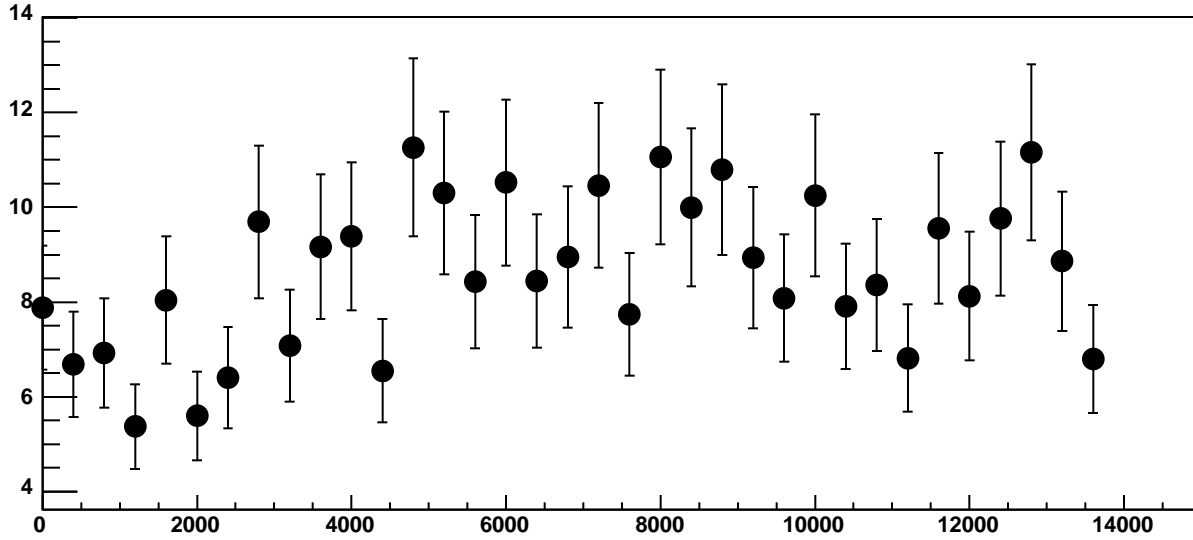


Chip 10, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

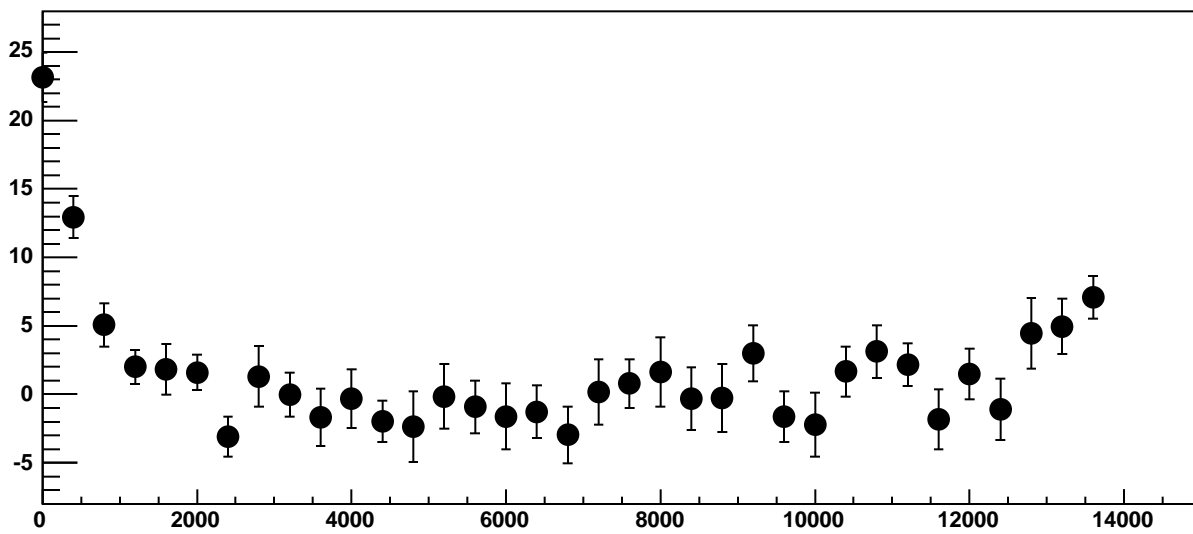


χ^2 / ndf 24.28 / 23
p0 95.41 ± 0.7477
p1 0.02491 ± 0.0001219

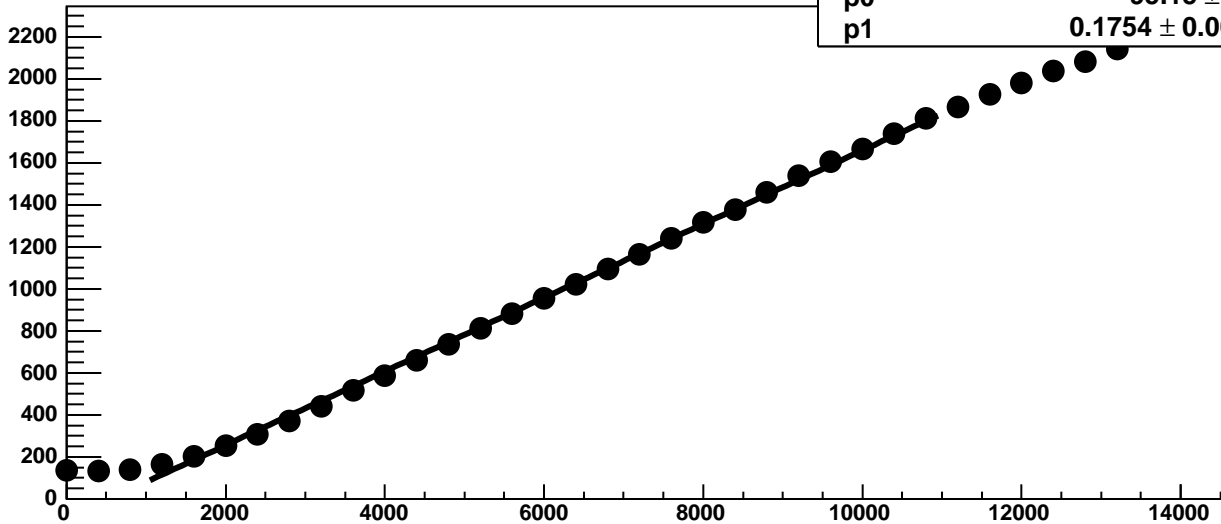
Chip 10, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



Chip 10, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

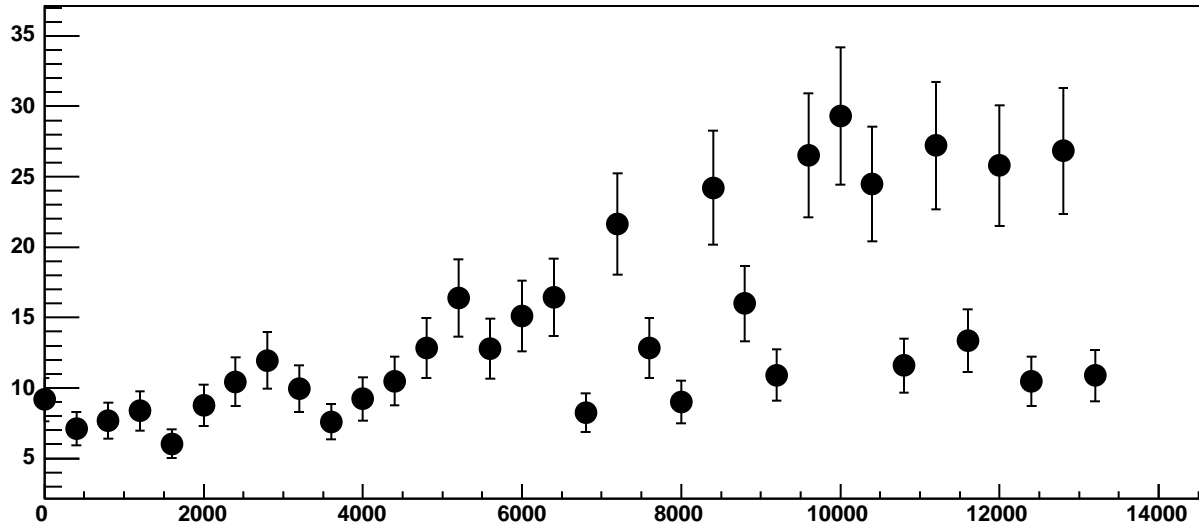


Chip 10, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

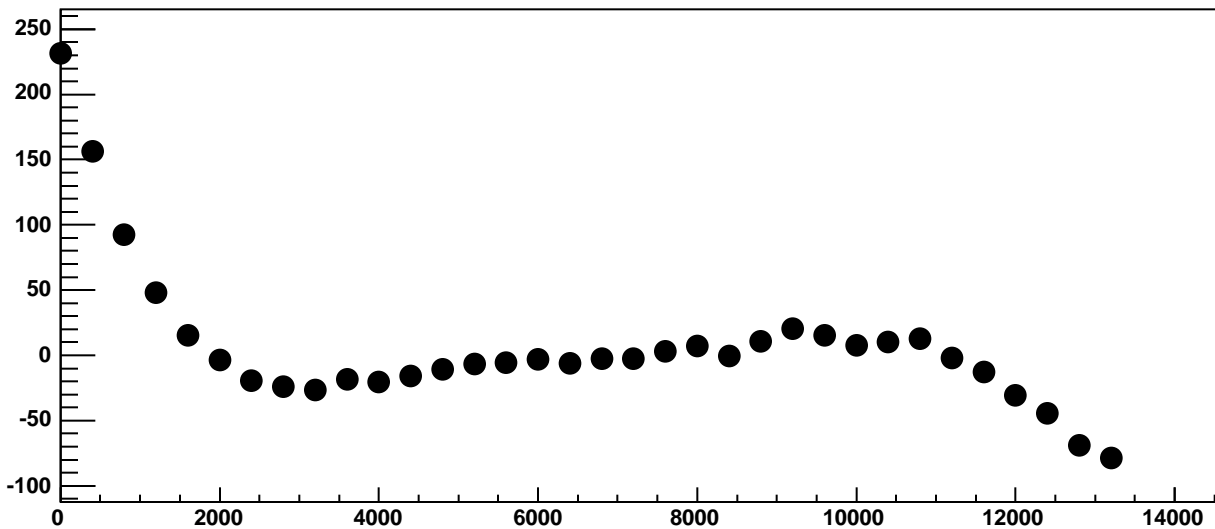


χ^2 / ndf 1436 / 23
p0 -95.15 ± 0.9862
p1 0.1754 ± 0.0001812

Chip 10, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

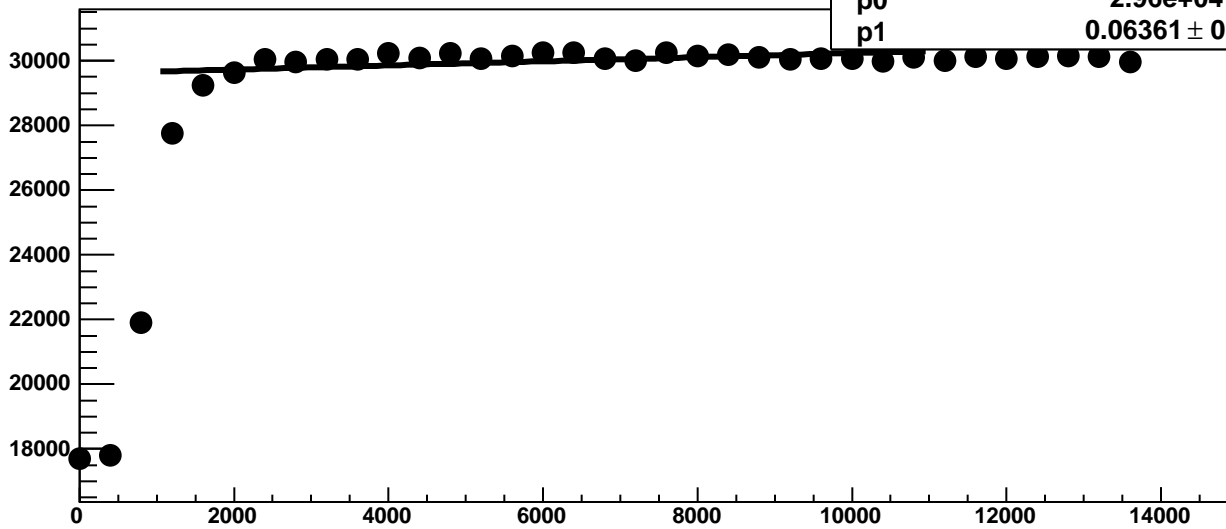
492.3 / 23

p0

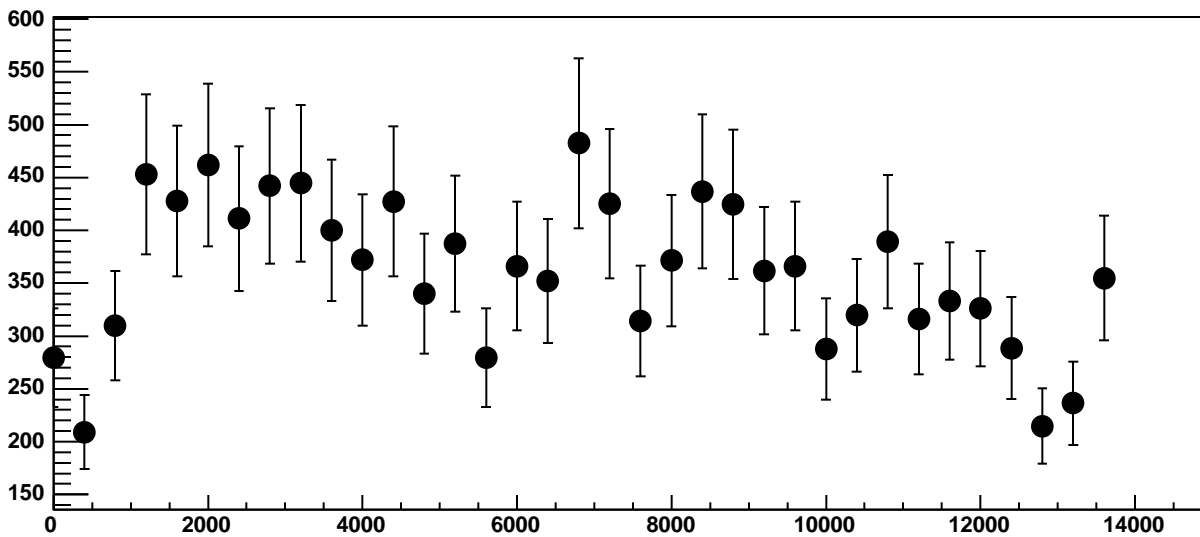
$2.96 \times 10^4 \pm 43.06$

p1

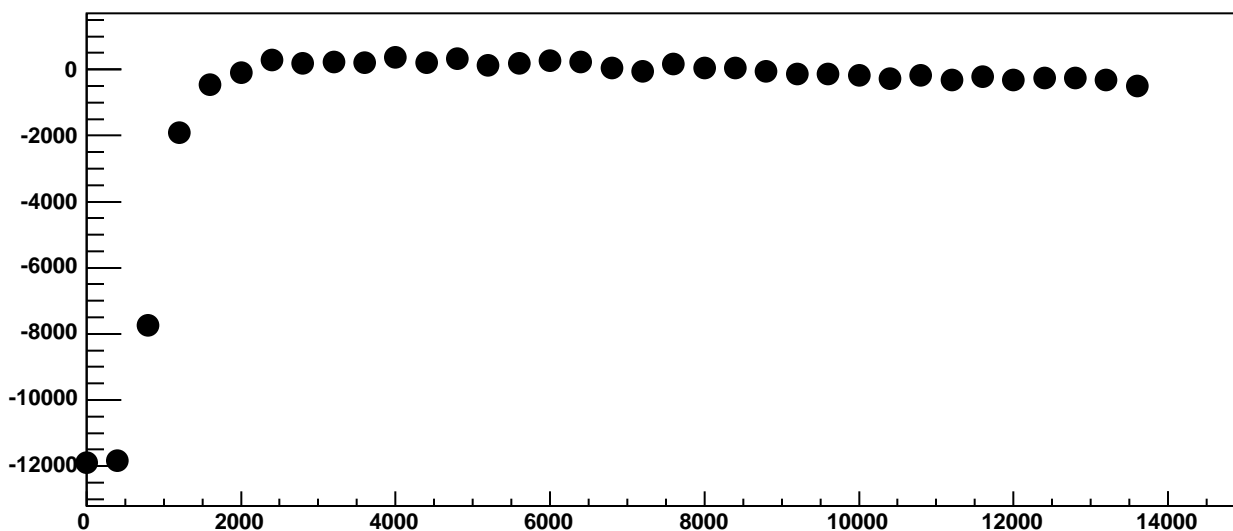
0.06361 ± 0.006155



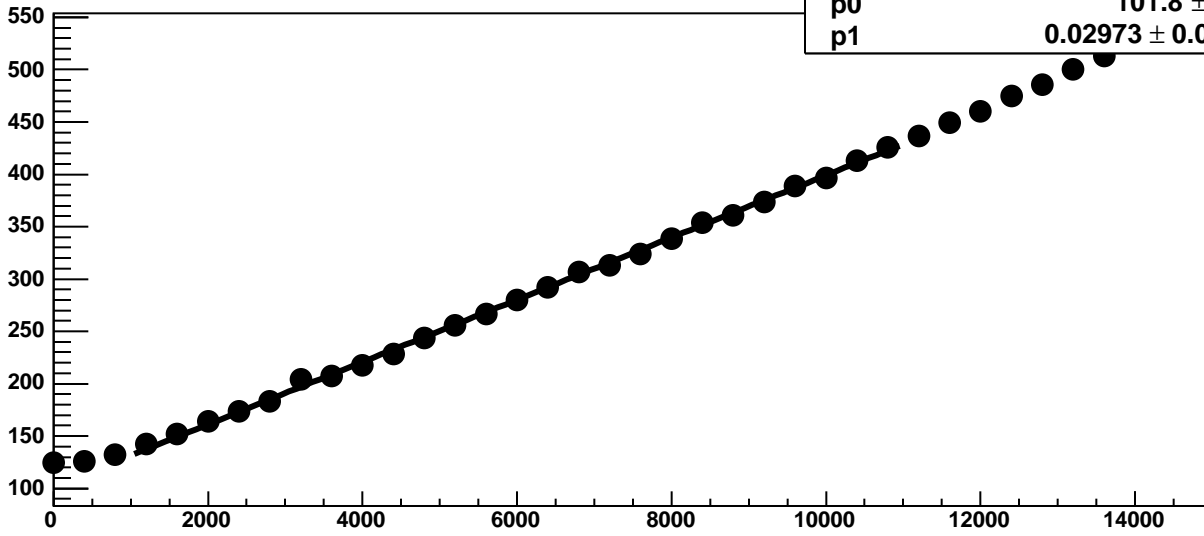
Chip 10, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

64.32 / 23

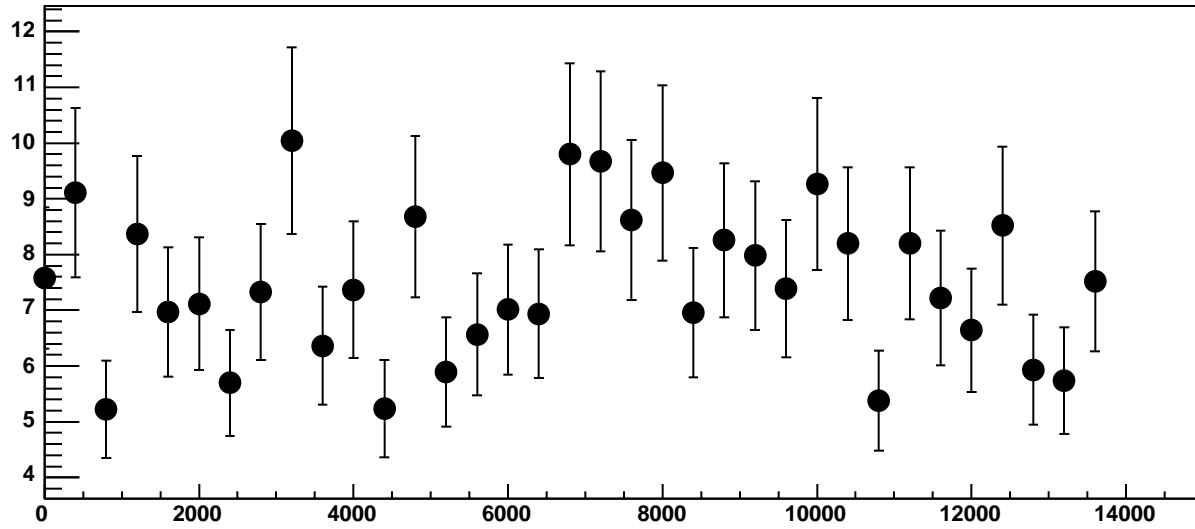
p0

101.8 ± 0.7427

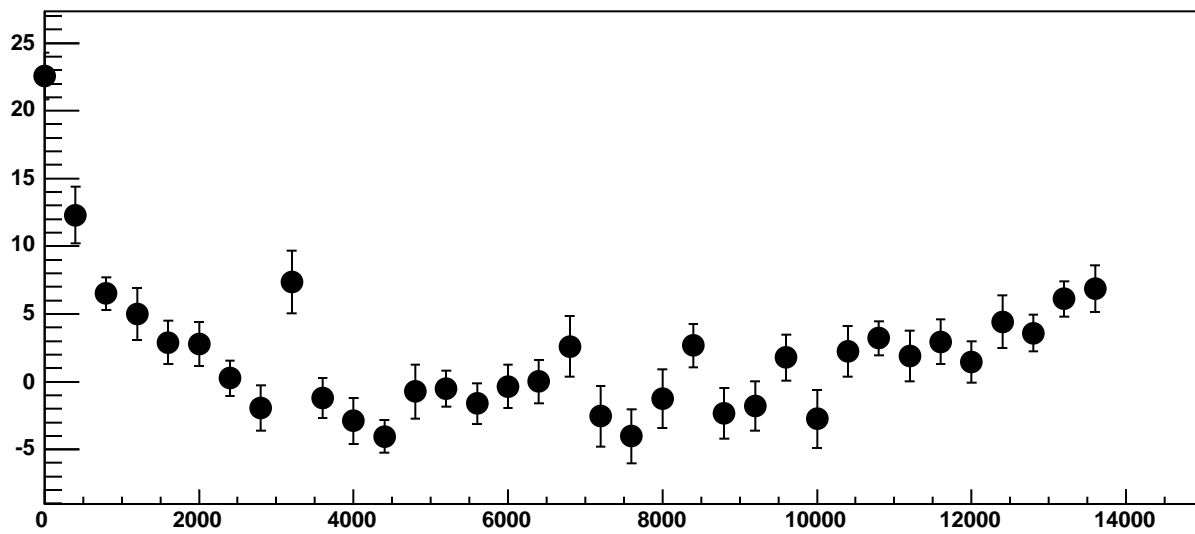
p1

0.02973 ± 0.0001139

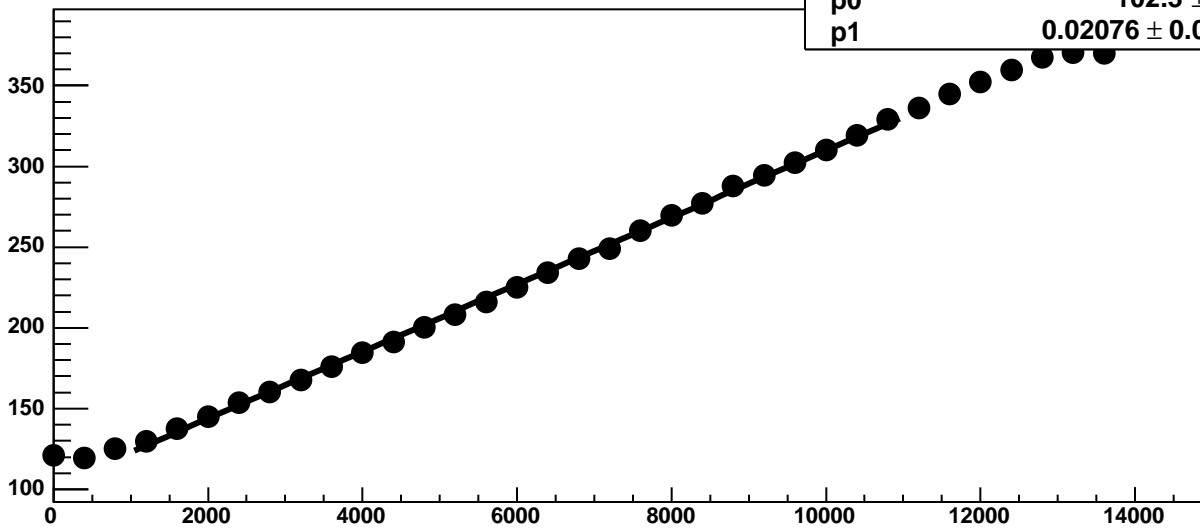
Chip 10, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

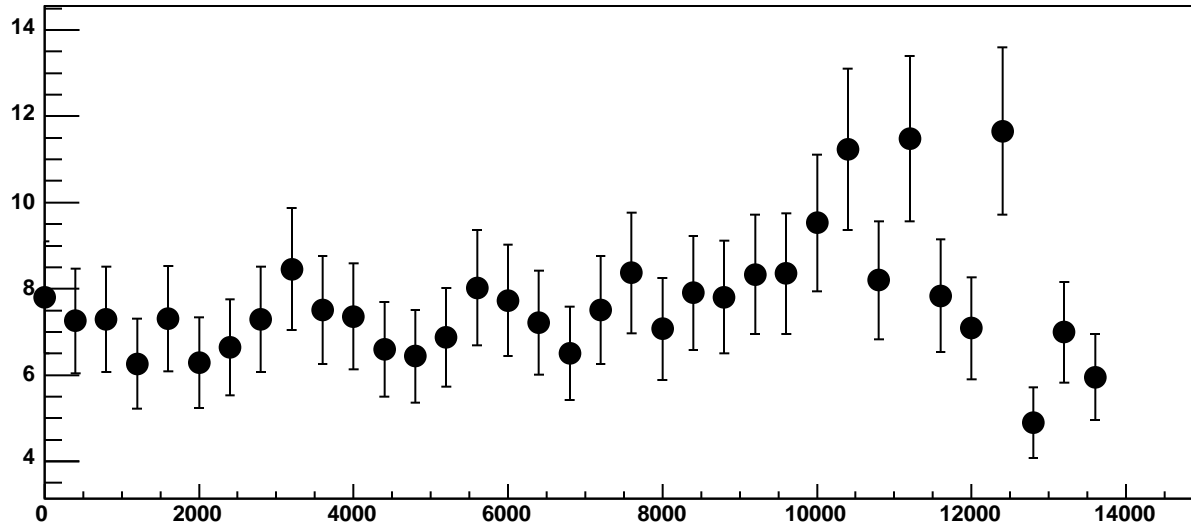


Chip 10, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

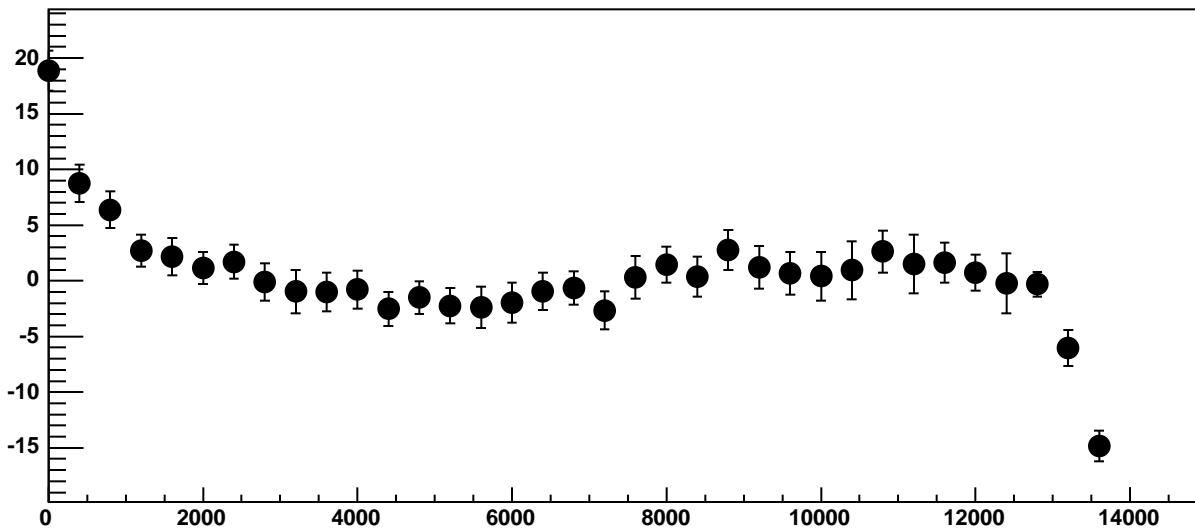


χ^2 / ndf 25.42 / 23
p0 102.3 ± 0.7574
p1 0.02076 ± 0.0001222

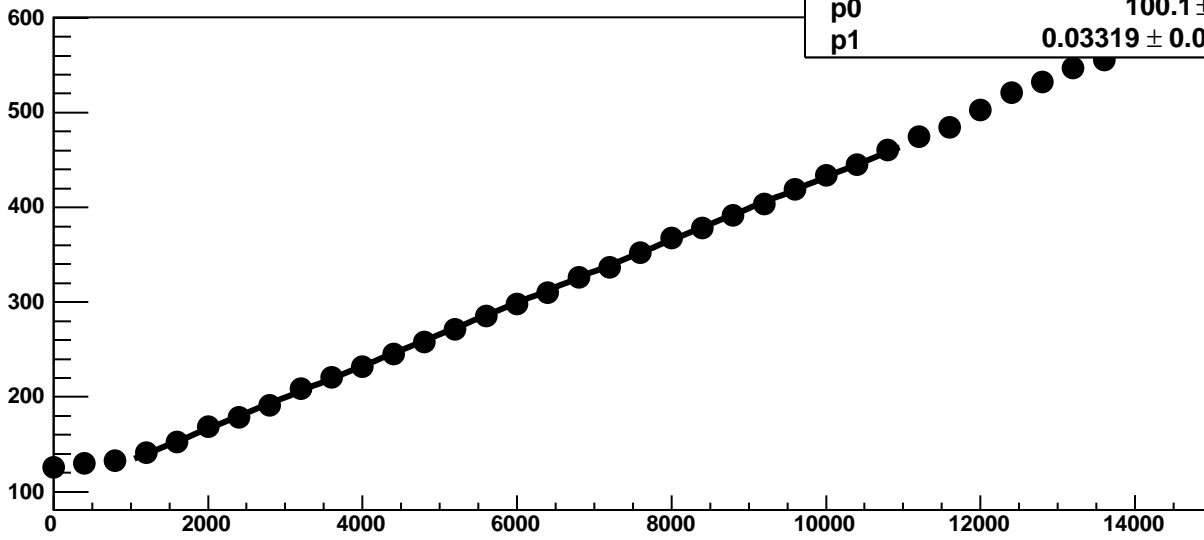
Chip 10, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

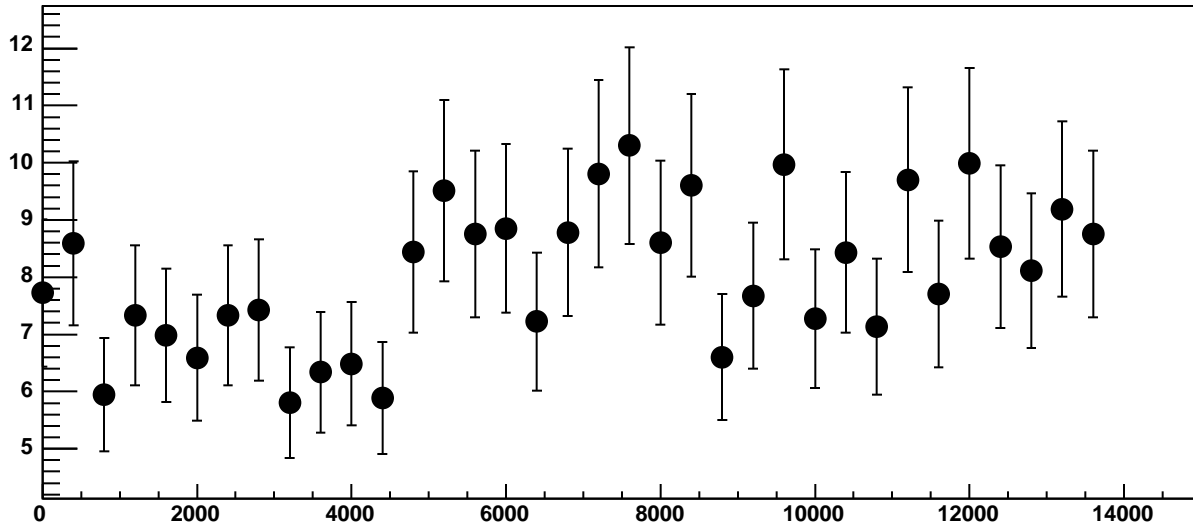


Chip 10, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

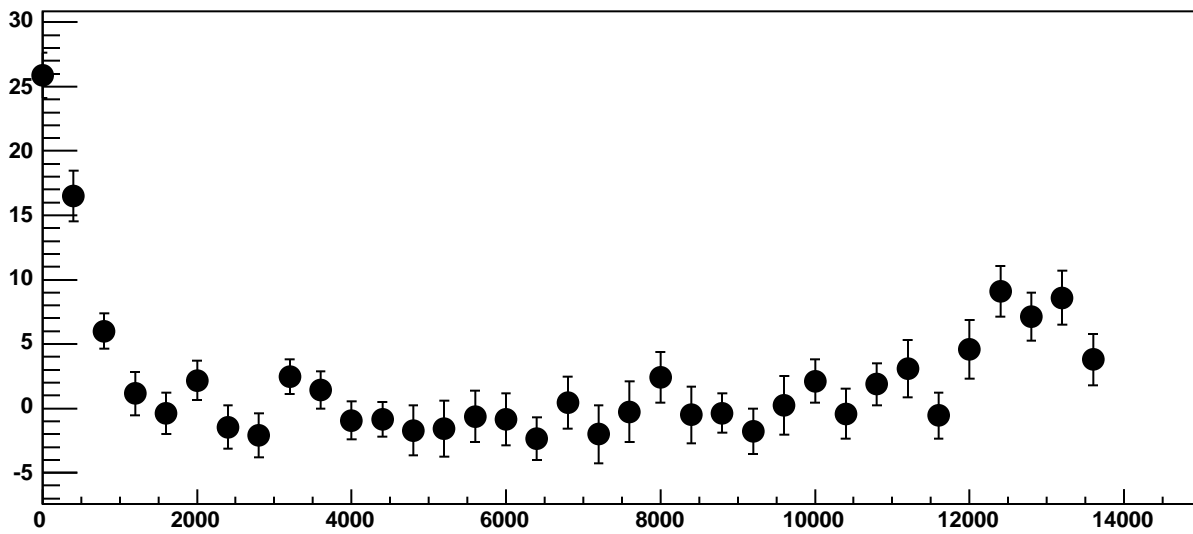


χ^2 / ndf 20.09 / 23
p0 100.1 ± 0.7441
p1 0.03319 ± 0.0001177

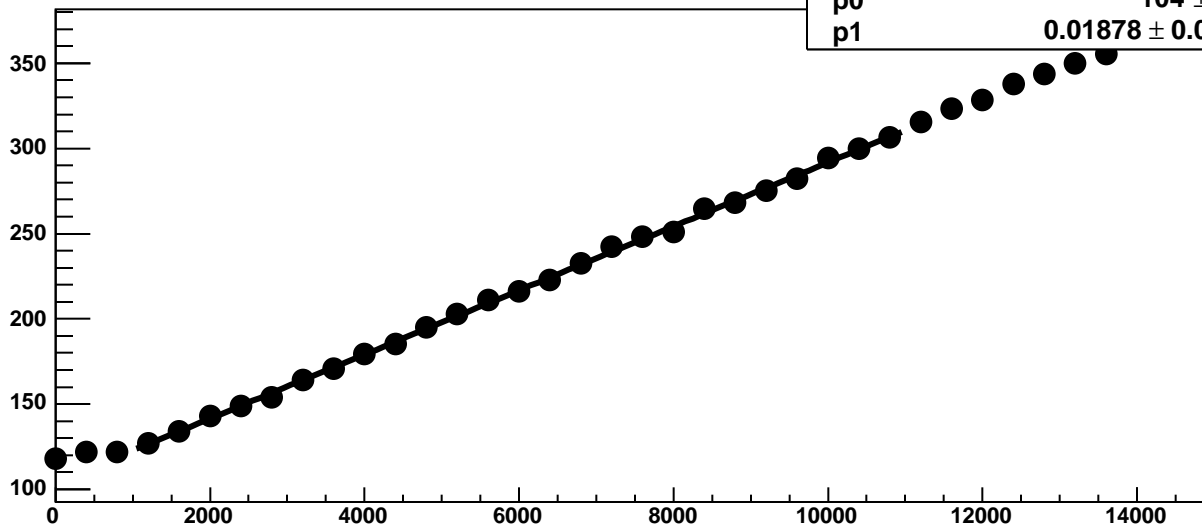
Chip 10, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

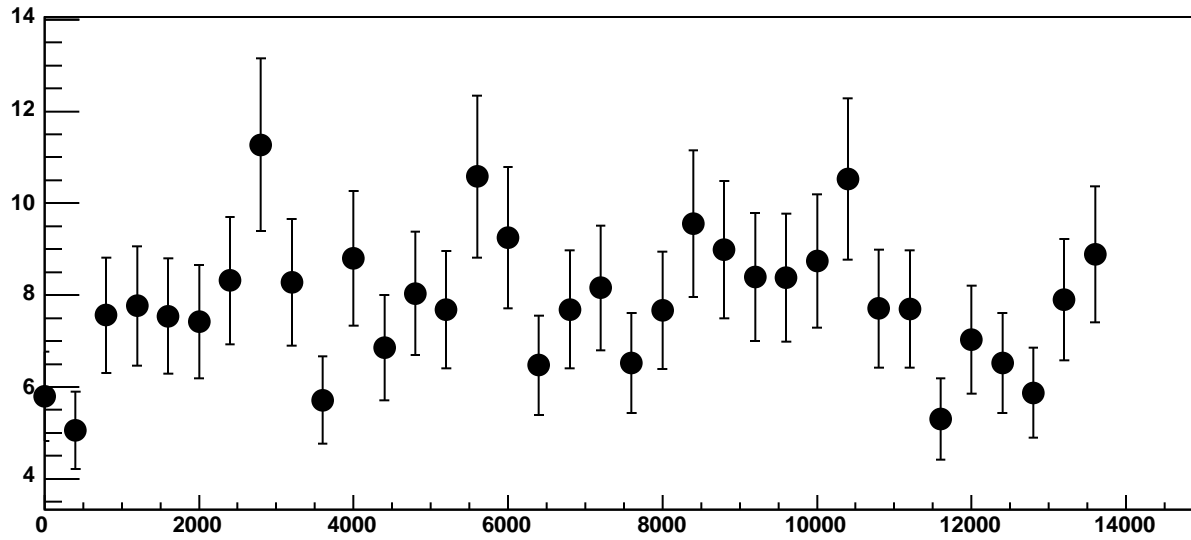


Chip 10, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

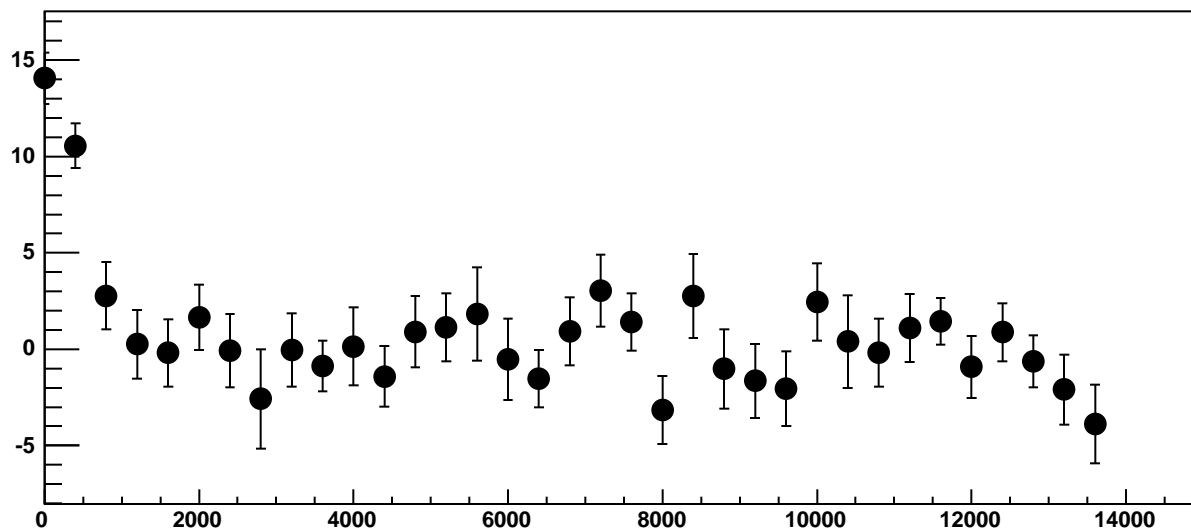


χ^2 / ndf 17.76 / 23
p0 104 ± 0.8384
p1 0.01878 ± 0.0001298

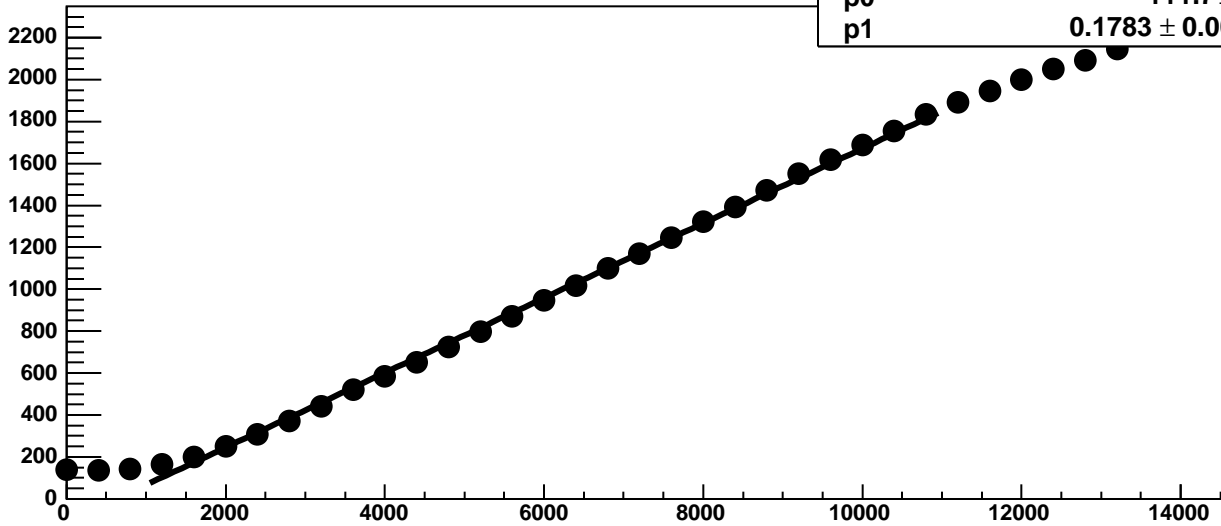
Chip 10, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 10, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1416 / 23

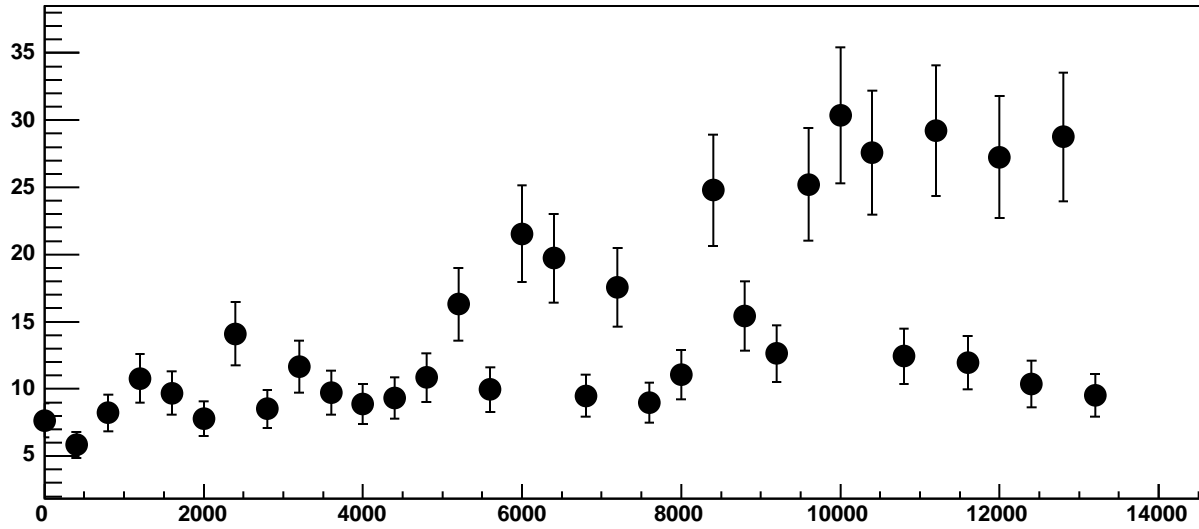
p0

-111.7 ± 1.137

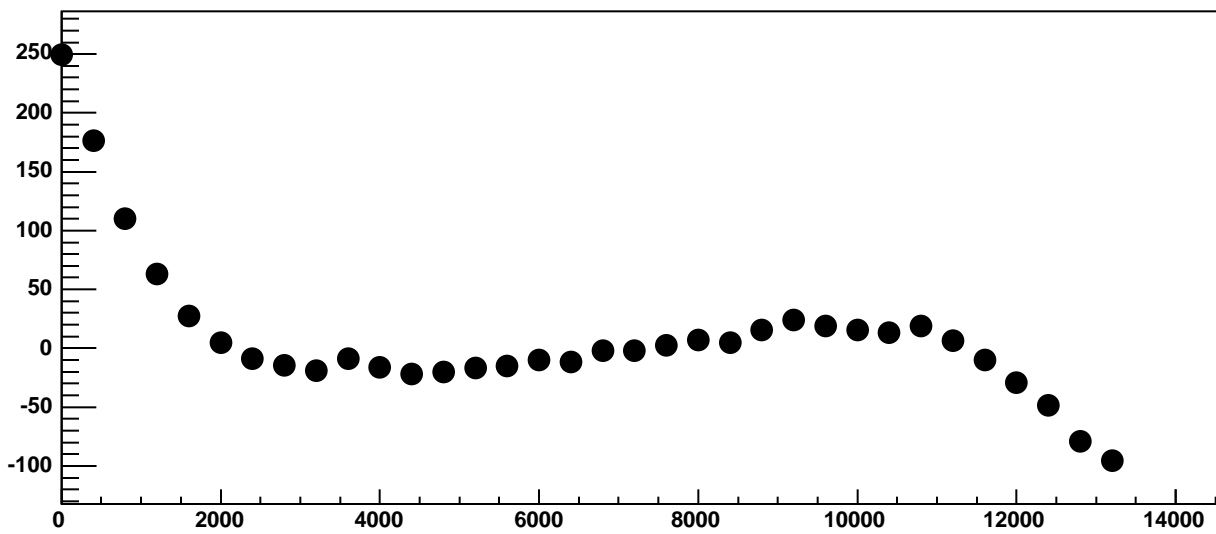
p1

0.1783 ± 0.0002024

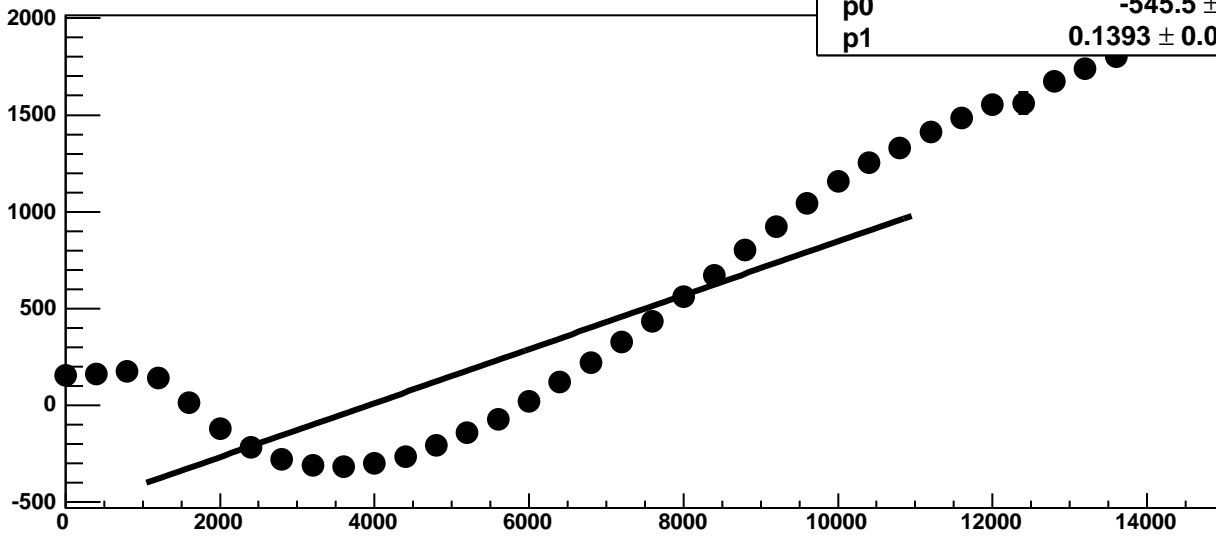
Chip 10, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



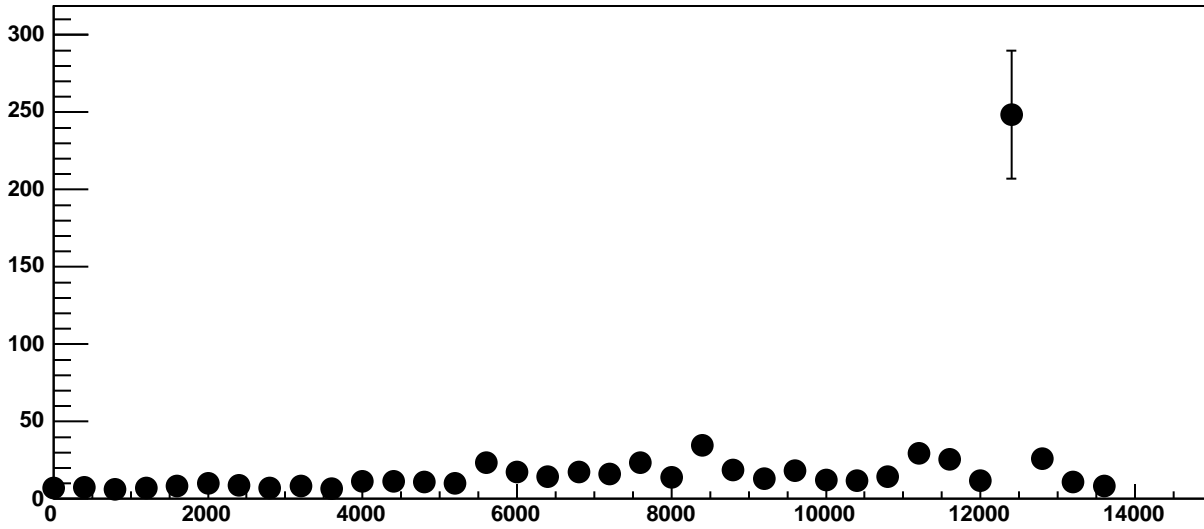
Chip 10, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



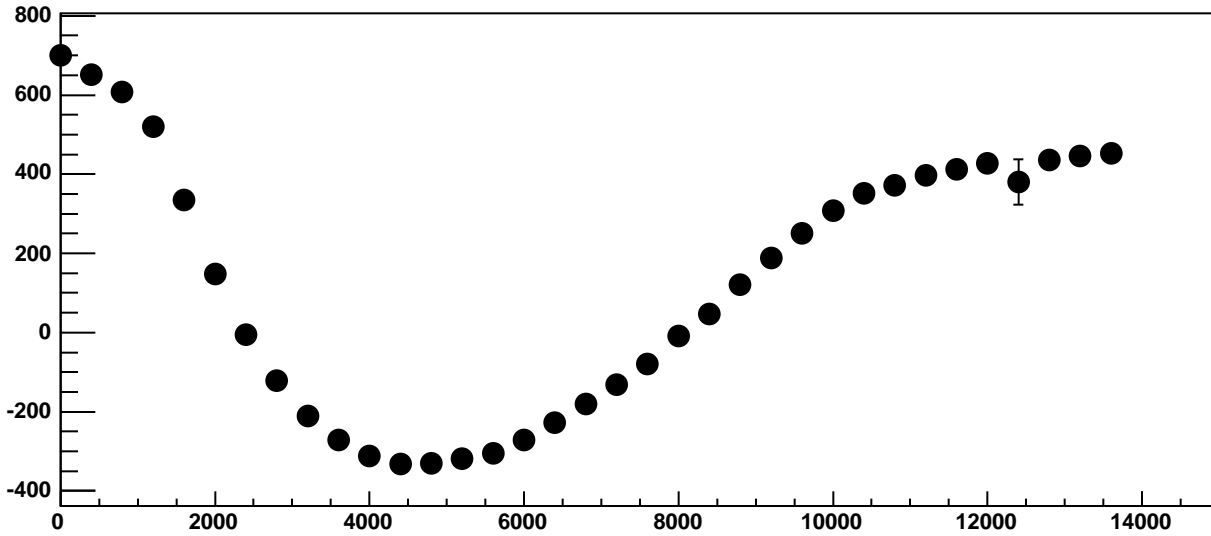
Chip 10, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



Chip 10, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

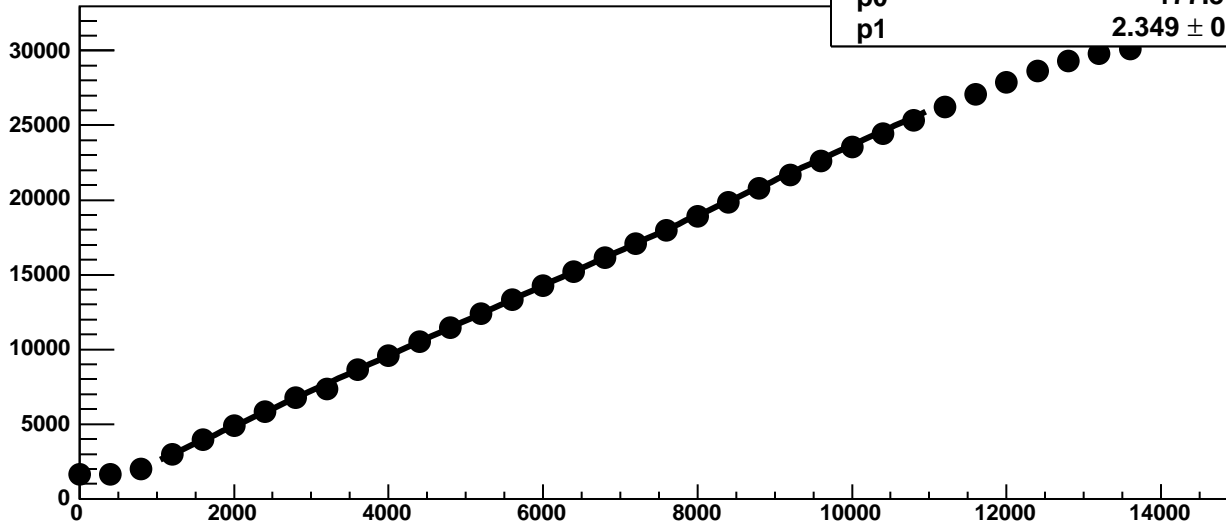


Chip 10, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

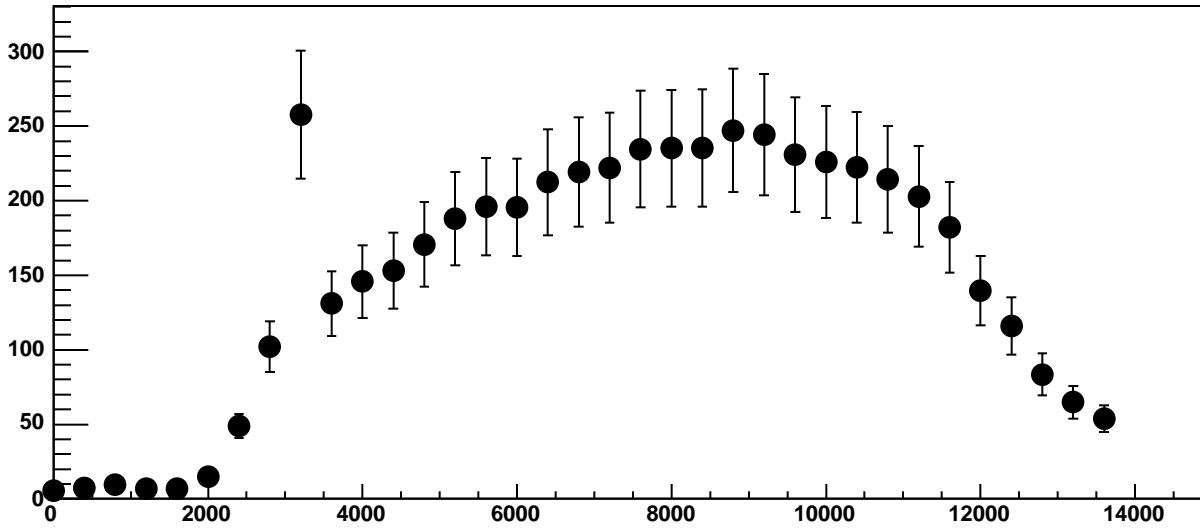


Chip 10, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC

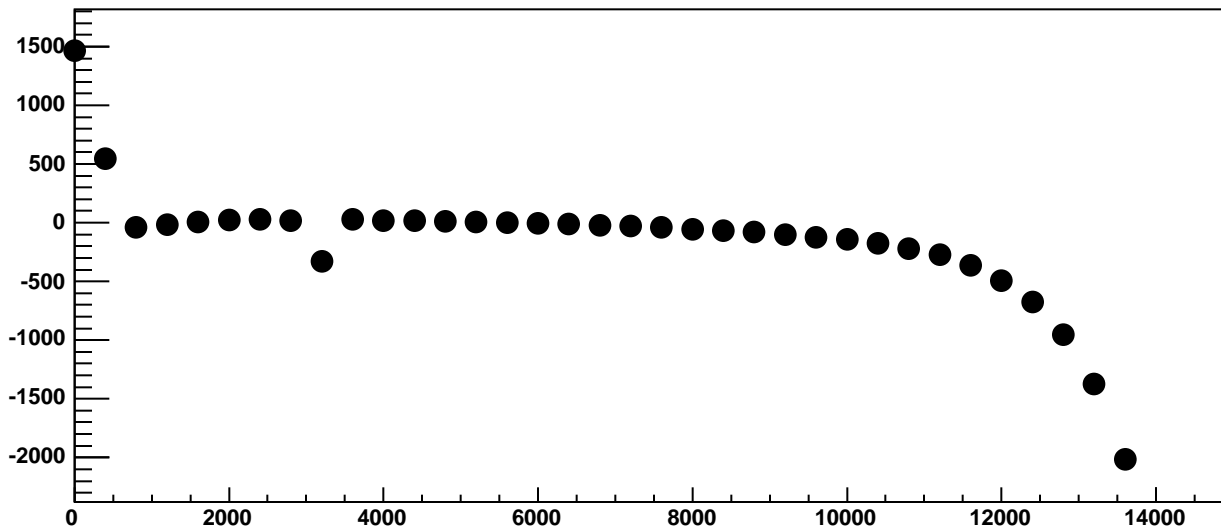
χ^2 / ndf 251.4 / 23
p0 177.3 ± 2.809
p1 2.349 ± 0.001696



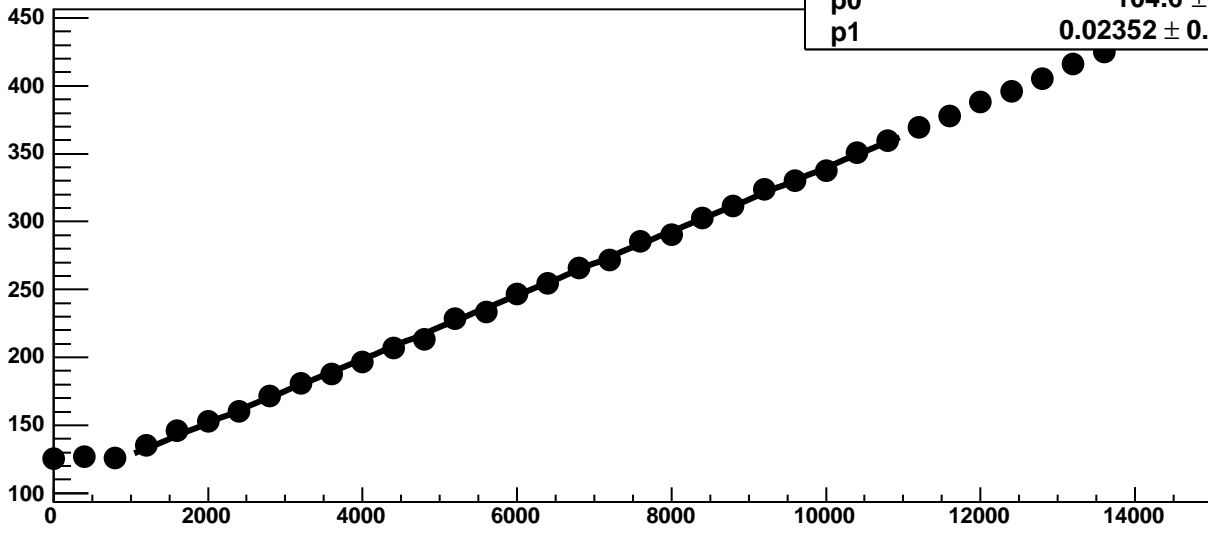
Chip 10, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC

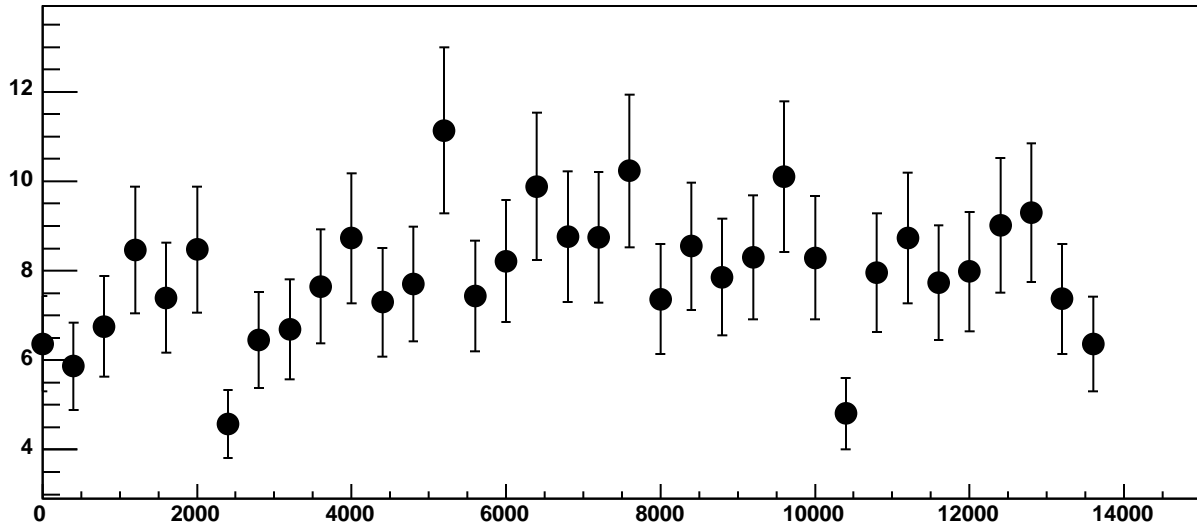


Chip 10, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

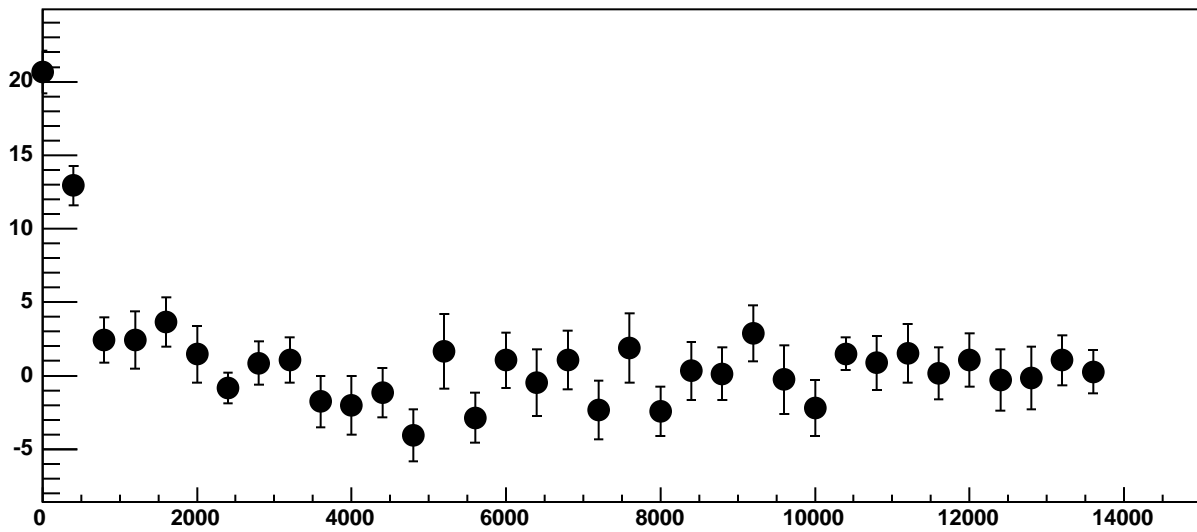


χ^2 / ndf 29.61 / 23
p0 104.6 ± 0.7379
p1 0.02352 ± 0.000112

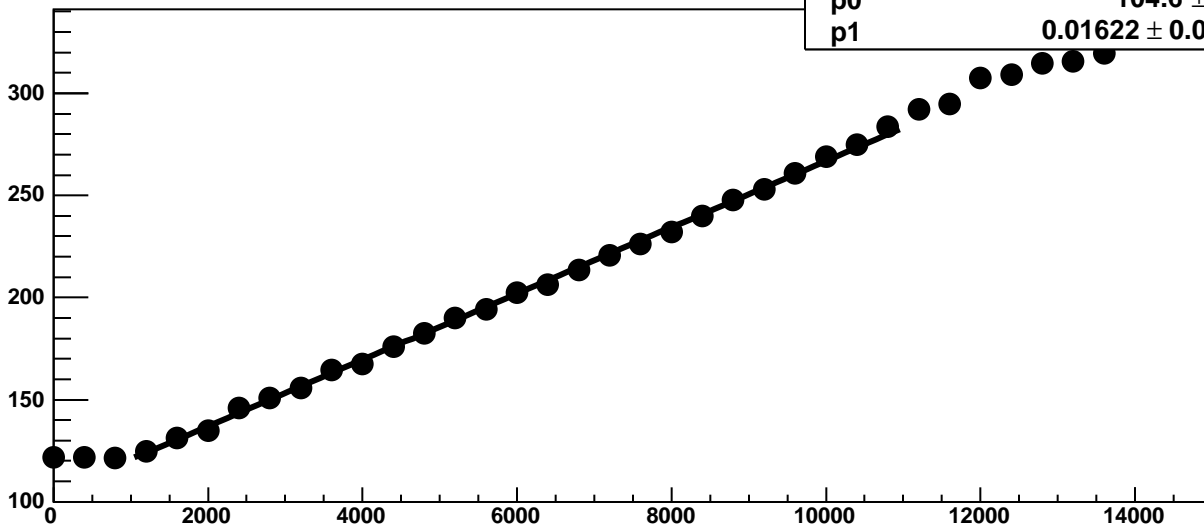
Chip 10, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

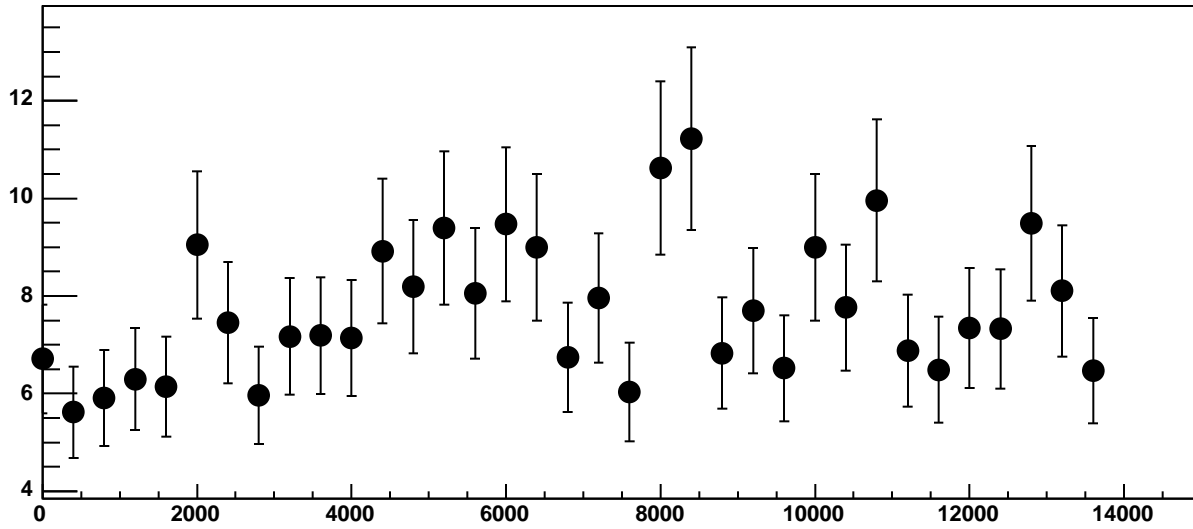


Chip 10, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

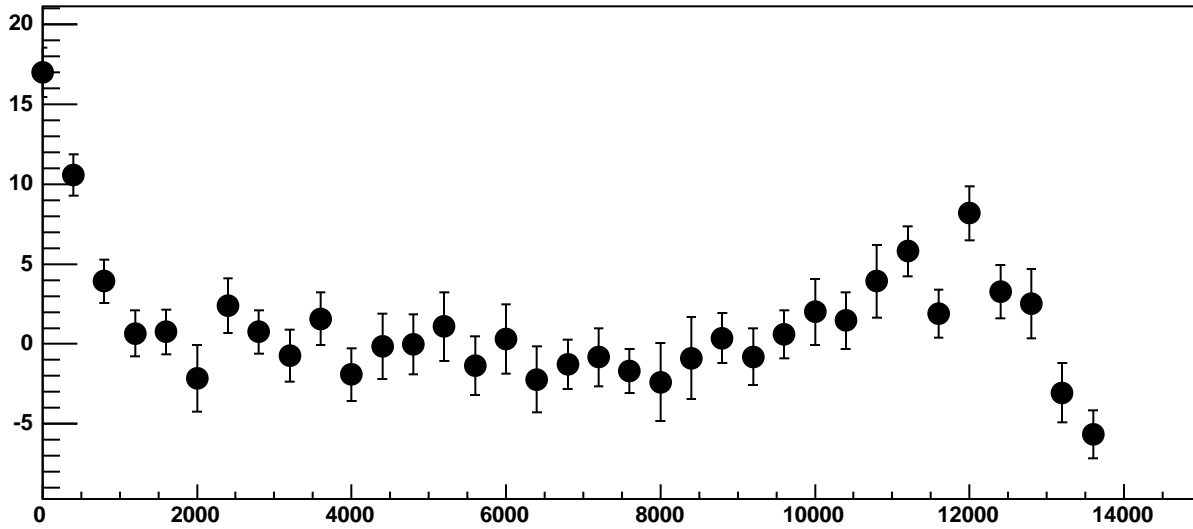


χ^2 / ndf 16.77 / 23
p0 104.6 ± 0.7567
p1 0.01622 ± 0.0001184

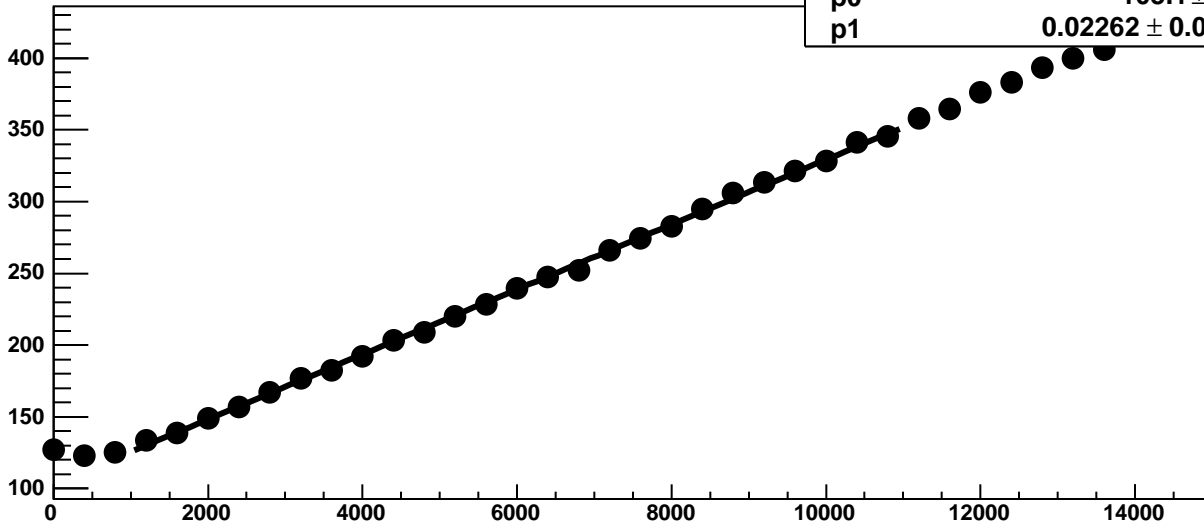
Chip 10, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

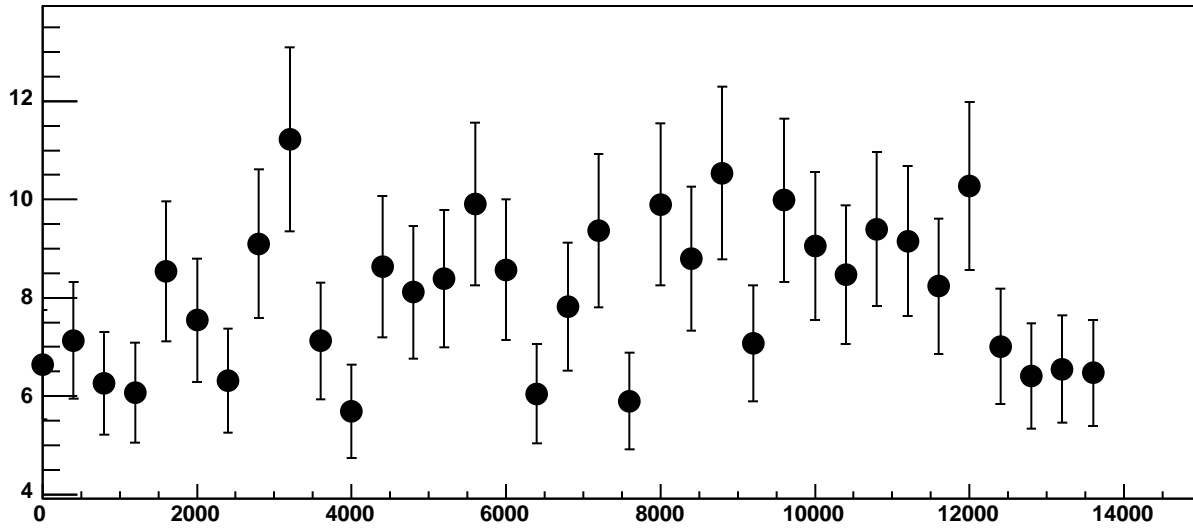


Chip 10, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

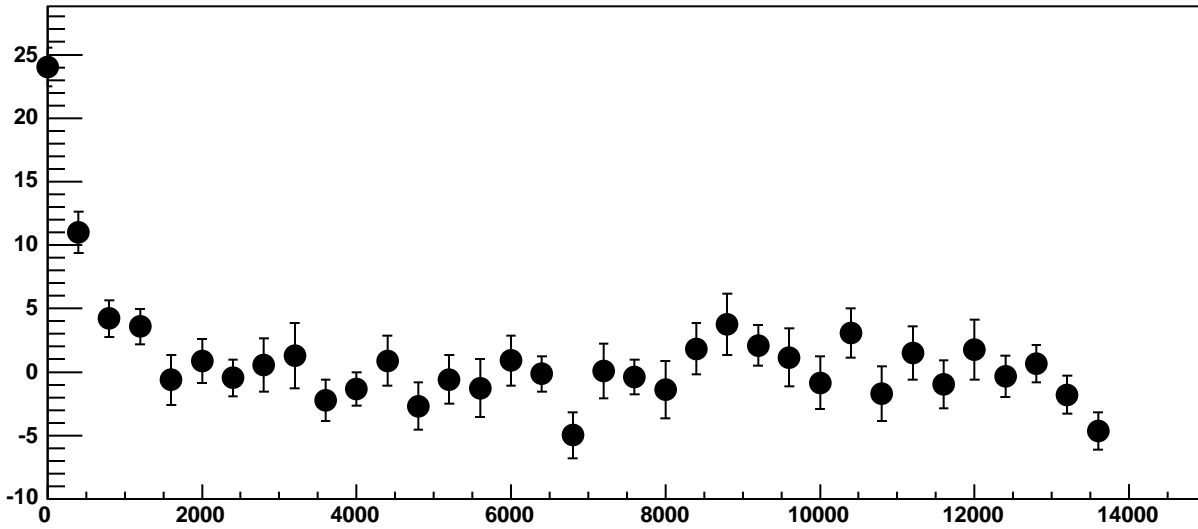


χ^2 / ndf 29.85 / 23
p0 103.1 ± 0.7986
p1 0.02262 ± 0.0001266

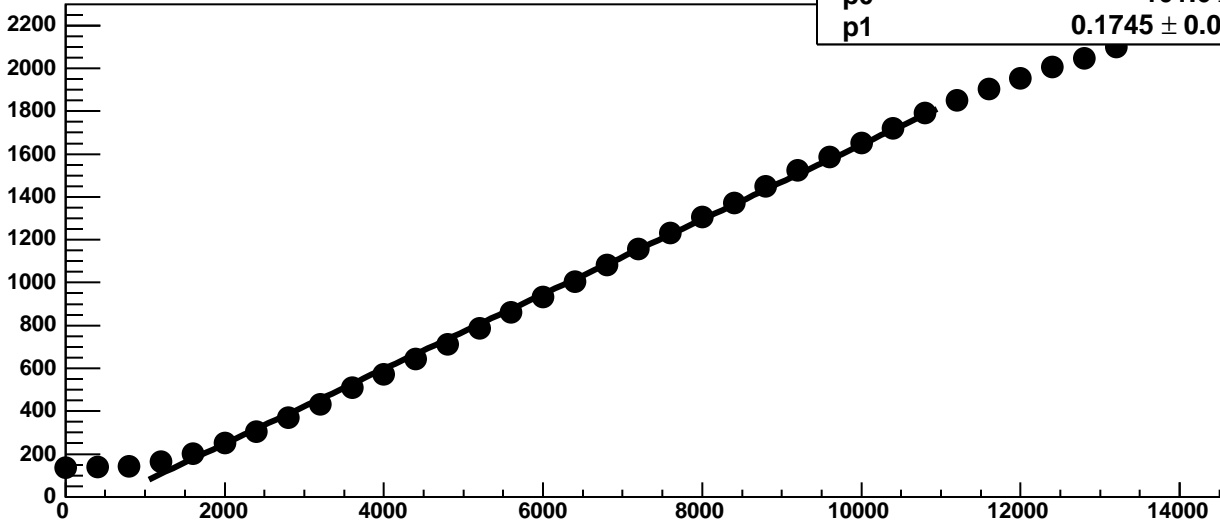
Chip 10, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



Chip 10, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1925 / 23

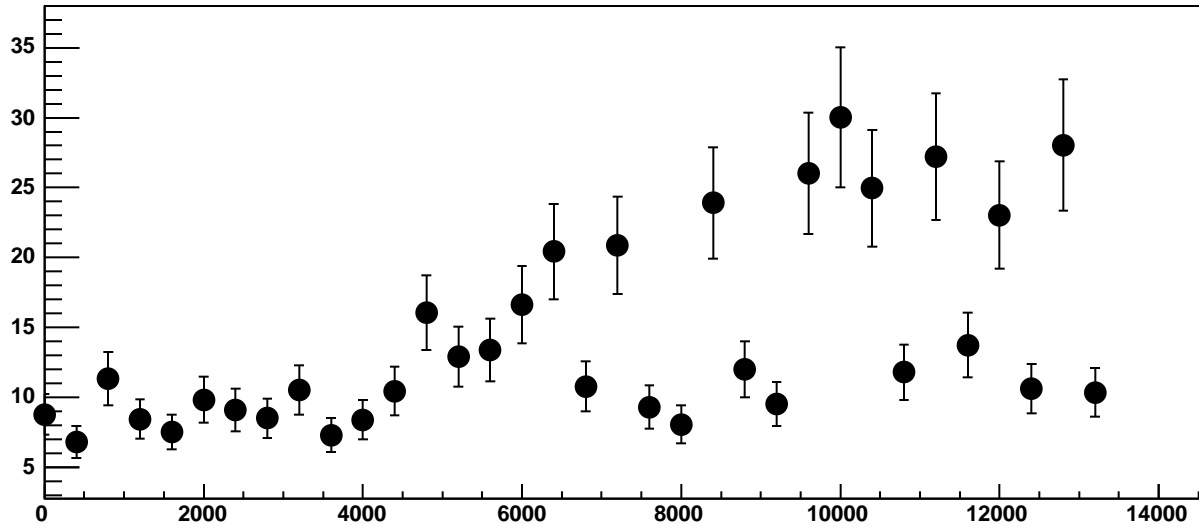
p0

-101.6 ± 1.007

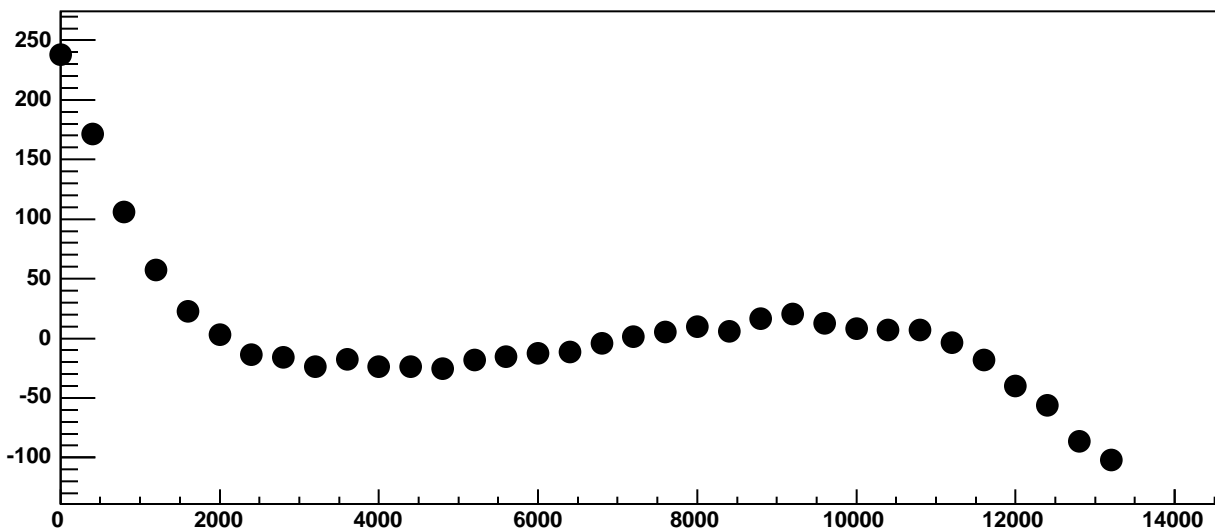
p1

0.1745 ± 0.0001781

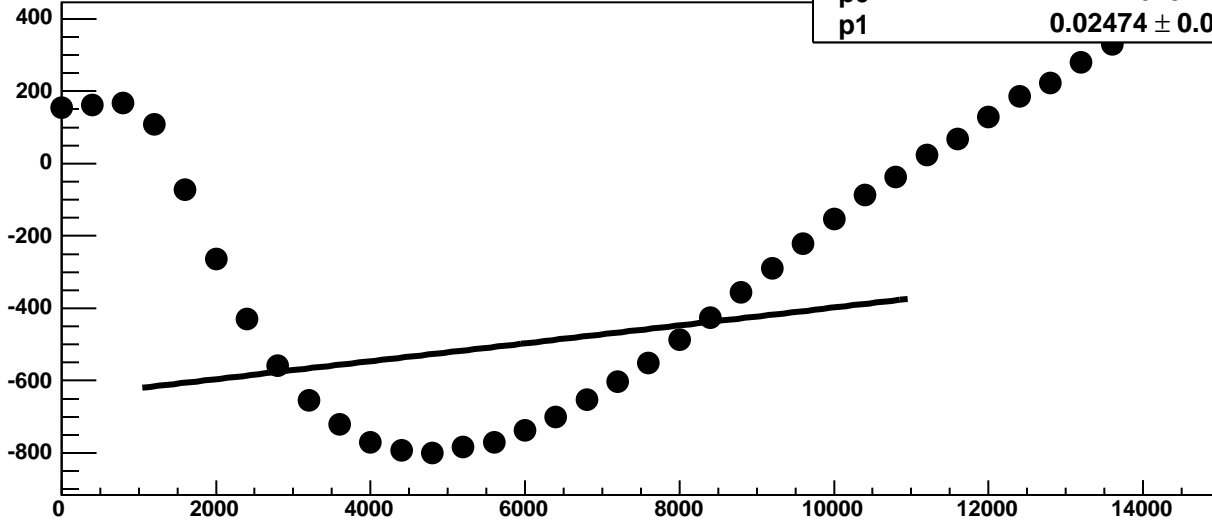
Chip 10, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

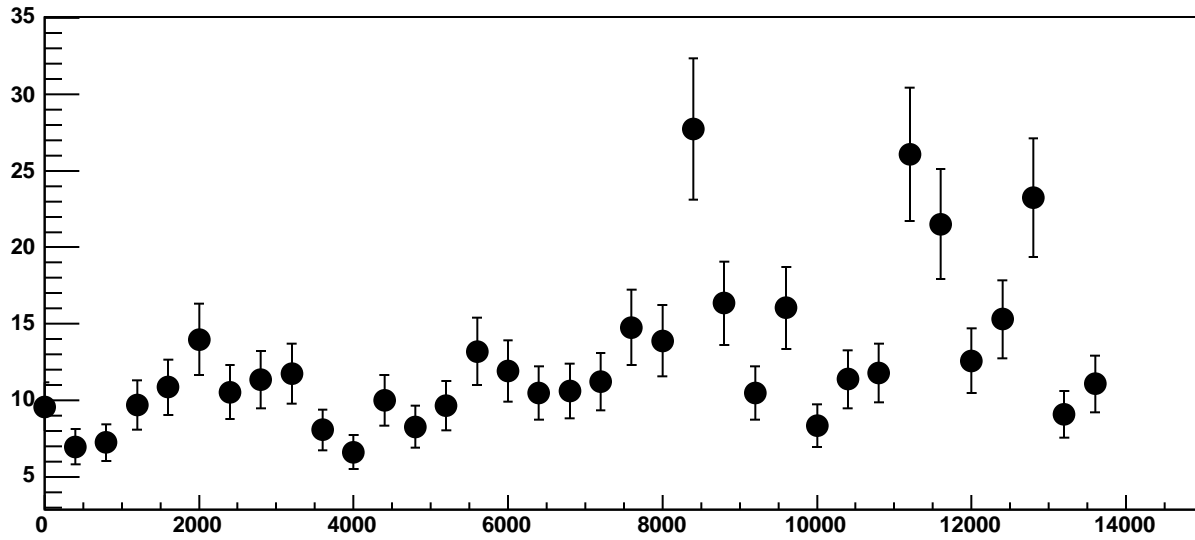


Chip 10, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

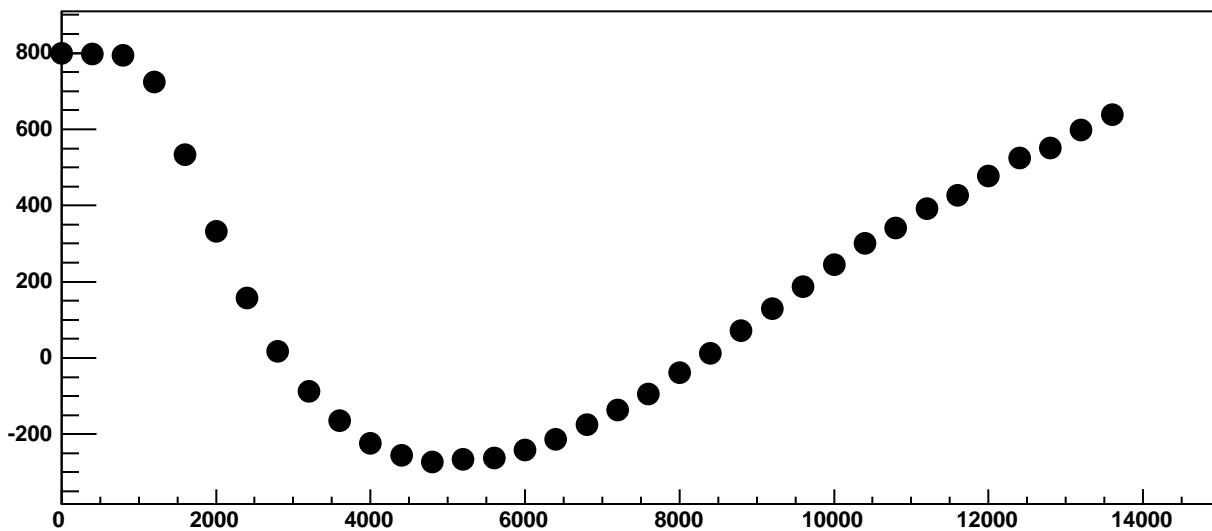


χ^2 / ndf 3.294e+05 / 23
p0 -645.4 ± 1.092
p1 0.02474 ± 0.0001746

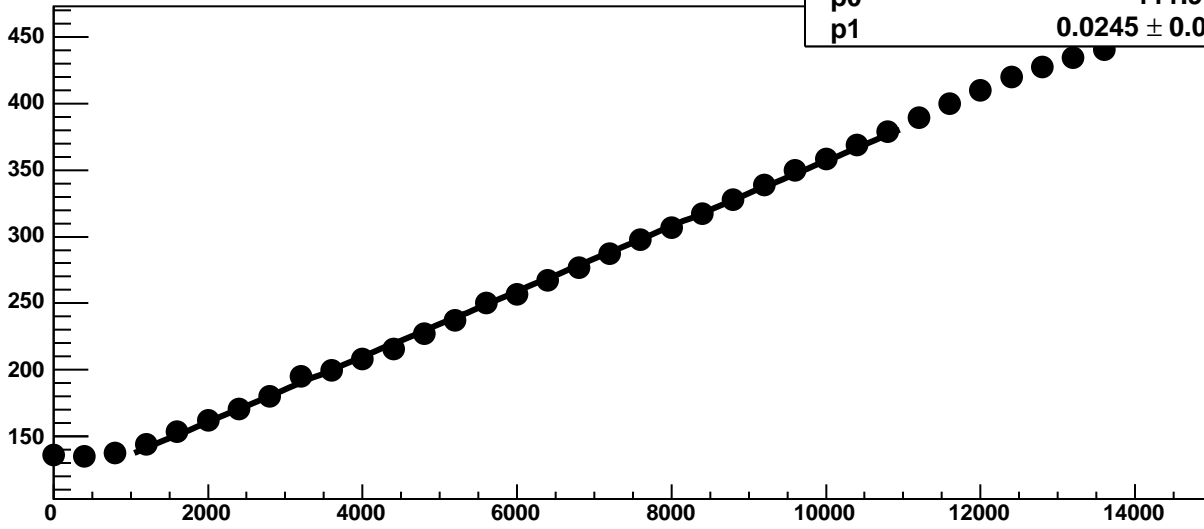
Chip 10, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

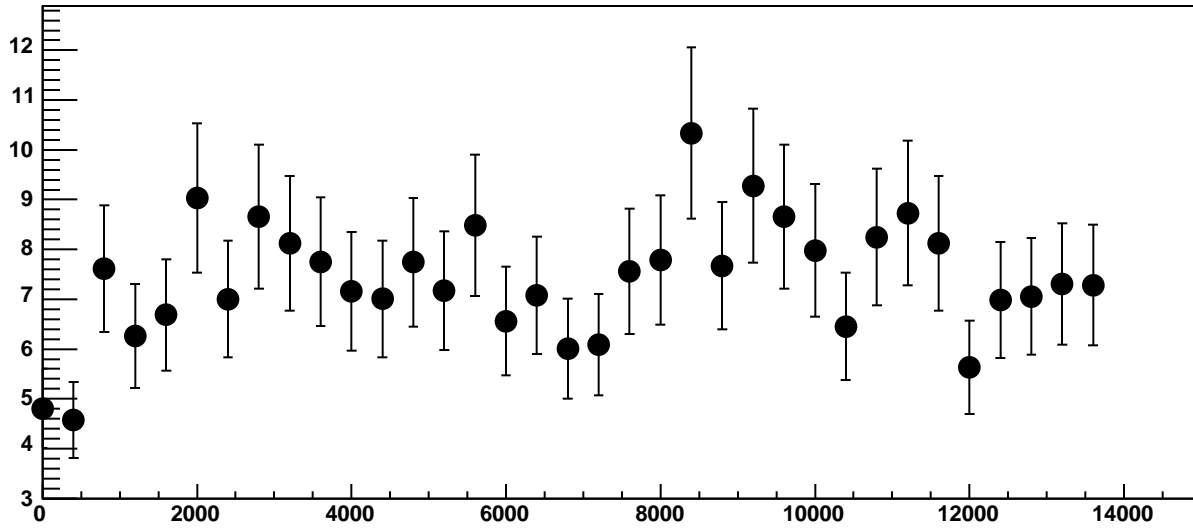


Chip 10, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

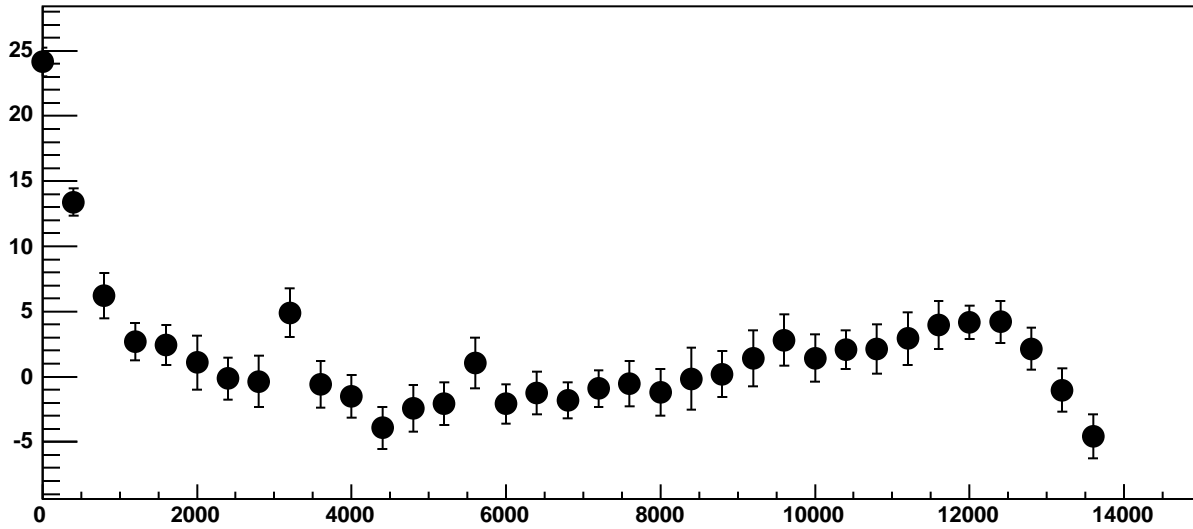


χ^2 / ndf 35.25 / 23
p0 111.9 ± 0.778
p1 0.0245 ± 0.0001193

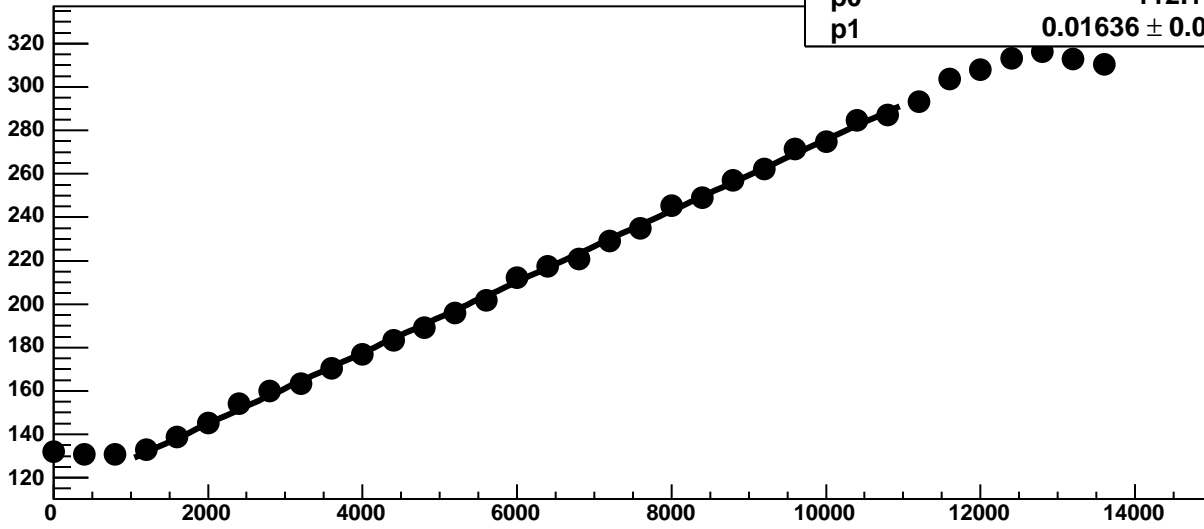
Chip 10, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



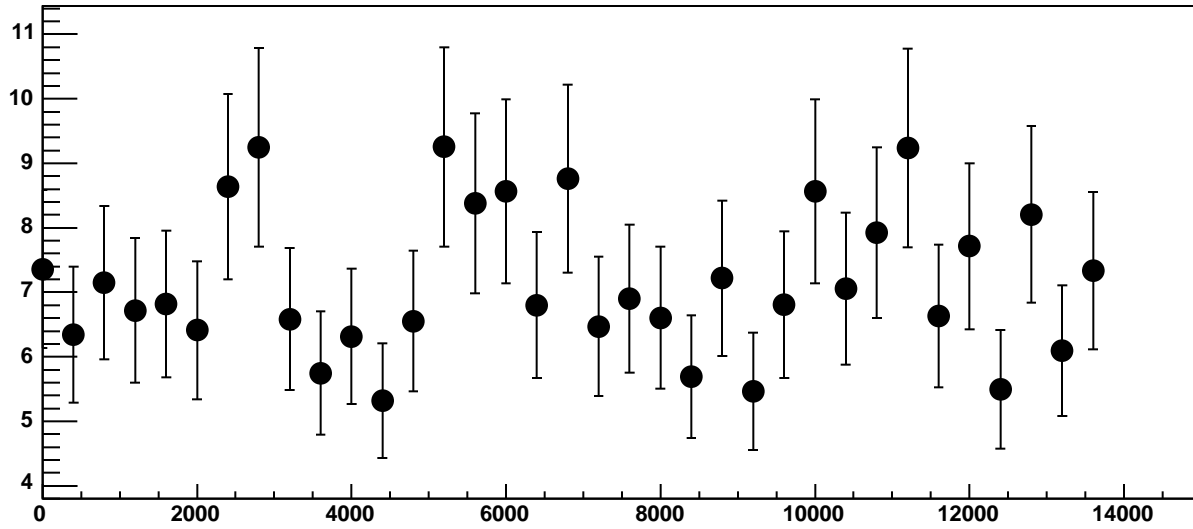
Chip 10, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC



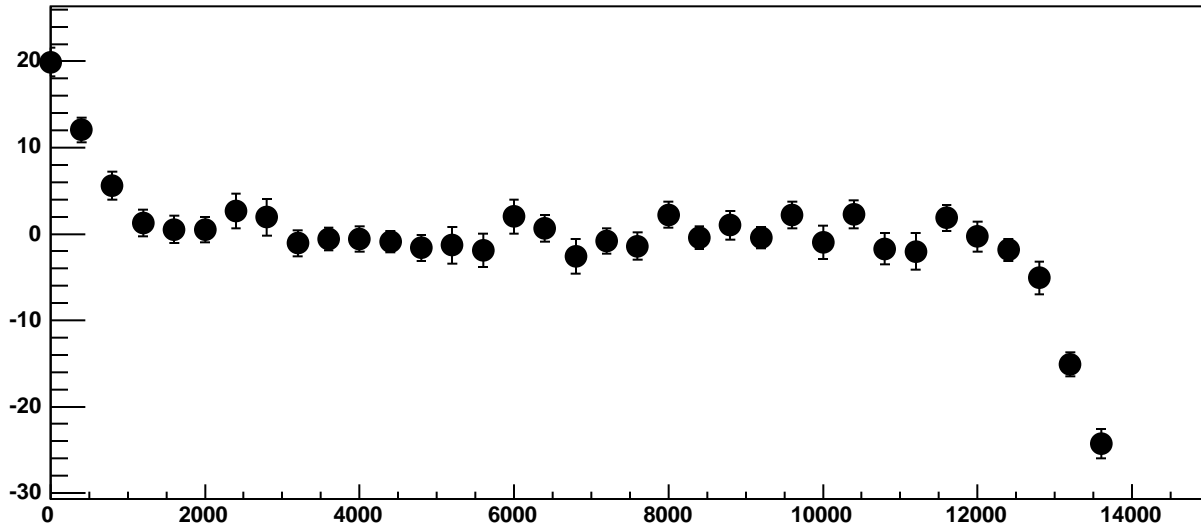
Chip 10, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



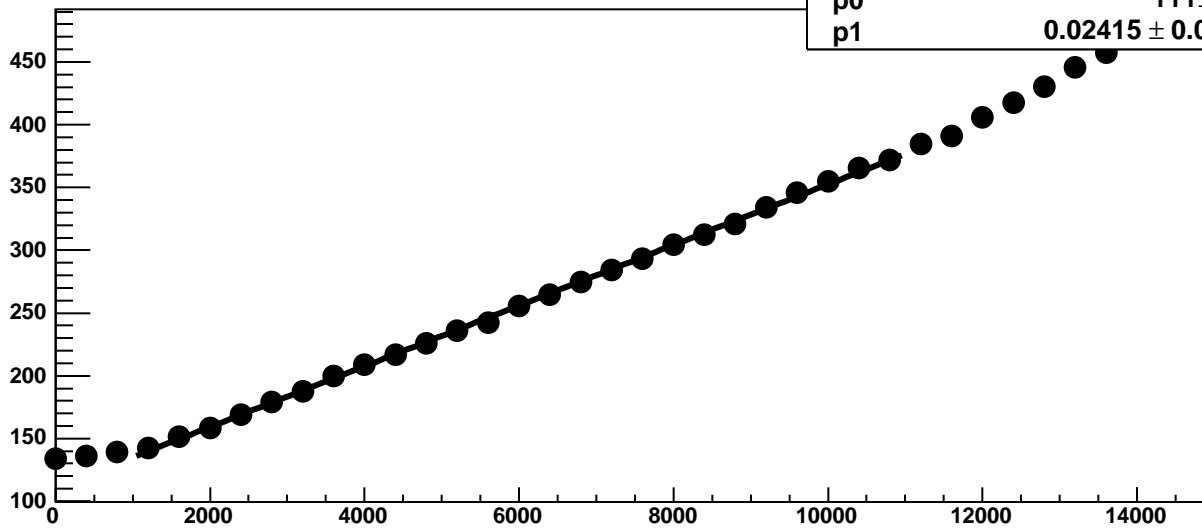
Chip 10, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

19.65 / 23

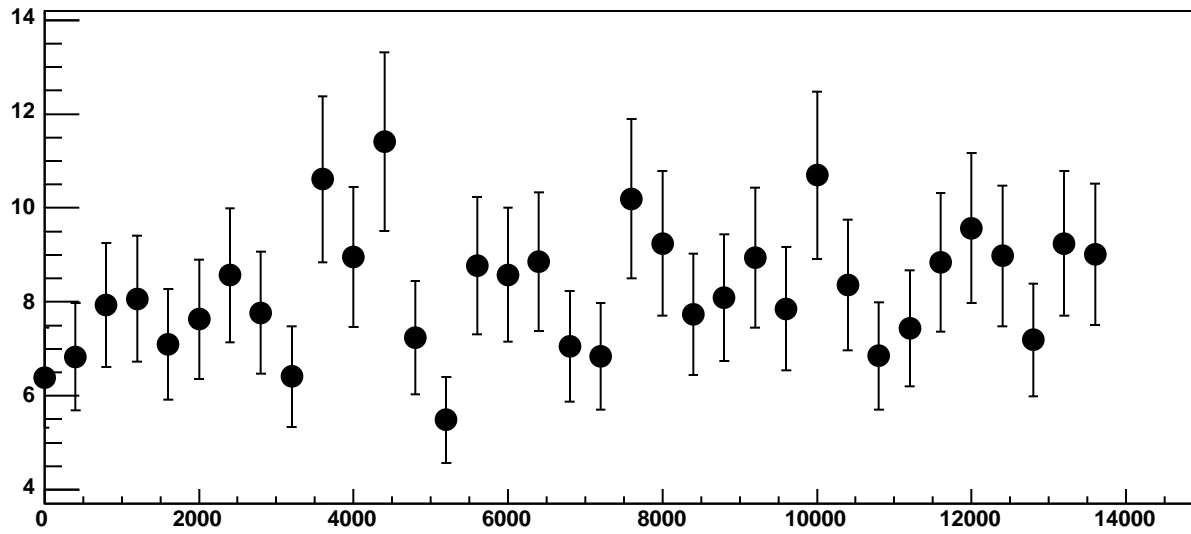
p0

111 ± 0.8281

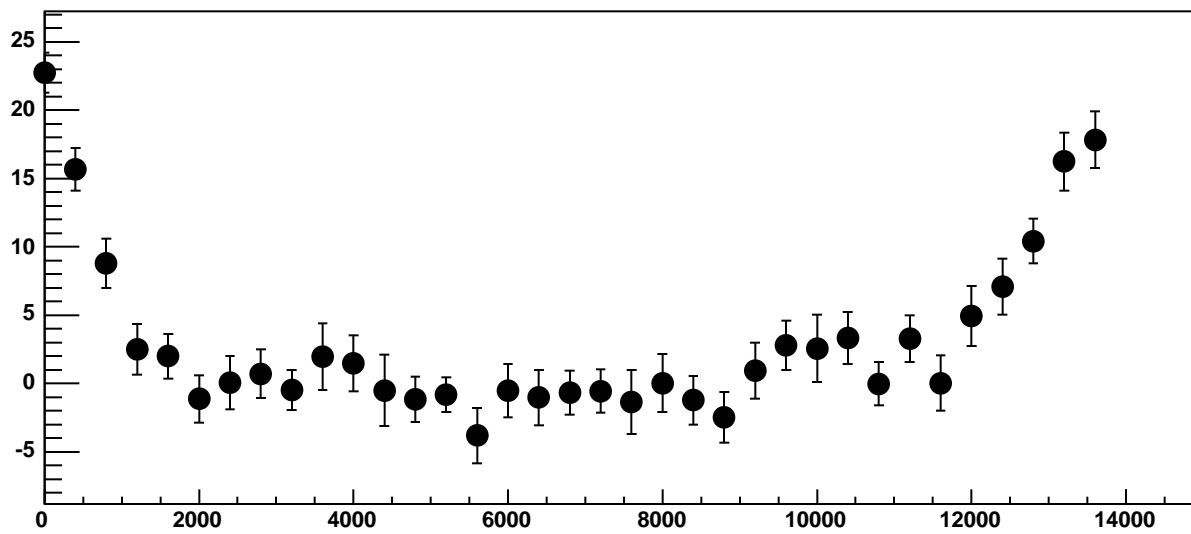
p1

0.02415 ± 0.0001264

Chip 10, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

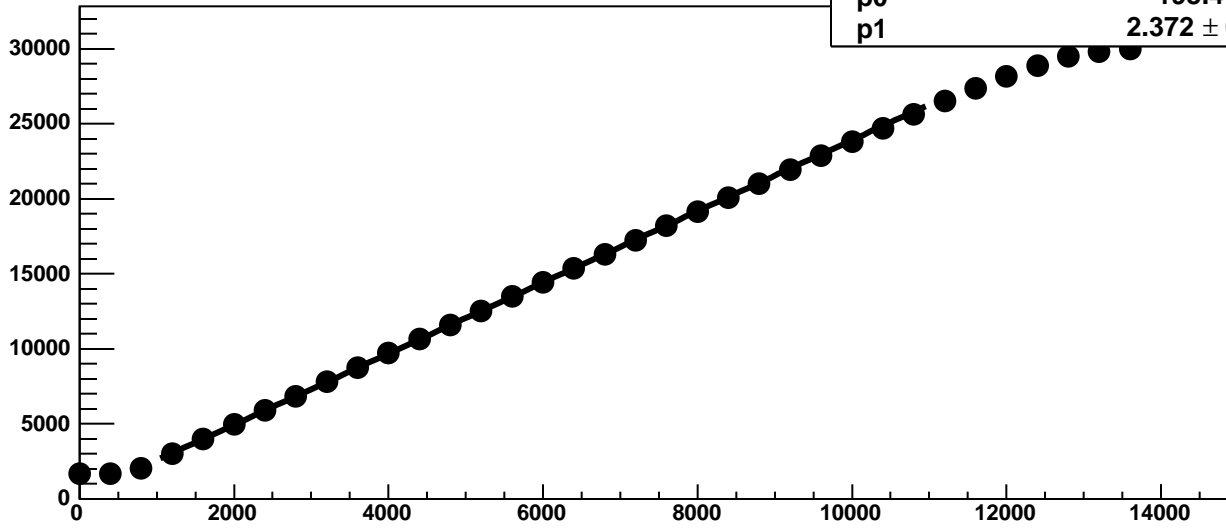


Chip 10, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

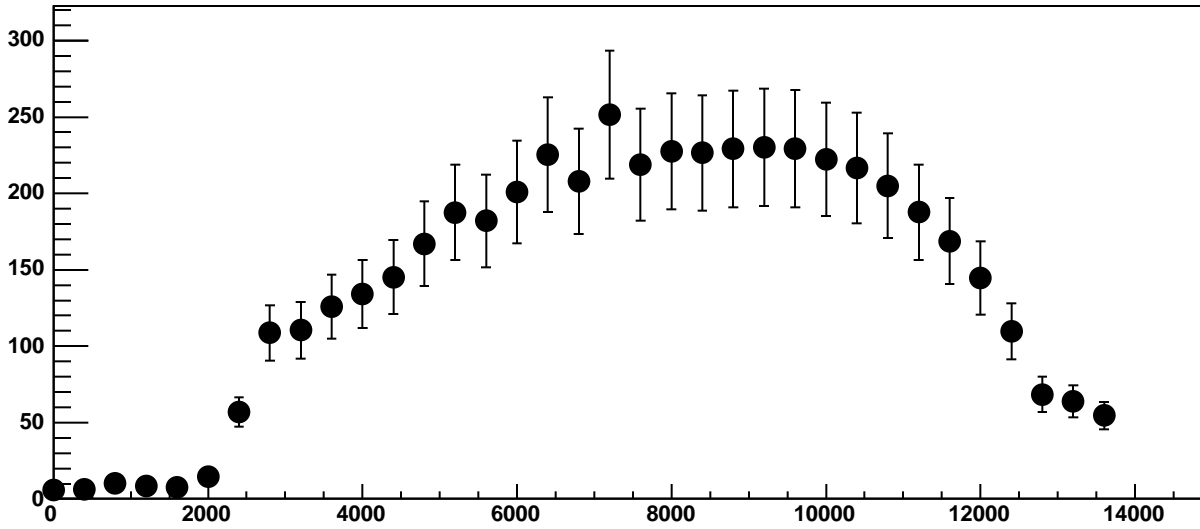


Chip 10, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC

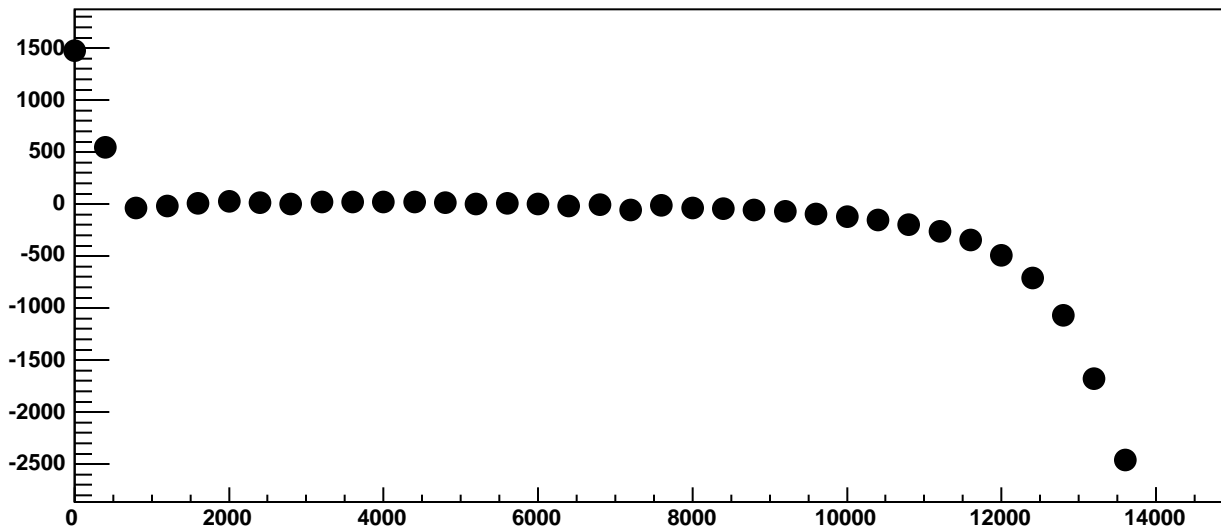
χ^2 / ndf 166.3 / 23
p0 193.4 ± 2.942
p1 2.372 ± 0.00169



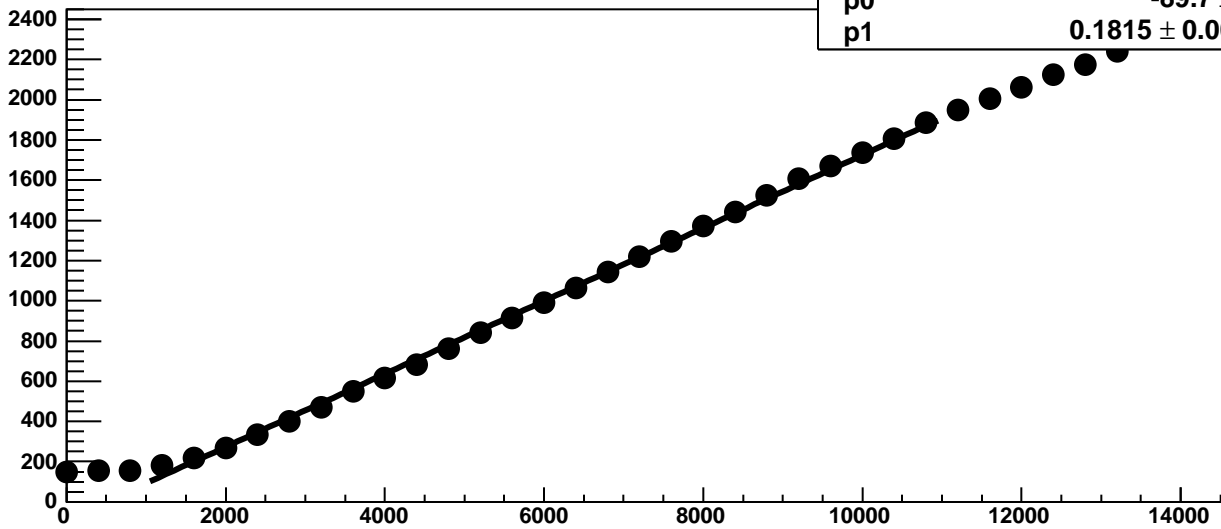
Chip 10, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1353 / 23

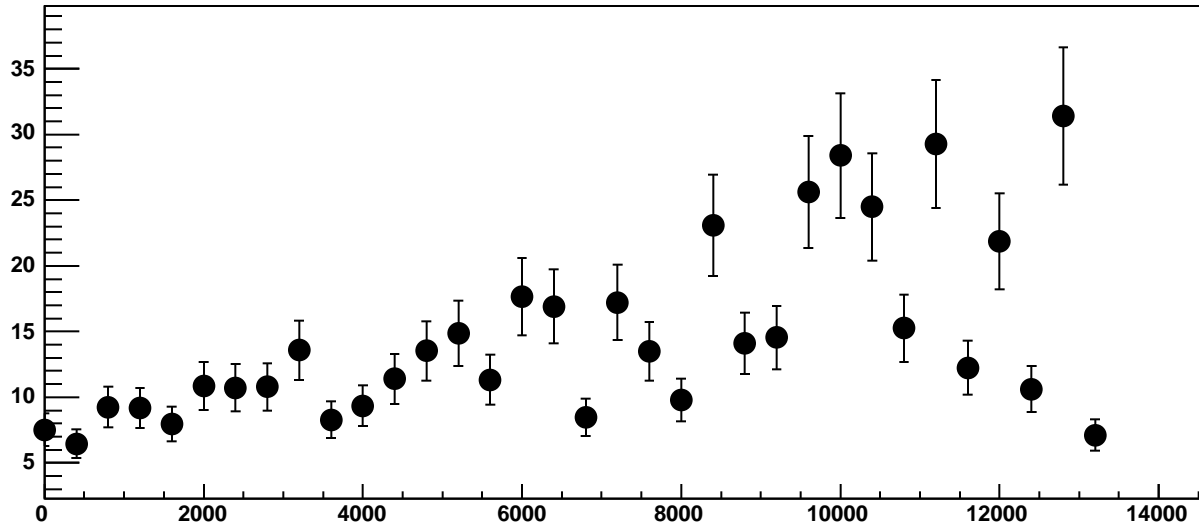
p0

-89.7 ± 1.145

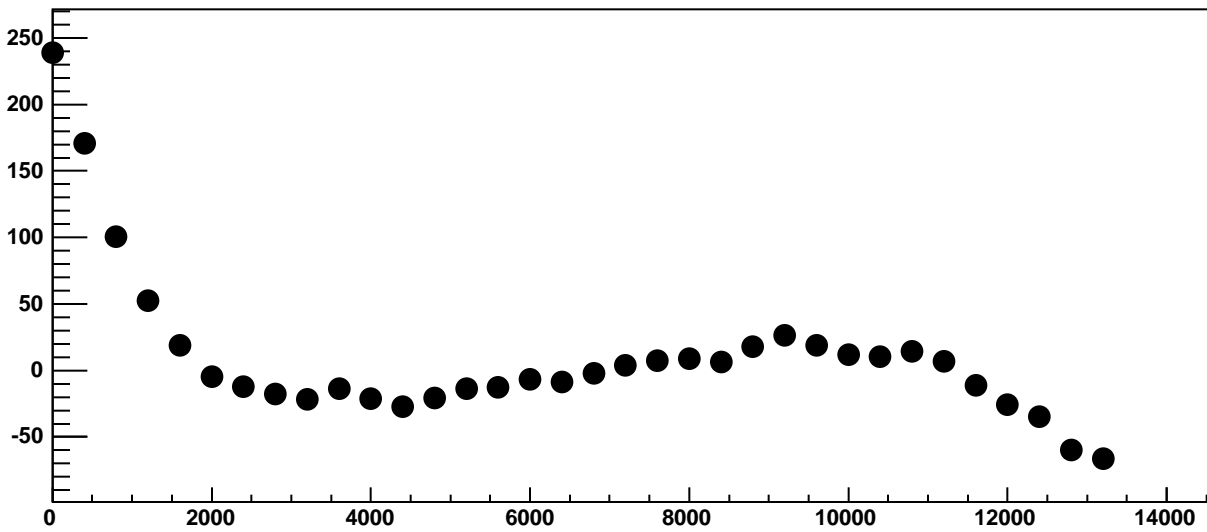
p1

0.1815 ± 0.0002072

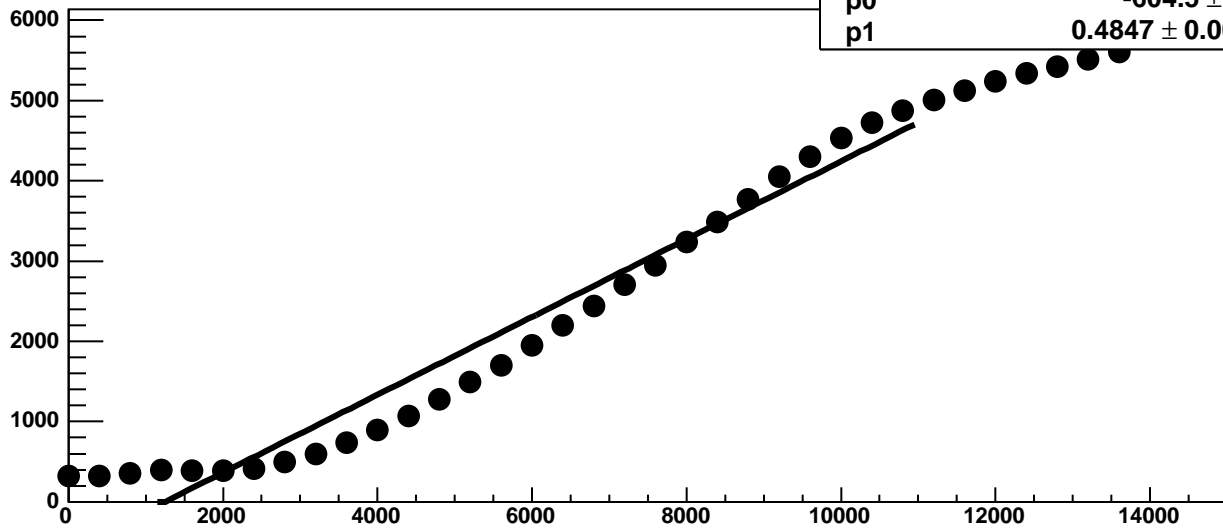
Chip 10, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



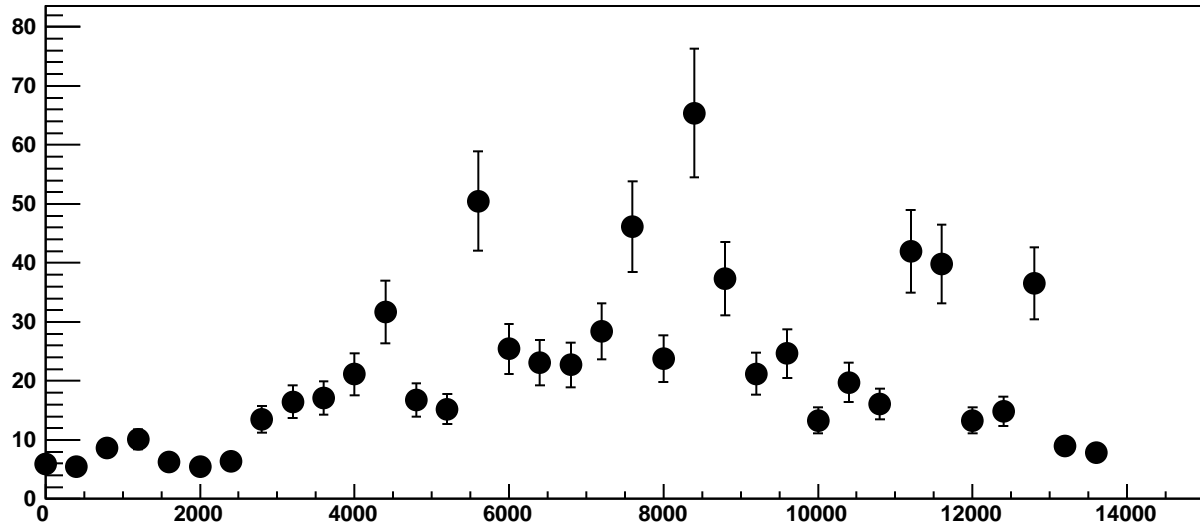
Chip 10, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



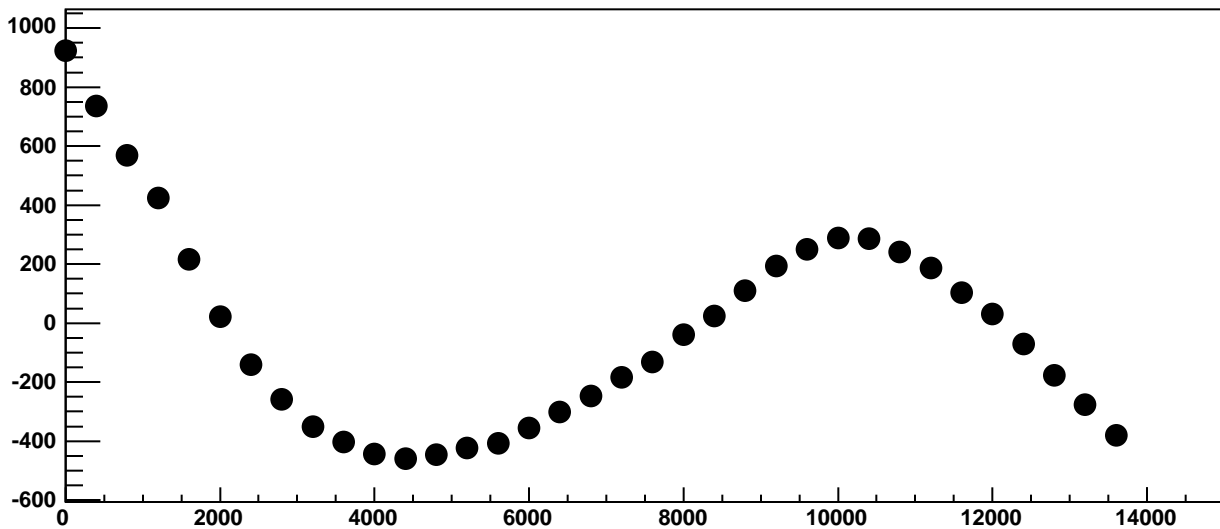
Chip 10, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



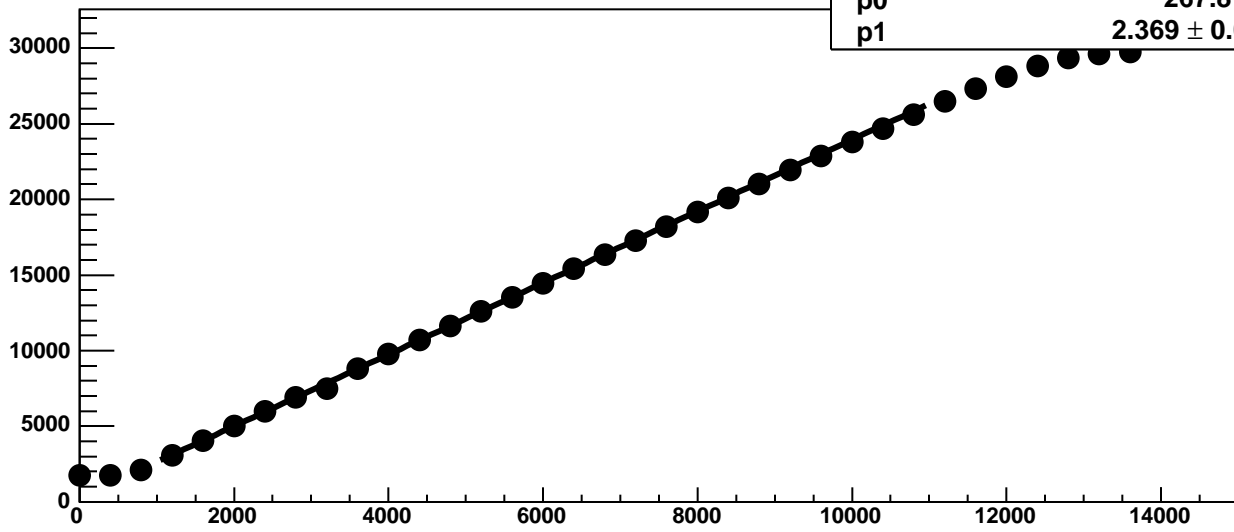
Chip 10, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



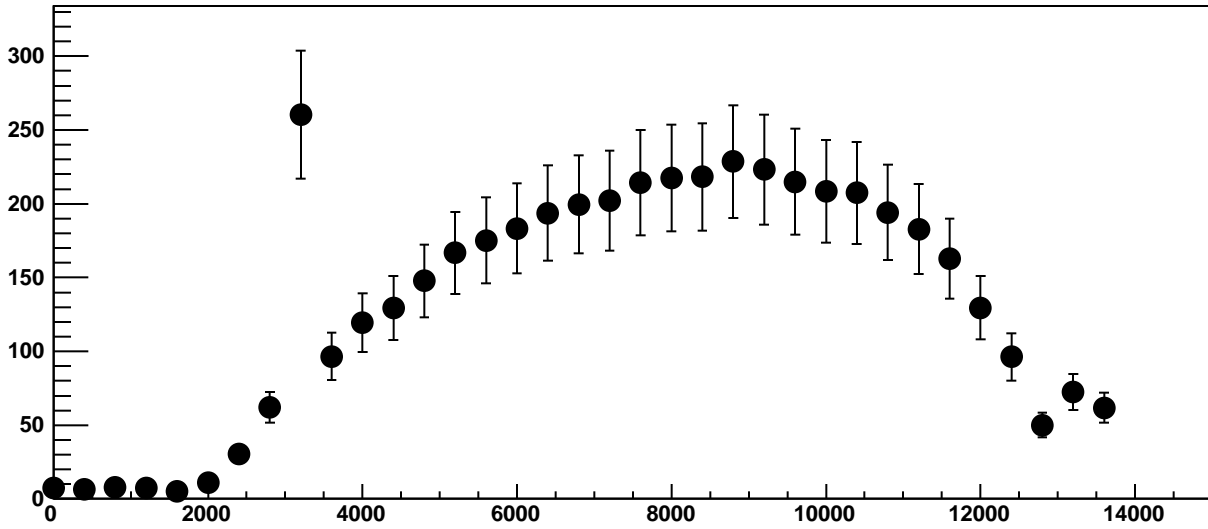
Chip 10, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



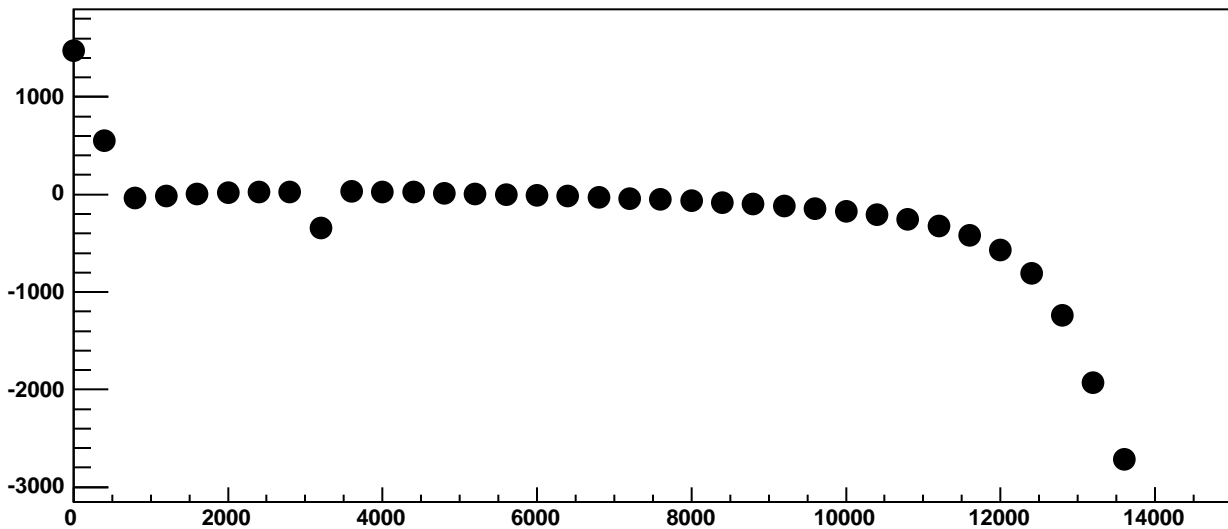
Chip 10, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC



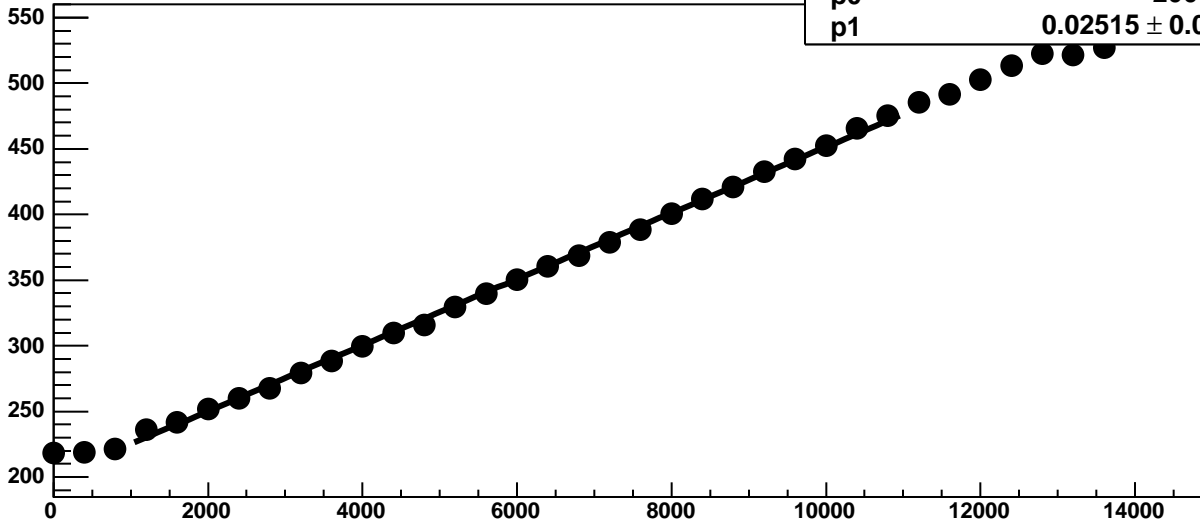
Chip 10, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

42.92 / 23

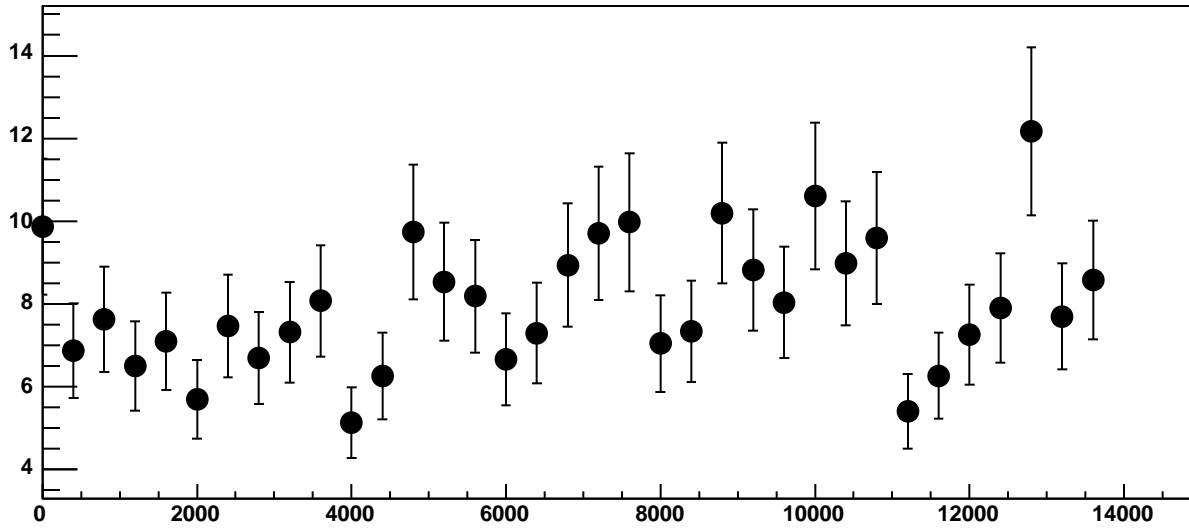
p0

200 ± 0.744

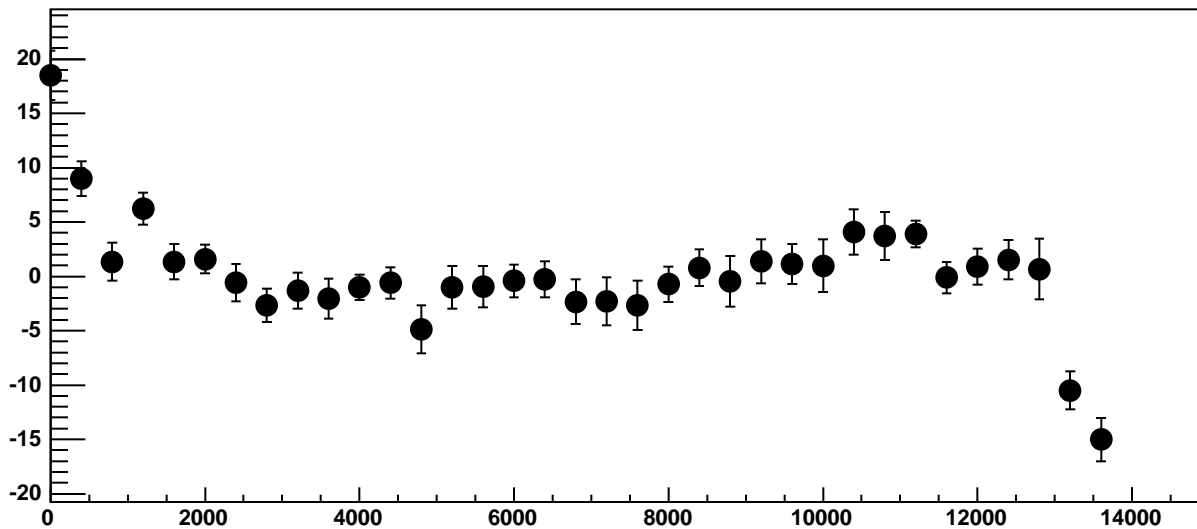
p1

0.02515 ± 0.0001237

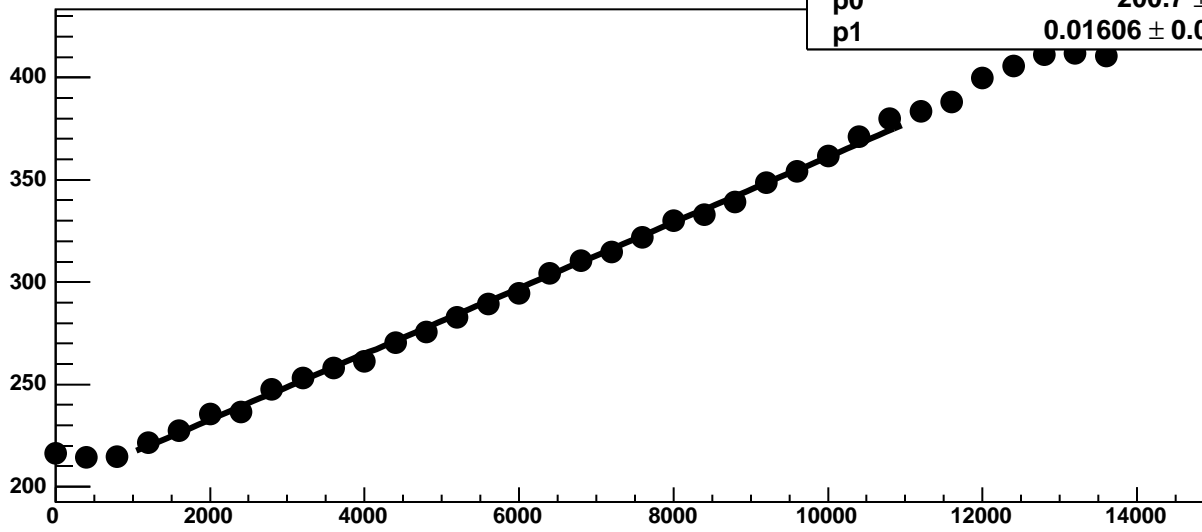
Chip 10, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

40.72 / 23

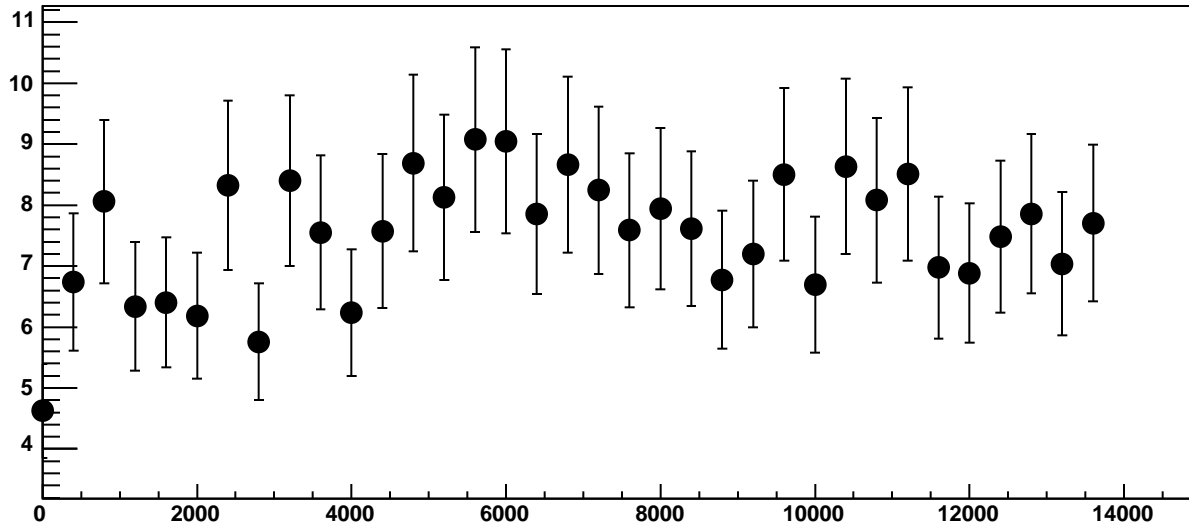
p0

200.7 ± 0.7317

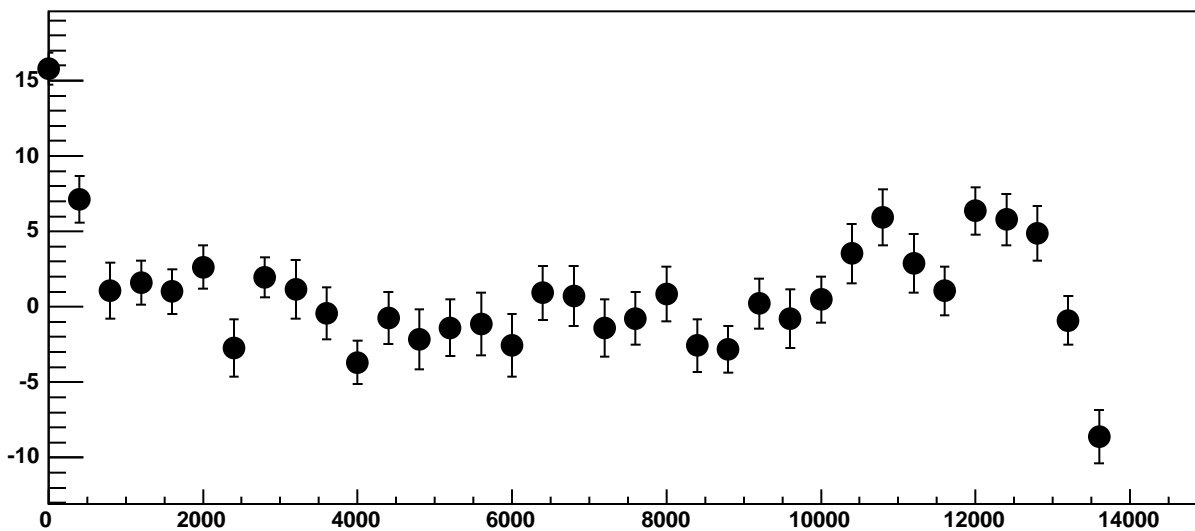
p1

0.01606 ± 0.0001138

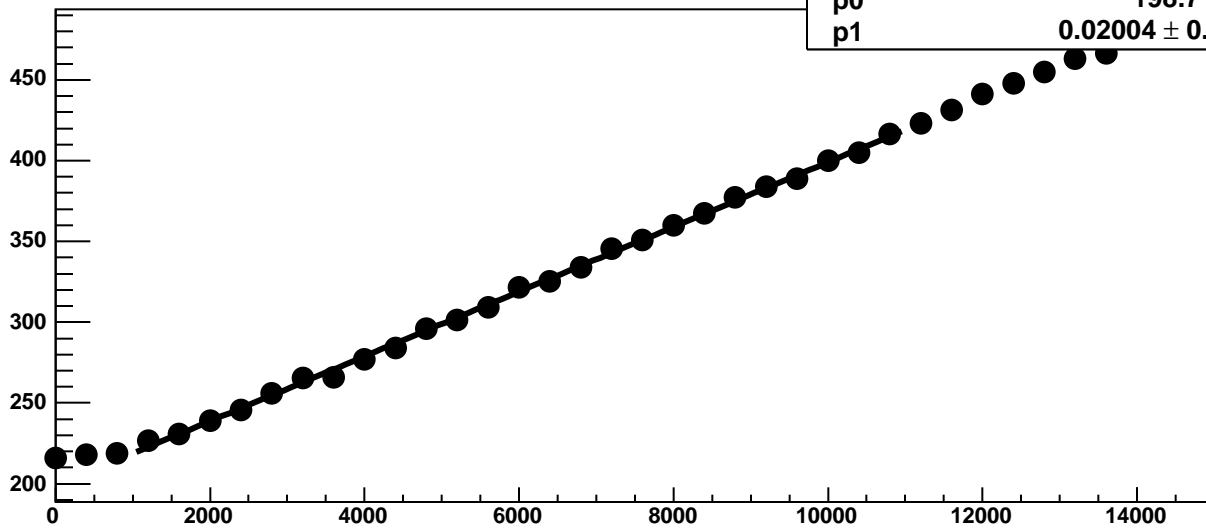
Chip 10, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

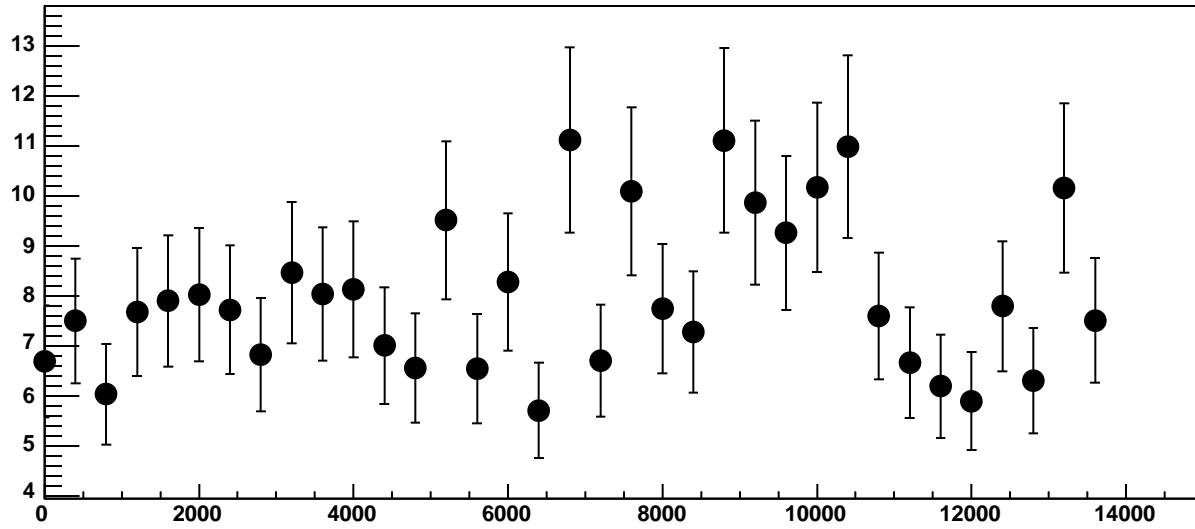


Chip 10, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

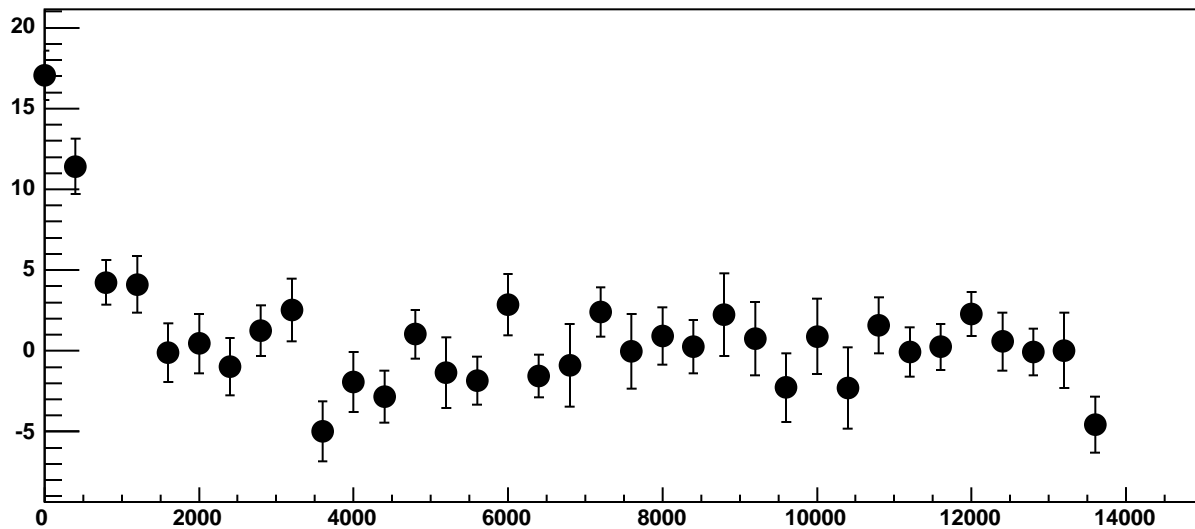


χ^2 / ndf 32.43 / 23
p0 198.7 ± 0.837
p1 0.02004 ± 0.000133

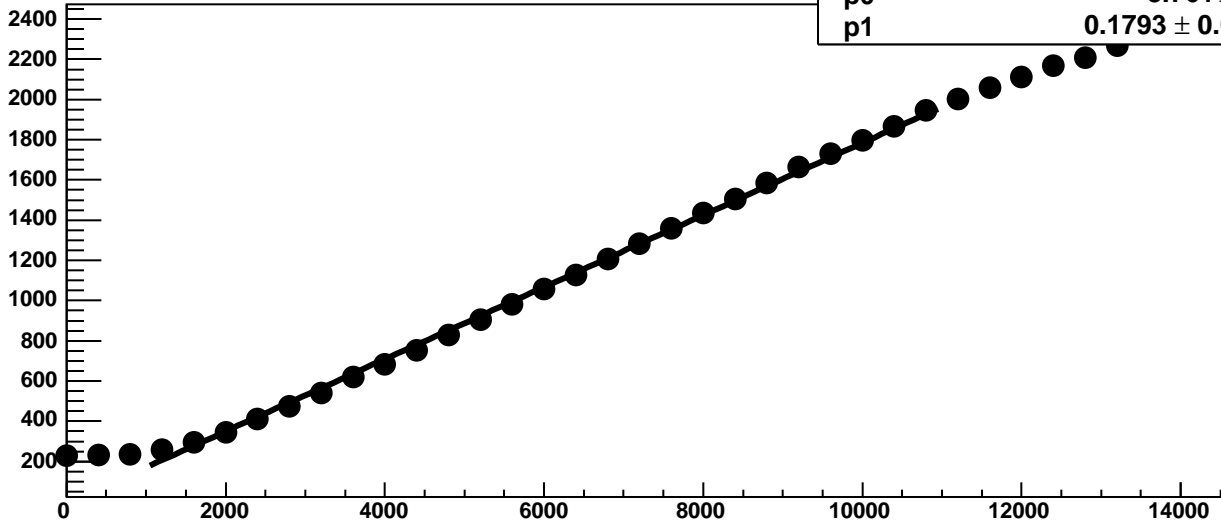
Chip 10, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



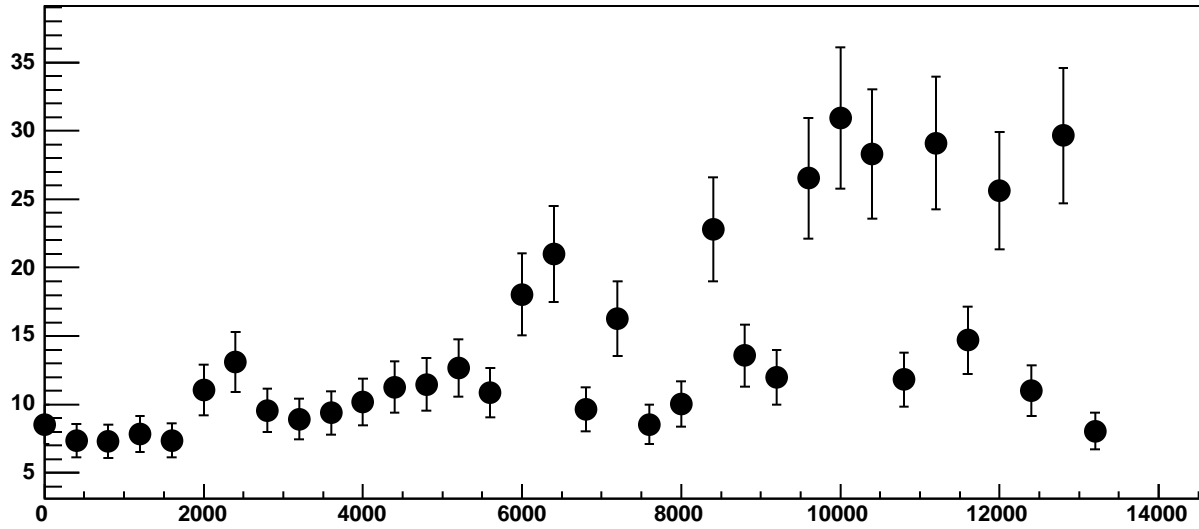
Chip 10, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



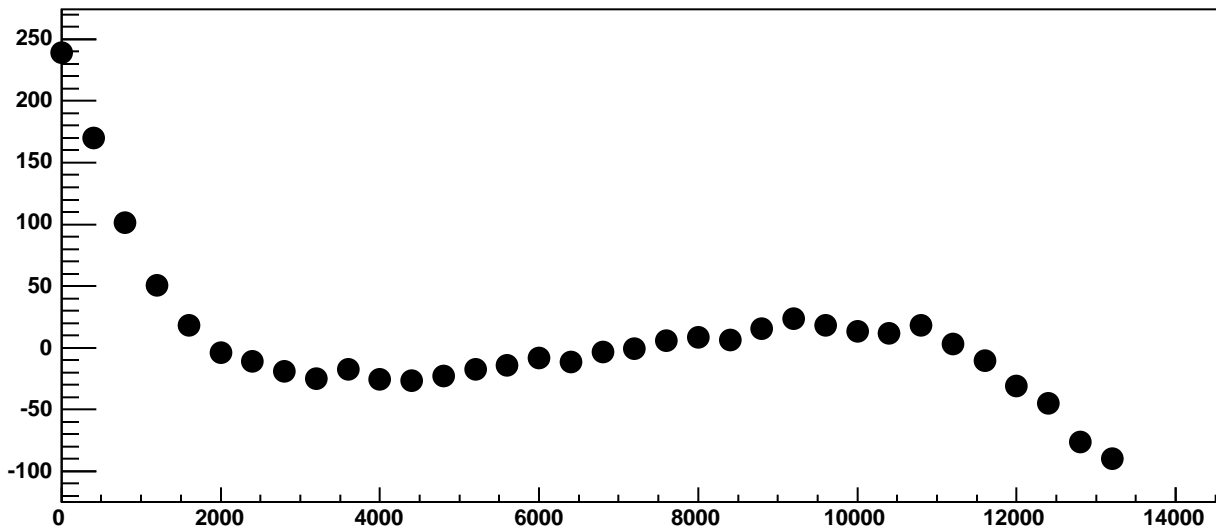
Chip 10, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



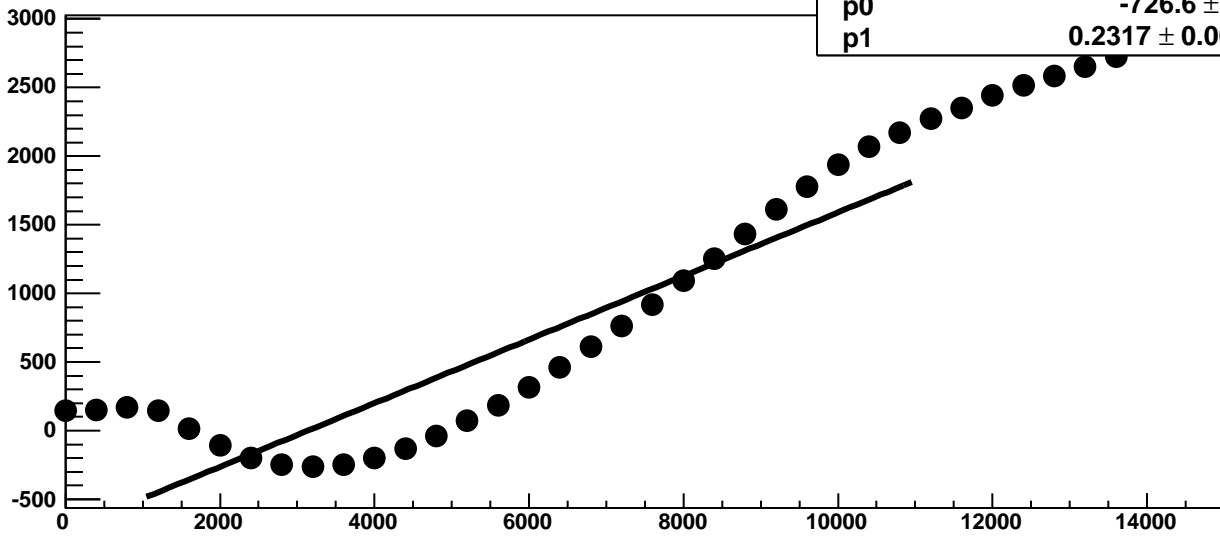
Chip 10, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

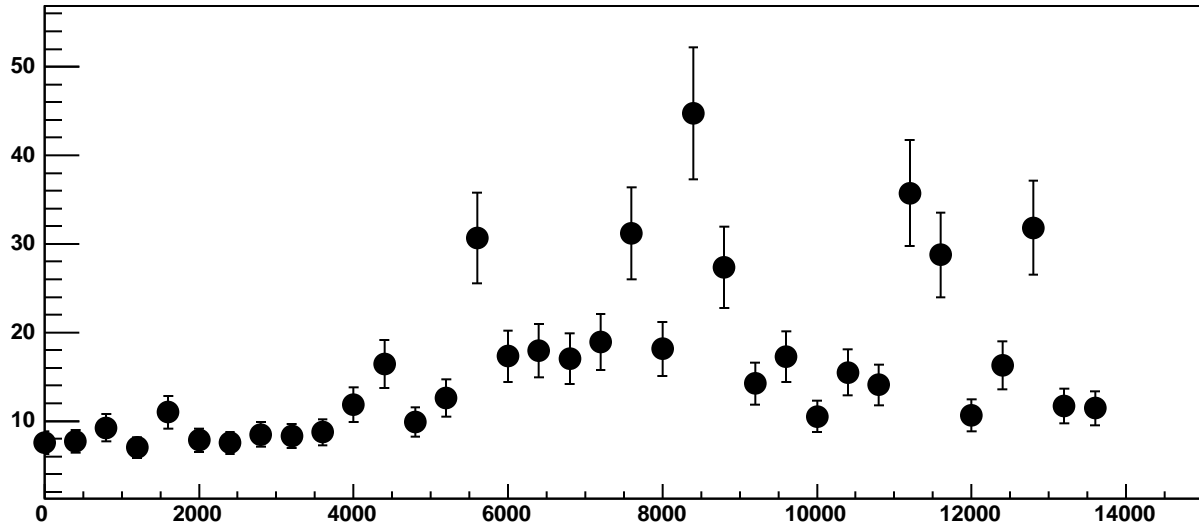


Chip 10, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

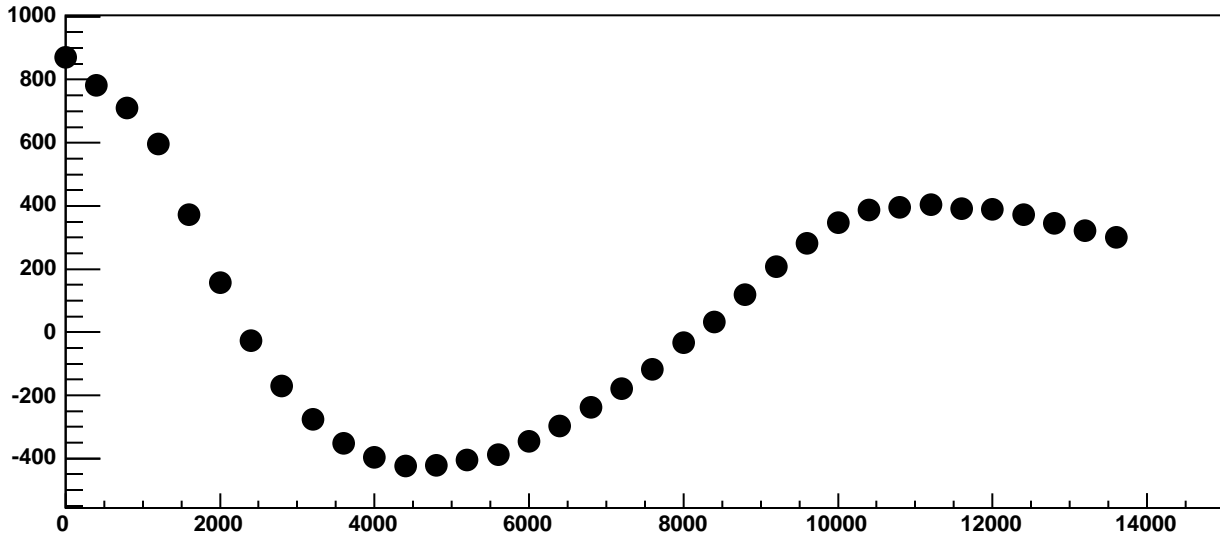


χ^2 / ndf 3.944e+05 / 23
p0 -726.6 ± 0.9809
p1 0.2317 ± 0.0001866

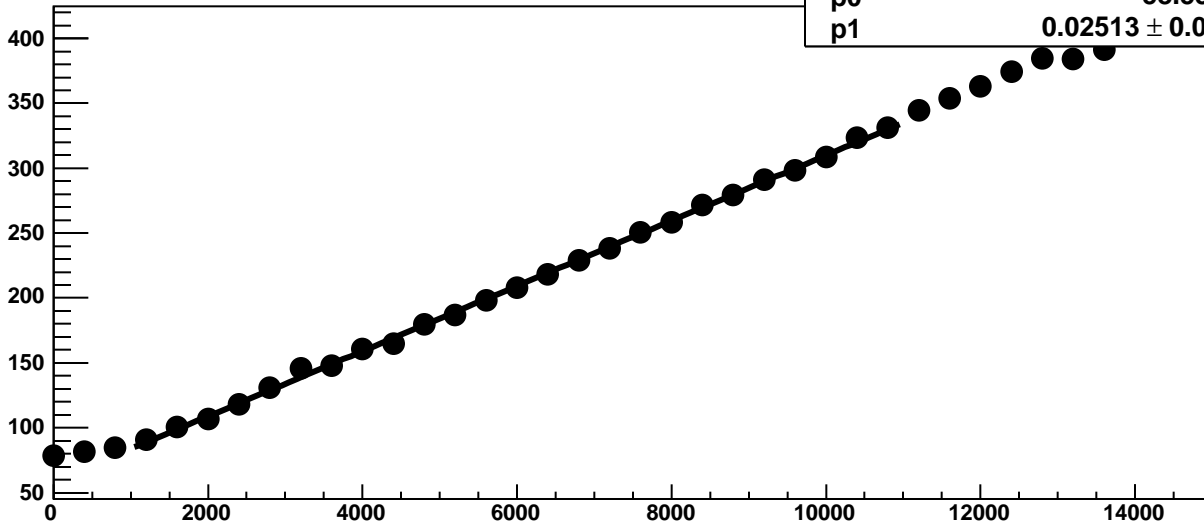
Chip 10, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

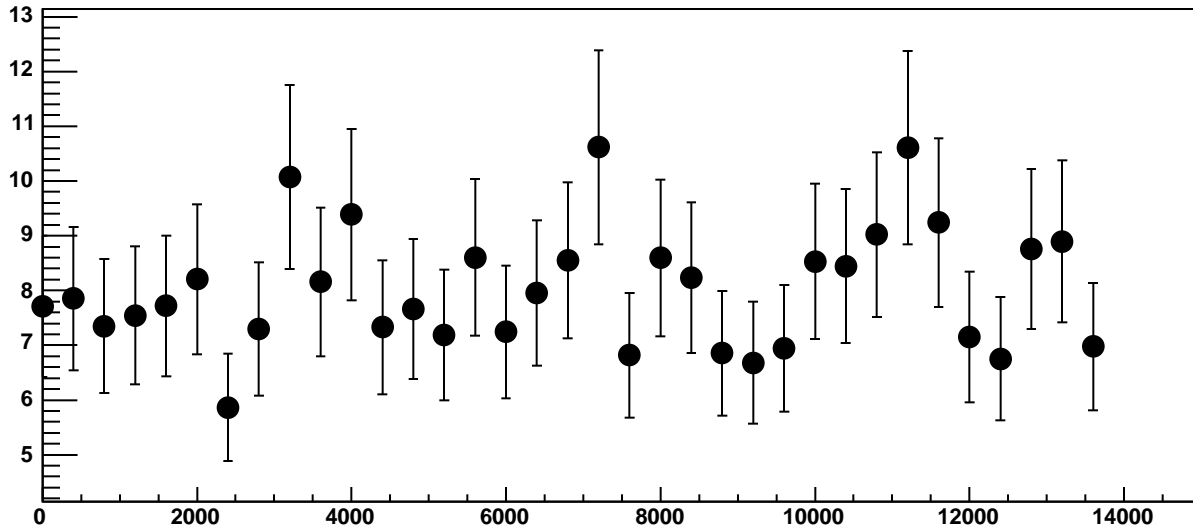


Chip 10, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

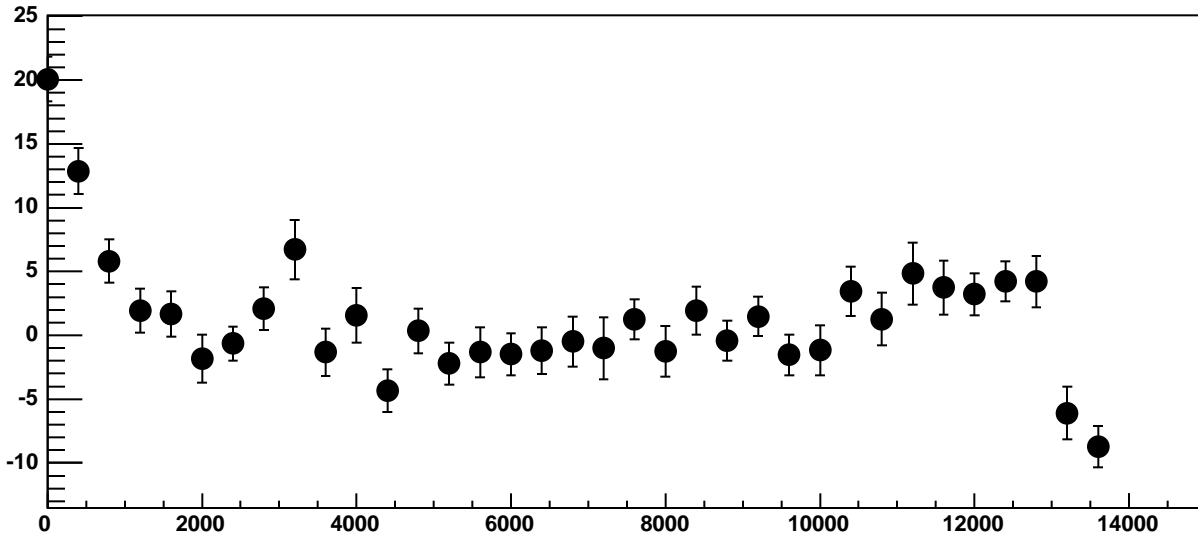


χ^2 / ndf 32.6 / 23
p0 58.55 ± 0.81
p1 0.02513 ± 0.0001229

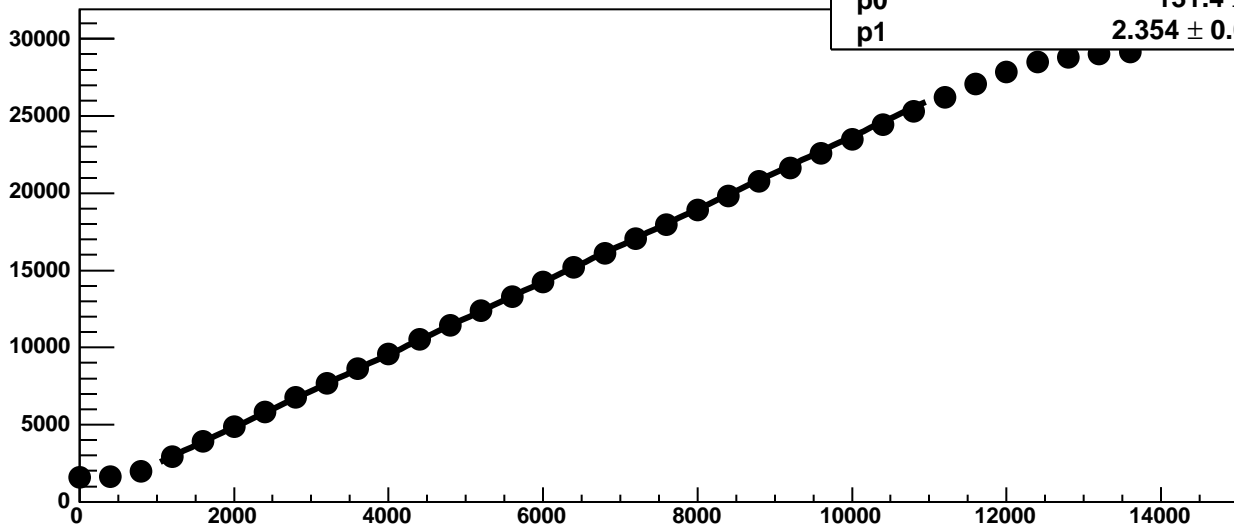
Chip 10, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



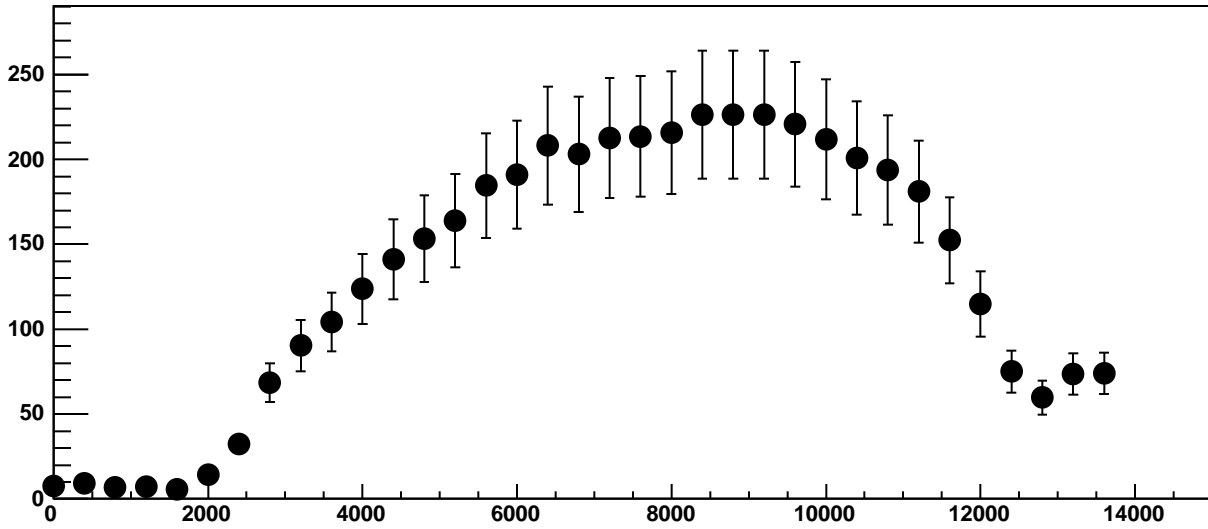
Chip 10, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



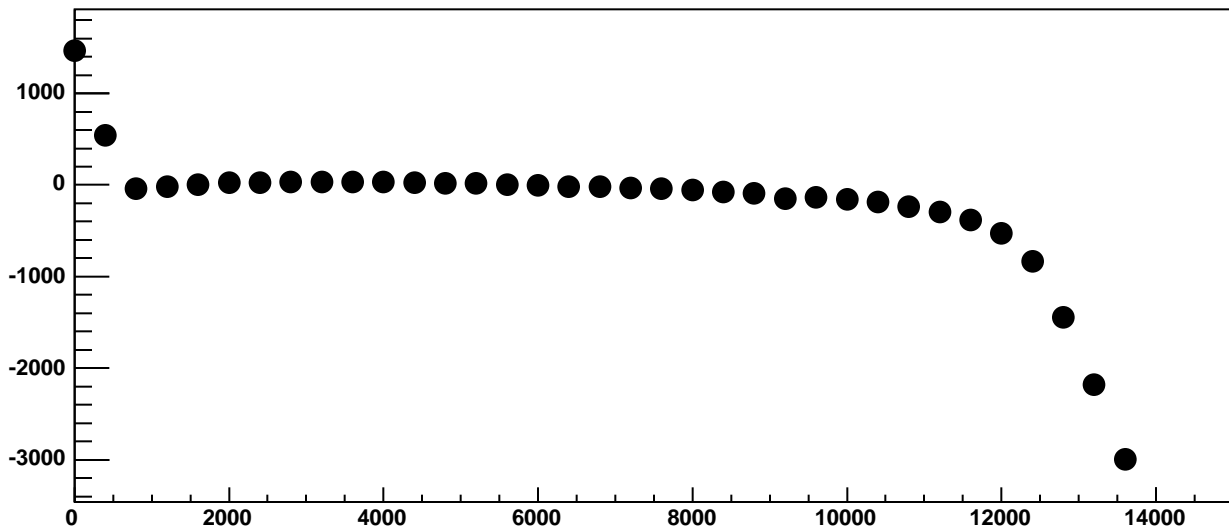
Chip 10, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



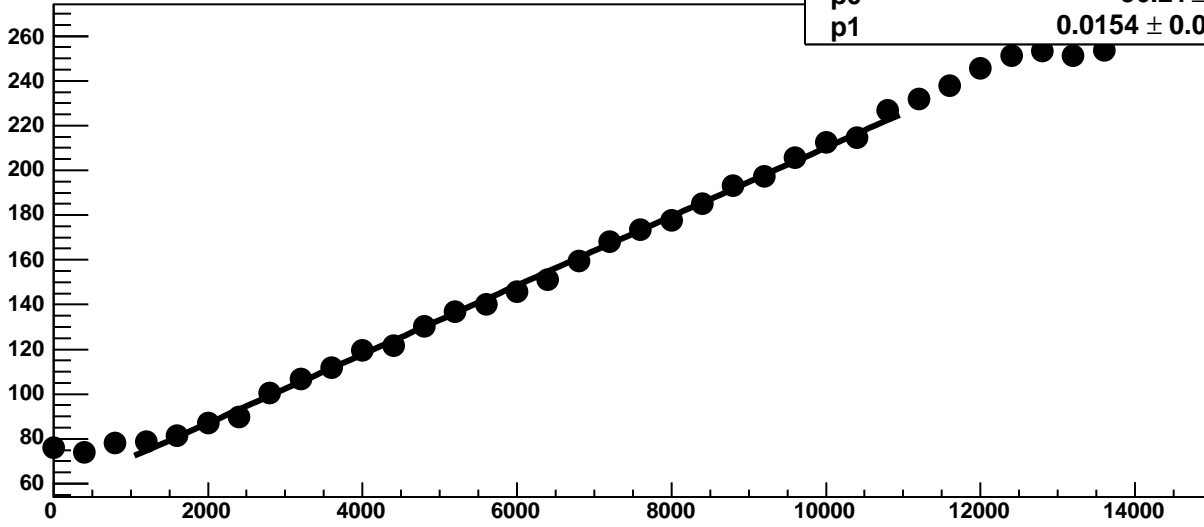
Chip 10, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



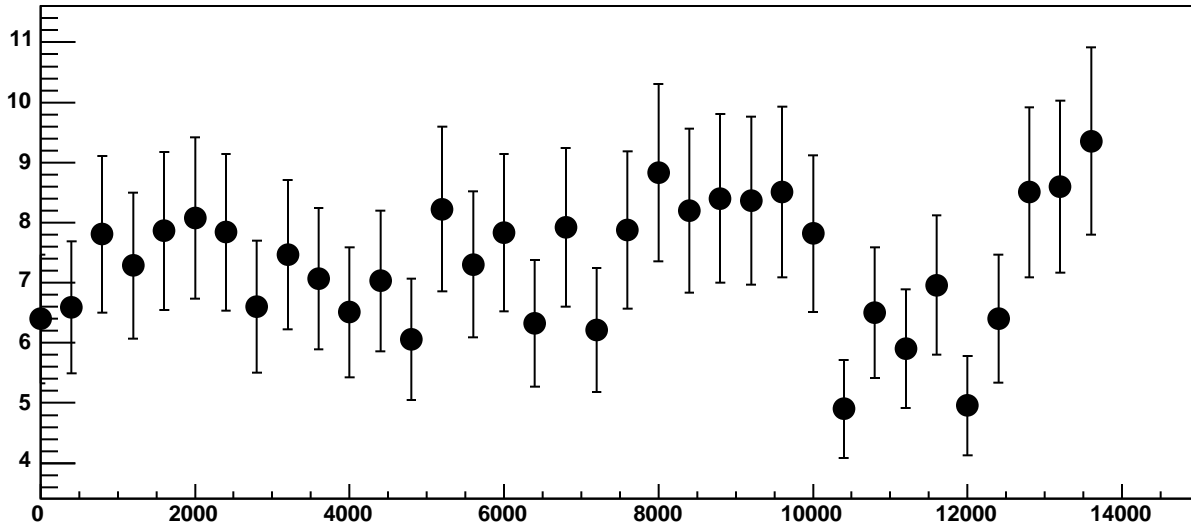
Chip 10, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC



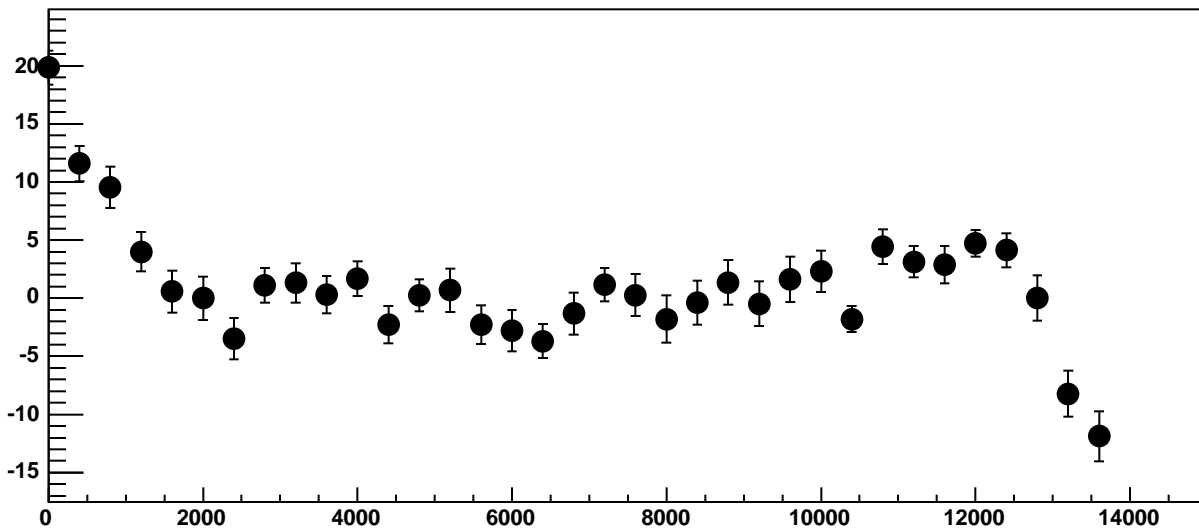
Chip 10, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



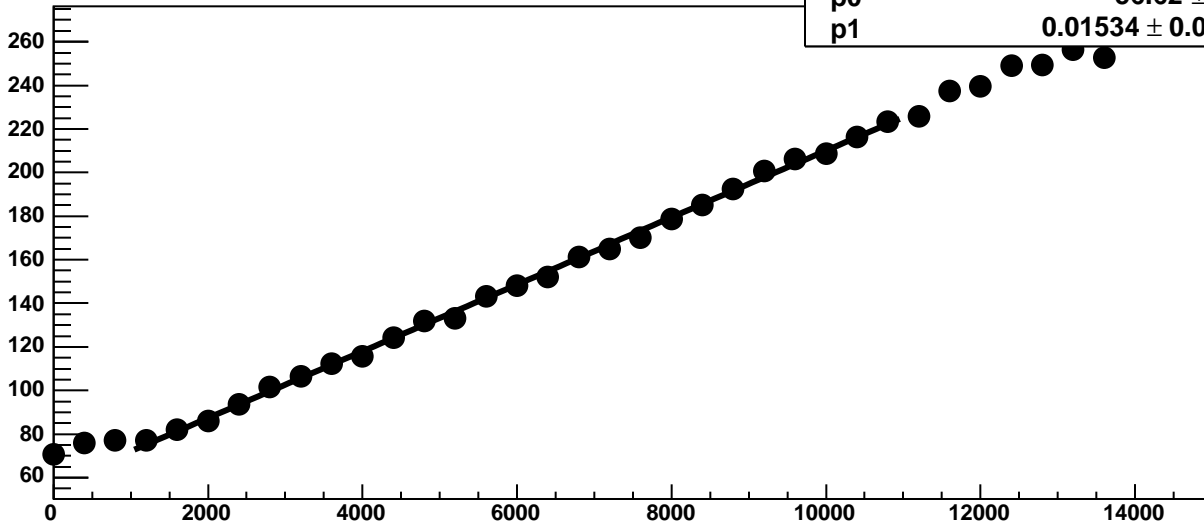
Chip 10, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

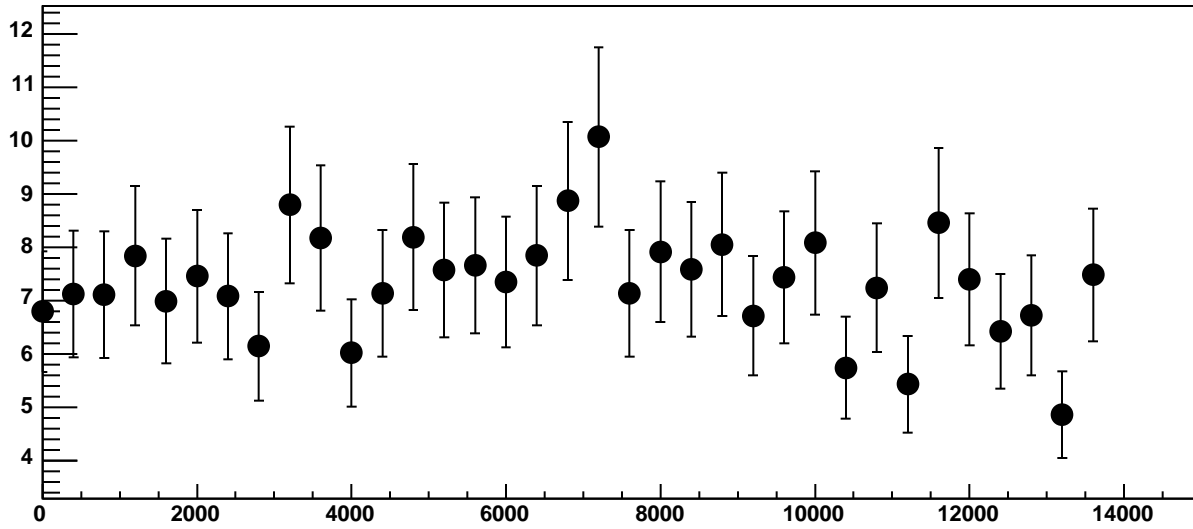


Chip 10, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

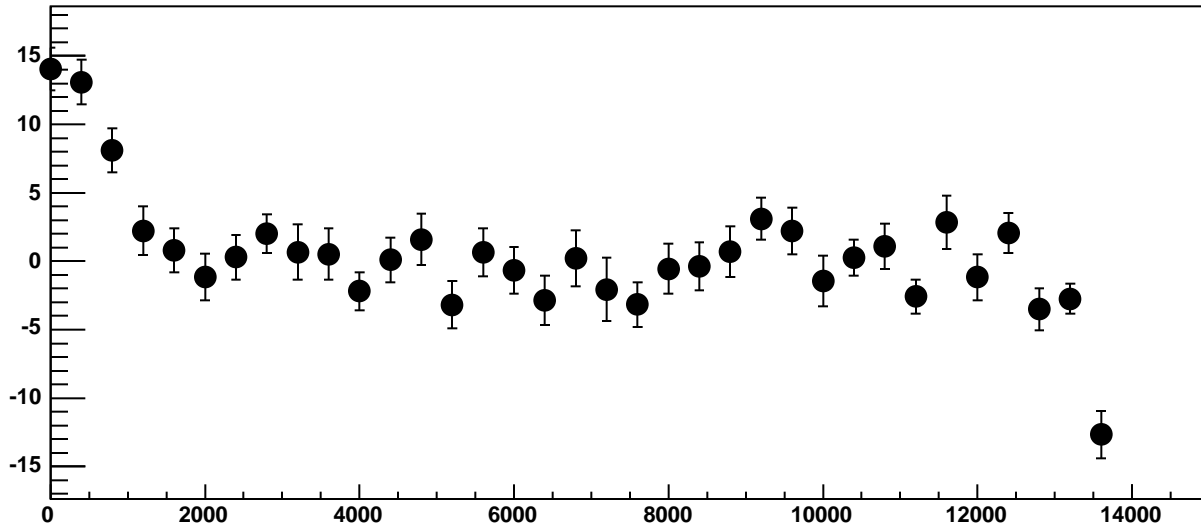


χ^2 / ndf 25.46 / 23
p0 56.62 ± 0.7633
p1 0.01534 ± 0.0001137

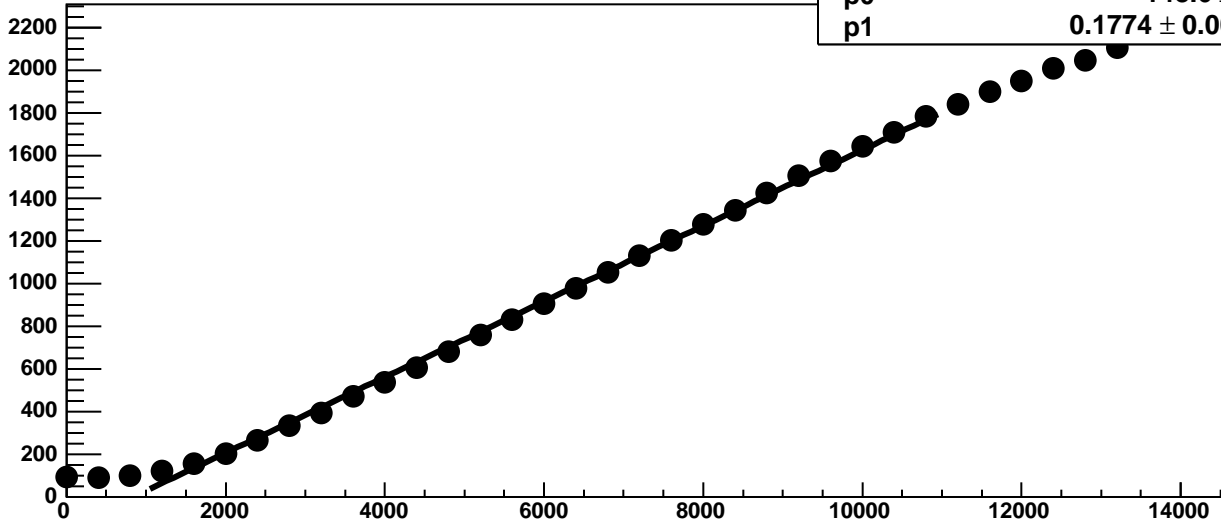
Chip 10, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



Chip 10, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1698 / 23

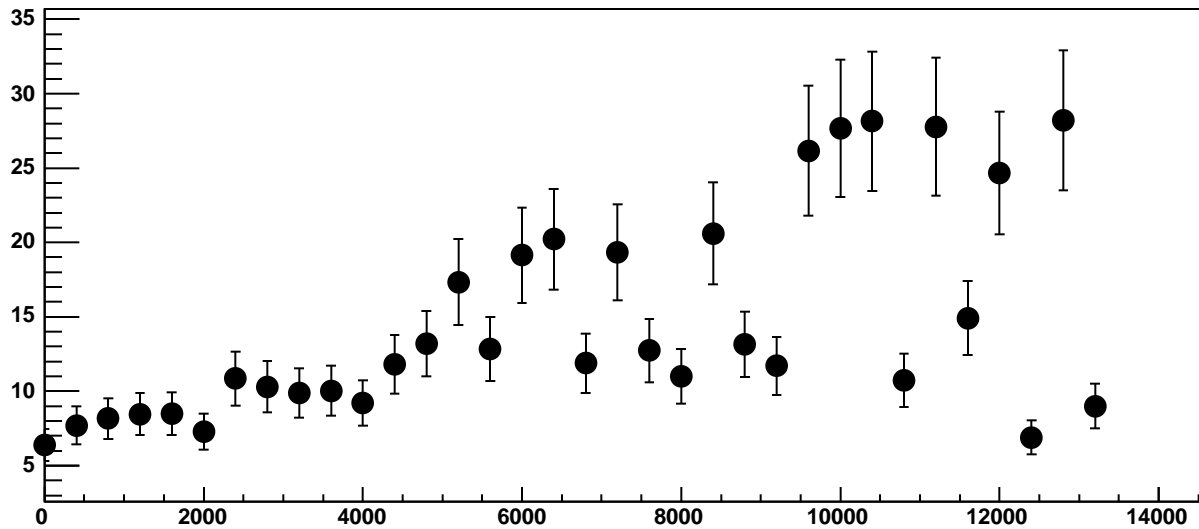
p0

-148.6 ± 1.044

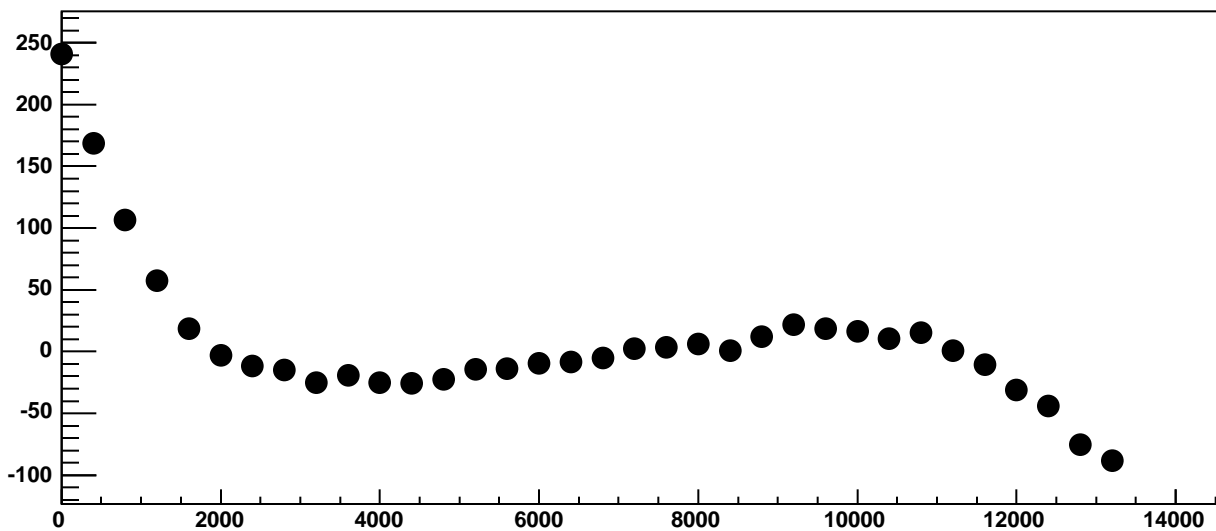
p1

0.1774 ± 0.0001884

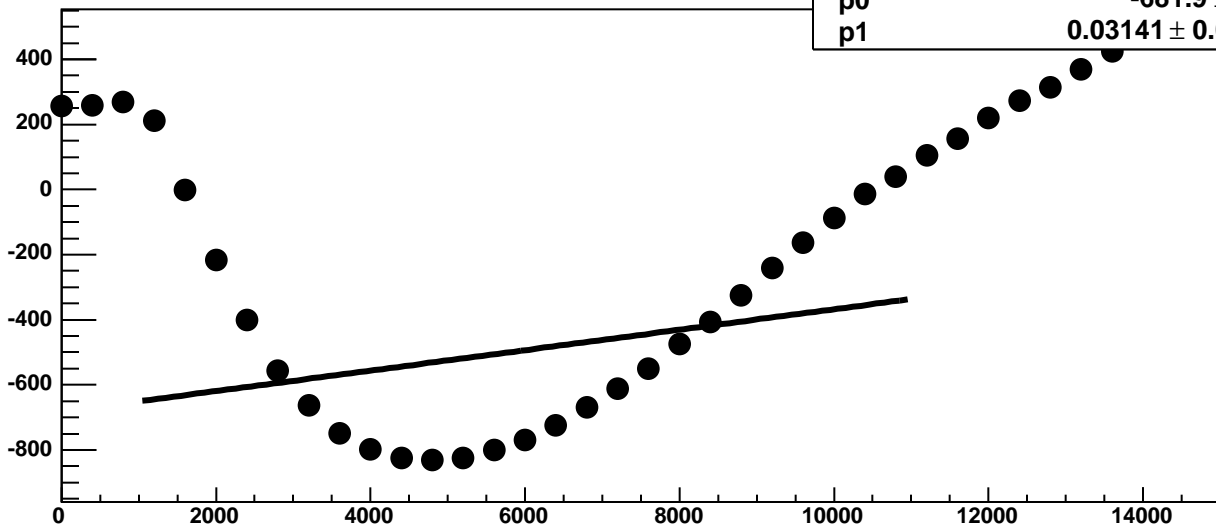
Chip 10, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC

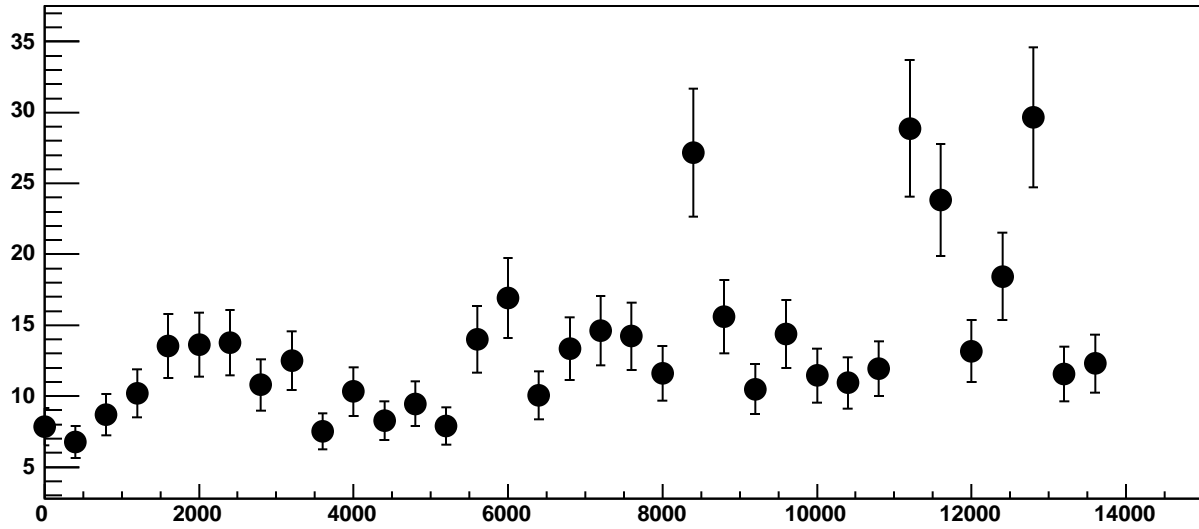


Chip 10, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

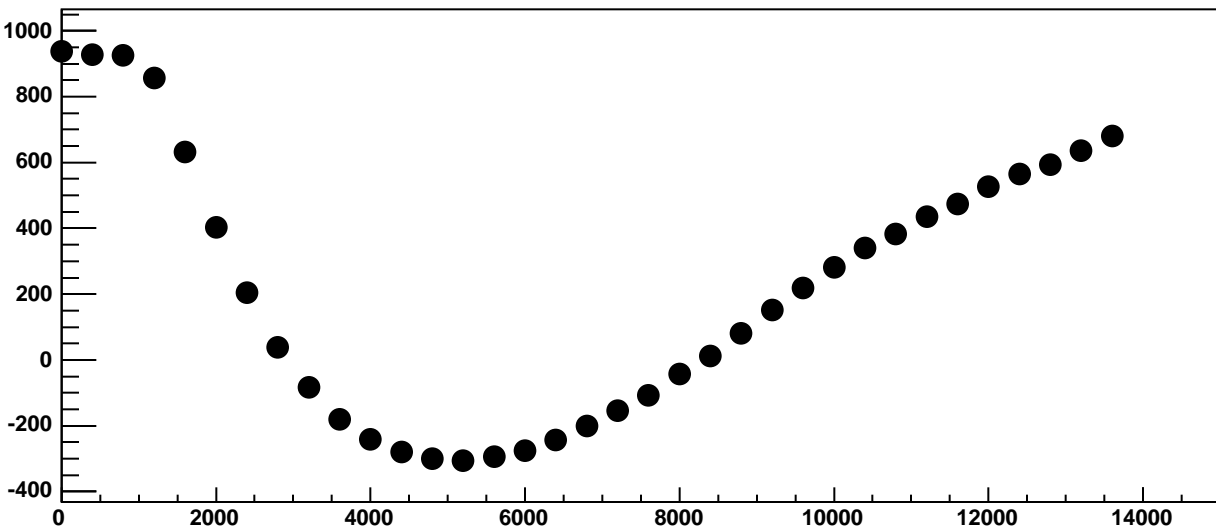


χ^2 / ndf 3.797e+05 / 23
p0 -681.9 ± 1.178
p1 0.03141 ± 0.000187

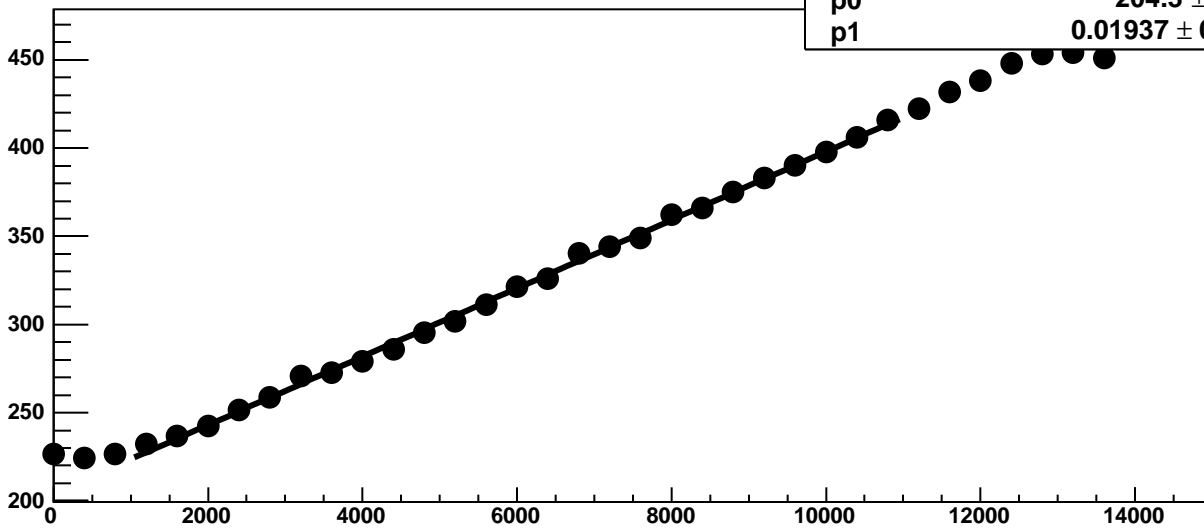
Chip 10, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

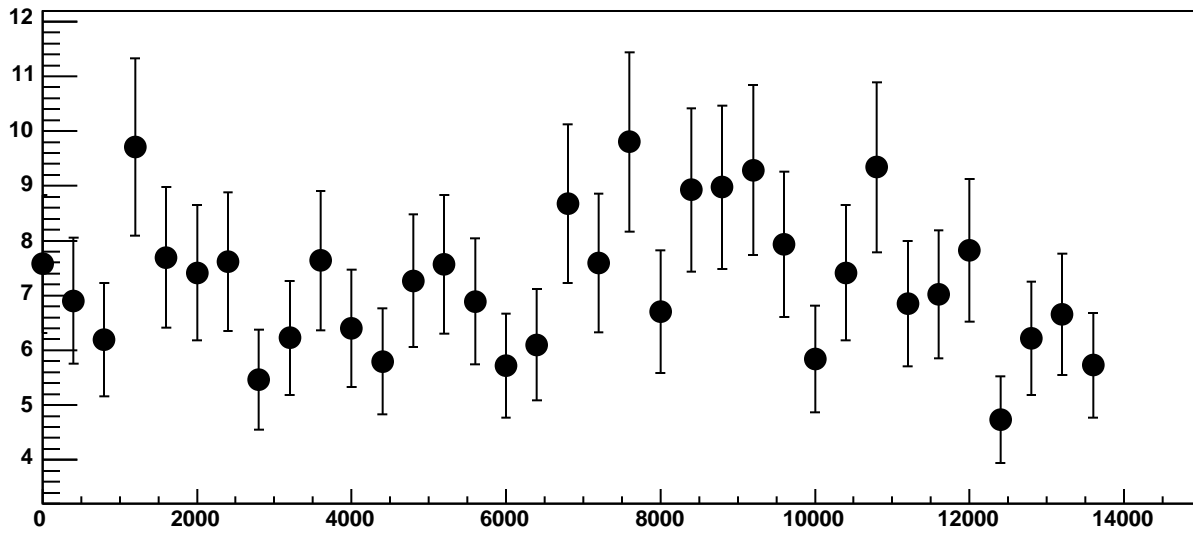


Chip 10, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

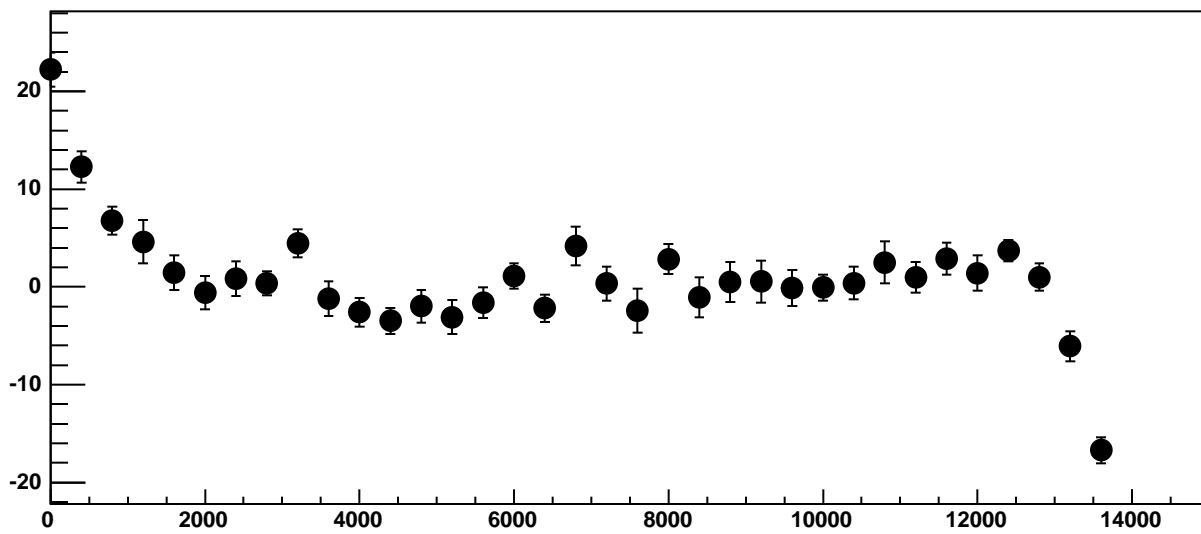


χ^2 / ndf 45 / 23
p0 204.3 ± 0.7679
p1 0.01937 ± 0.00012

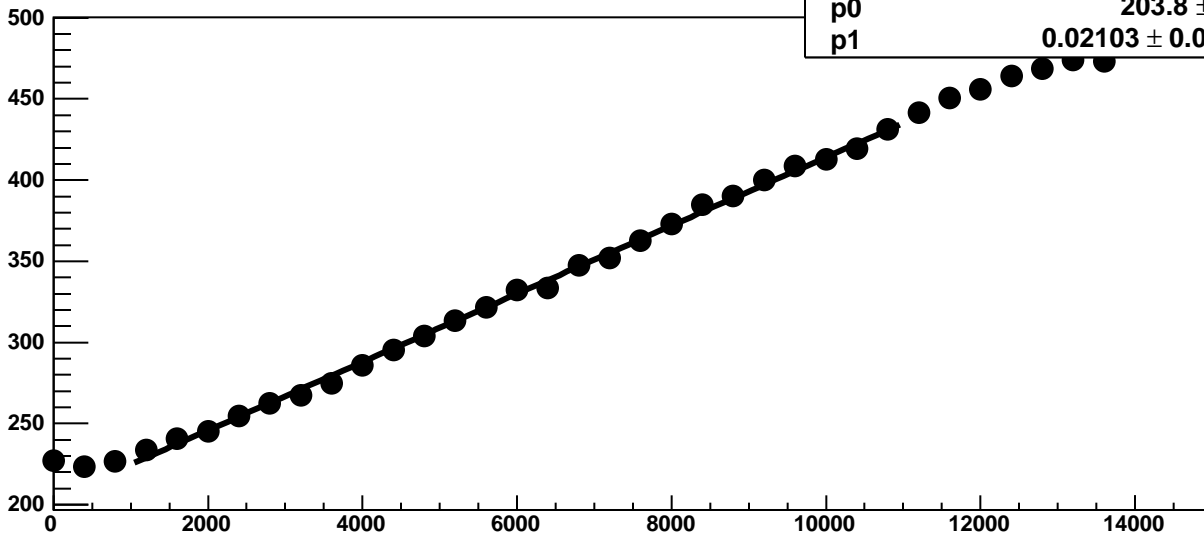
Chip 10, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

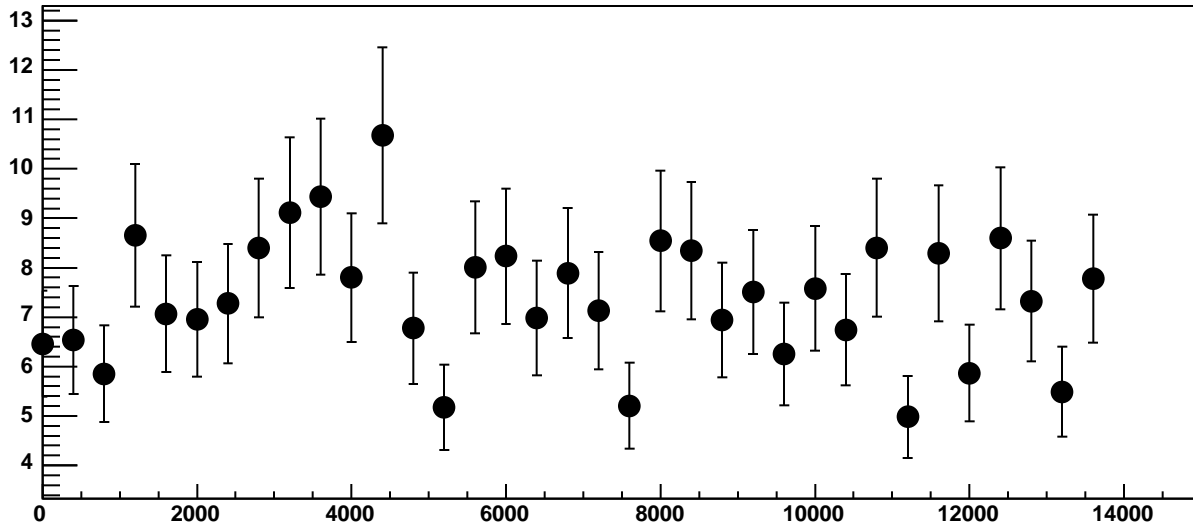


Chip 10, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC

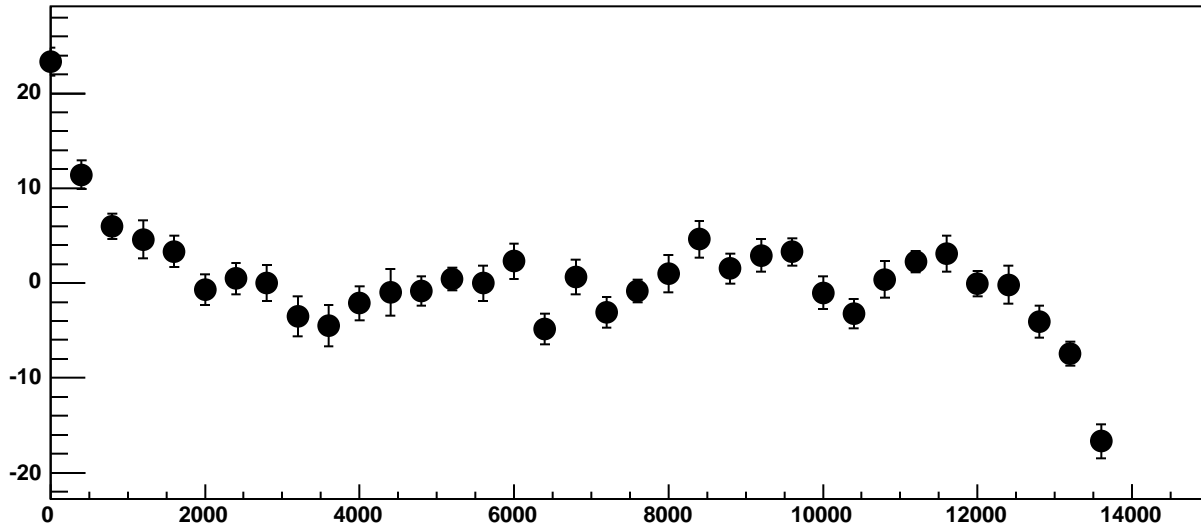


χ^2 / ndf 53.62 / 23
p0 203.8 ± 0.8131
p1 0.02103 ± 0.0001195

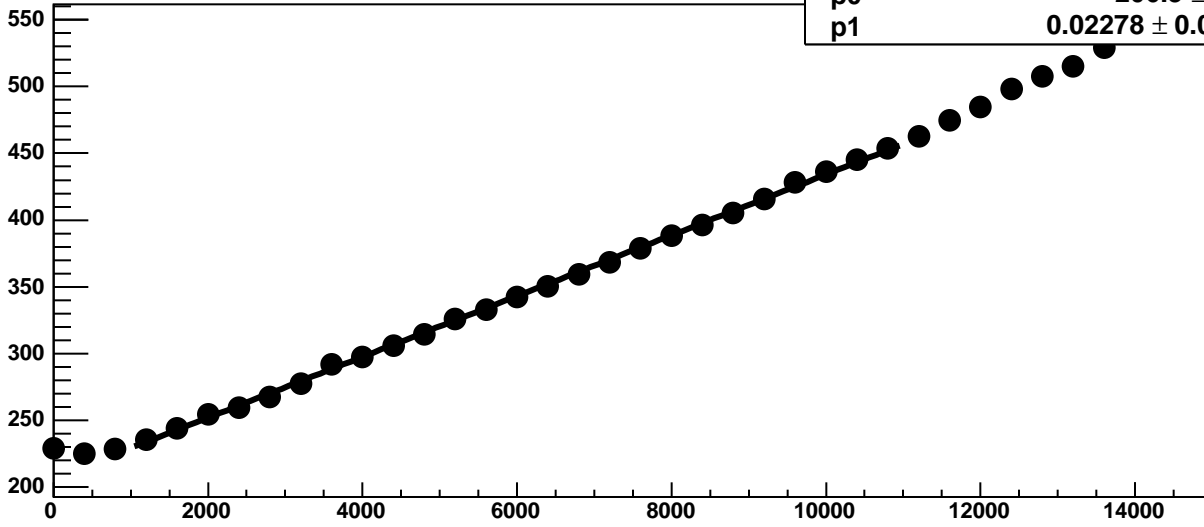
Chip 10, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

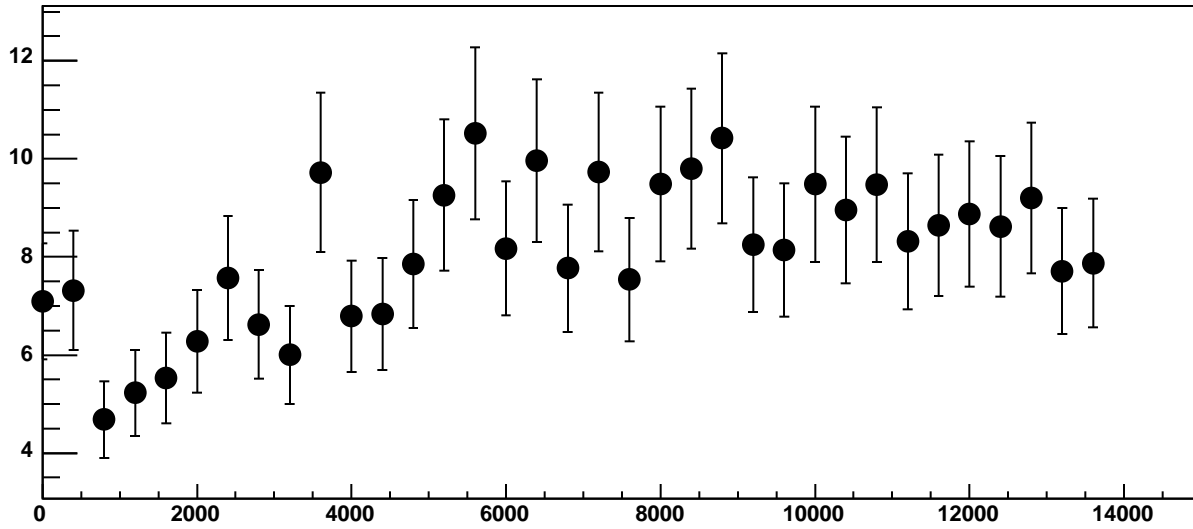


Chip 10, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

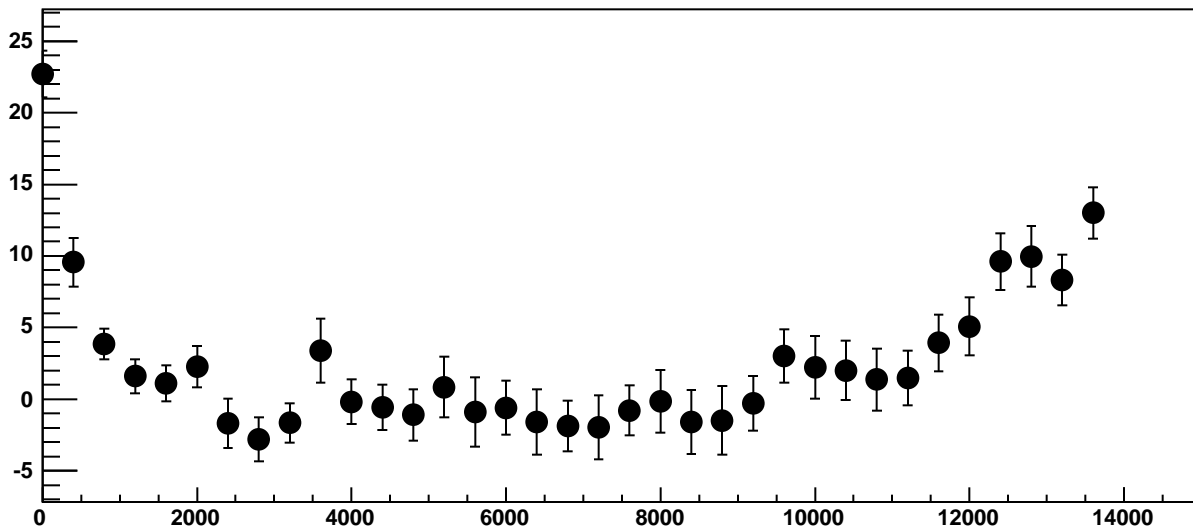


χ^2 / ndf 22.56 / 23
p0 206.3 ± 0.7014
p1 0.02278 ± 0.0001191

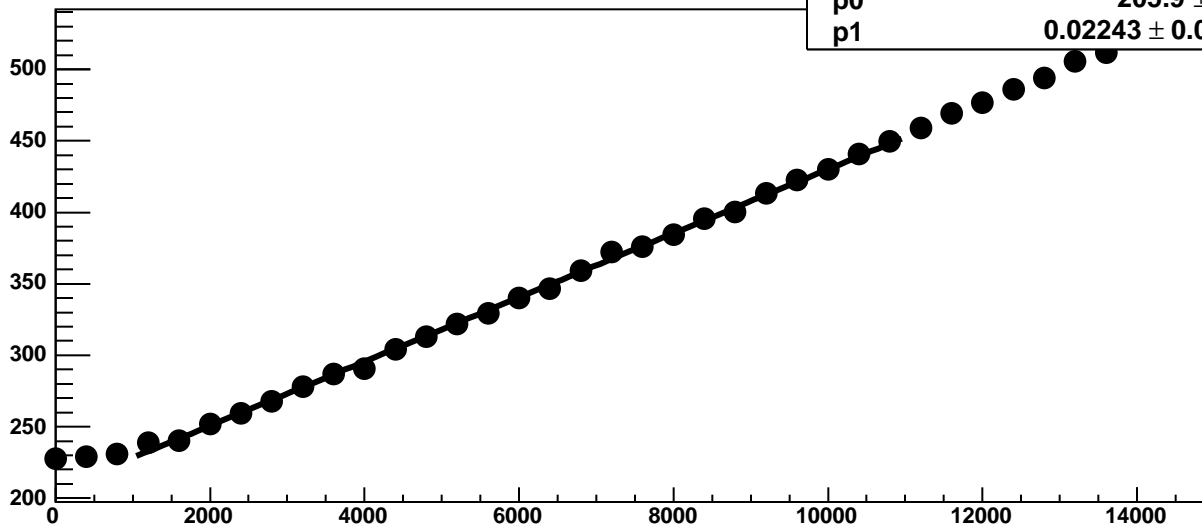
Chip 10, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

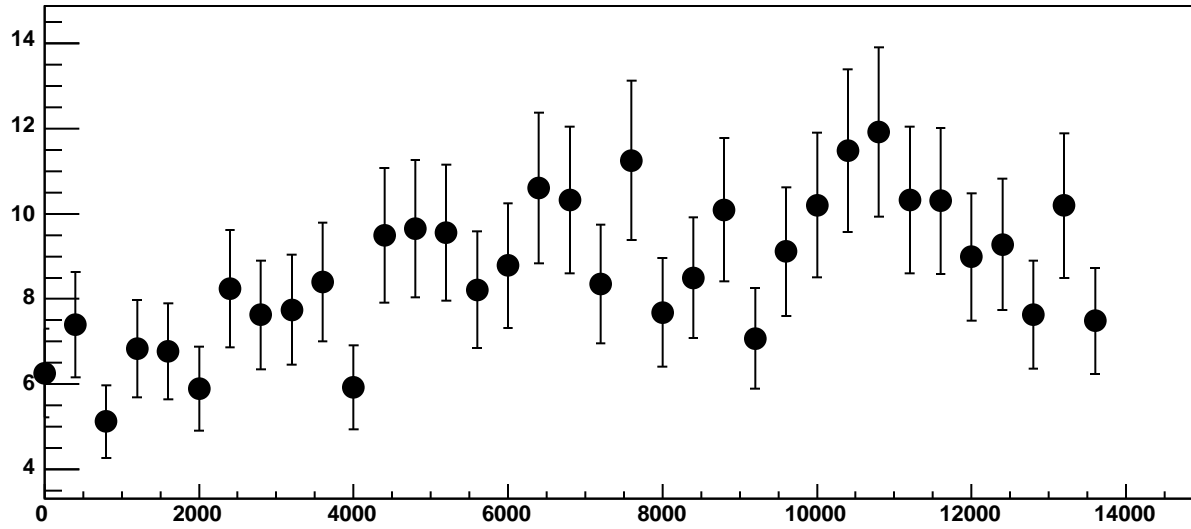


Chip 10, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

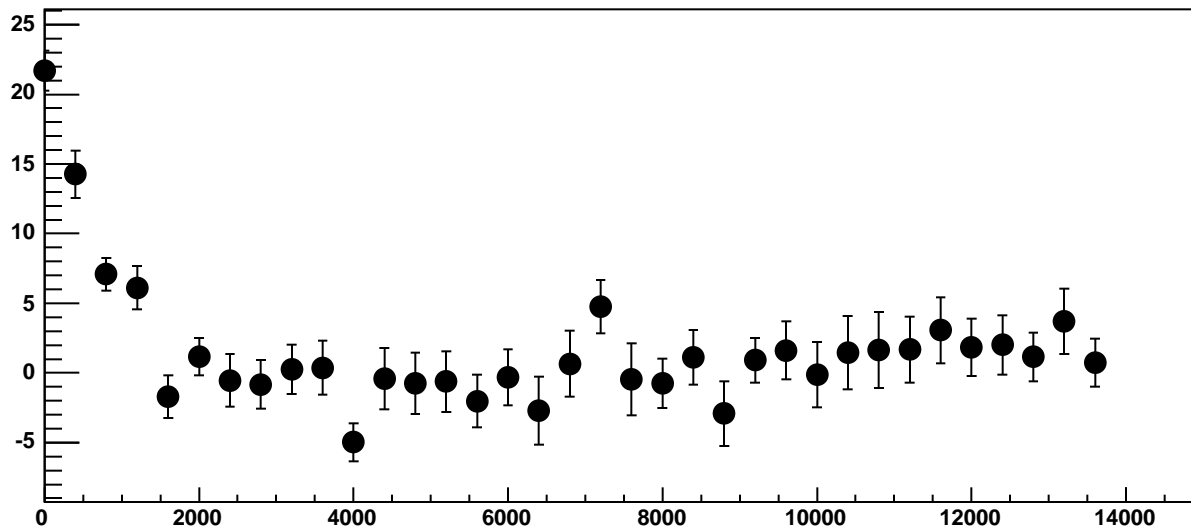


χ^2 / ndf 43.48 / 23
p0 205.9 ± 0.7942
p1 0.02243 ± 0.0001317

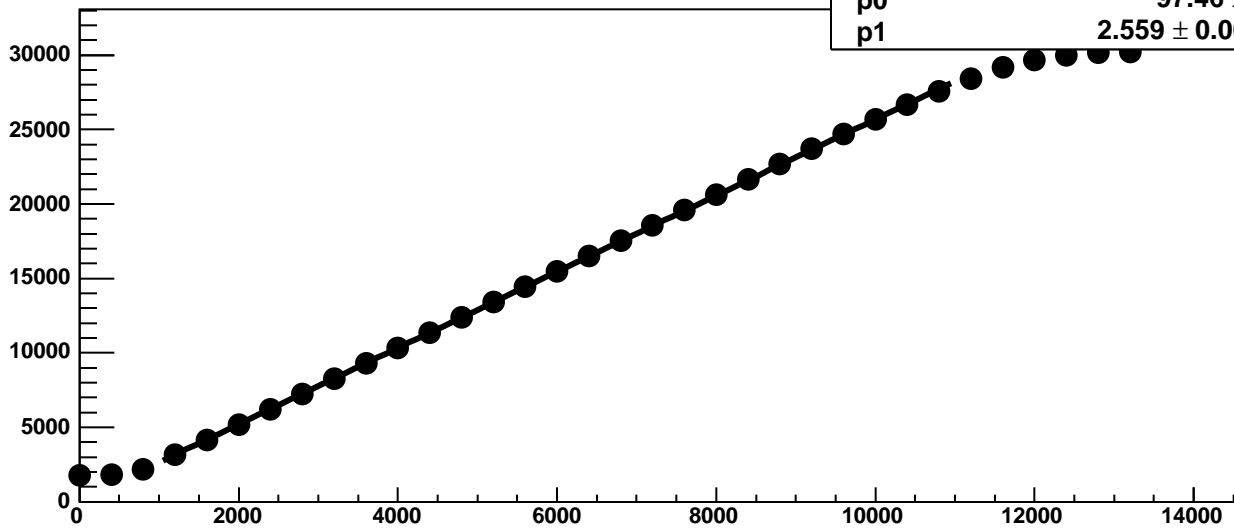
Chip 10, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



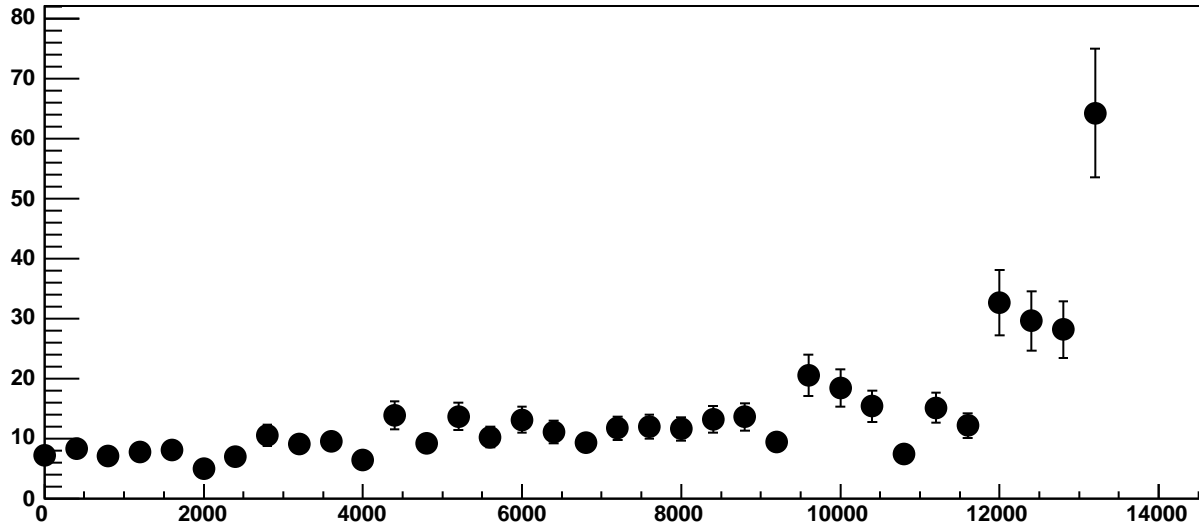
Chip 10, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



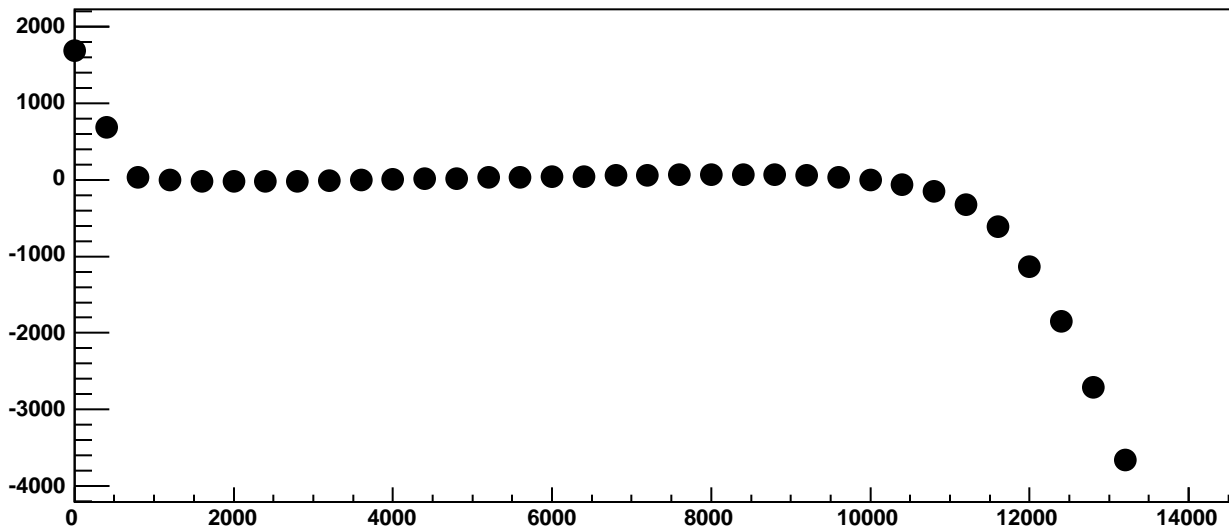
Chip 10, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC



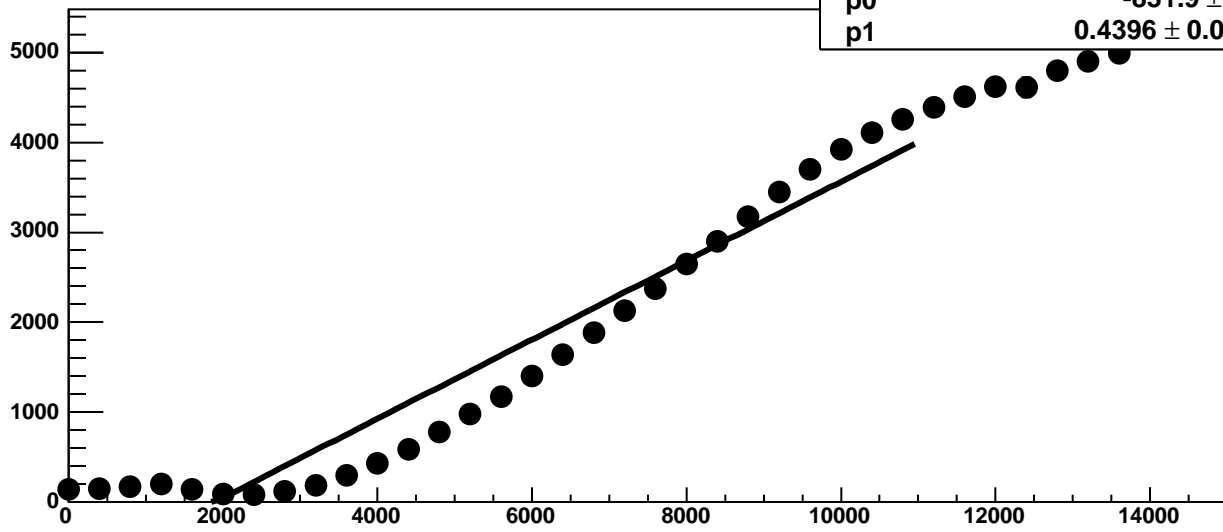
Chip 10, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



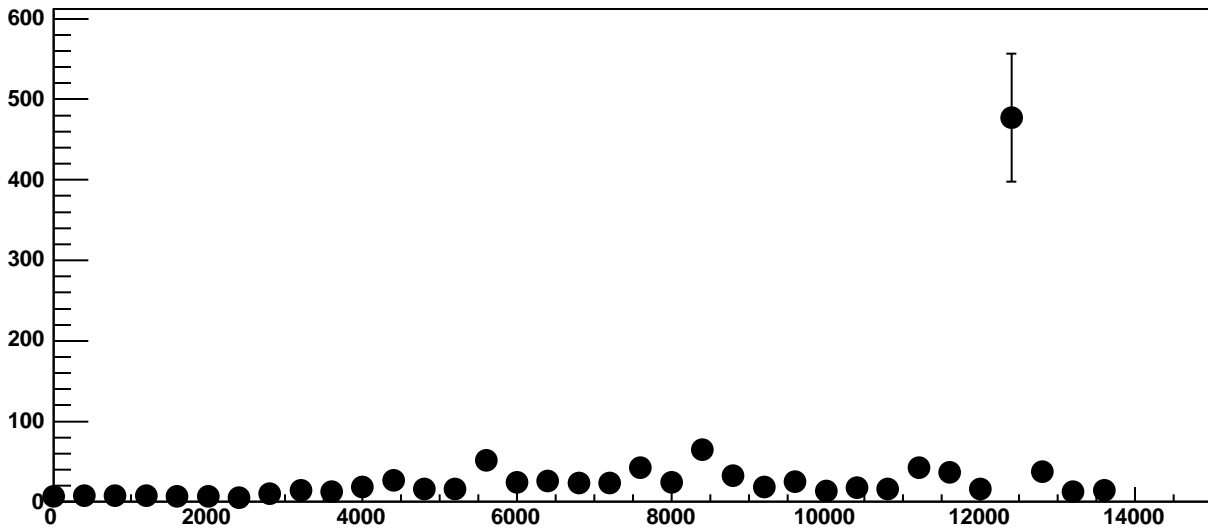
Chip 10, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC



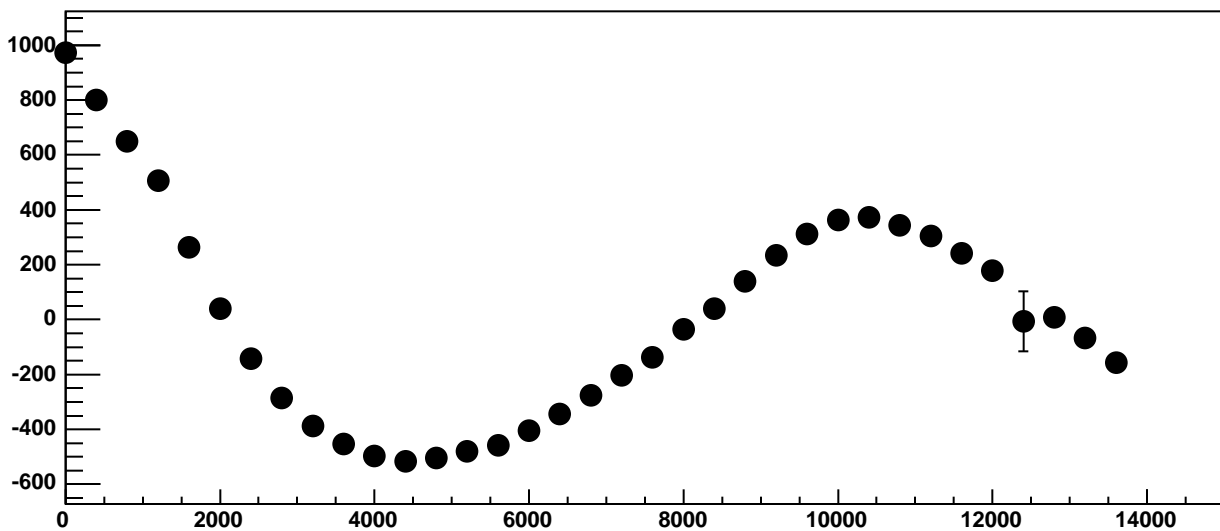
Chip 10, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



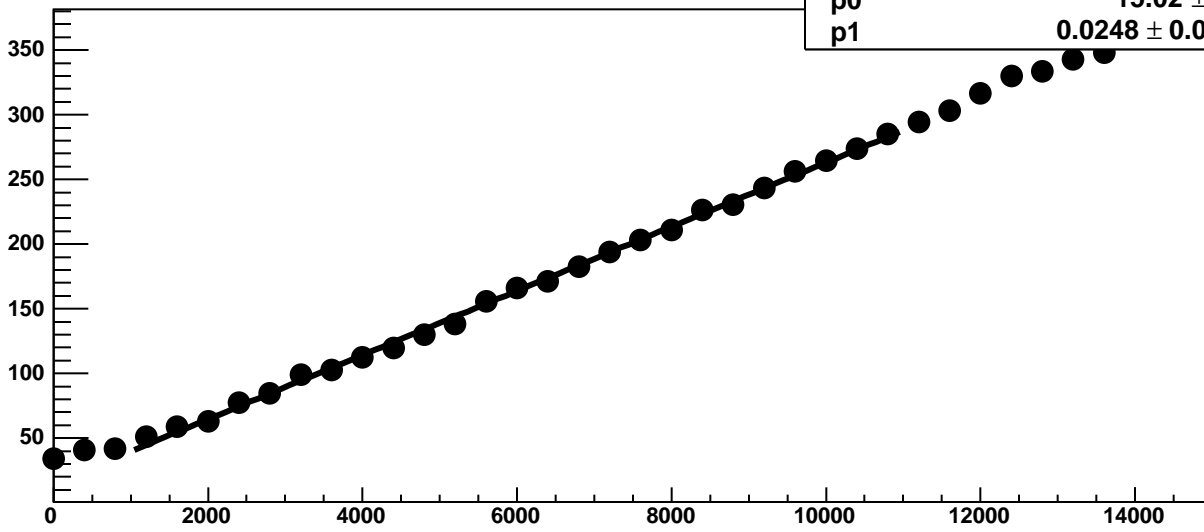
Chip 10, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

78.13 / 23

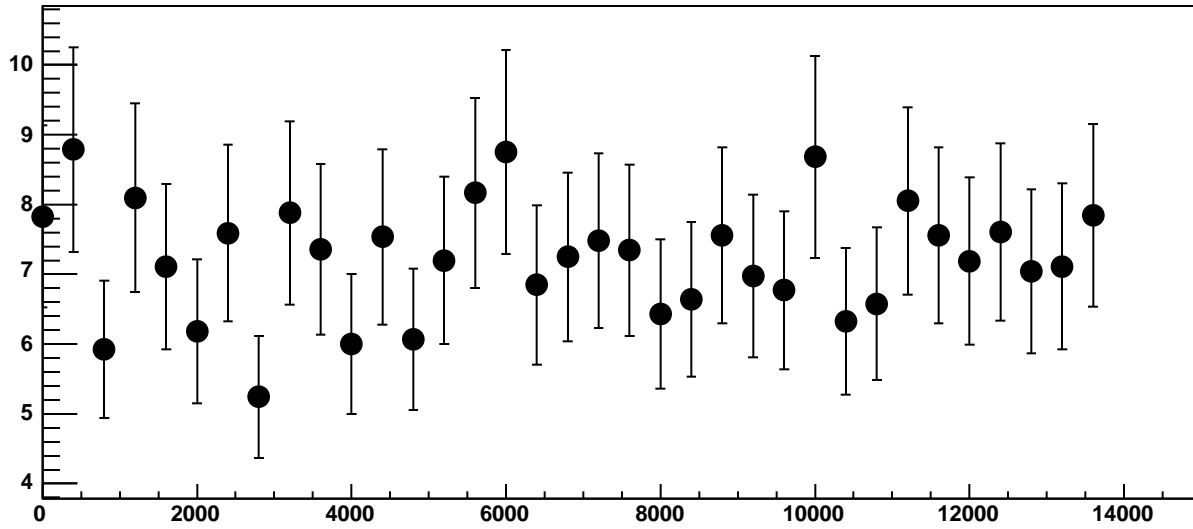
p0

15.02 ± 0.7273

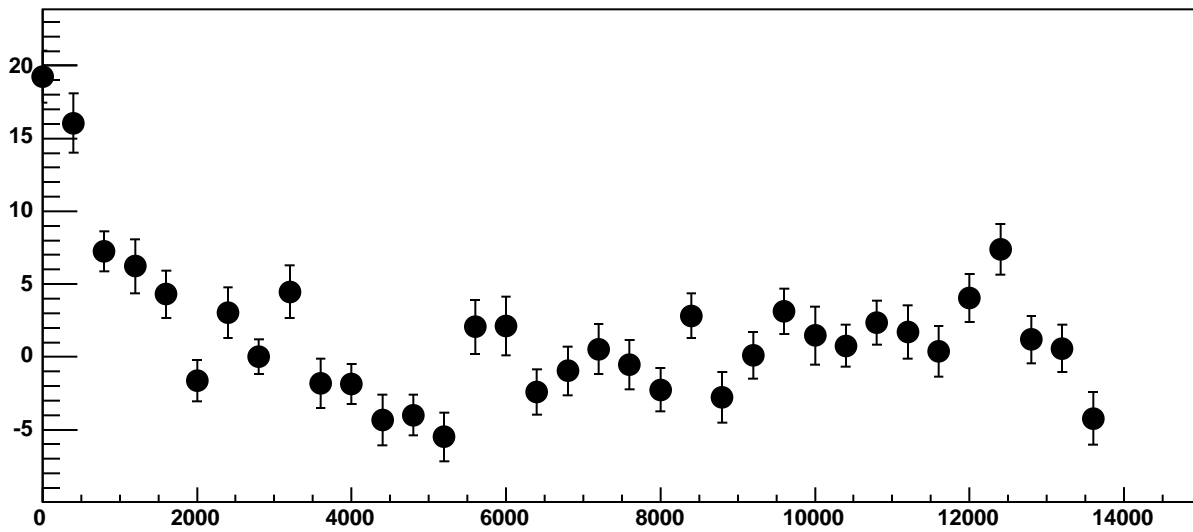
p1

0.0248 ± 0.0001098

Chip 10, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC

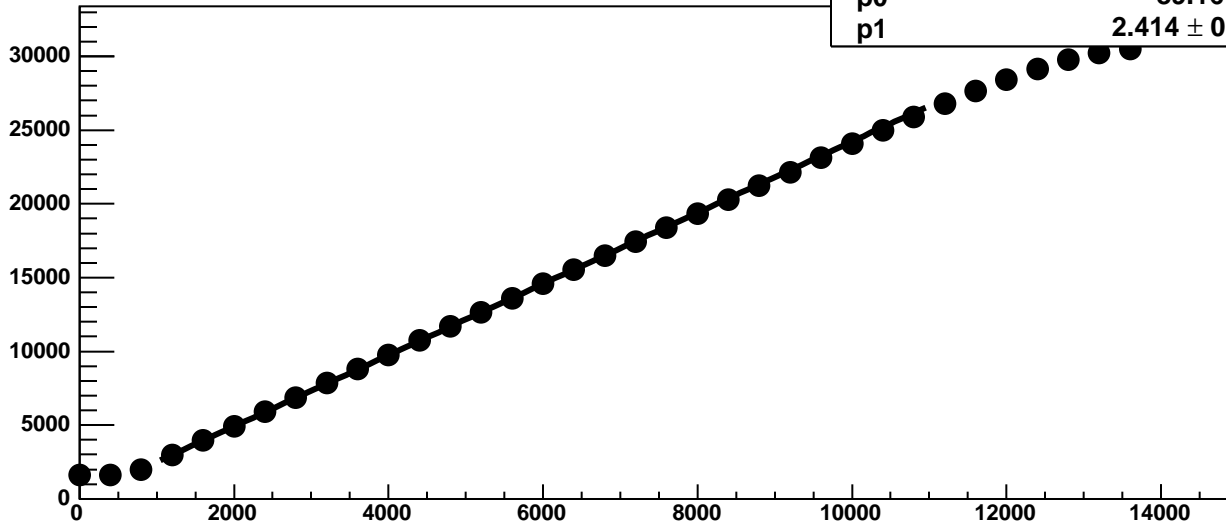


Chip 10, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

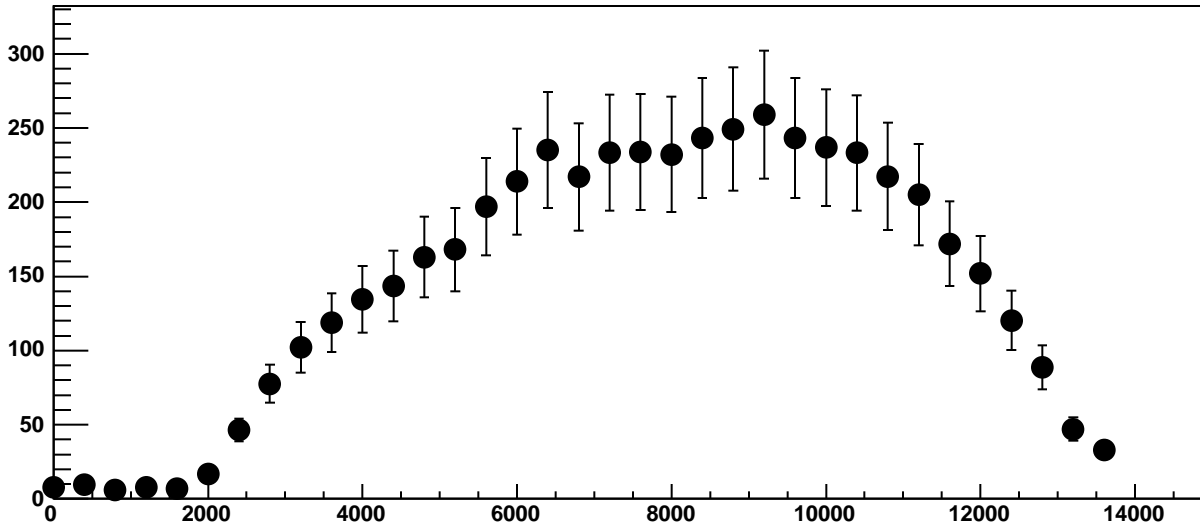


Chip 10, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC

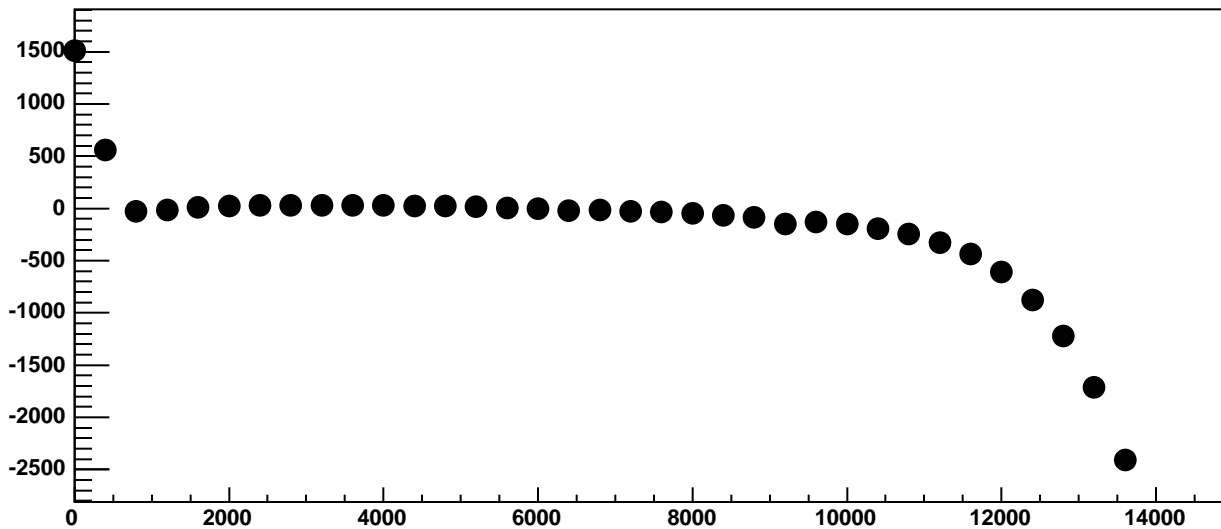
χ^2 / ndf 213.8 / 23
p0 89.16 ± 2.934
p1 2.414 ± 0.001738



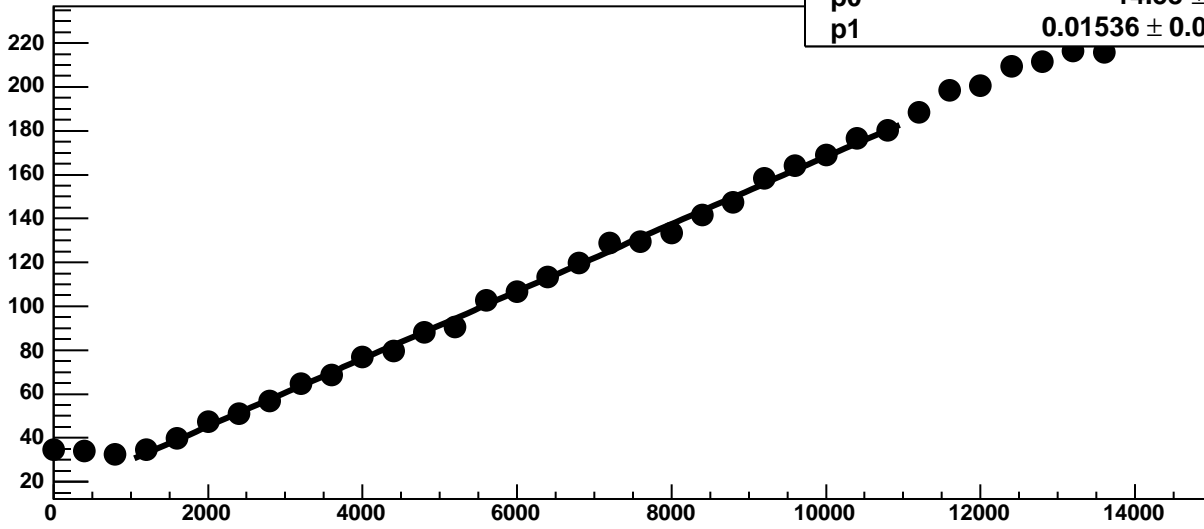
Chip 10, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC

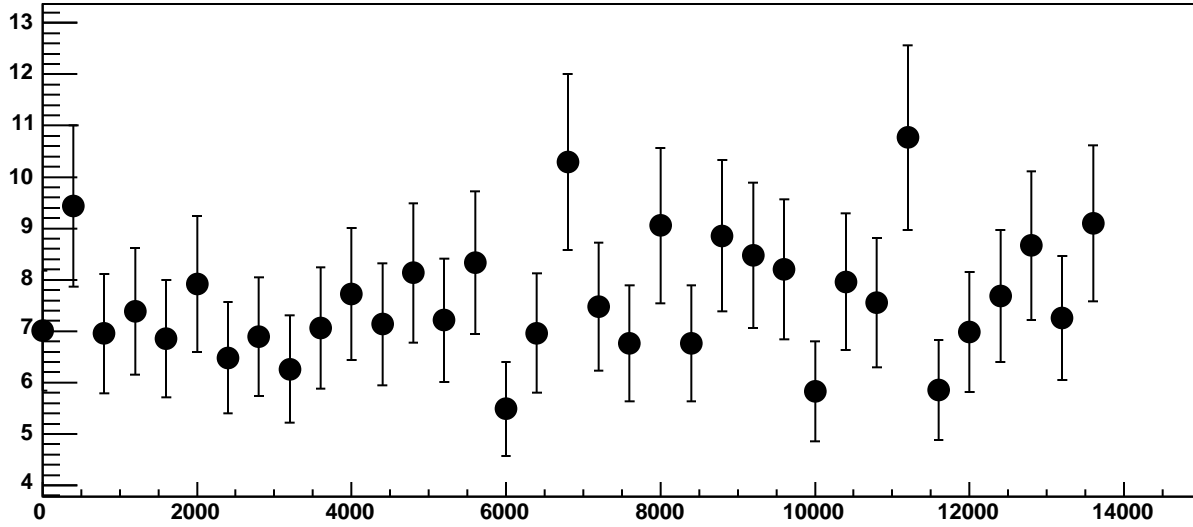


Chip 10, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

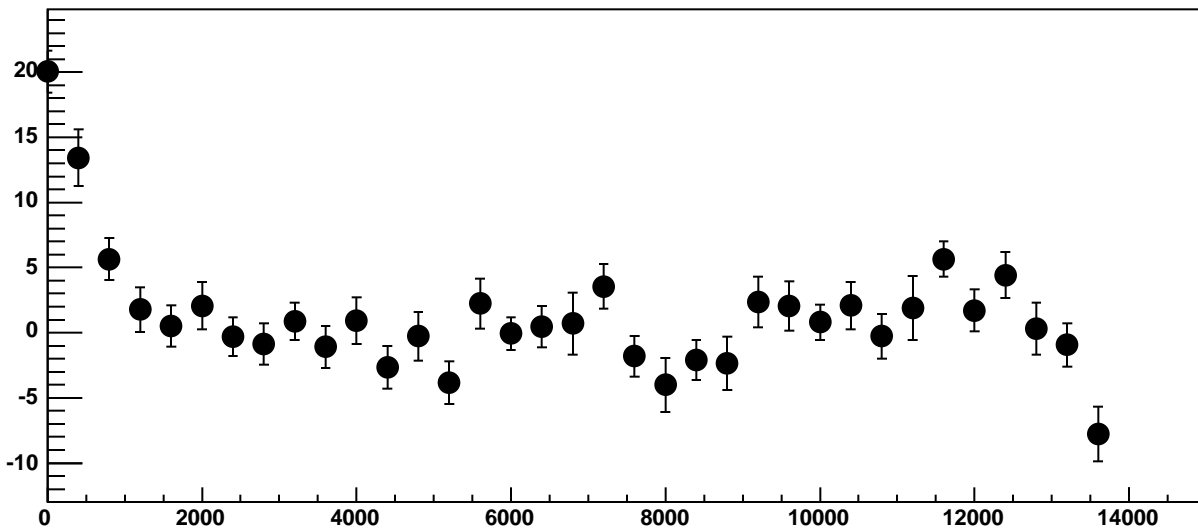


χ^2 / ndf 30.3 / 23
p0 14.58 ± 0.7536
p1 0.01536 ± 0.0001154

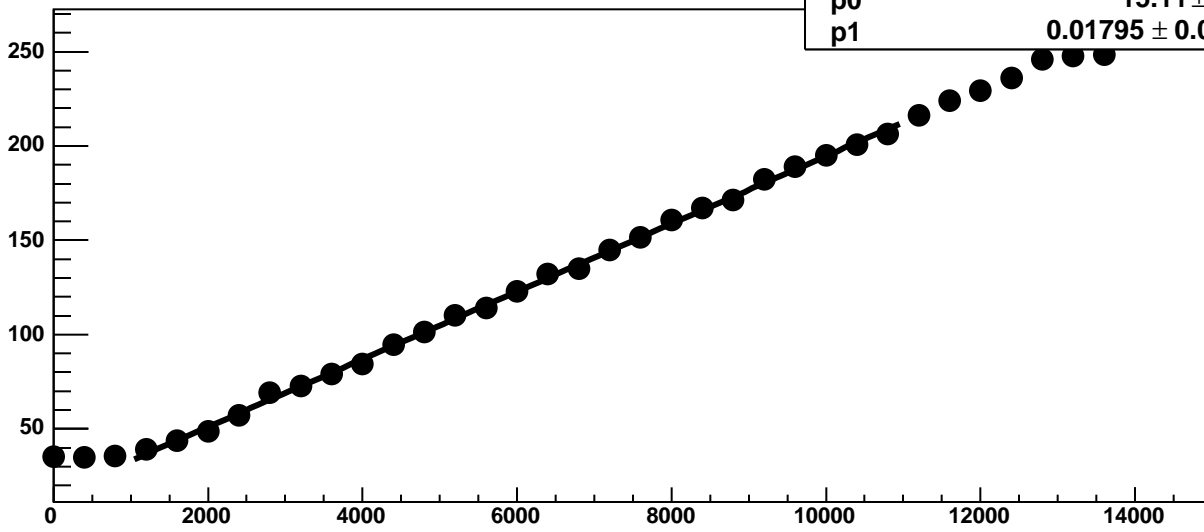
Chip 10, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

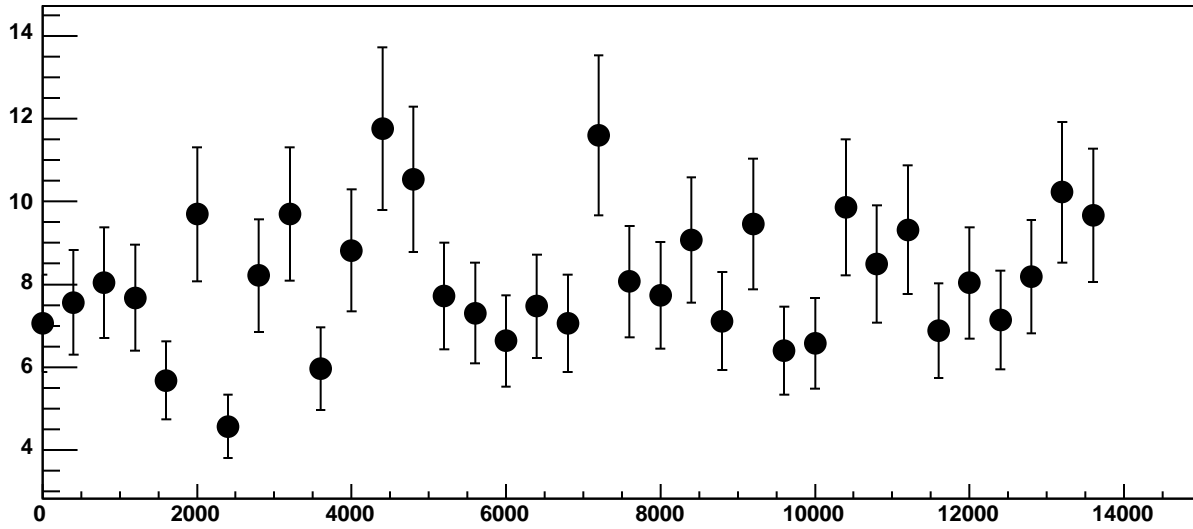


Chip 10, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

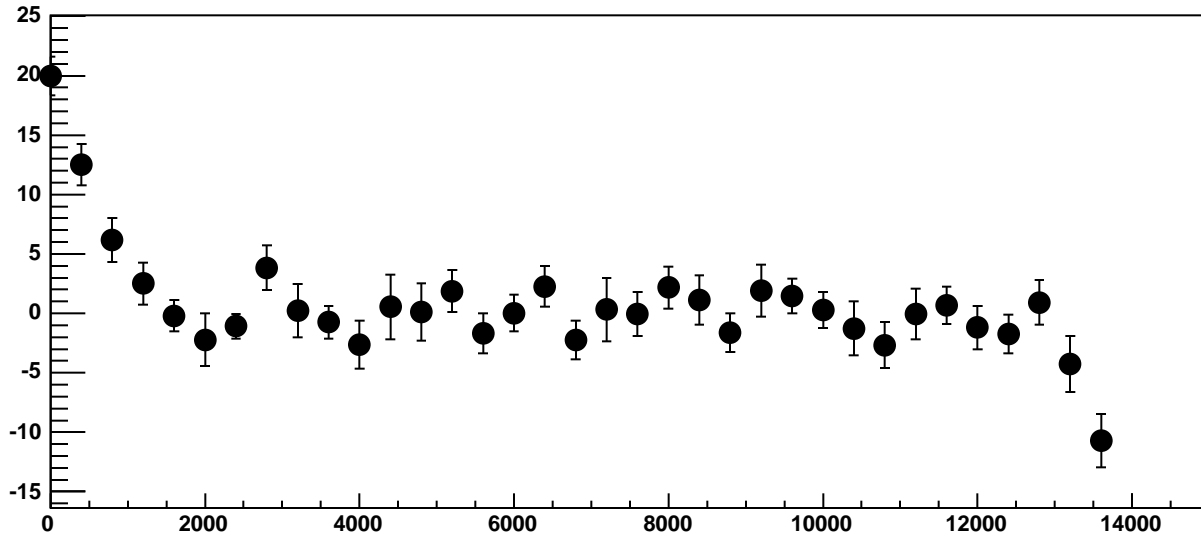


χ^2 / ndf 22.83 / 23
p0 15.11 ± 0.7438
p1 0.01795 ± 0.0001161

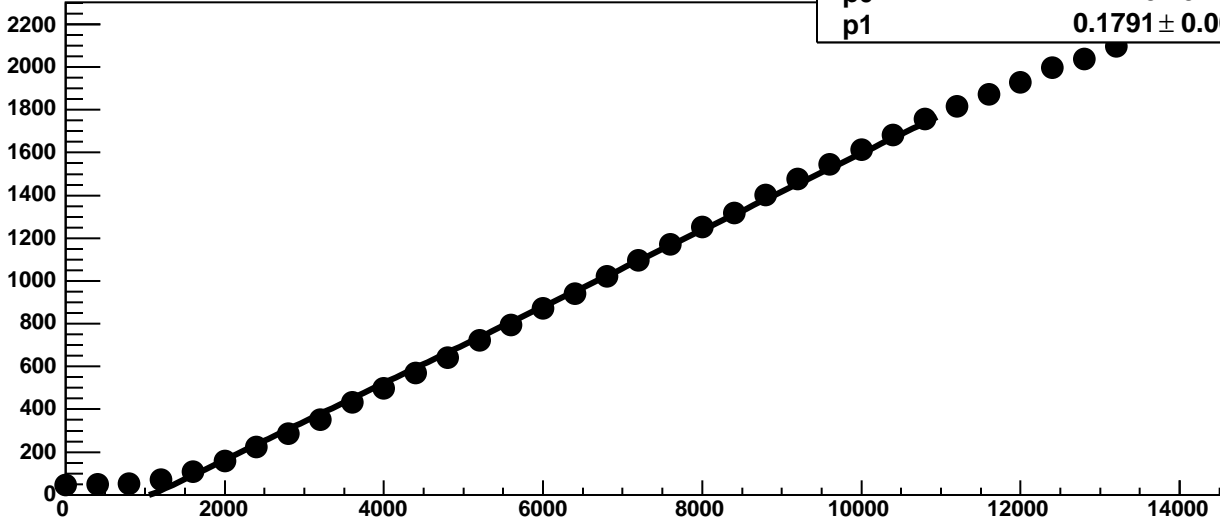
Chip 10, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



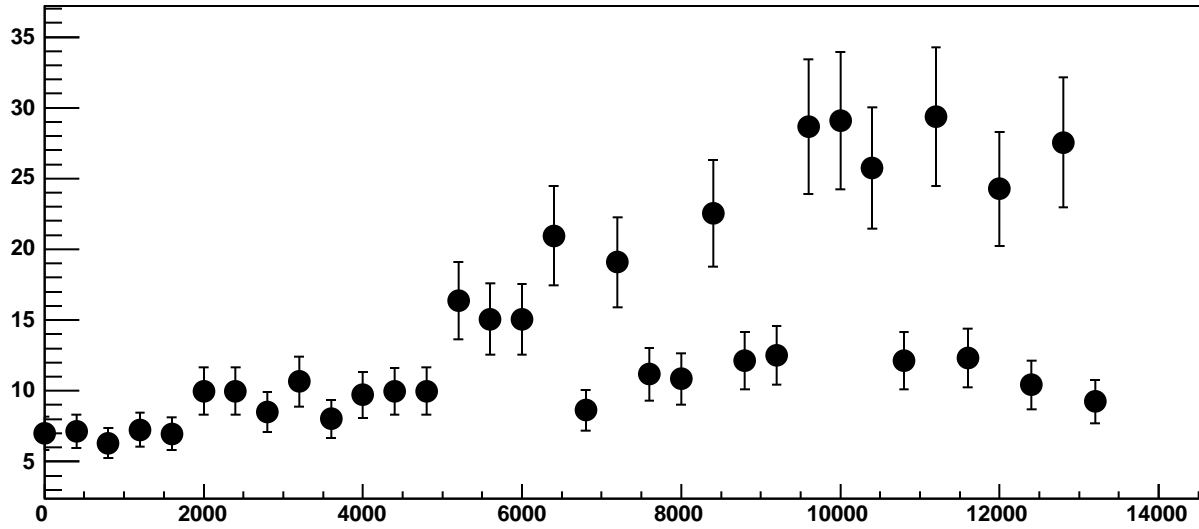
Chip 10, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



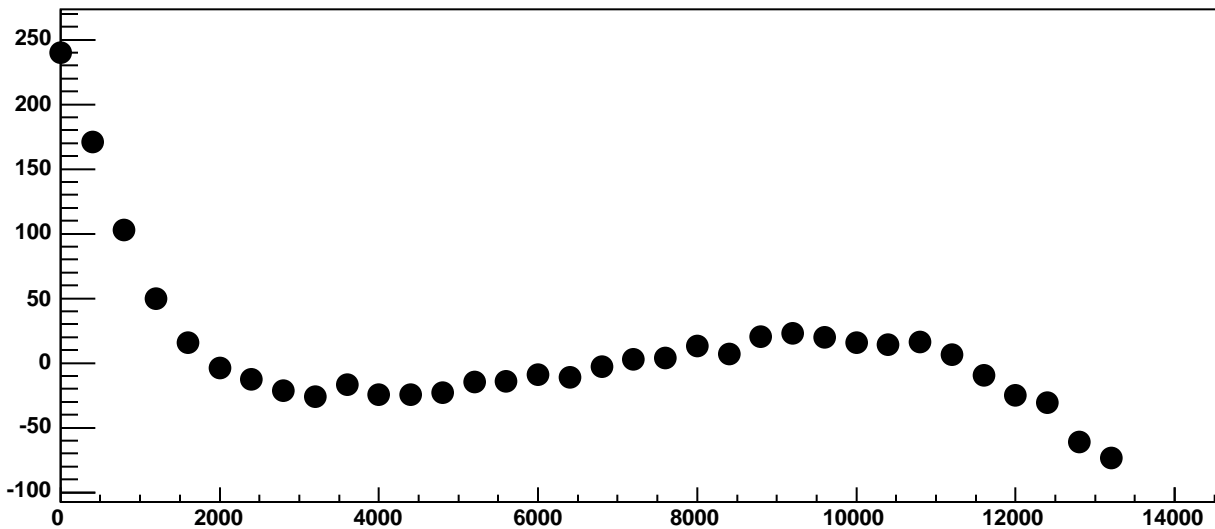
Chip 10, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC



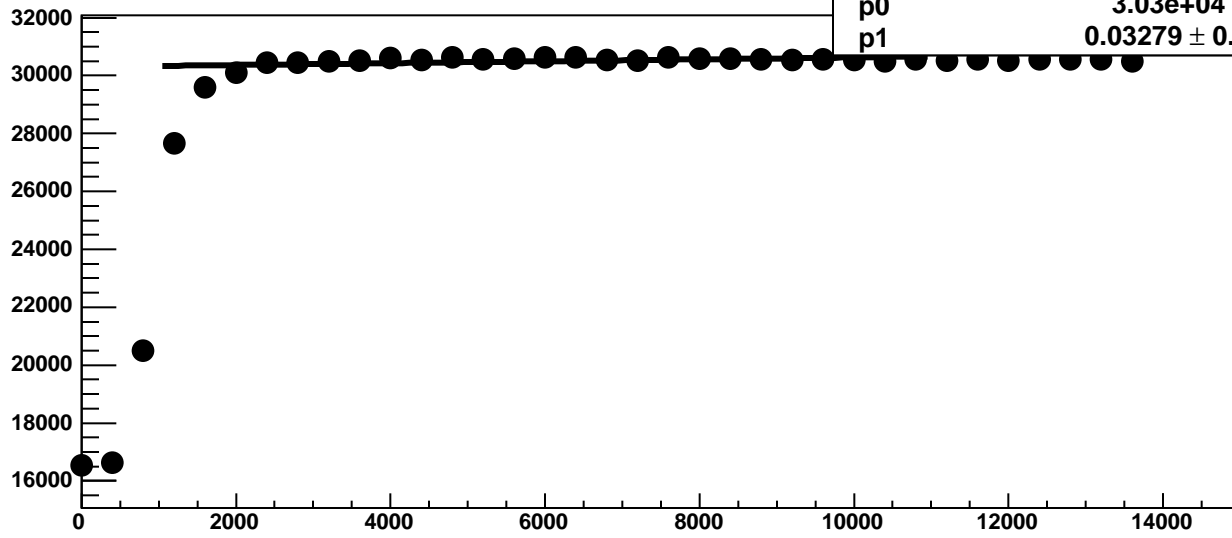
Chip 10, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1054 / 23

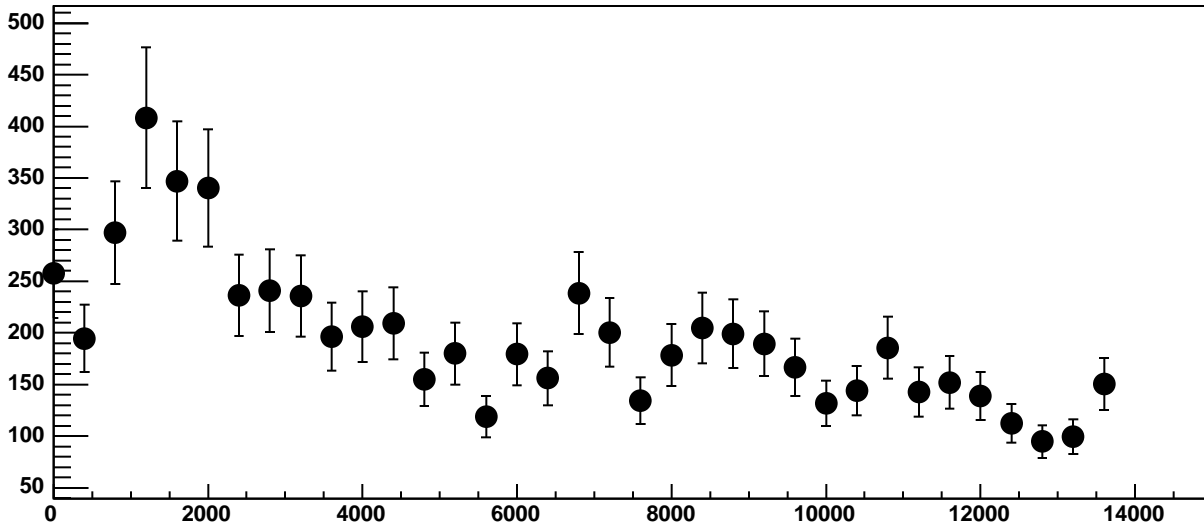
p0

$3.03\text{e}+04 \pm 24.16$

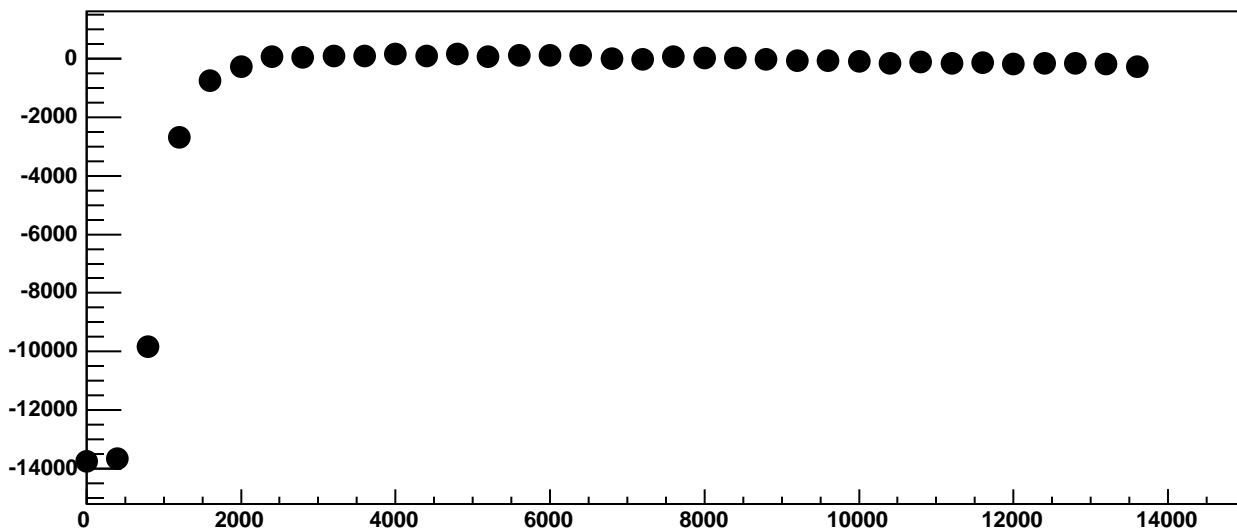
p1

0.03279 ± 0.003307

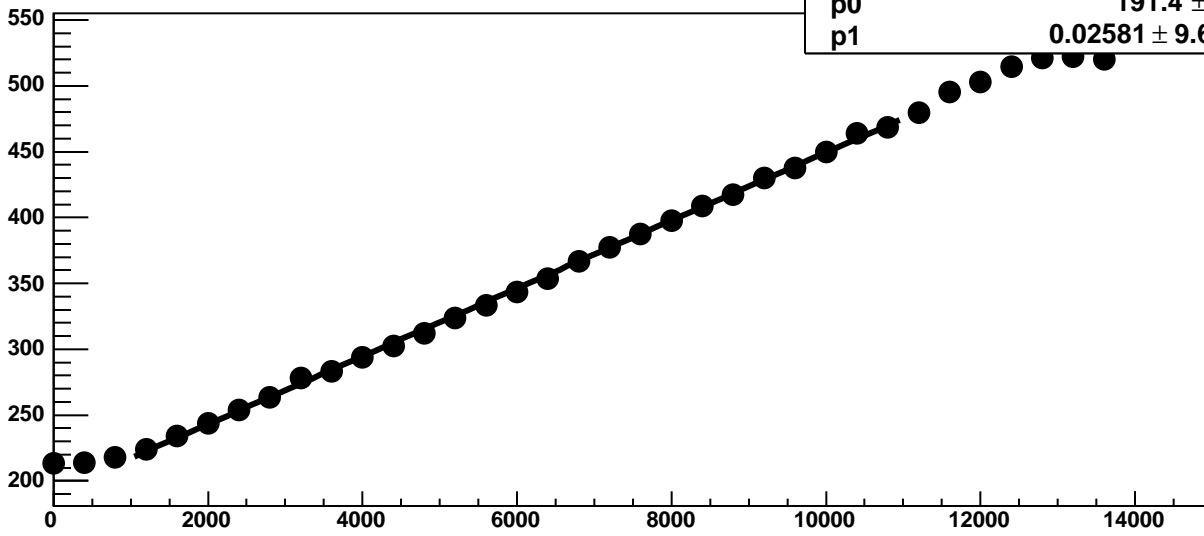
Chip 10, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC

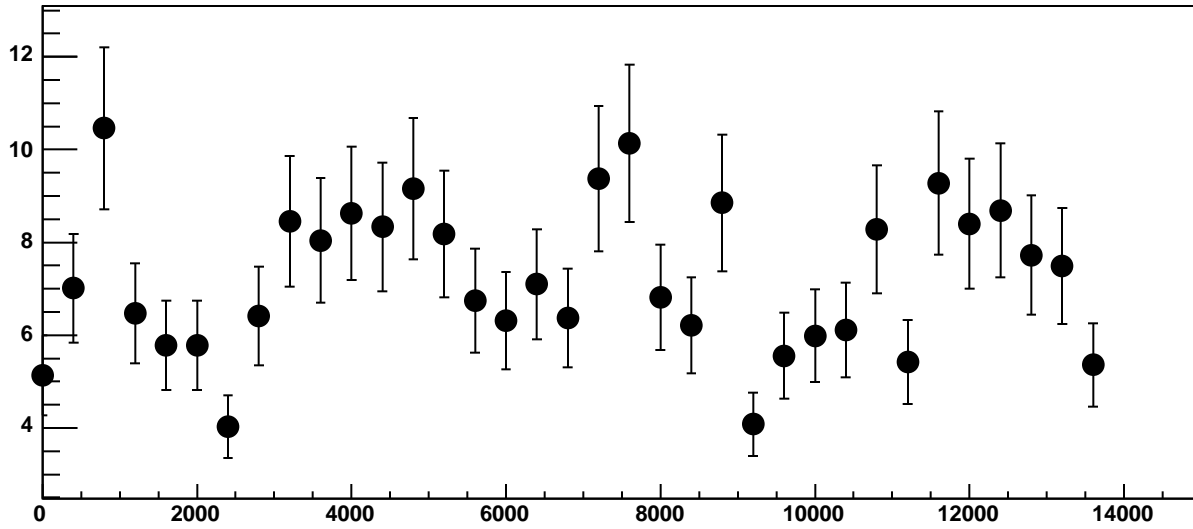


Chip 10, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

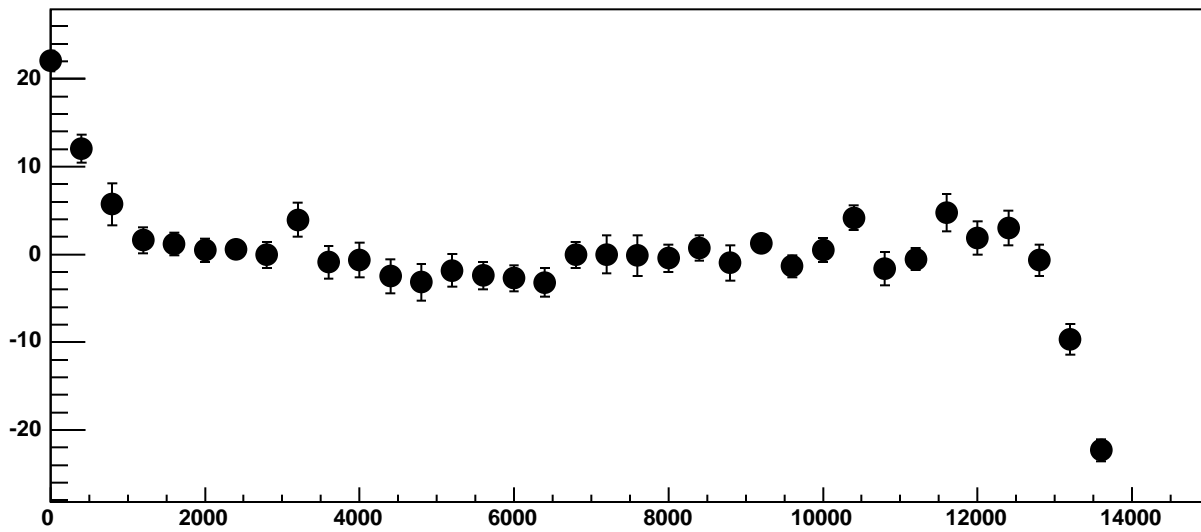


χ^2 / ndf 34.91 / 23
p0 191.4 ± 0.6463
p1 0.02581 ± 9.604e-05

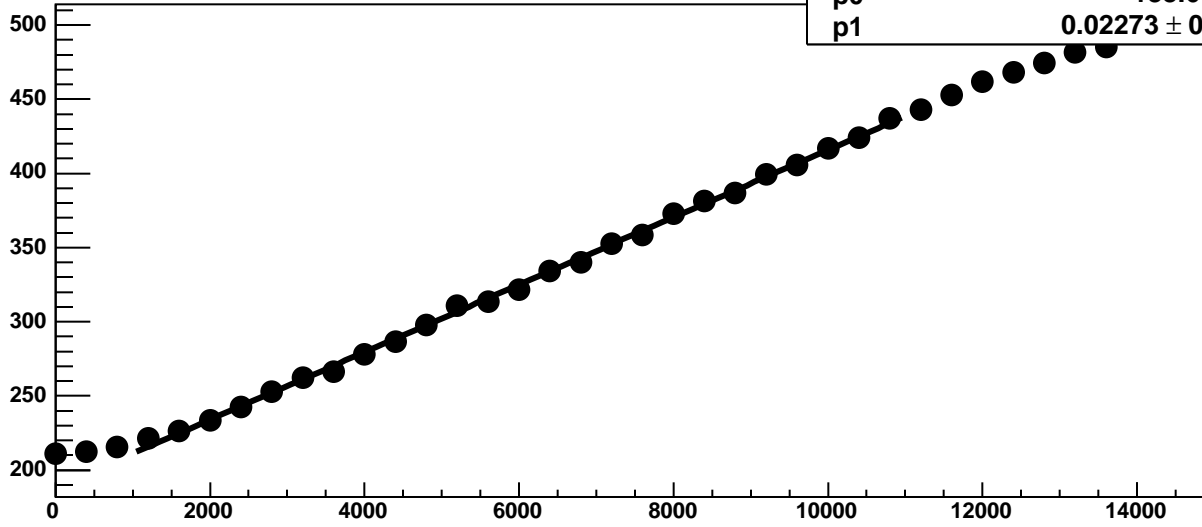
Chip 10, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

52.34 / 23

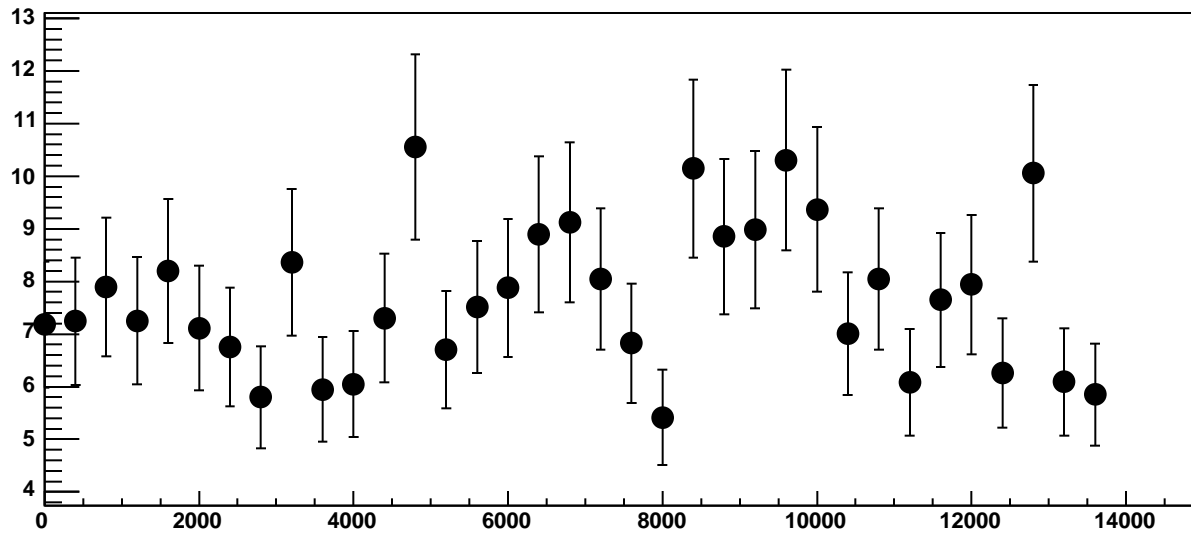
p0

188.6 ± 0.764

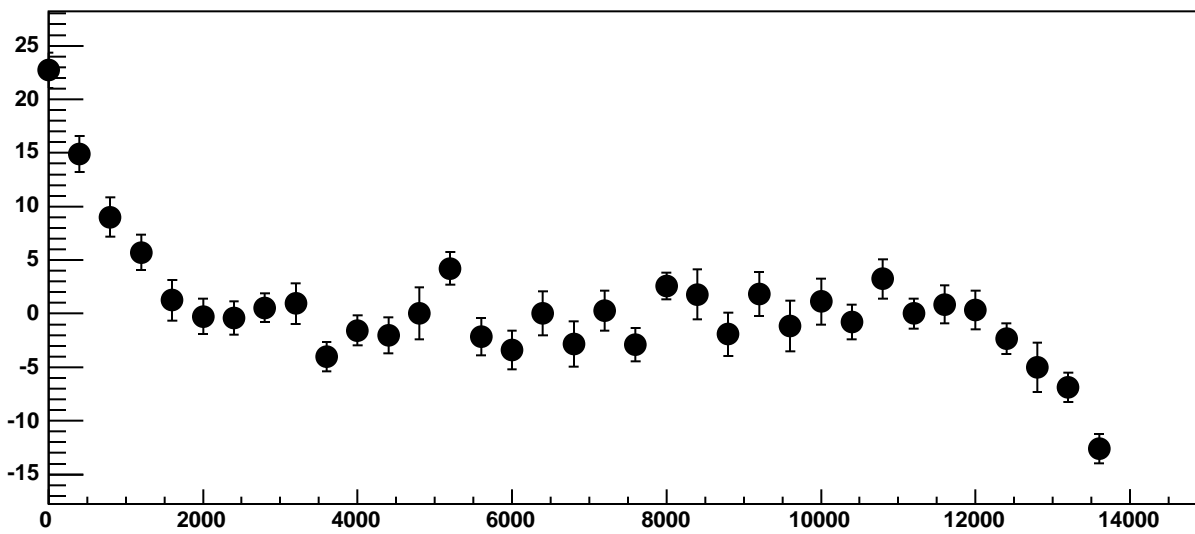
p1

0.02273 ± 0.000121

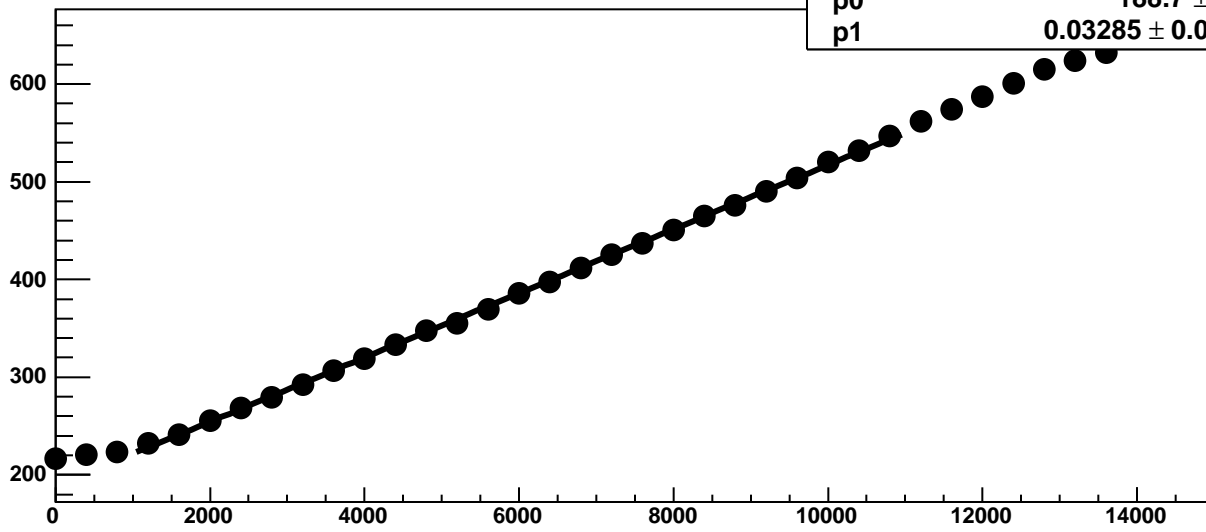
Chip 10, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

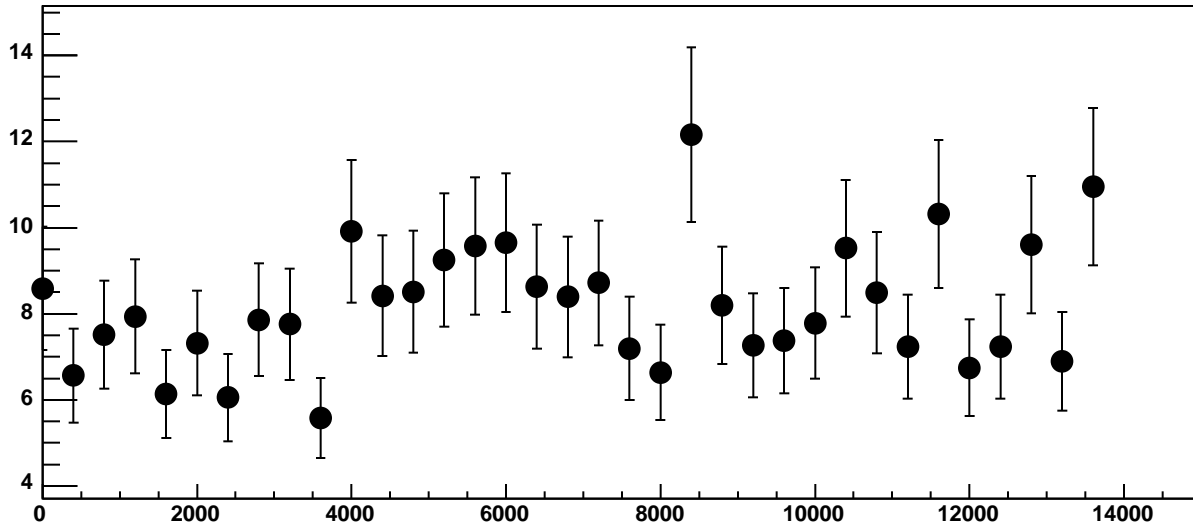


Chip 10, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

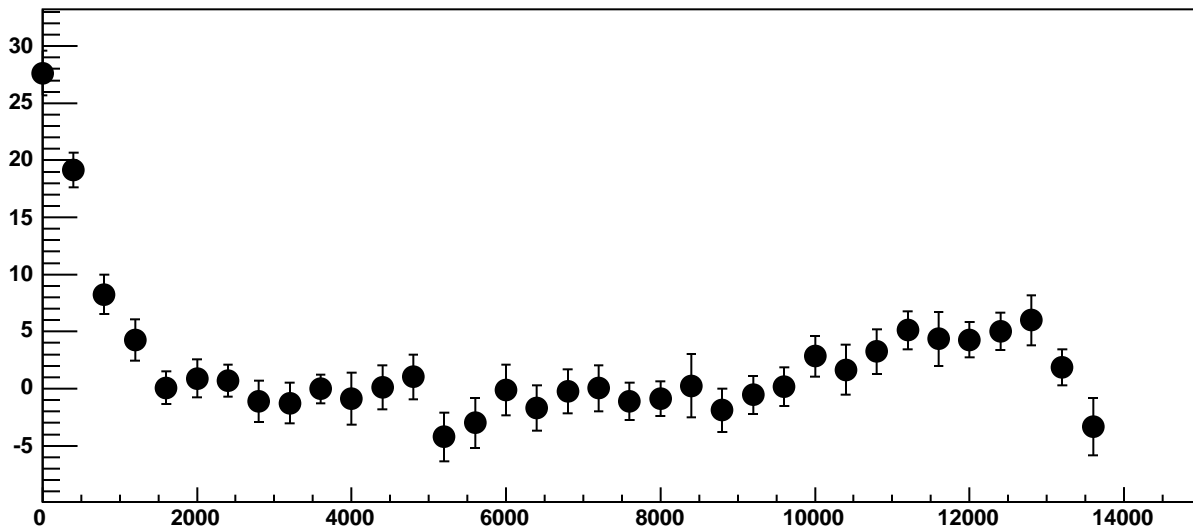


χ^2 / ndf 21.75 / 23
p0 188.7 ± 0.7703
p1 0.03285 ± 0.0001205

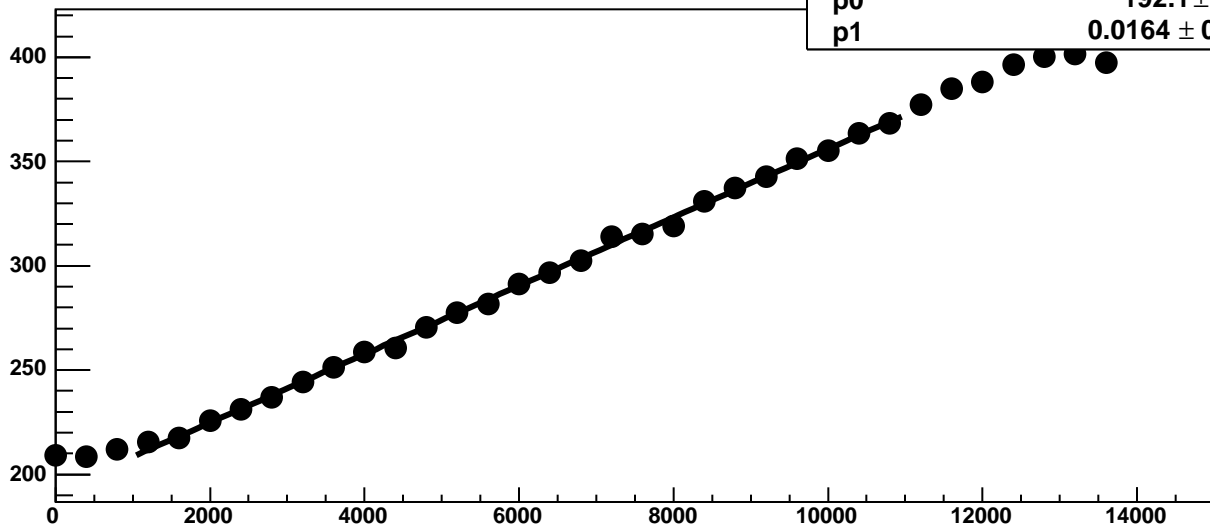
Chip 10, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

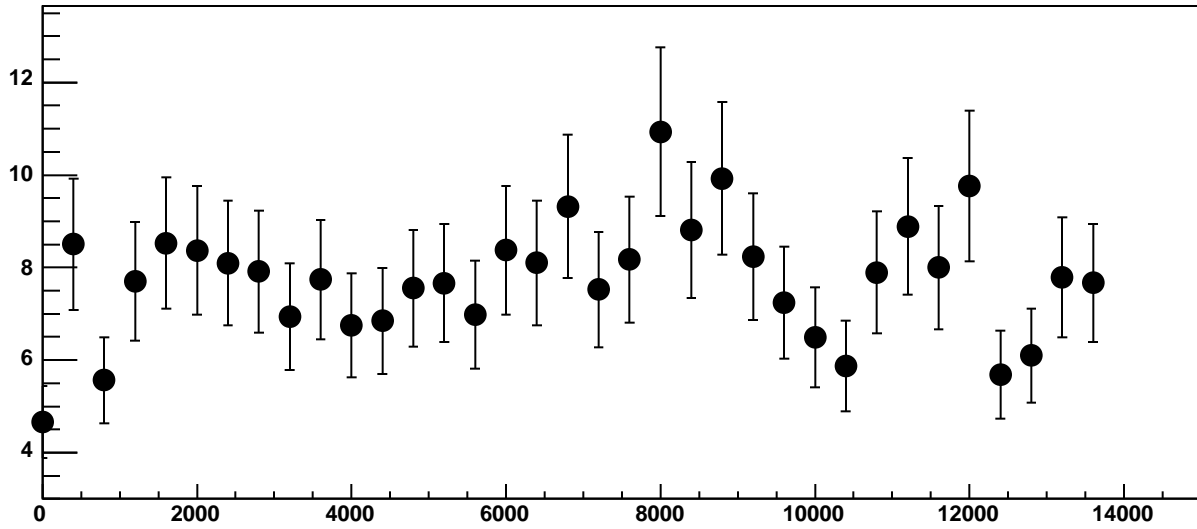


Chip 10, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

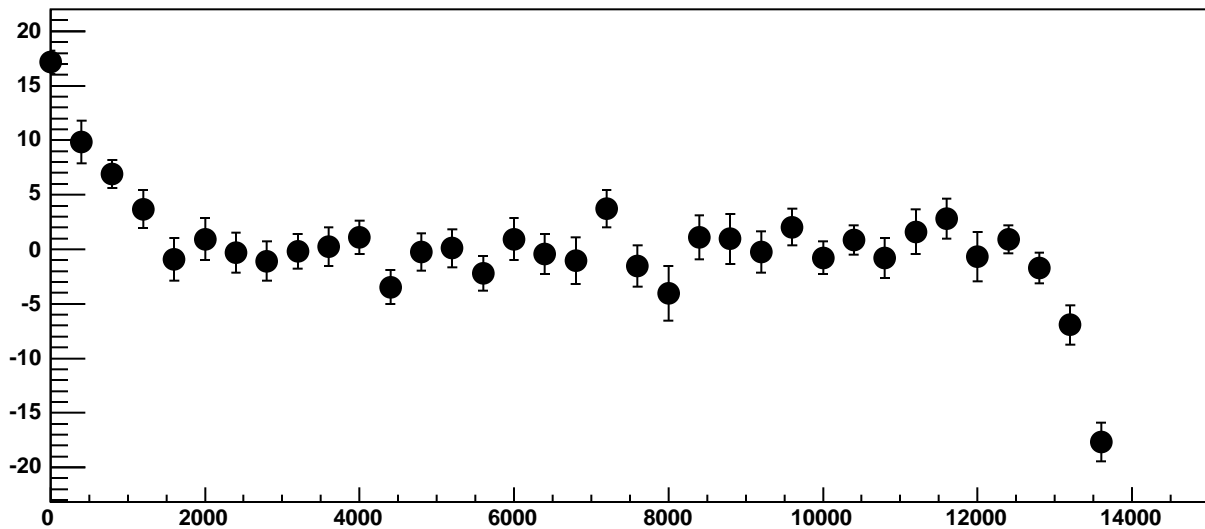


χ^2 / ndf 23.8 / 23
p0 192.1 ± 0.8099
p1 0.0164 ± 0.00012

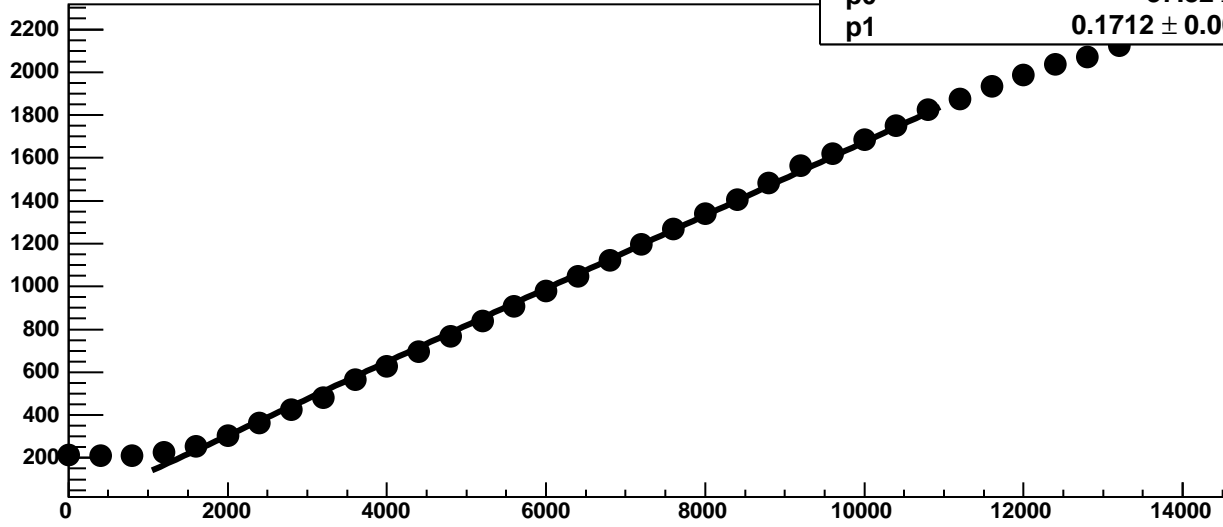
Chip 10, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



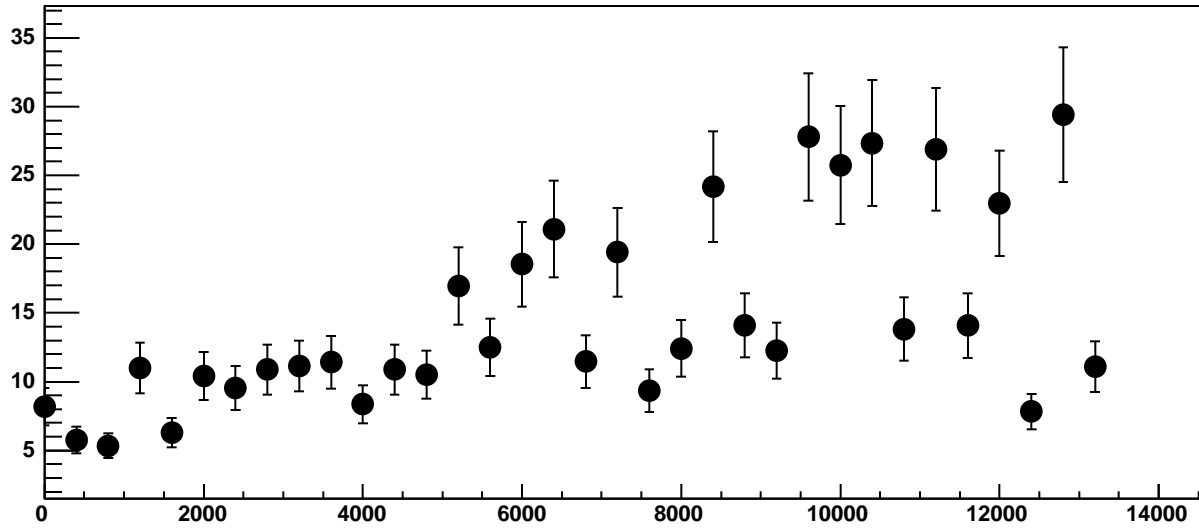
Chip 10, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



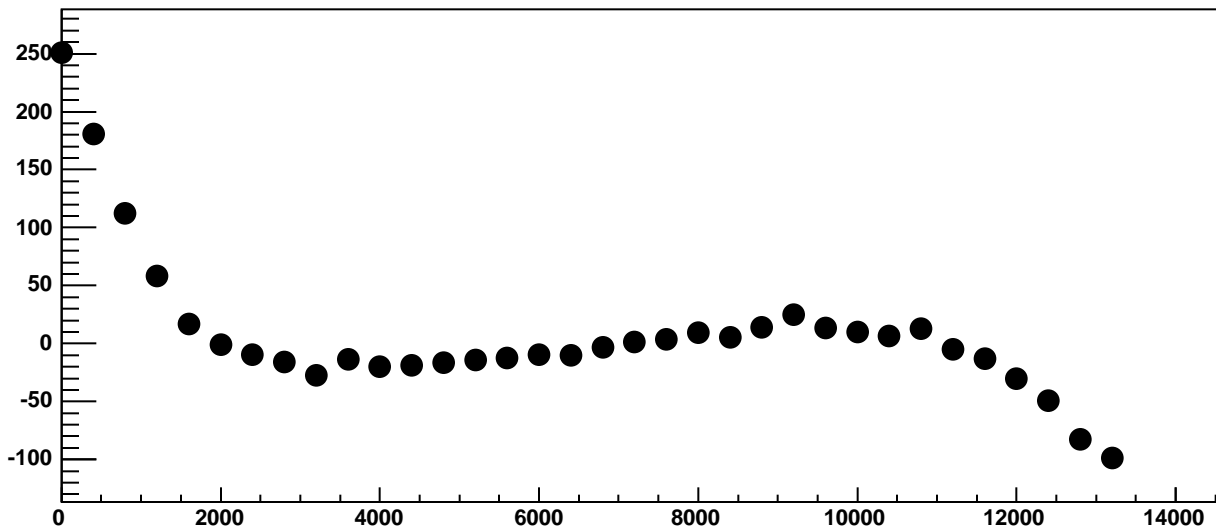
Chip 10, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



Chip 10, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

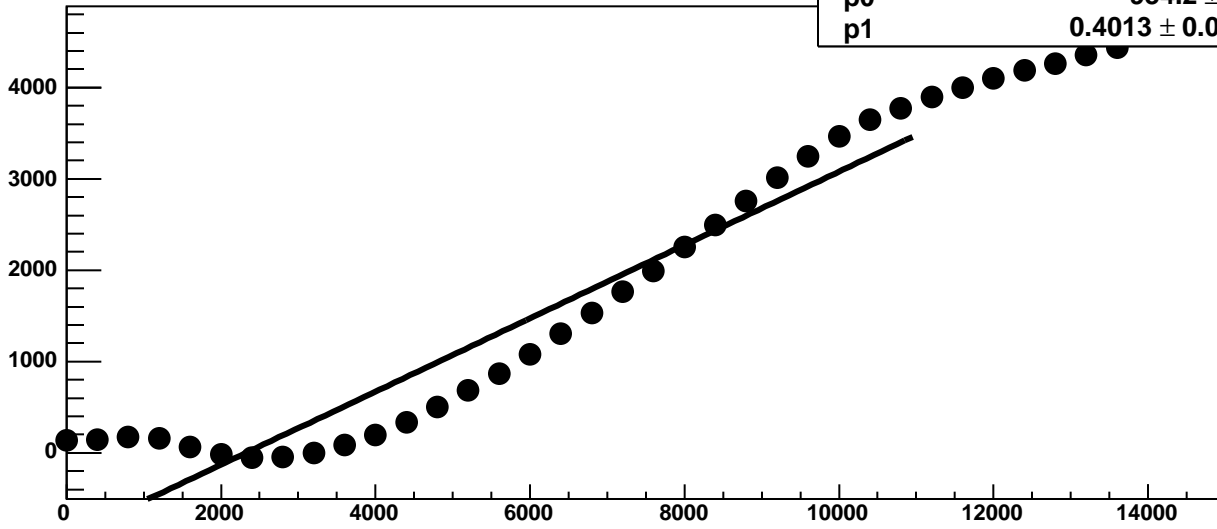


Chip 10, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

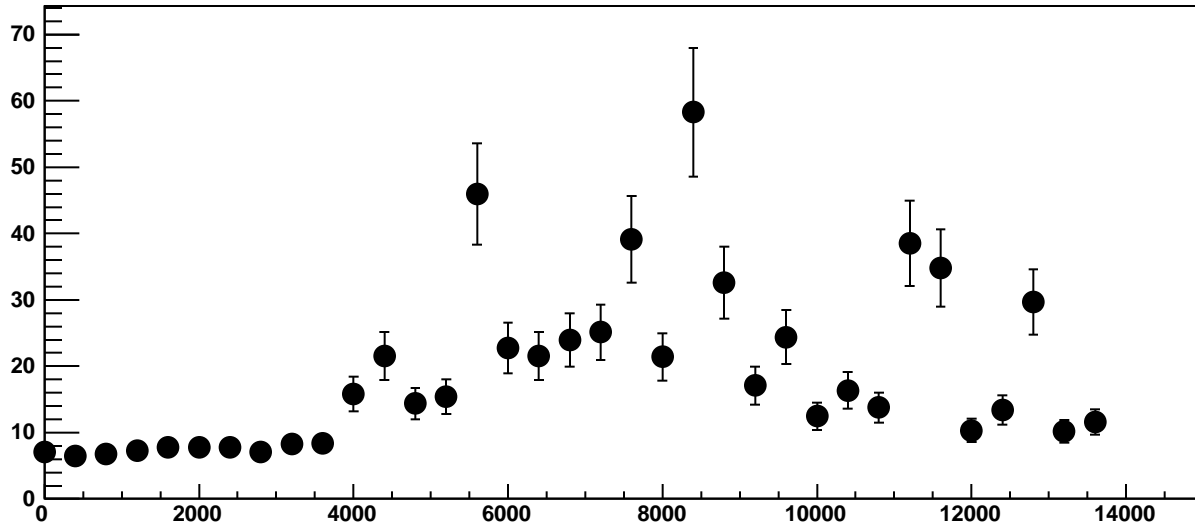


Chip 10, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC

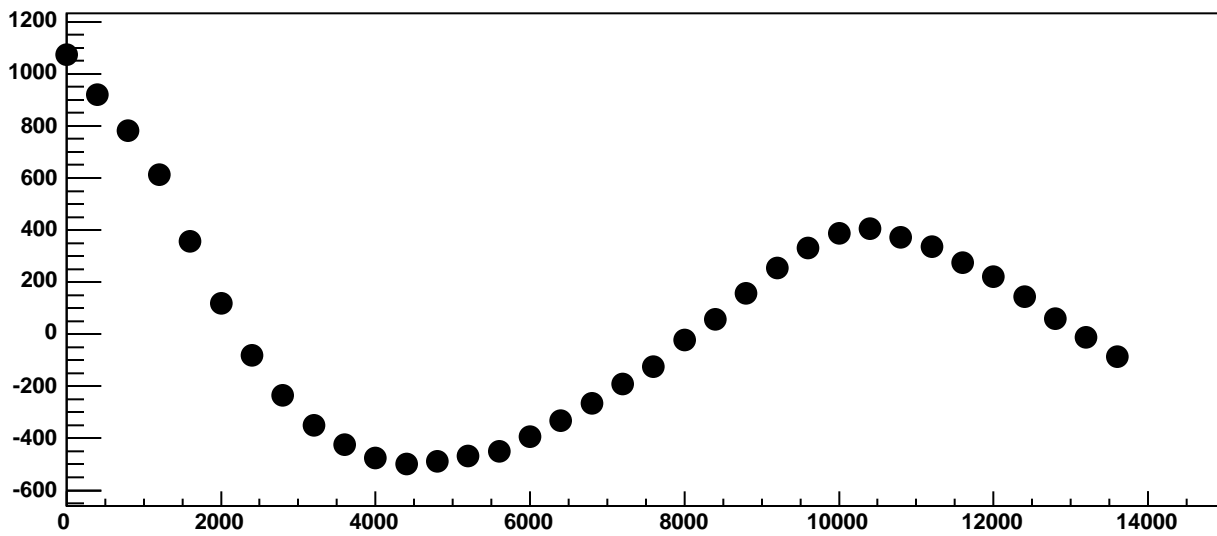
χ^2 / ndf 4.203e+05 / 23
p0 -934.2 ± 0.9626
p1 0.4013 ± 0.0002007



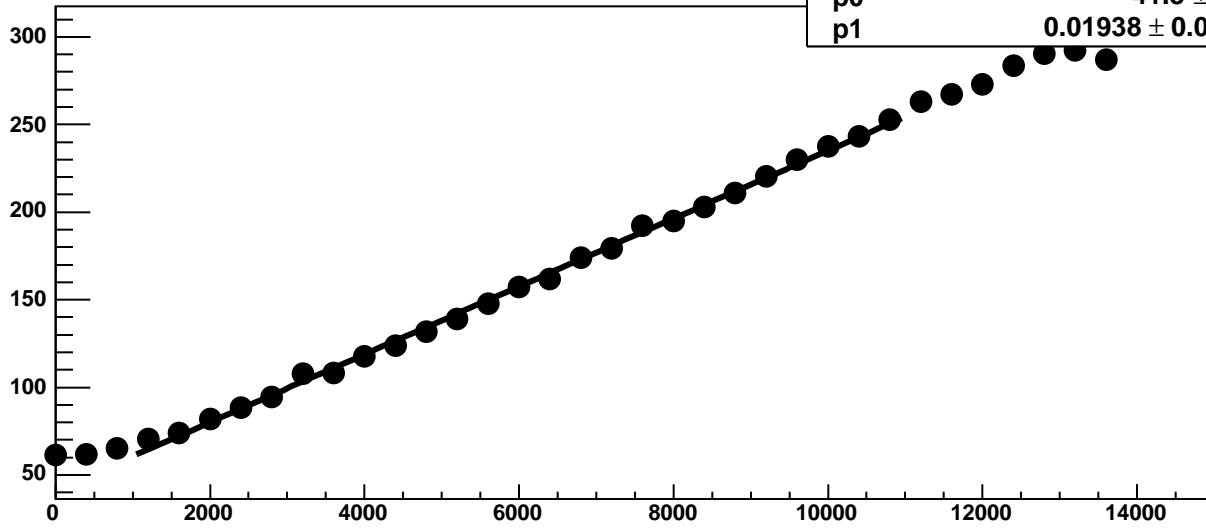
Chip 10, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC

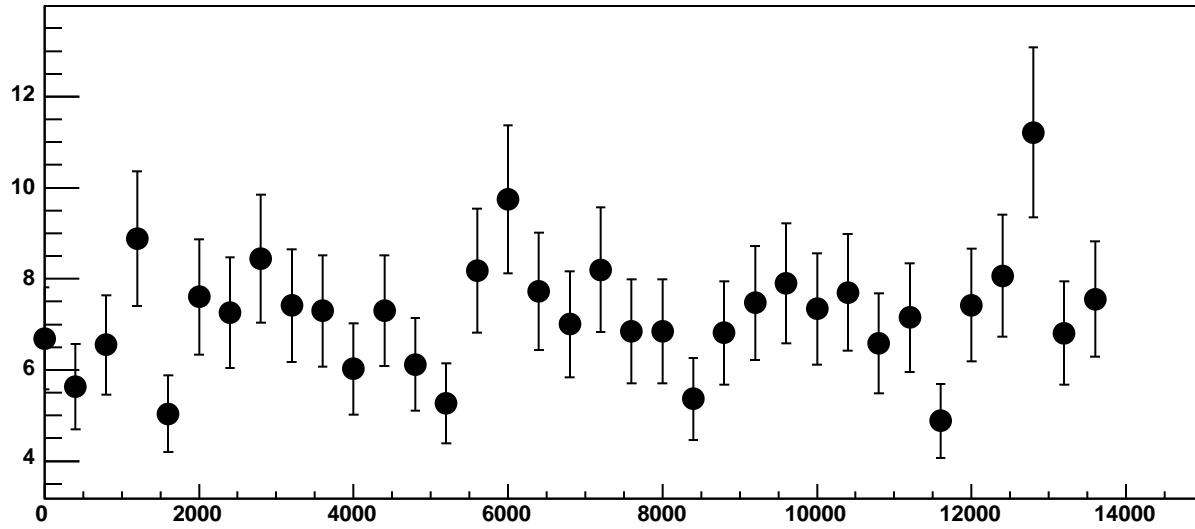


Chip 10, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

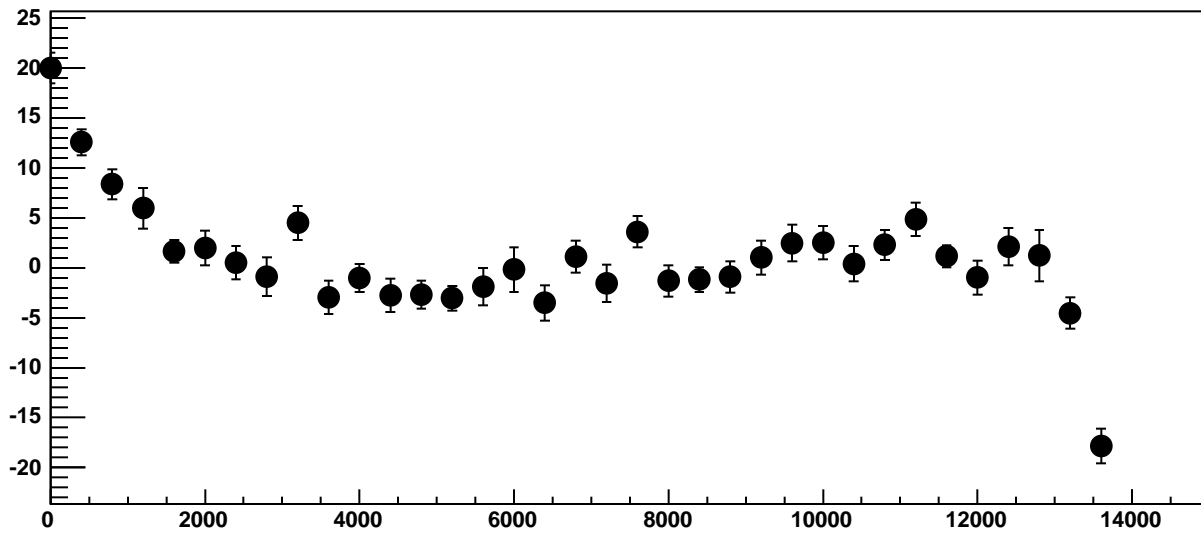


χ^2 / ndf 55.7 / 23
p0 41.3 ± 0.7319
p1 0.01938 ± 0.0001108

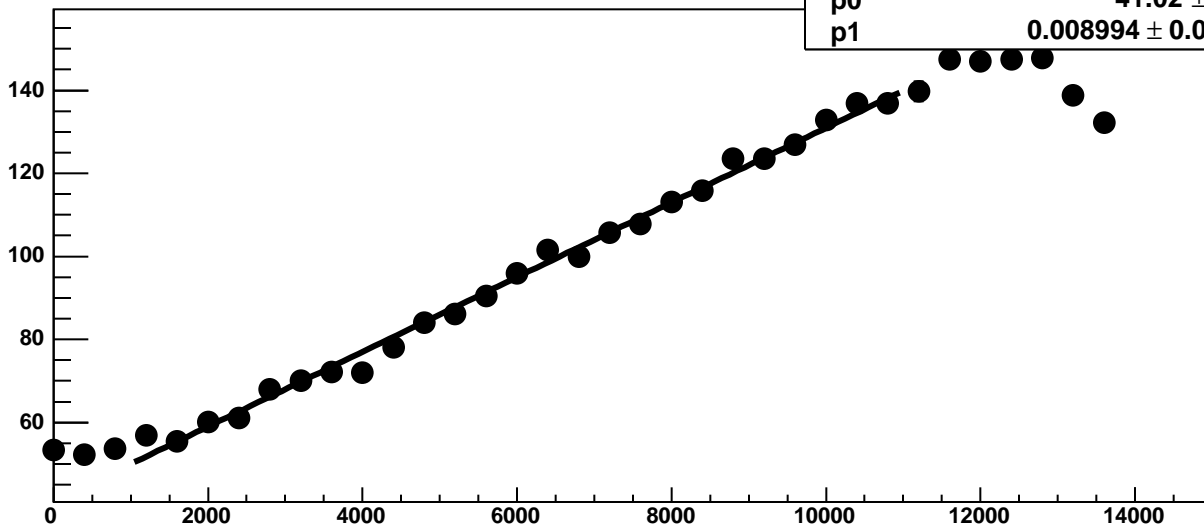
Chip 10, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



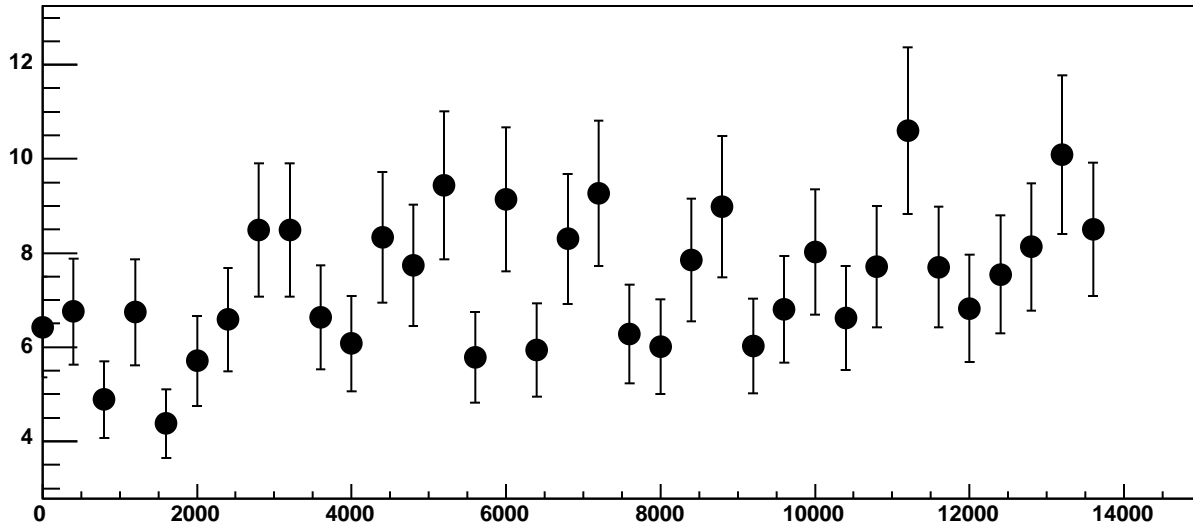
Chip 10, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



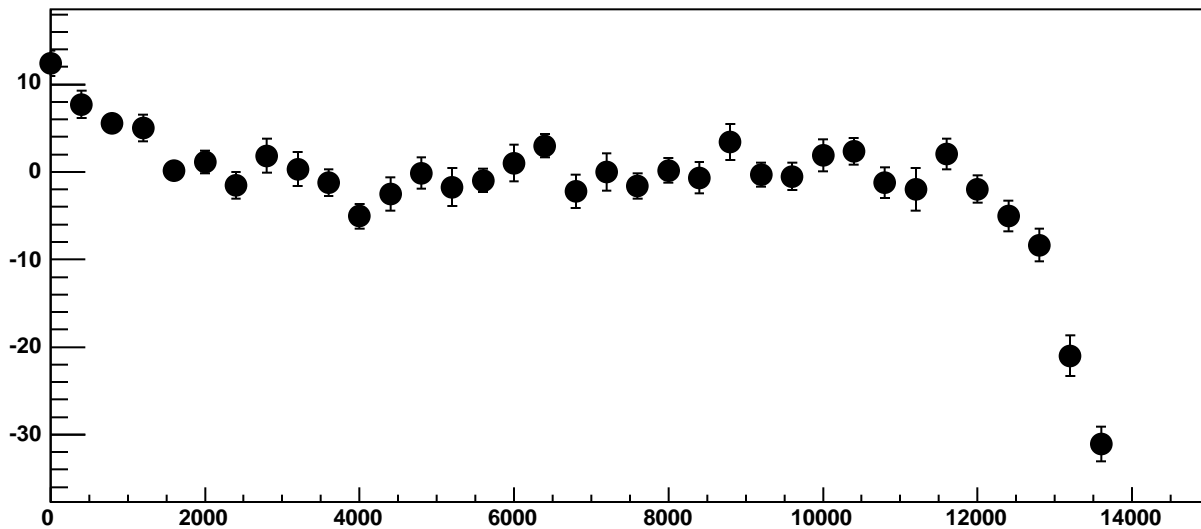
Chip 10, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



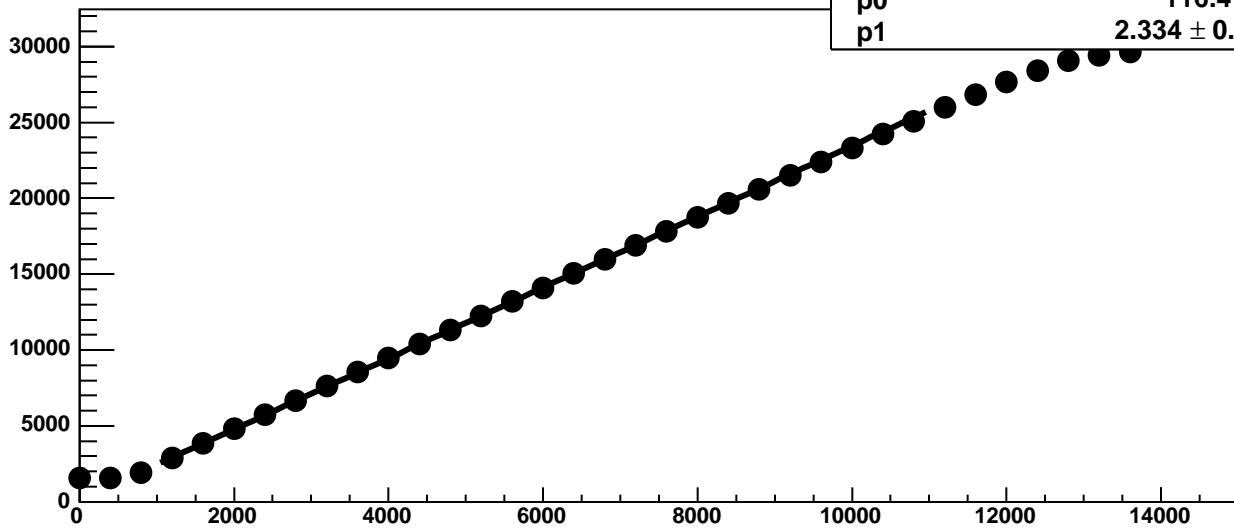
Chip 10, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



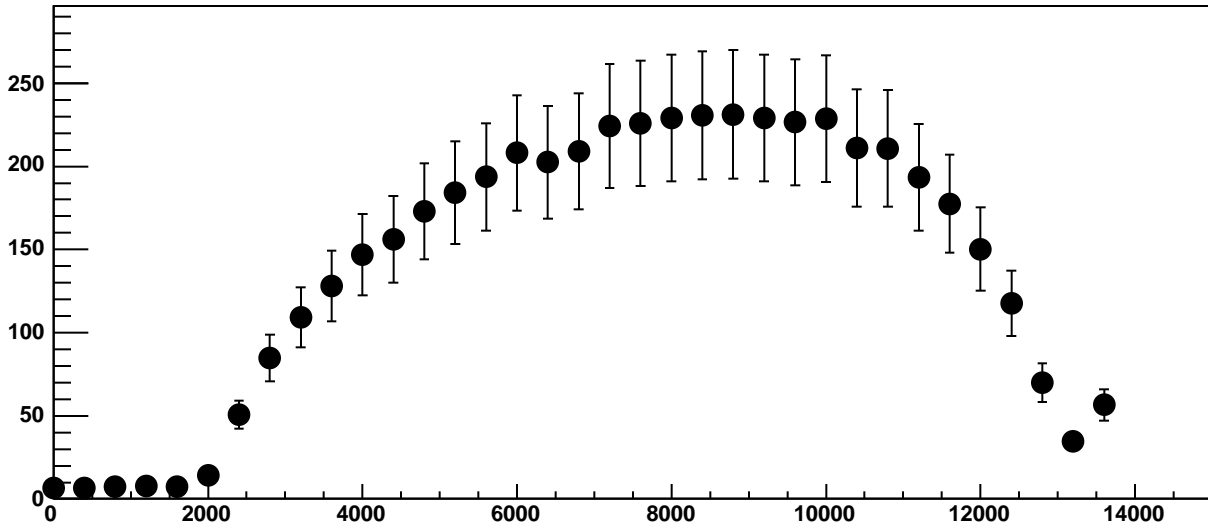
Chip 10, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



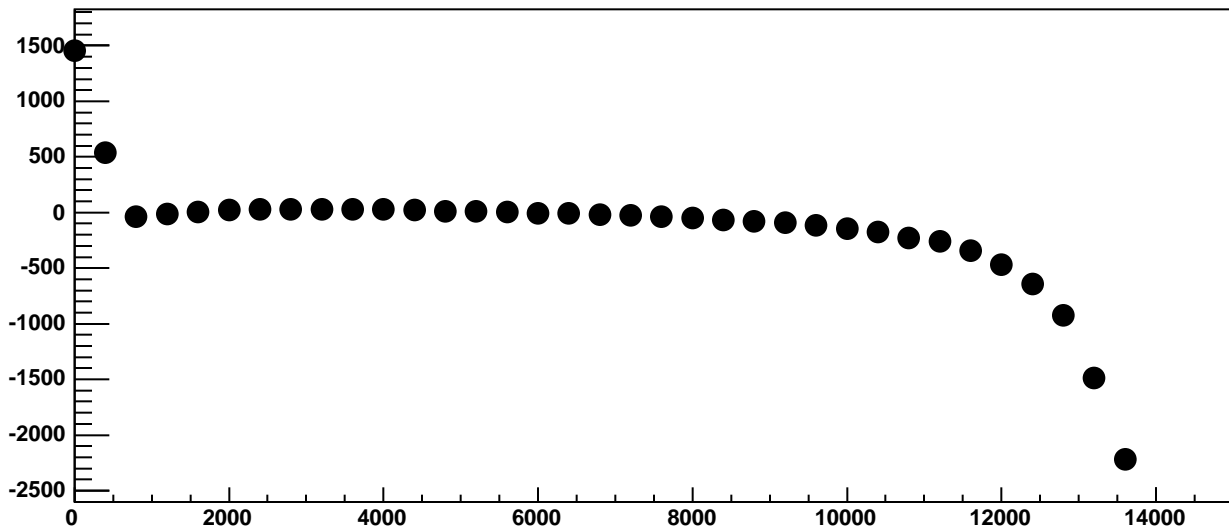
Chip 10, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC



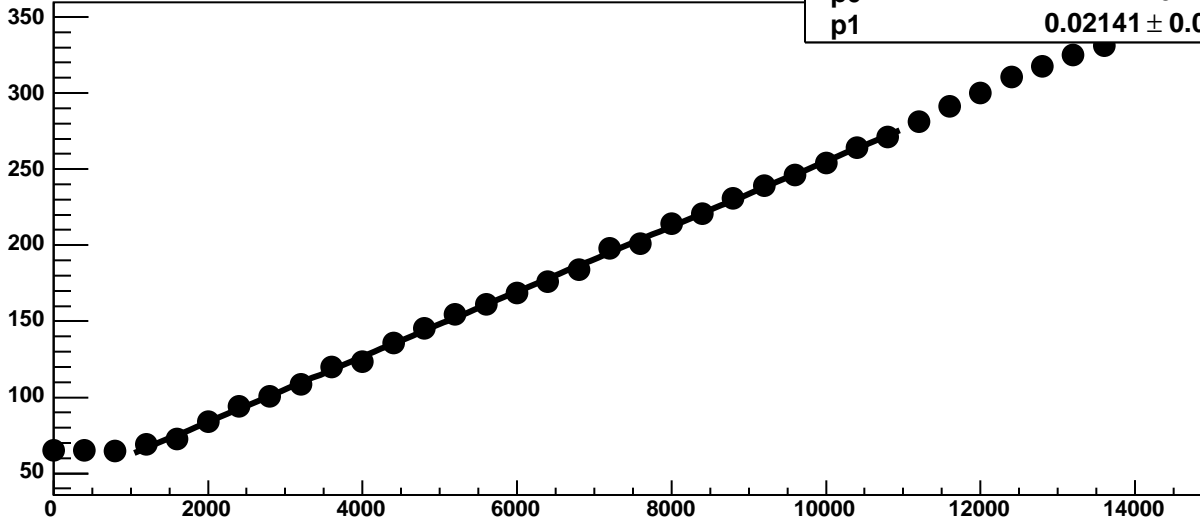
Chip 10, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC

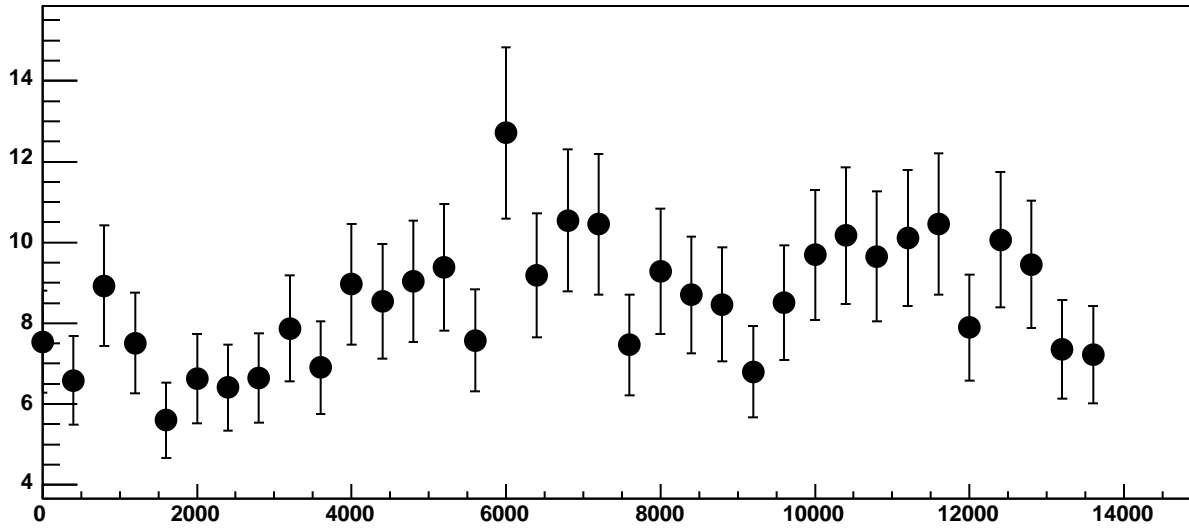


Chip 10, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

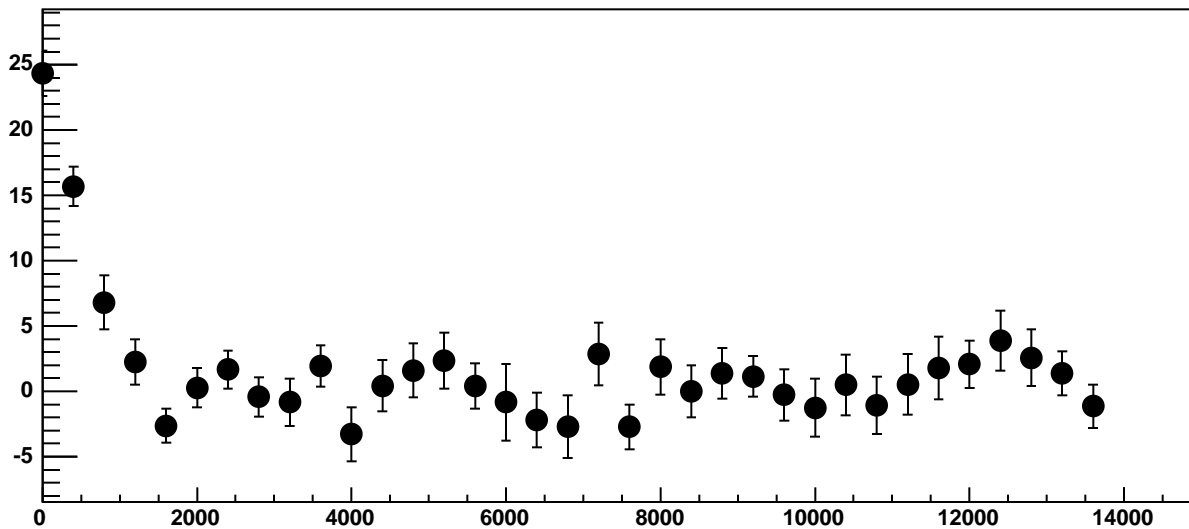


χ^2 / ndf 22.14 / 23
p0 41.01 ± 0.7586
p1 0.02141 ± 0.0001237

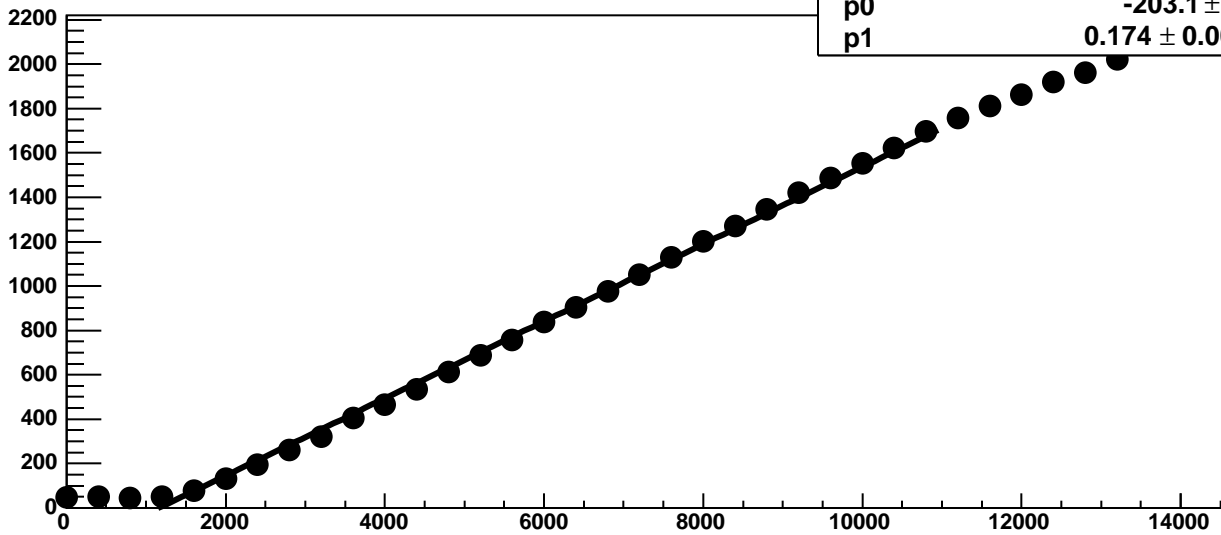
Chip 10, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



Chip 10, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

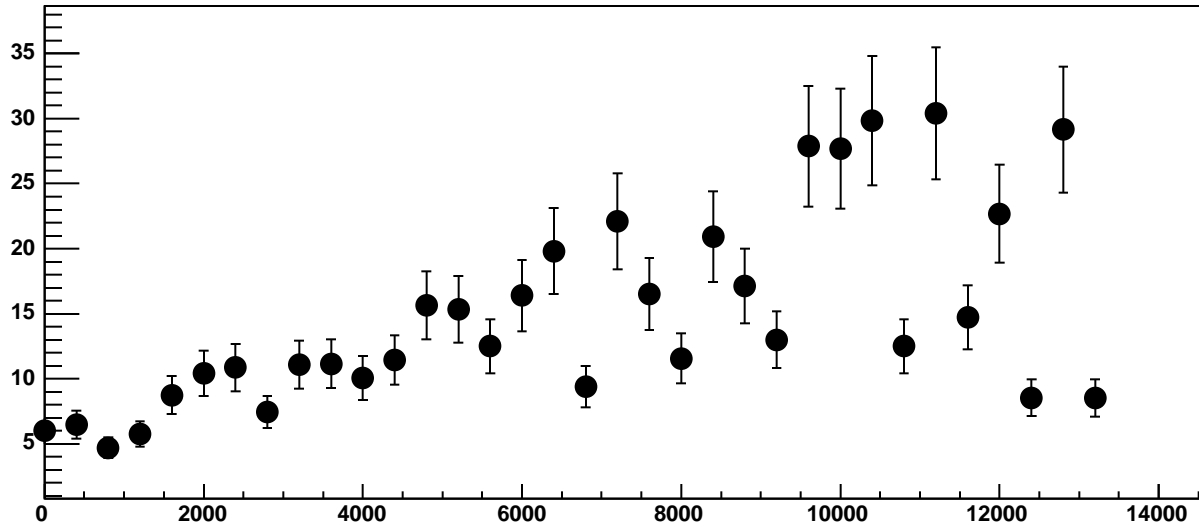


Chip 10, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

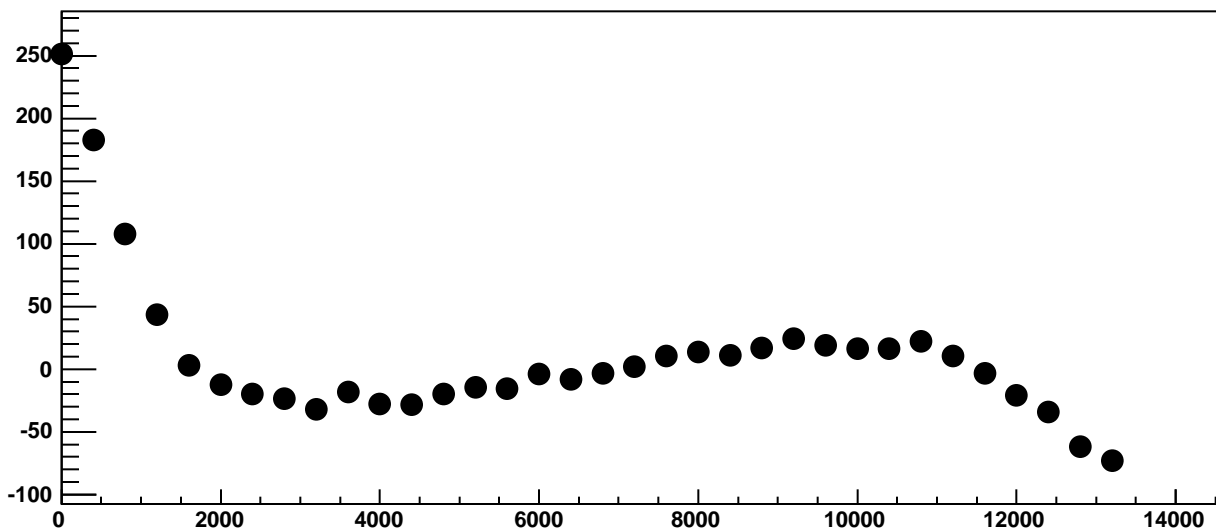


χ^2 / ndf 2116 / 23
p0 -203.1 ± 0.9944
p1 0.174 ± 0.0001922

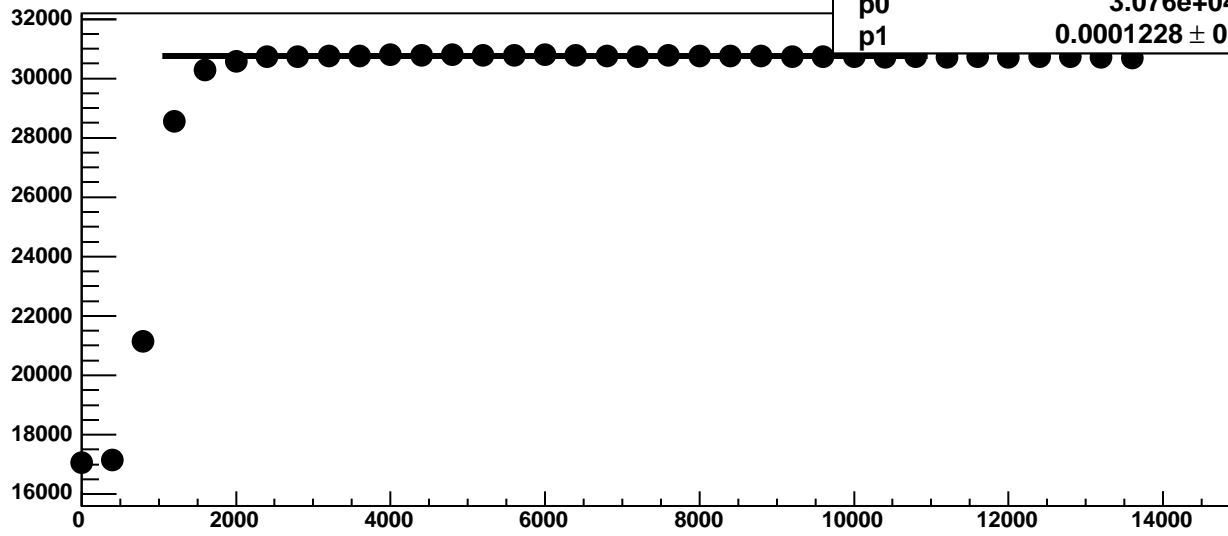
Chip 10, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

772.4 / 23

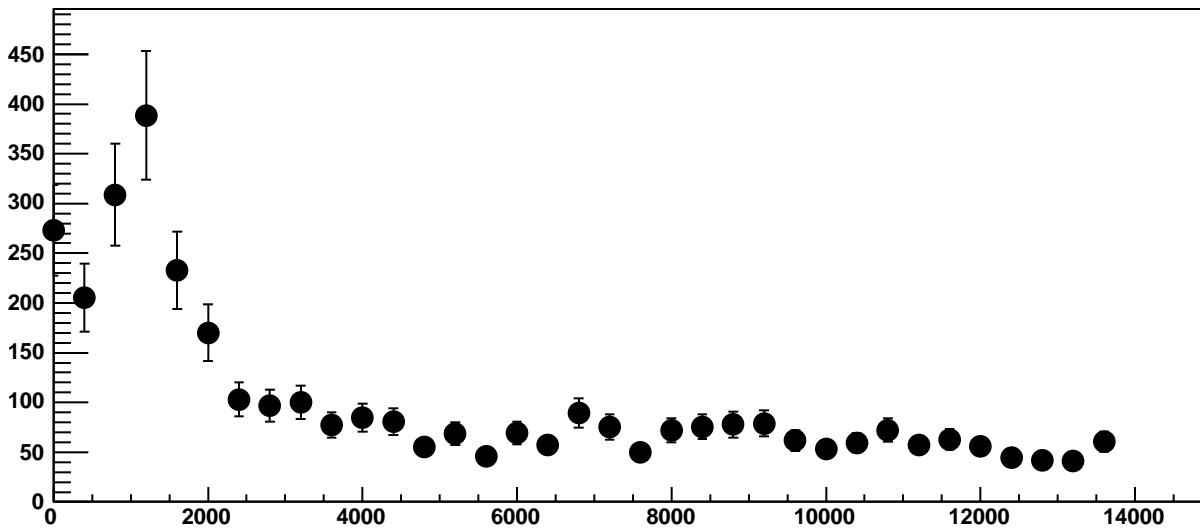
p0

$3.076e+04 \pm 10.1$

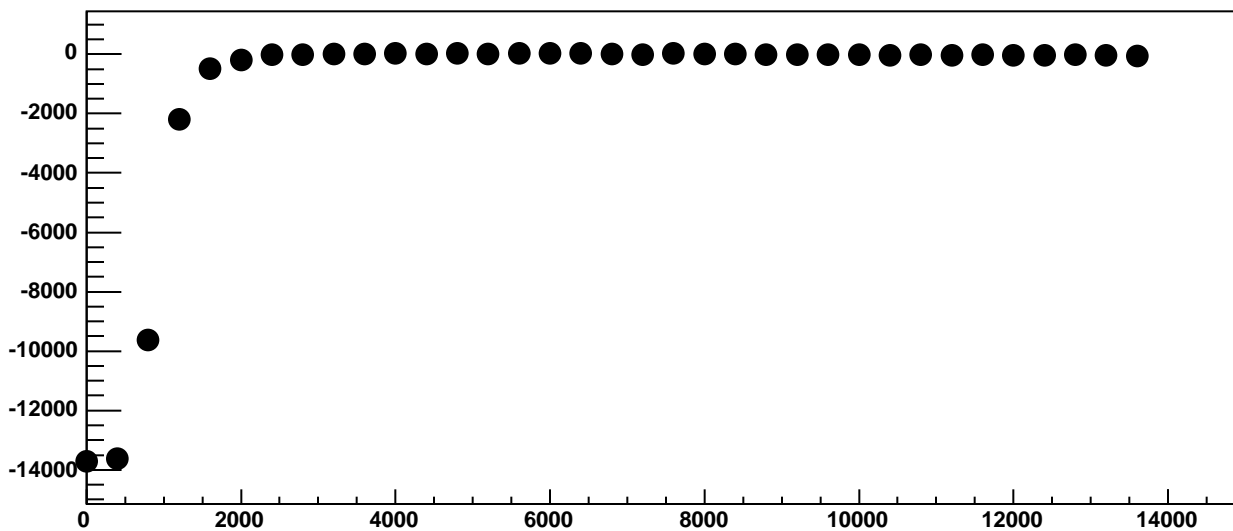
p1

0.0001228 ± 0.001377

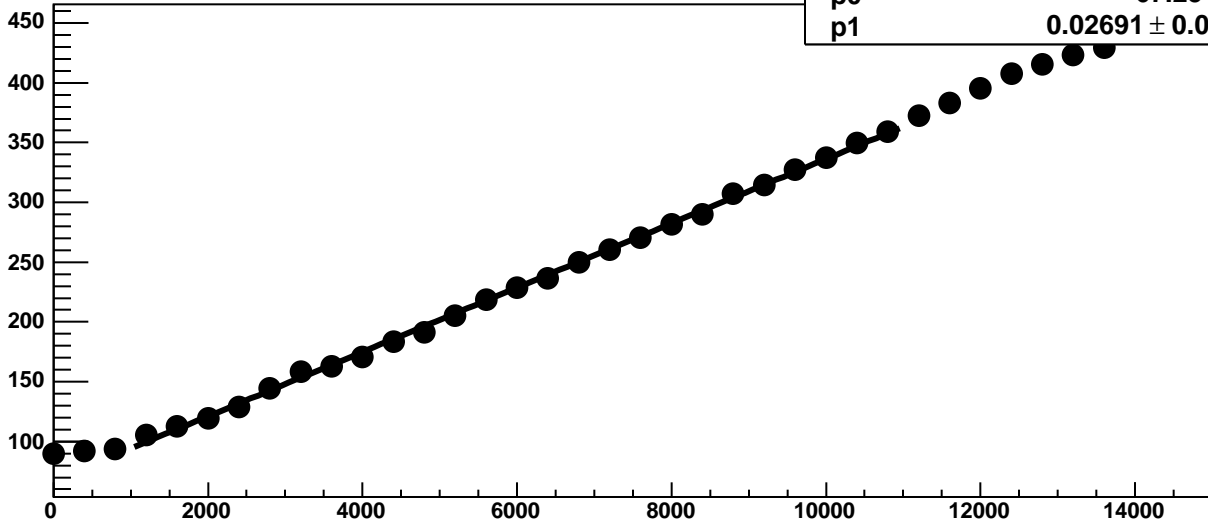
Chip 10, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC

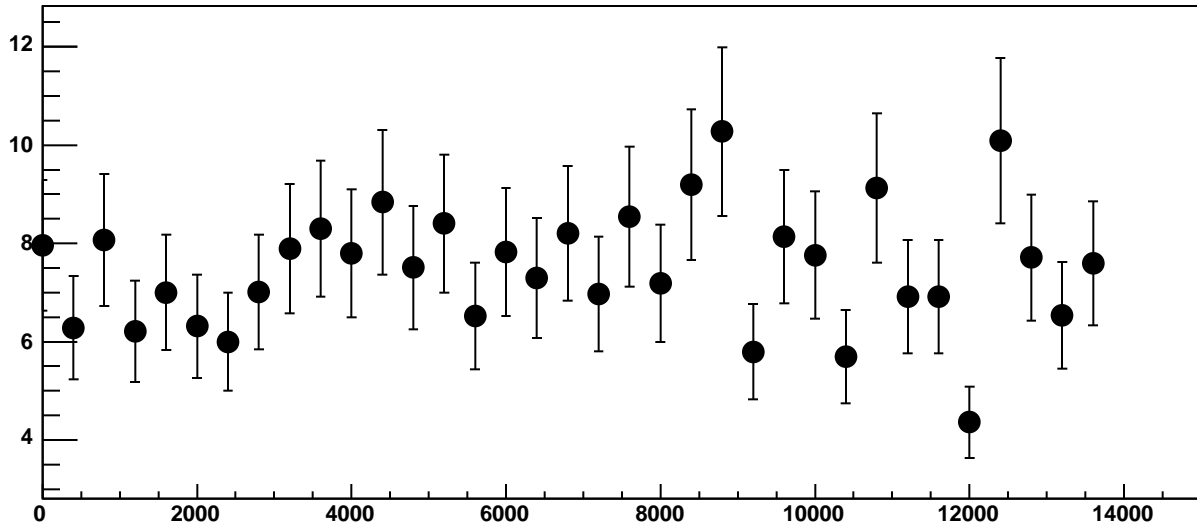


Chip 10, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

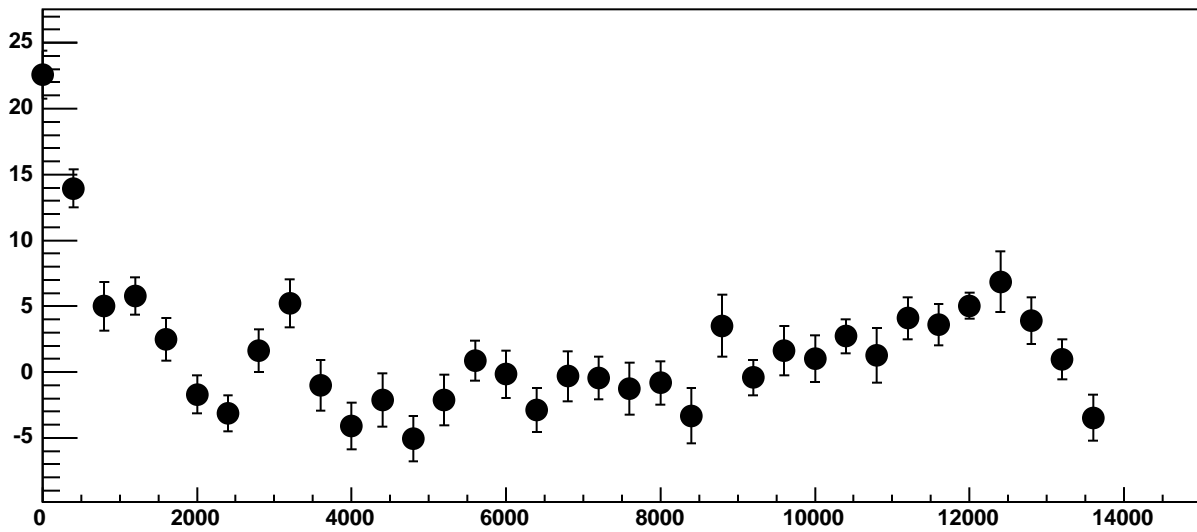


χ^2 / ndf 65.65 / 23
p0 67.25 ± 0.731
p1 0.02691 ± 0.0001116

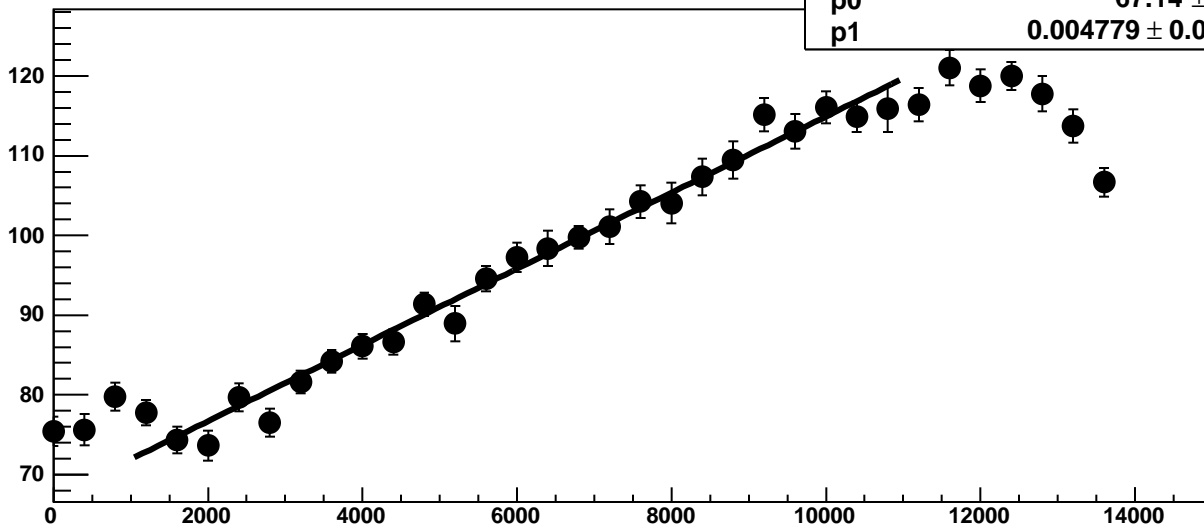
Chip 10, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

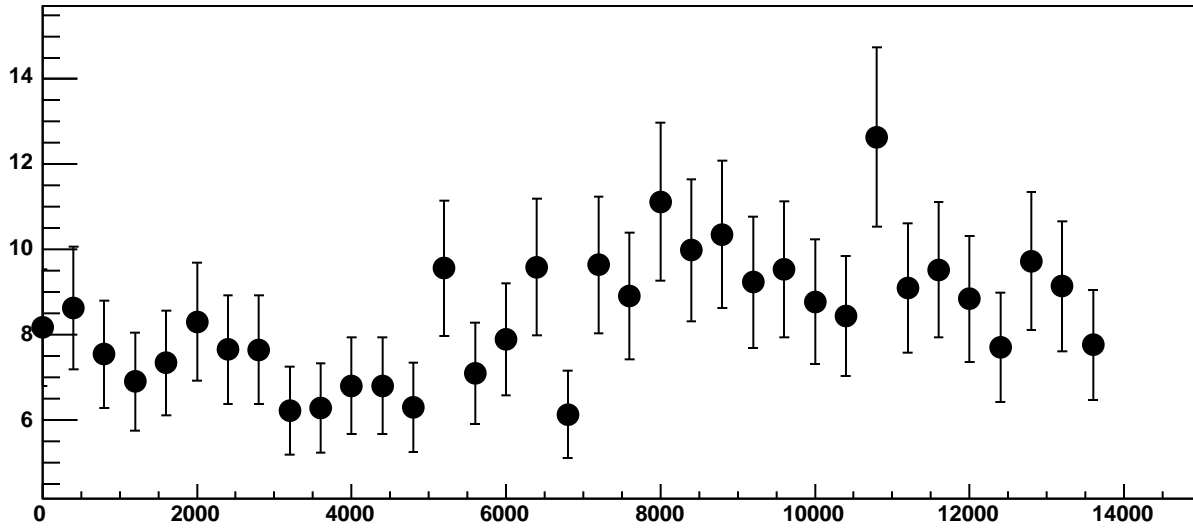


Chip 10, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

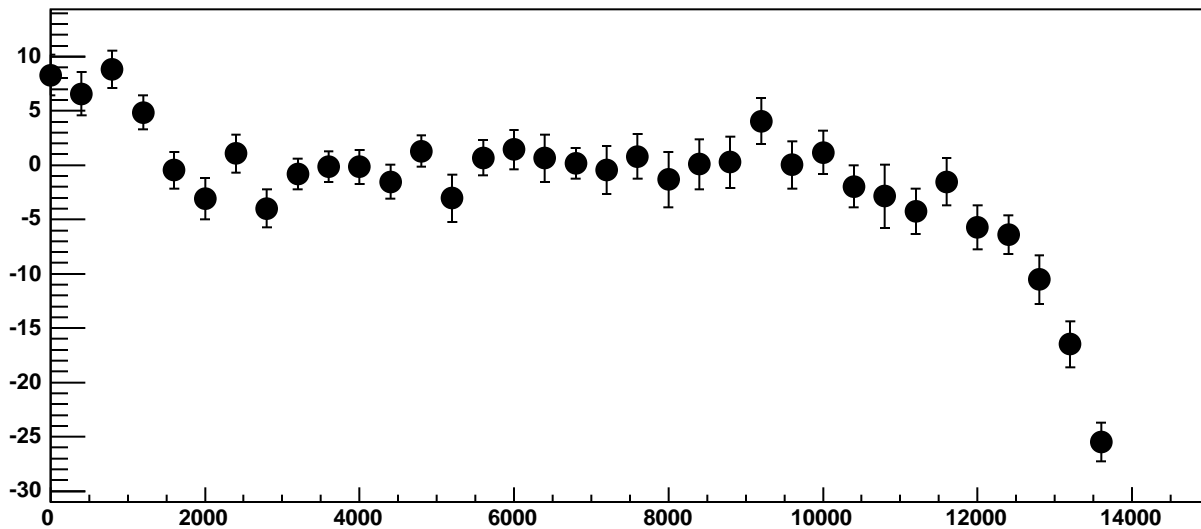


χ^2 / ndf 29.05 / 23
p0 67.14 ± 0.7995
p1 0.004779 ± 0.0001337

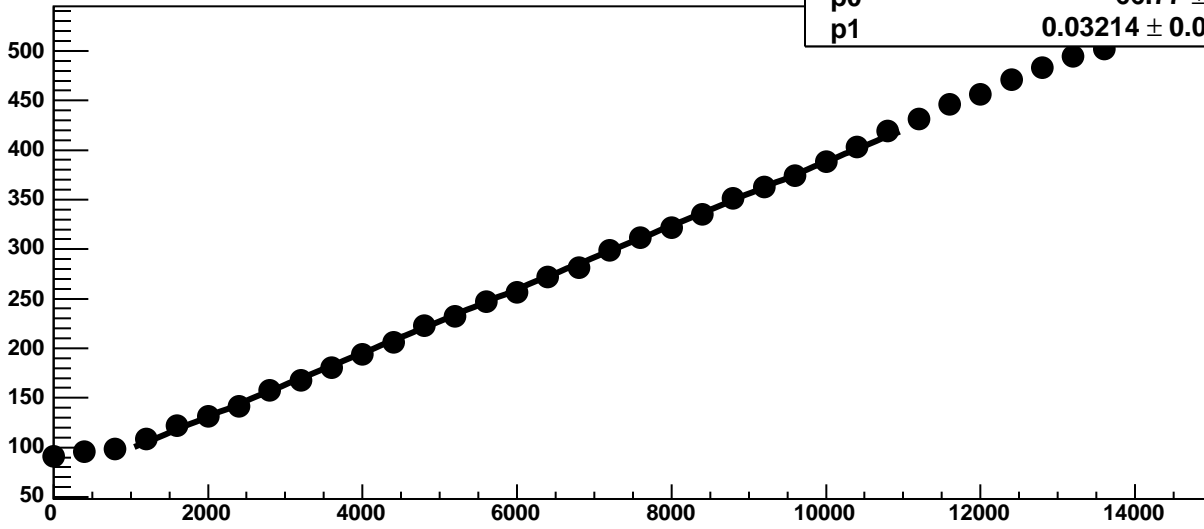
Chip 10, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

39.98 / 23

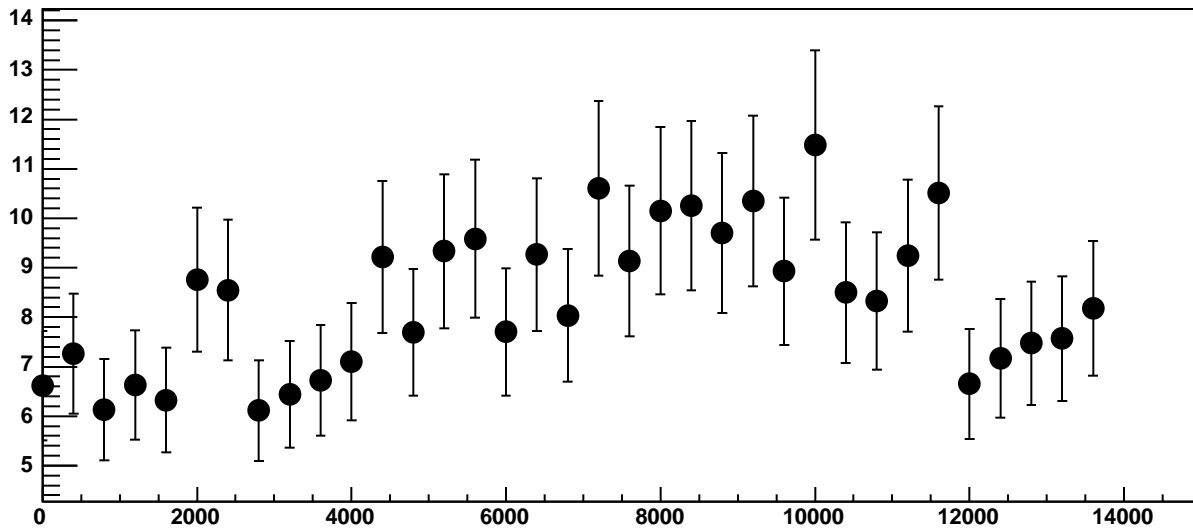
p0

66.77 ± 0.7802

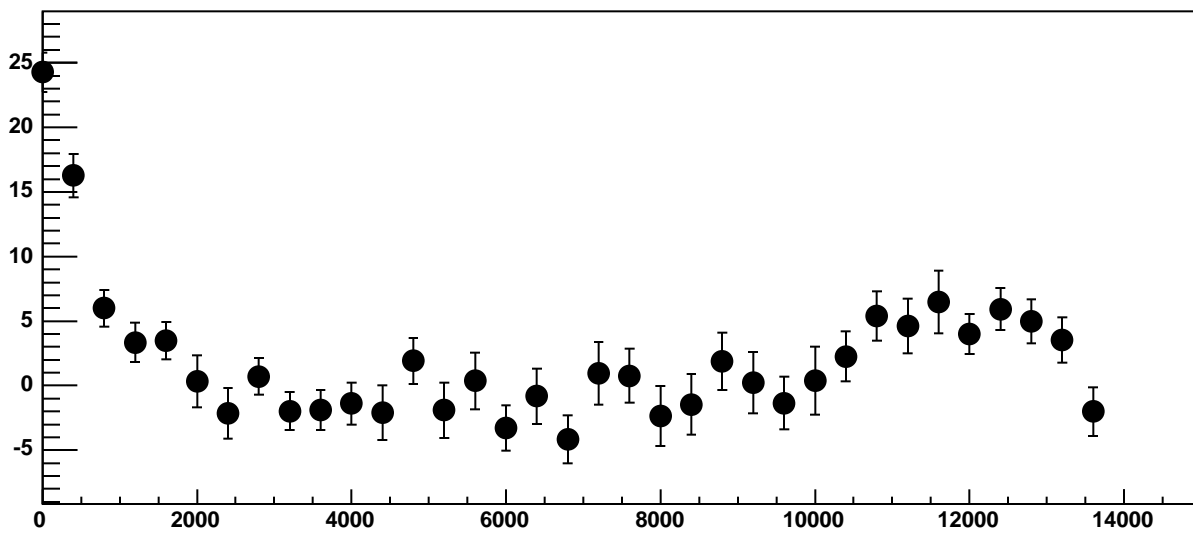
p1

0.03214 ± 0.0001292

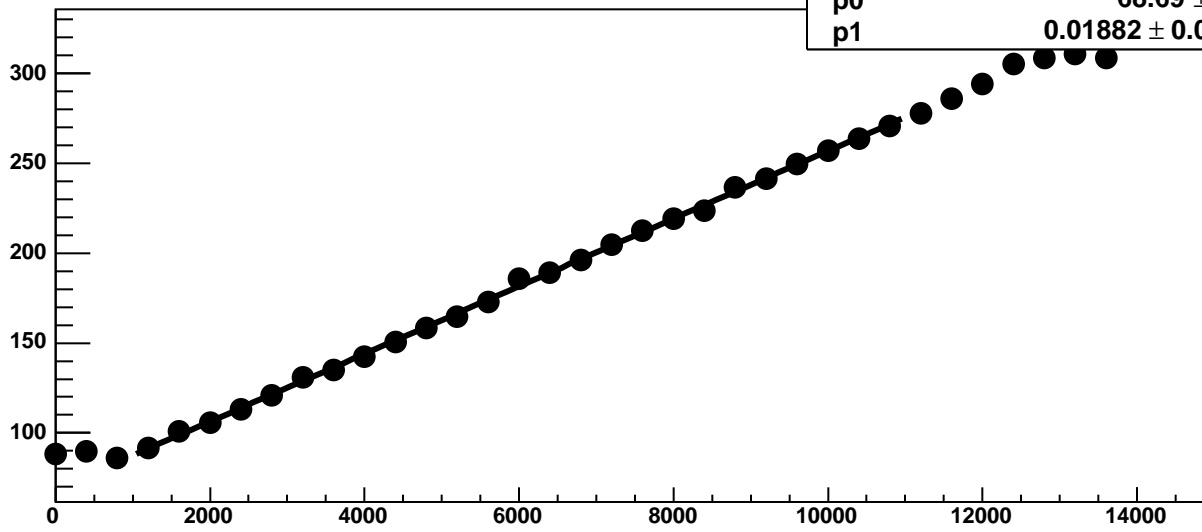
Chip 10, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

17.51 / 23

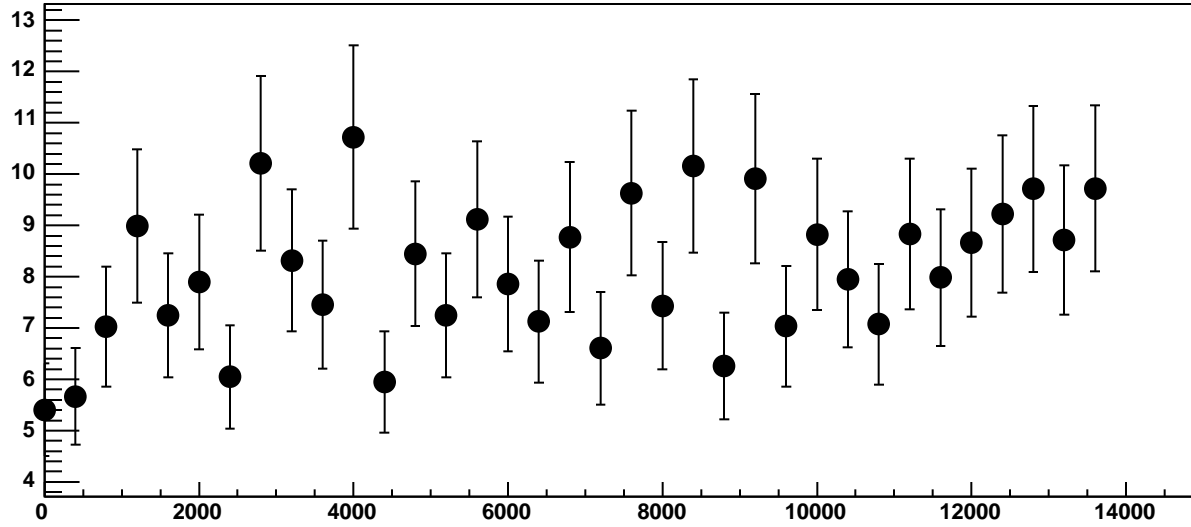
p0

68.69 ± 0.8205

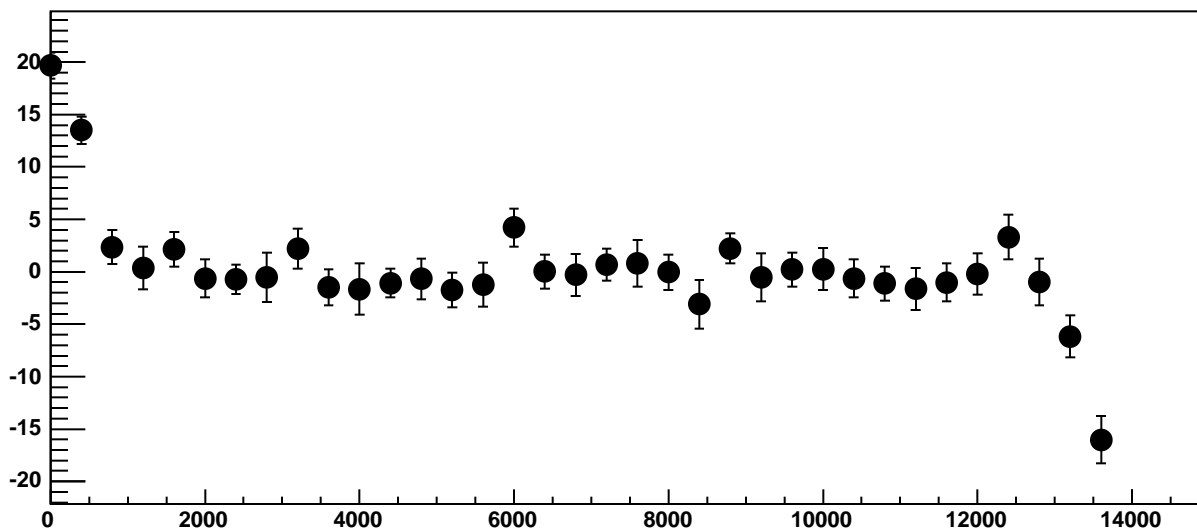
p1

0.01882 ± 0.0001233

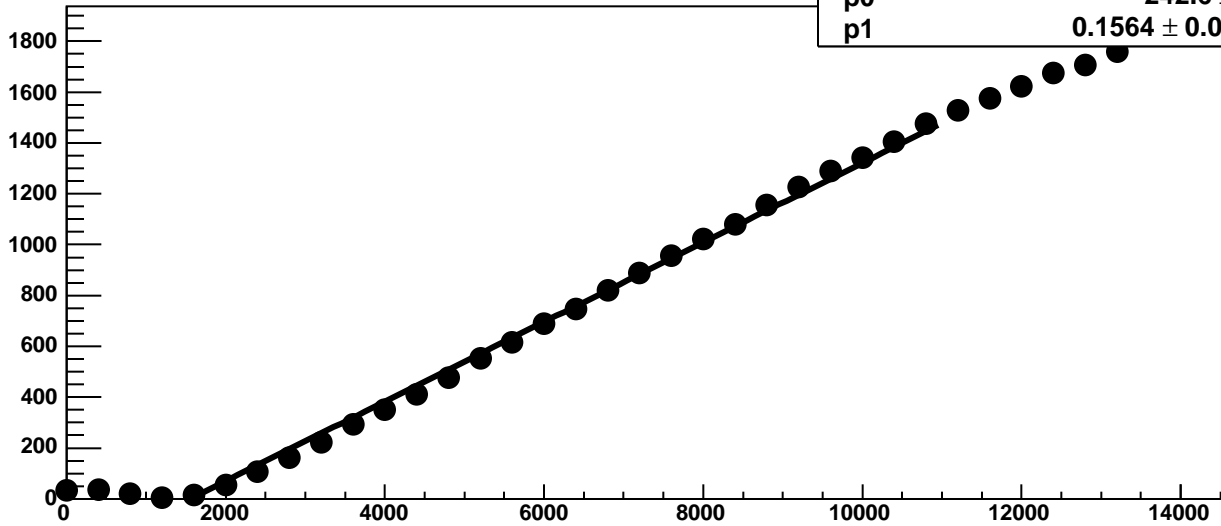
Chip 10, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



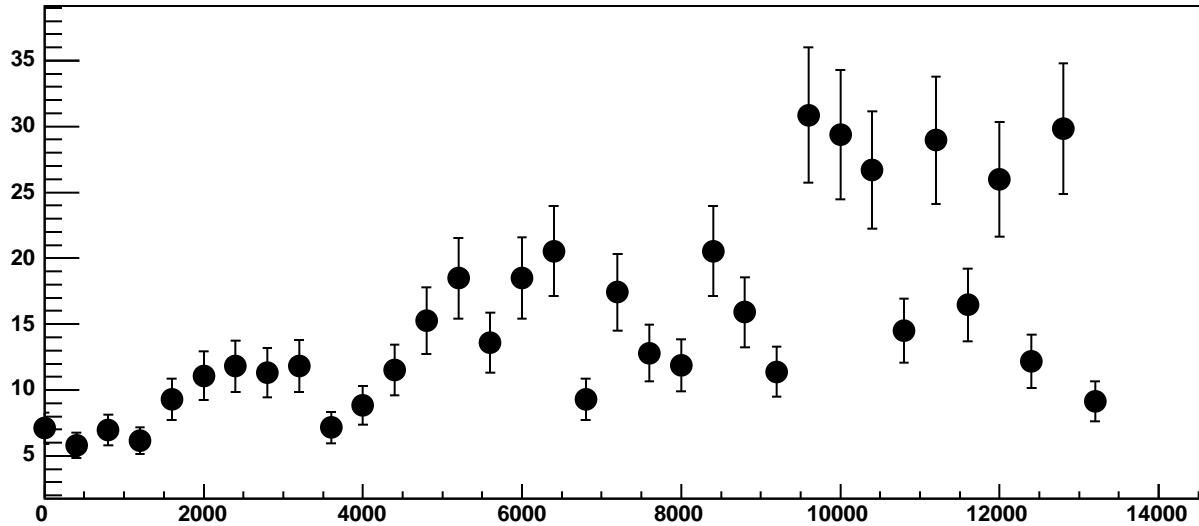
Chip 10, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



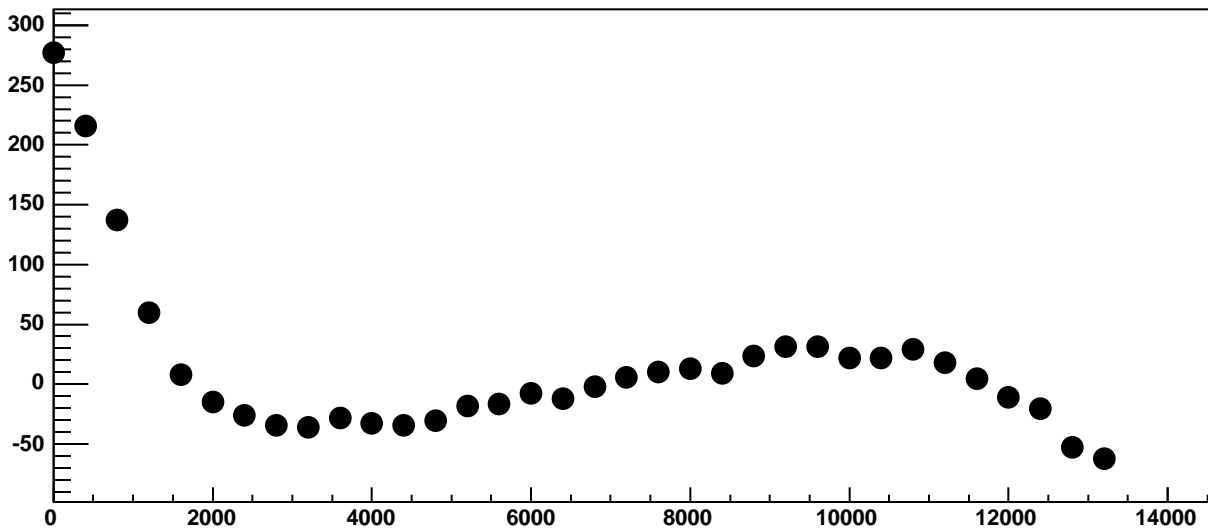
Chip 10, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



Chip 10, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

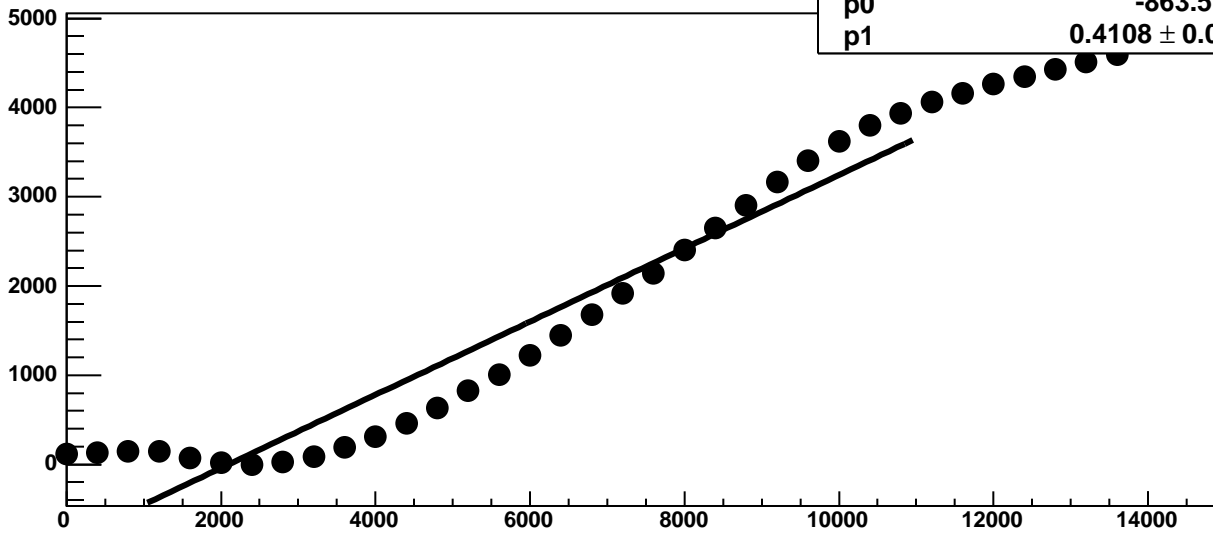


Chip 10, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

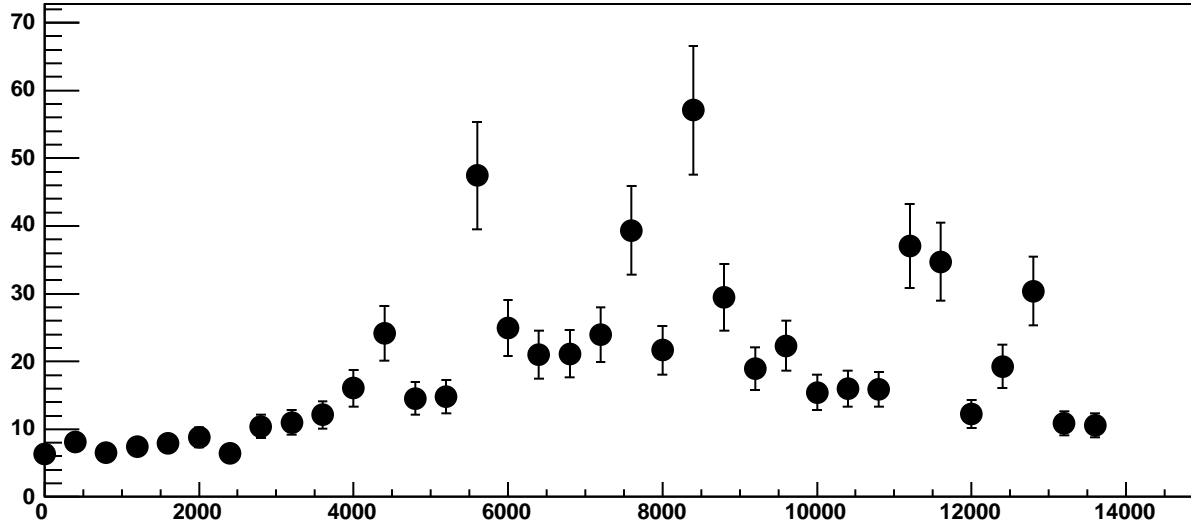


Chip 10, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

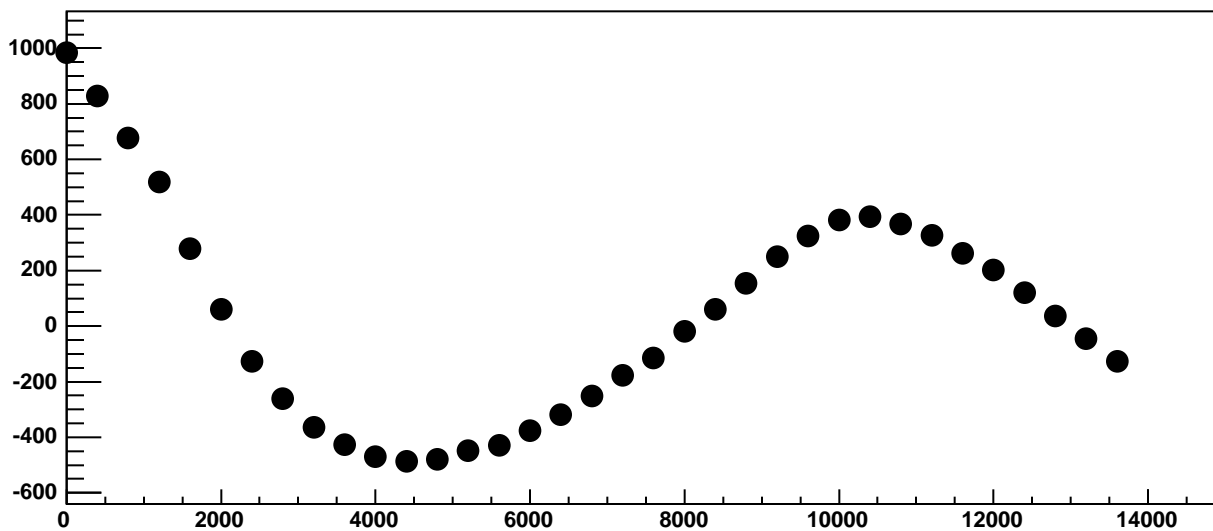
χ^2 / ndf 2.985e+05 / 23
p0 -863.5 ± 1.028
p1 0.4108 ± 0.0002142



Chip 10, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

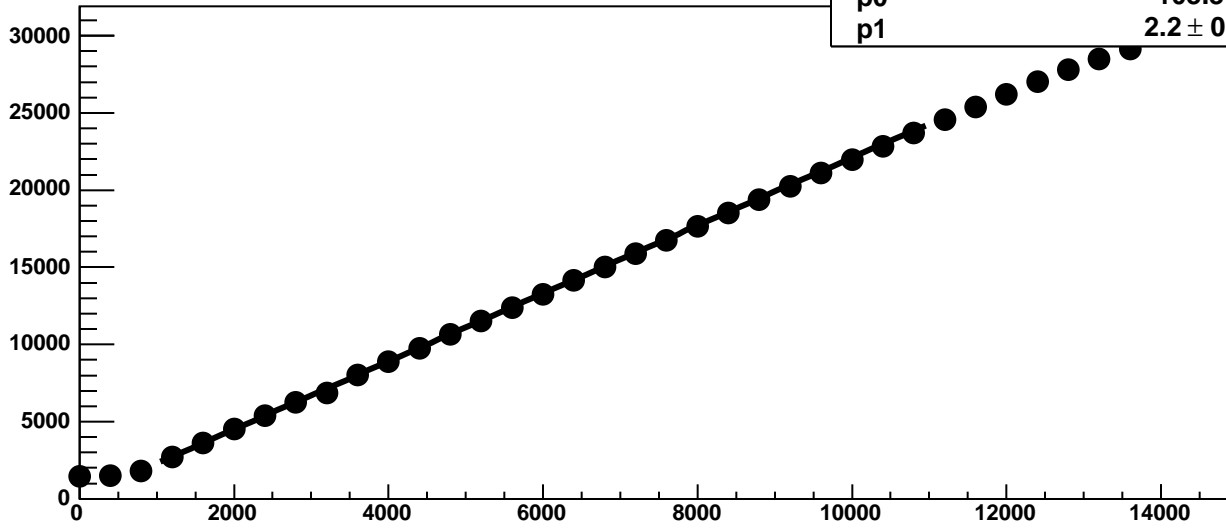


Chip 10, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

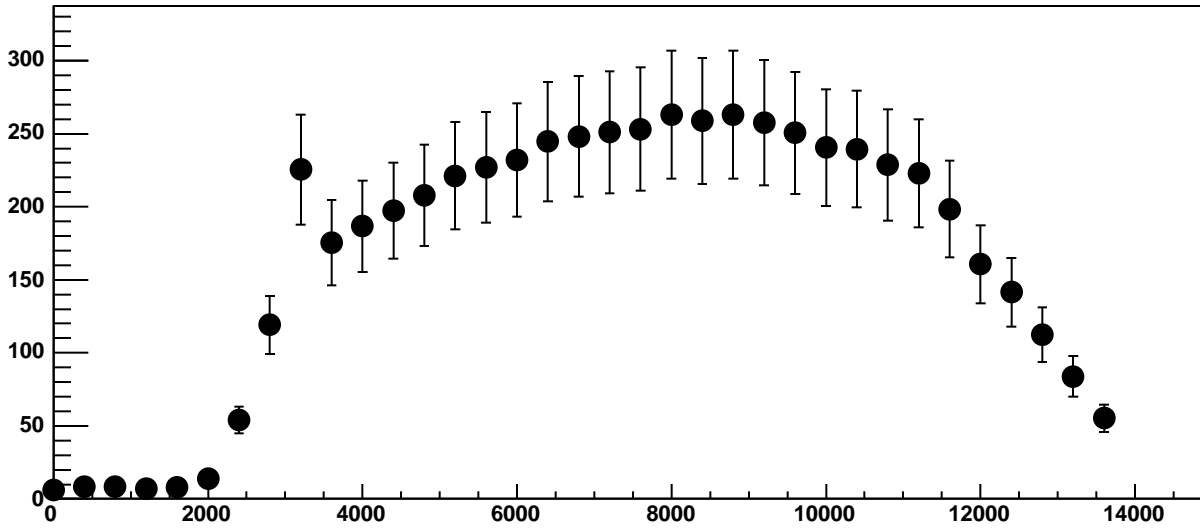


Chip 10, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC

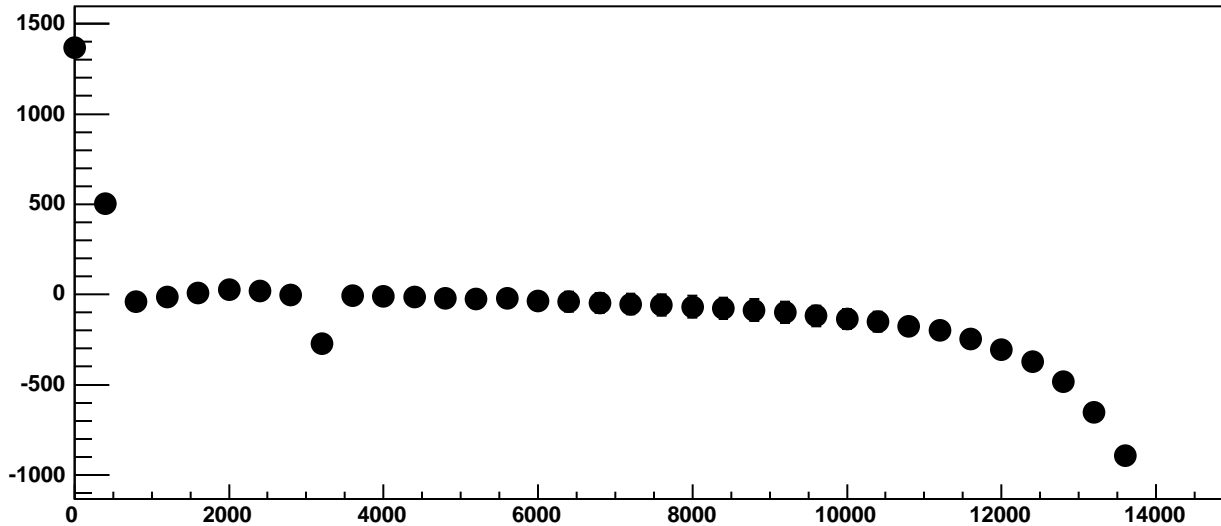
χ^2 / ndf 225.7 / 23
p0 103.3 ± 3.015
p1 2.2 ± 0.001836



Chip 10, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC

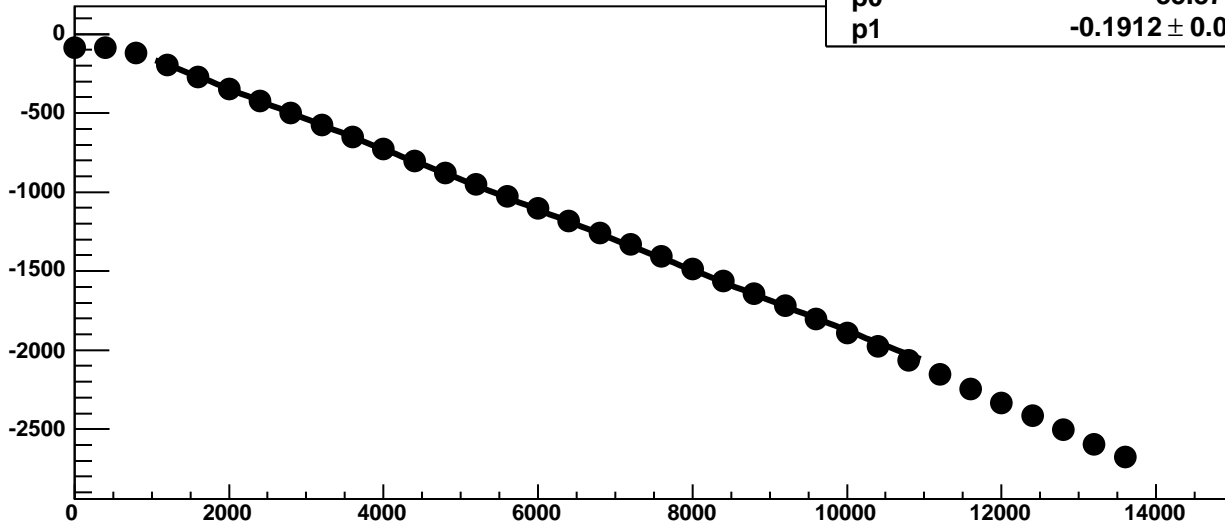


Chip 10, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC

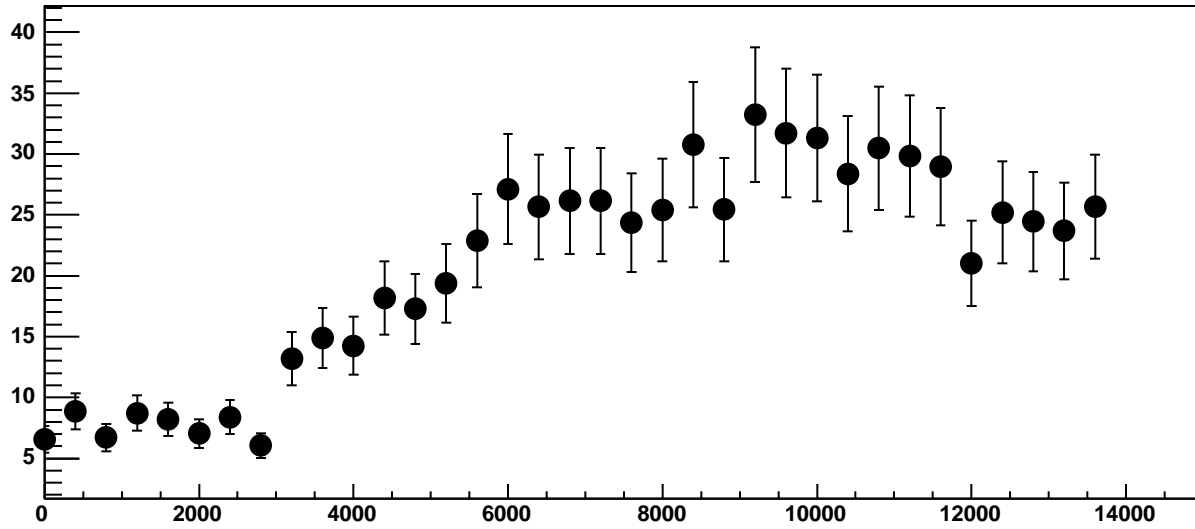


Chip 10, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

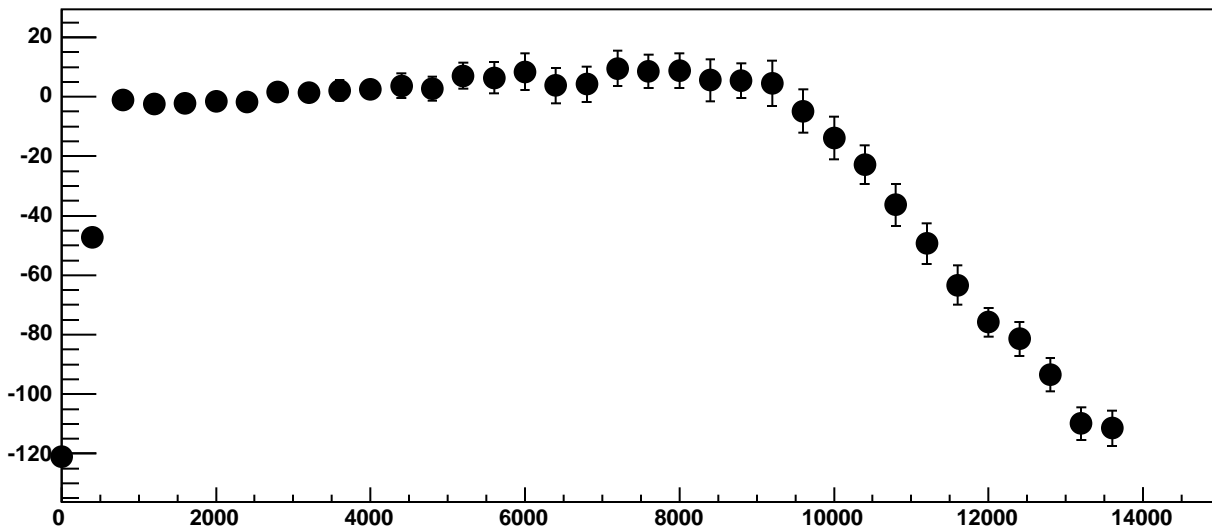
χ^2 / ndf 67.86 / 23
p0 35.87 ± 1.141
p1 -0.1912 ± 0.0002887



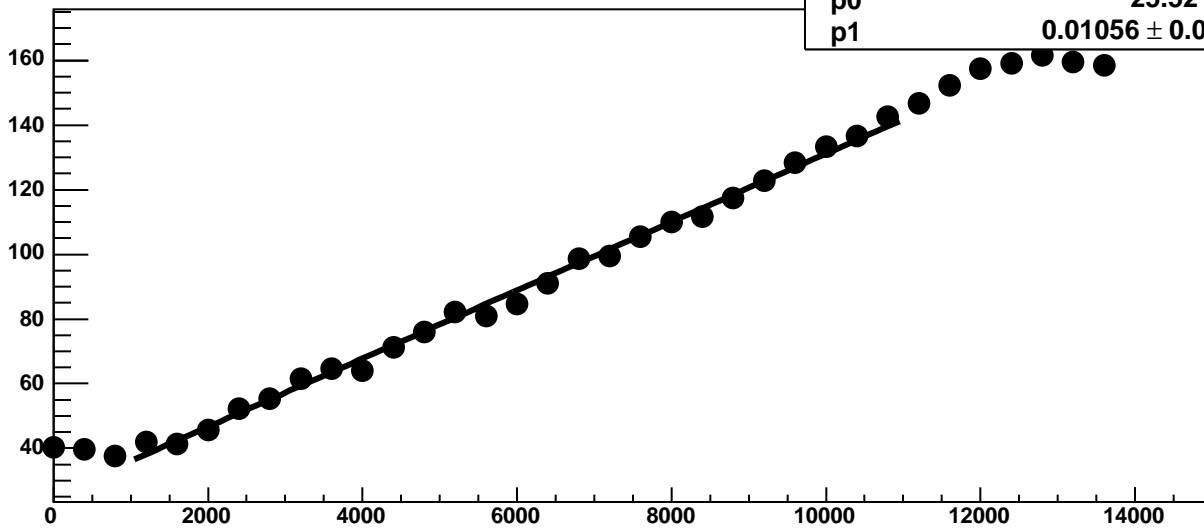
Chip 10, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

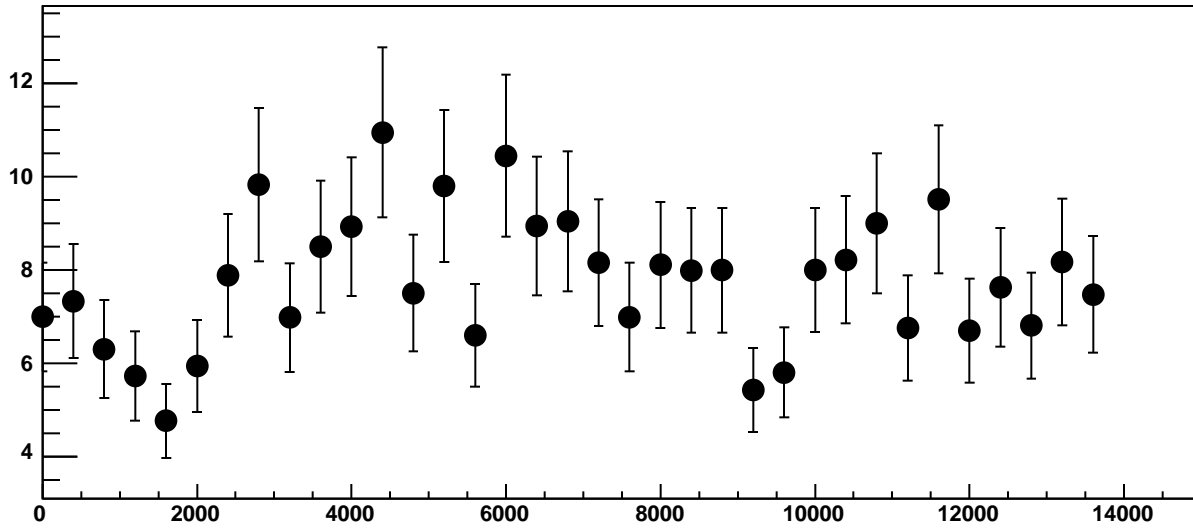


Chip 10, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

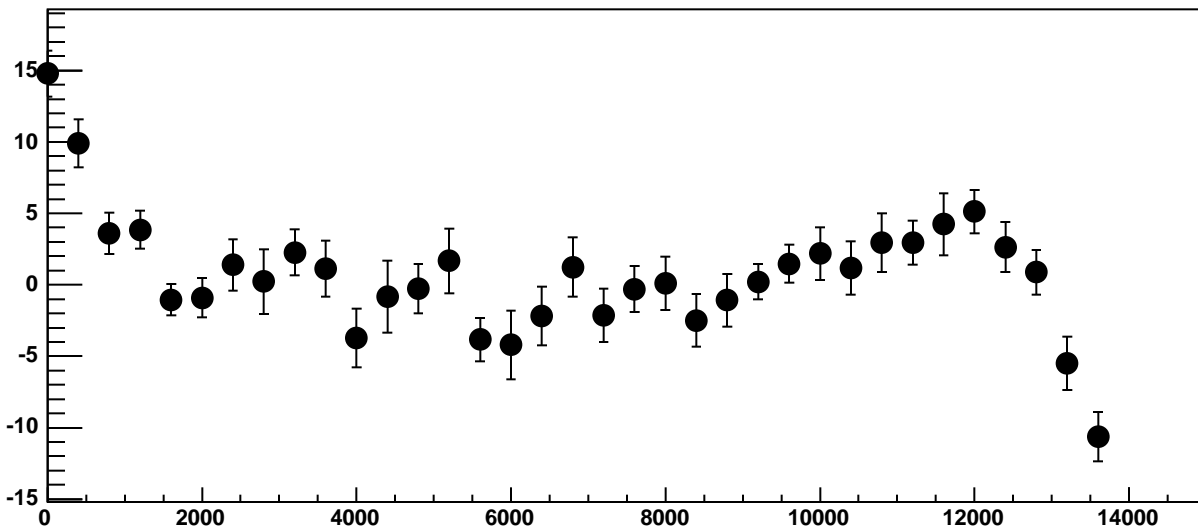


χ^2 / ndf 36.43 / 23
p0 25.52 ± 0.702
p1 0.01056 ± 0.0001076

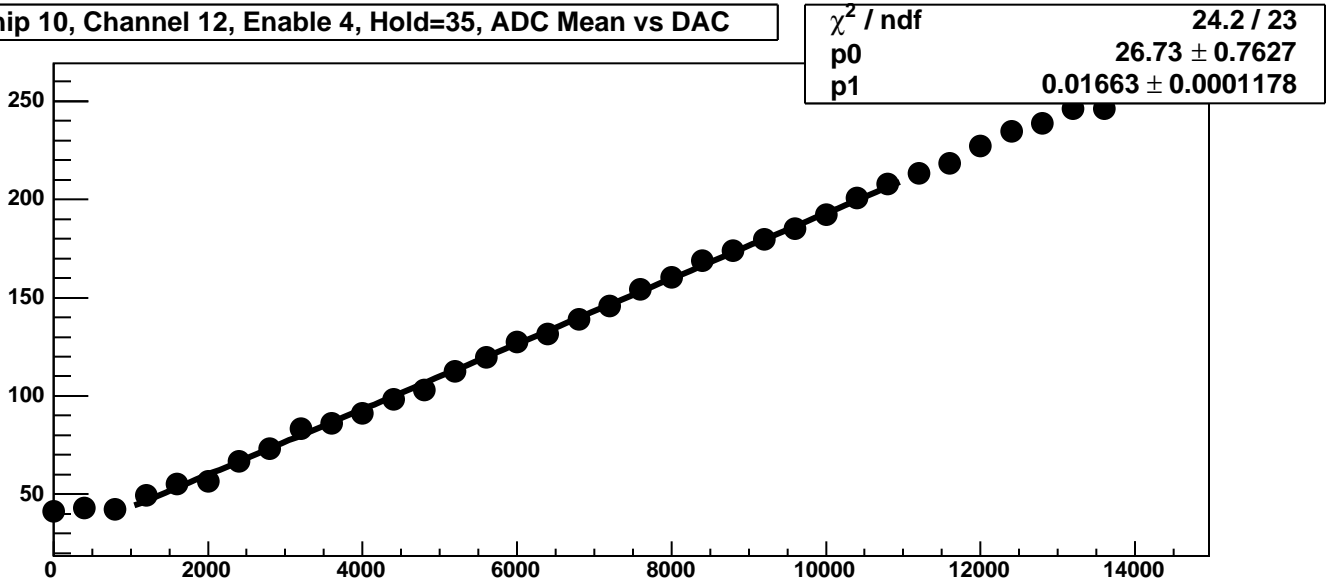
Chip 10, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



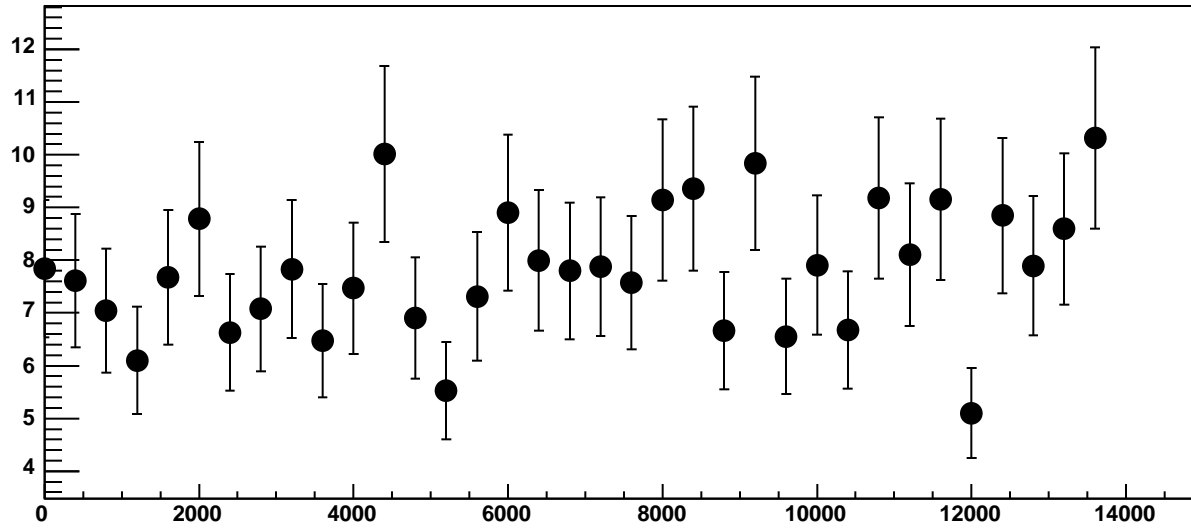
Chip 10, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



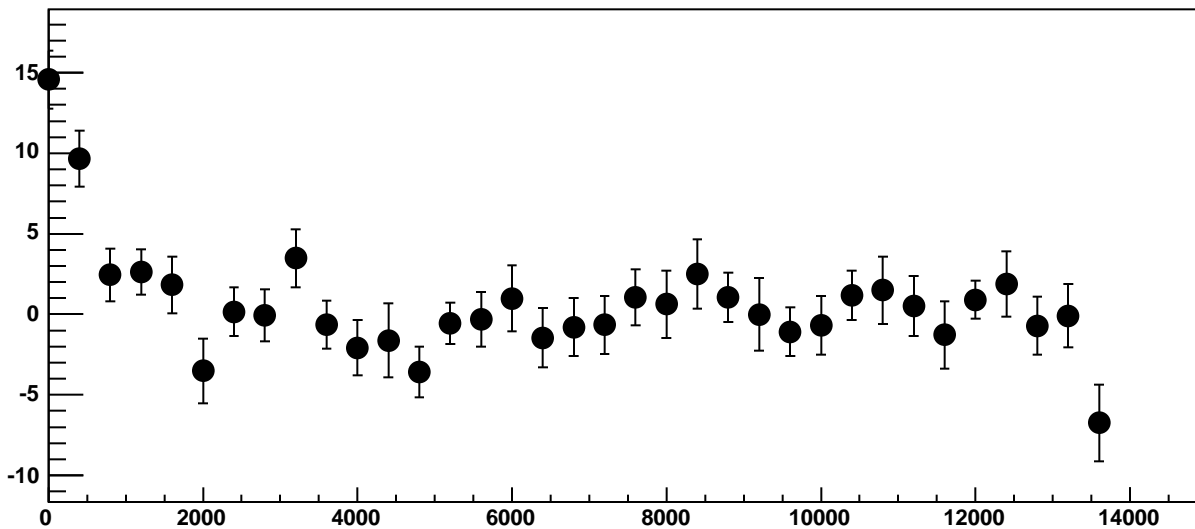
Chip 10, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC



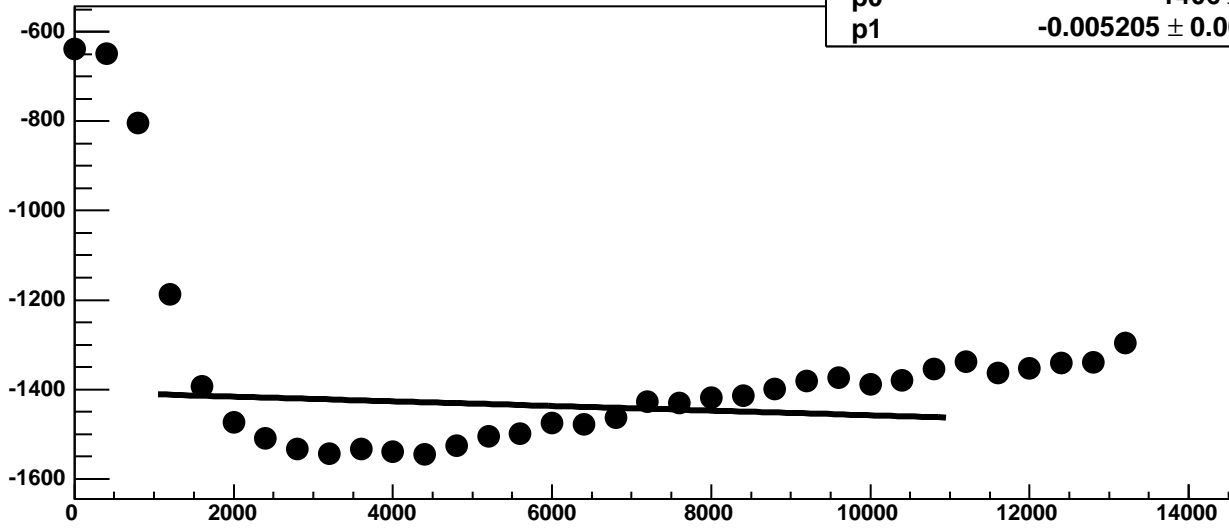
Chip 10, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



Chip 10, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

6866 / 23

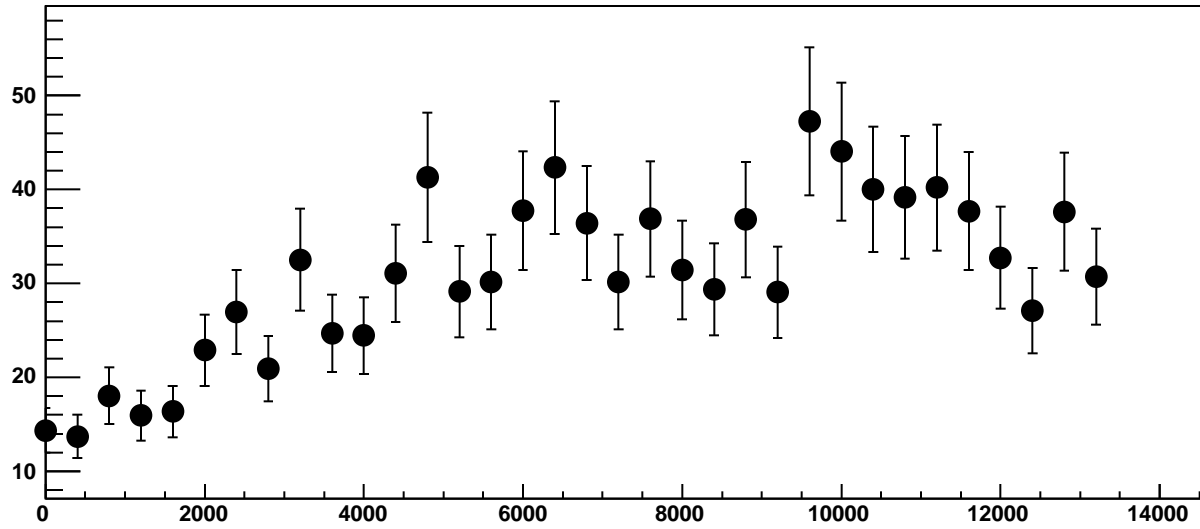
p0

-1406 ± 2.389

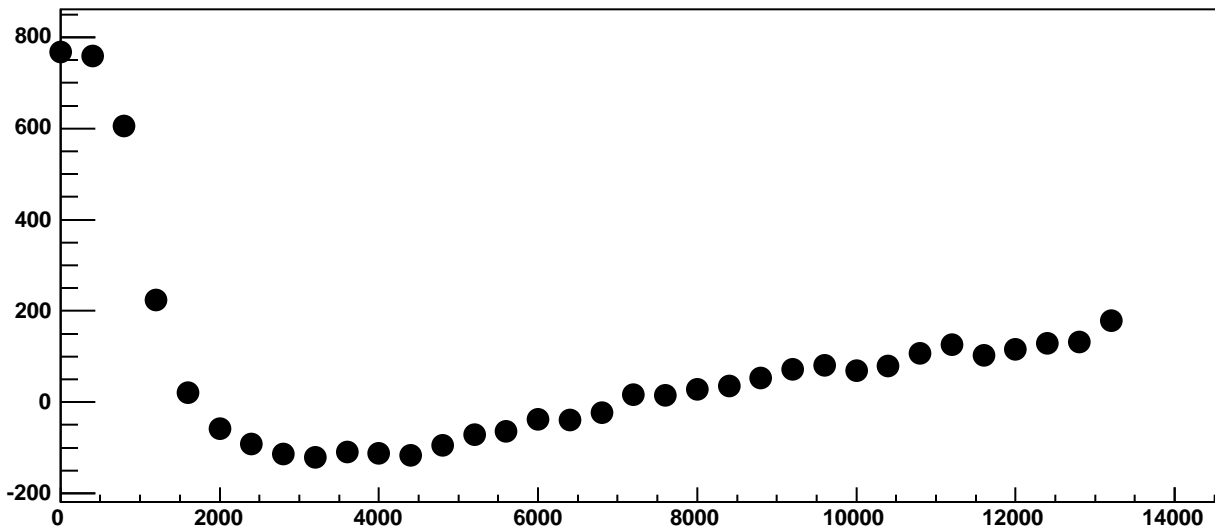
p1

-0.005205 ± 0.0004404

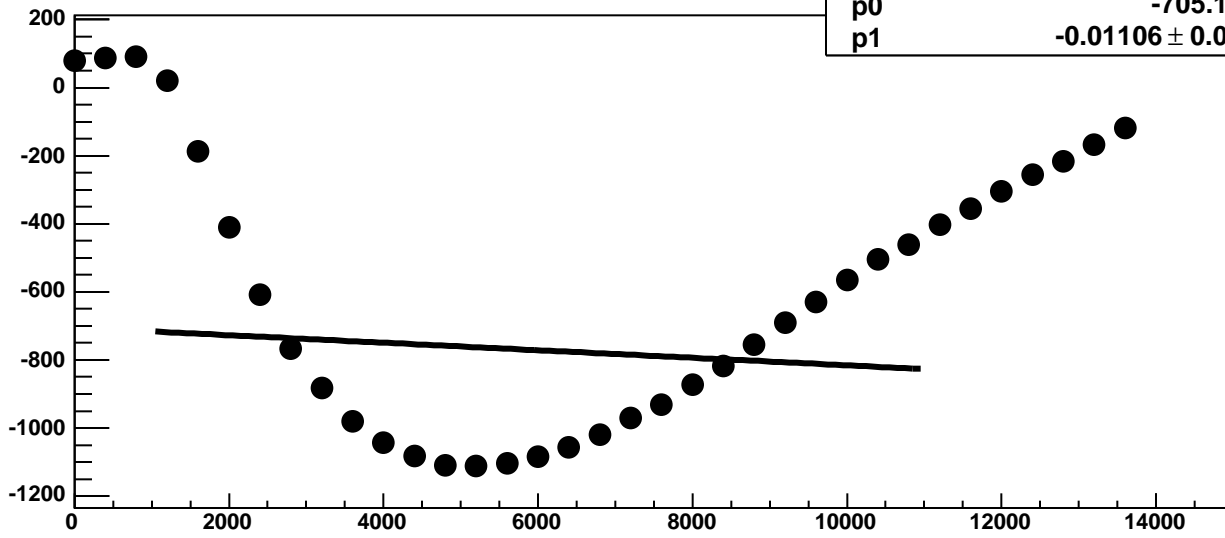
Chip 10, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

5.313e+05 / 23

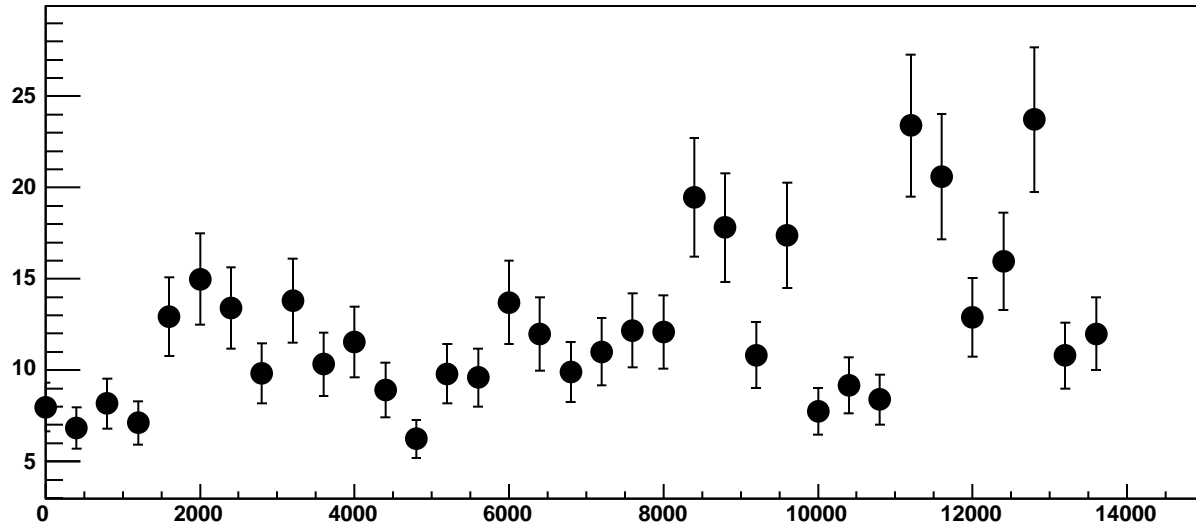
p0

-705.1 ± 1.051

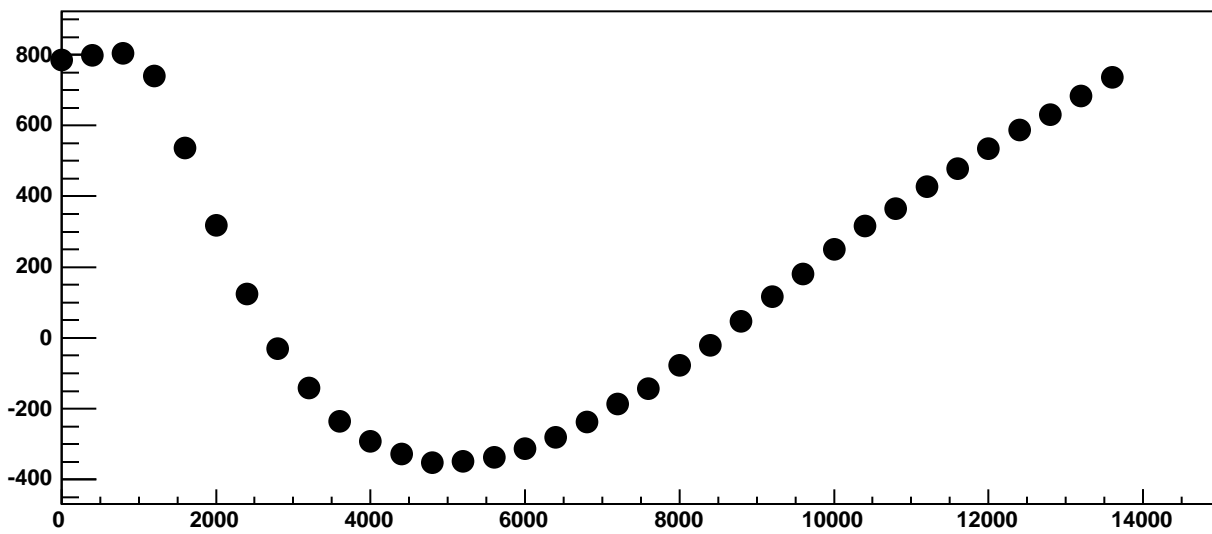
p1

-0.01106 ± 0.0001583

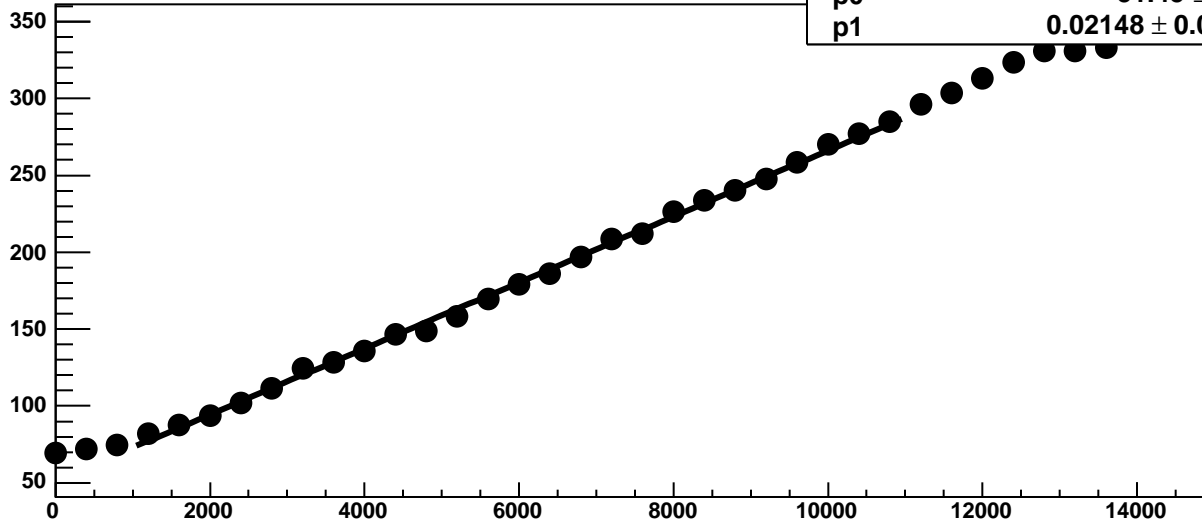
Chip 10, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

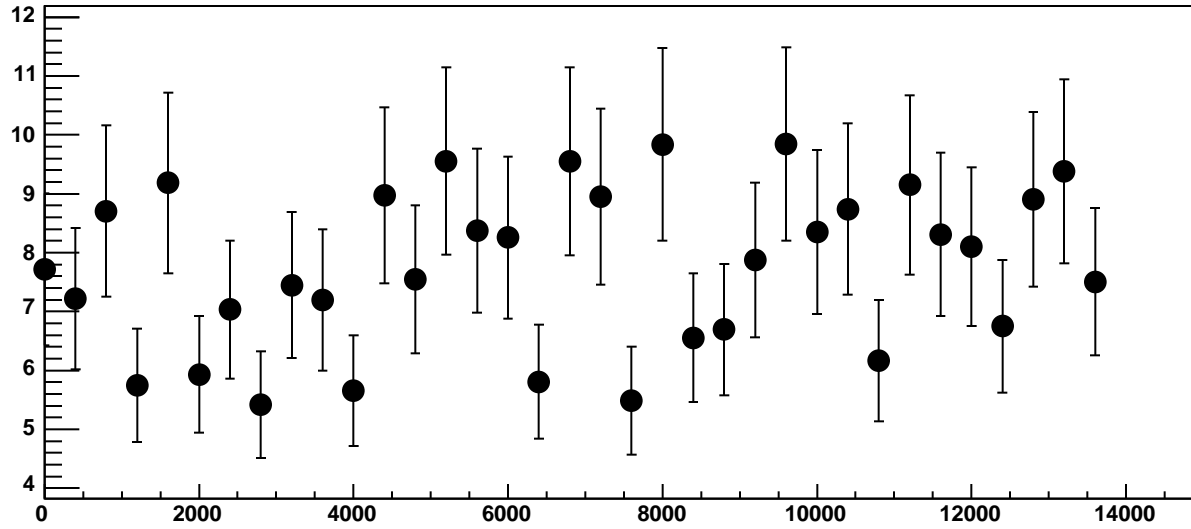


Chip 10, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC

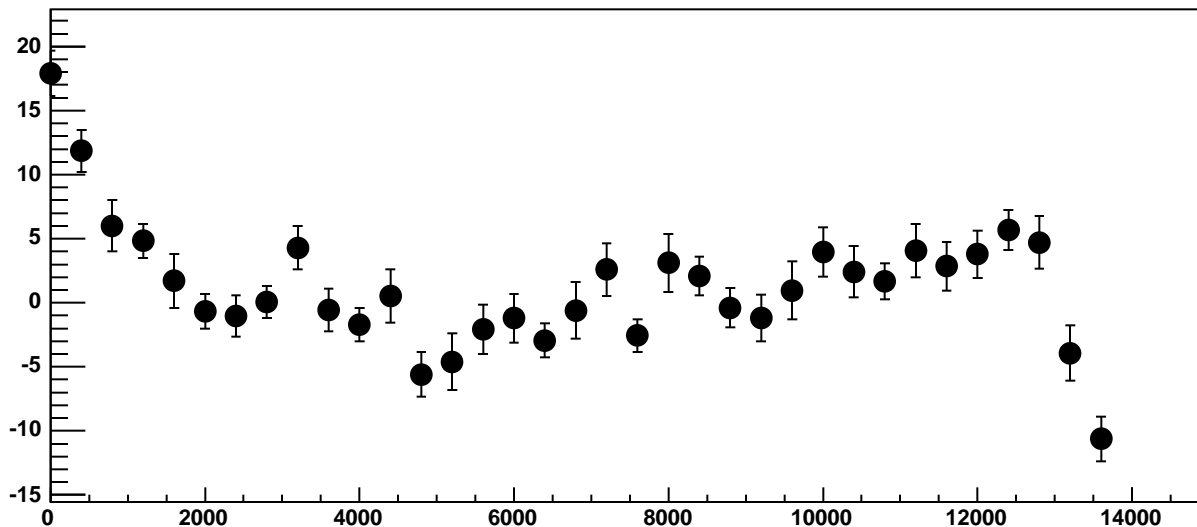


χ^2 / ndf 61.73 / 23
p0 51.45 ± 0.7144
p1 0.02148 ± 0.0001114

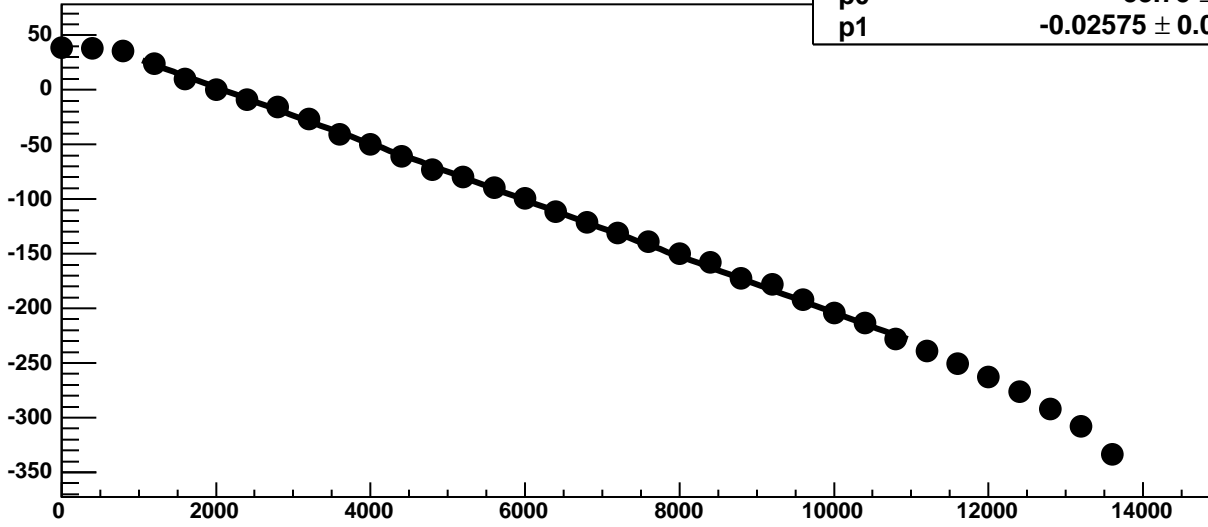
Chip 10, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

38.79 / 23

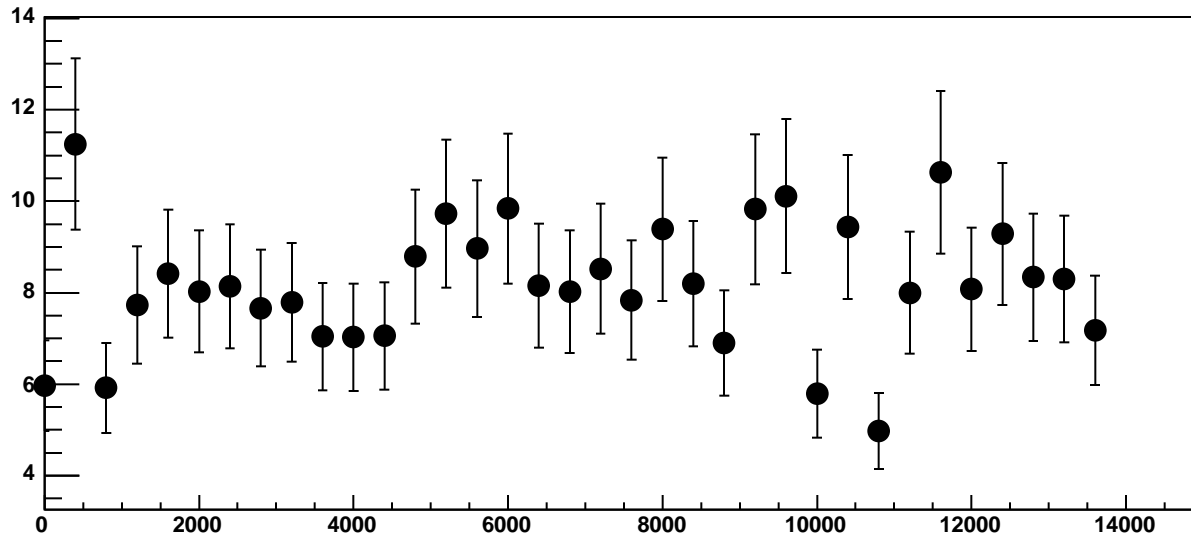
p0

53.75 ± 0.8097

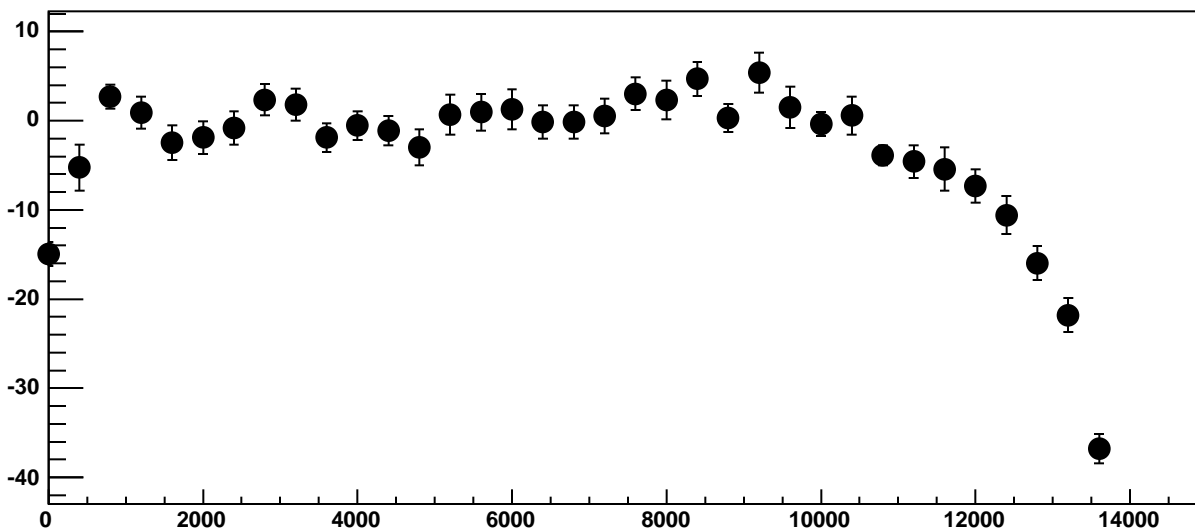
p1

-0.02575 ± 0.0001164

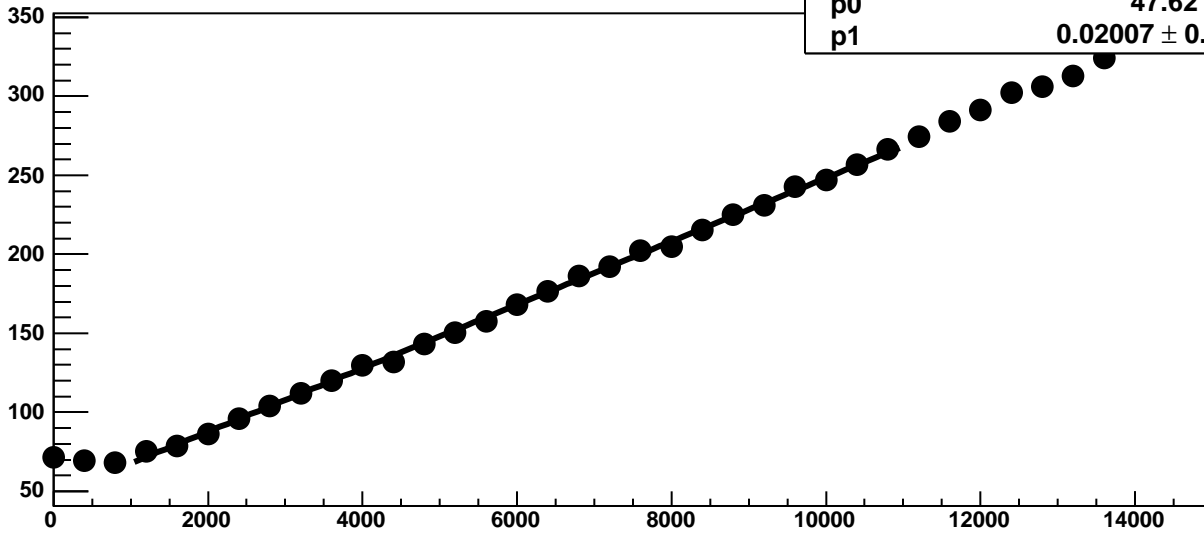
Chip 10, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

17.68 / 23

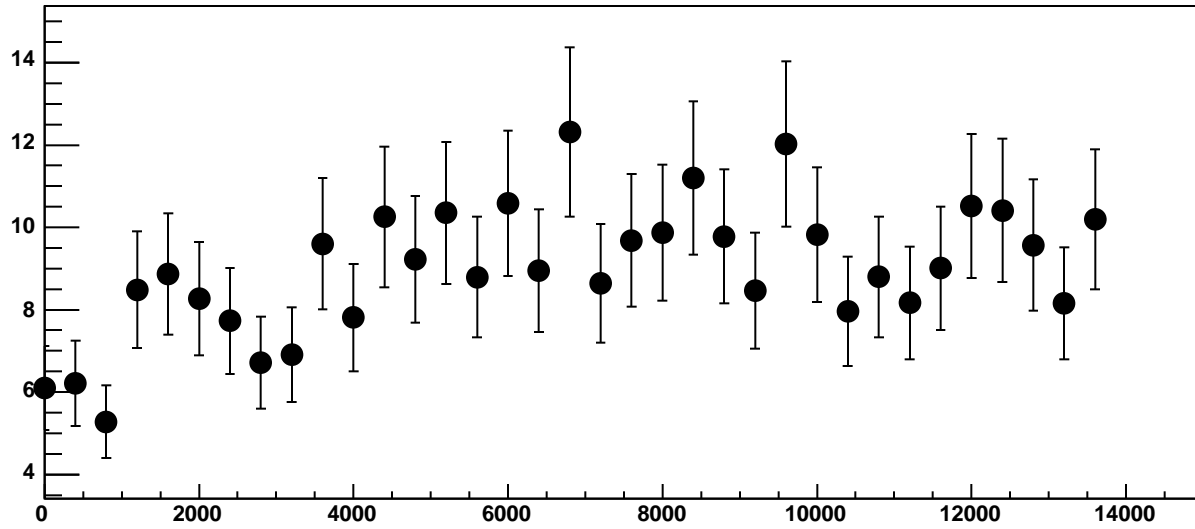
p0

47.62 ± 0.878

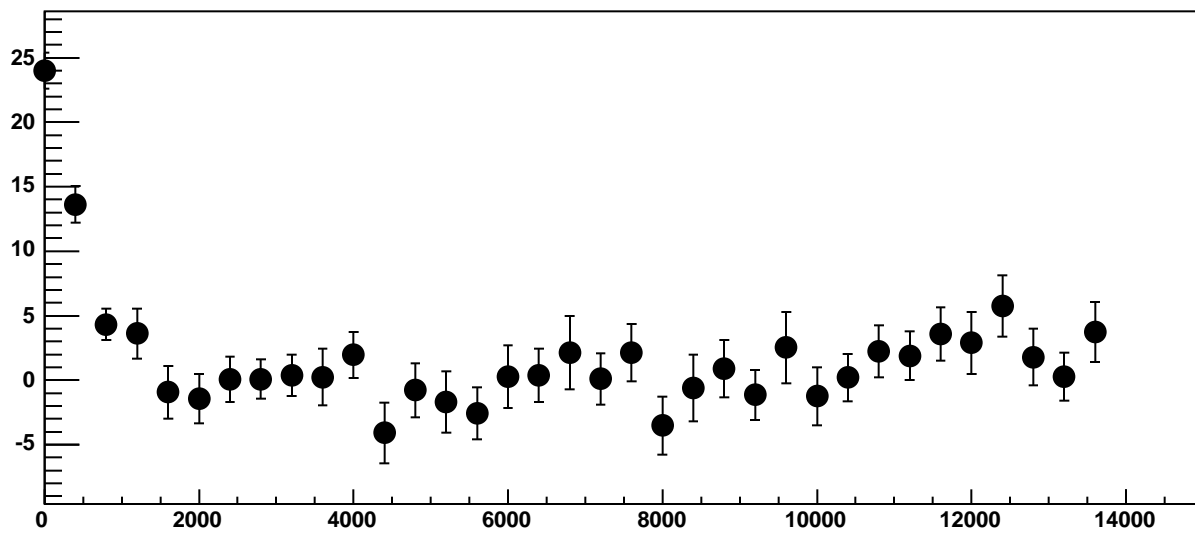
p1

0.02007 ± 0.000138

Chip 10, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

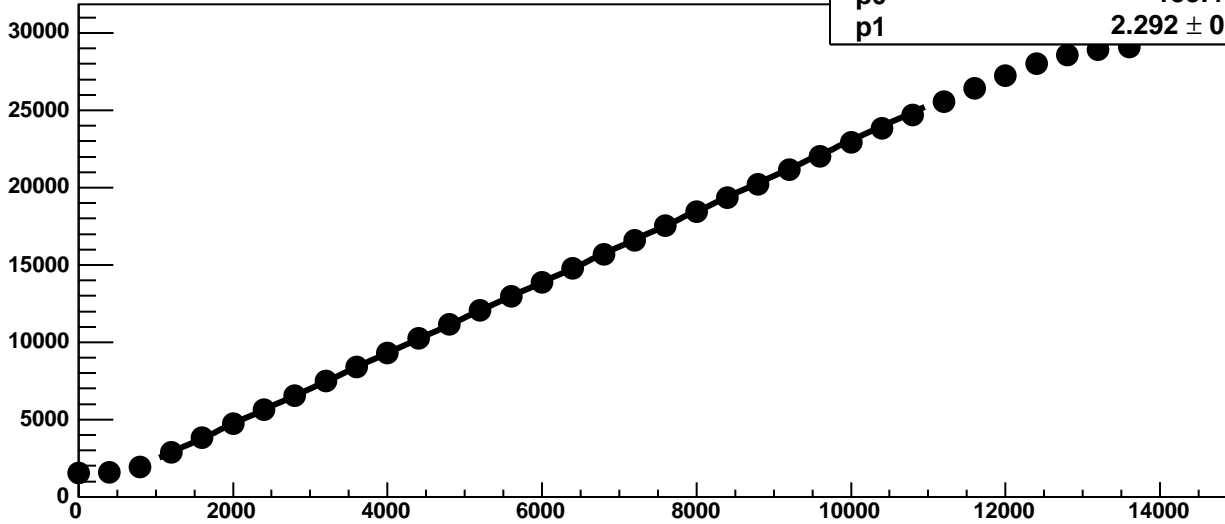


Chip 10, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

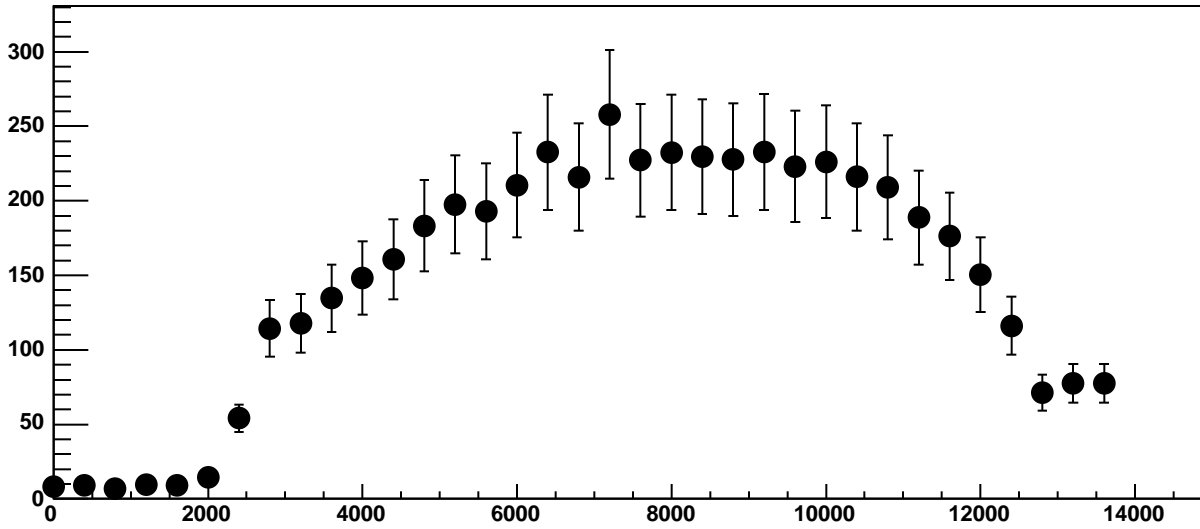


Chip 10, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

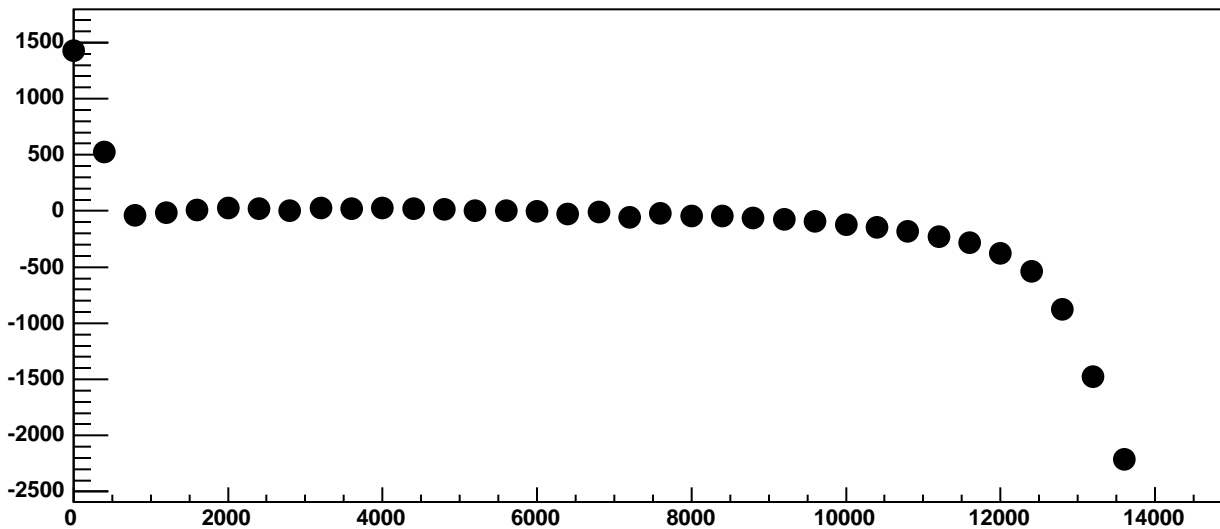
χ^2 / ndf 172.9 / 23
p0 133.4 ± 3.105
p1 2.292 ± 0.001734



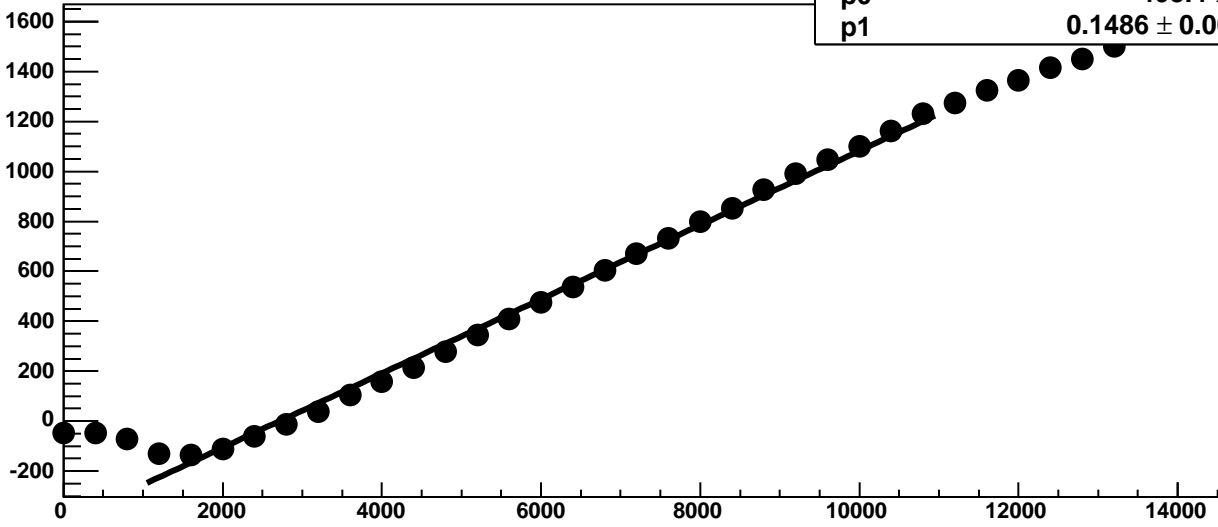
Chip 10, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC



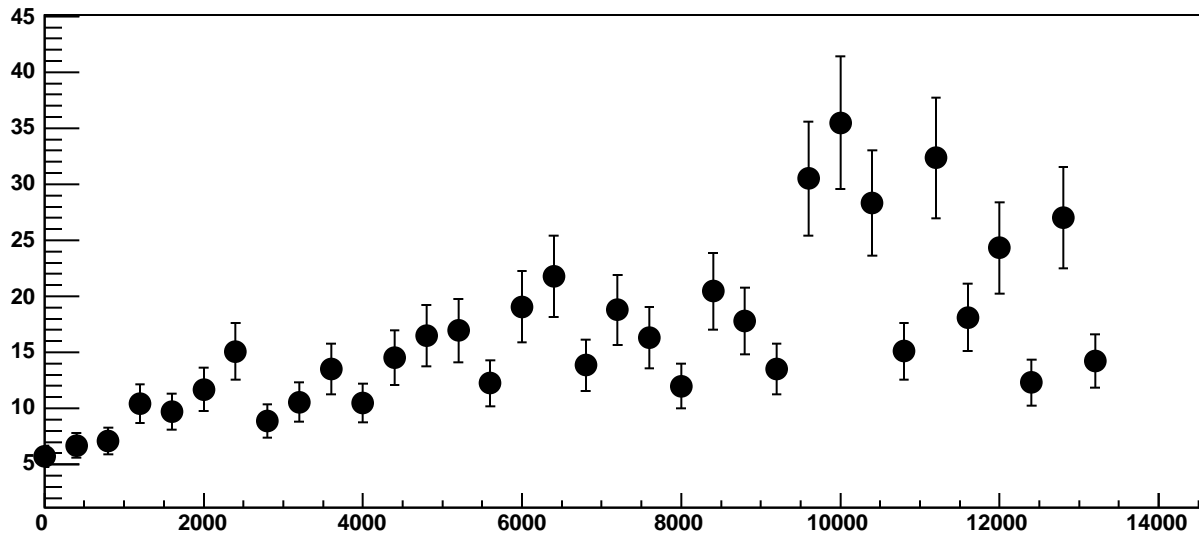
Chip 10, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC



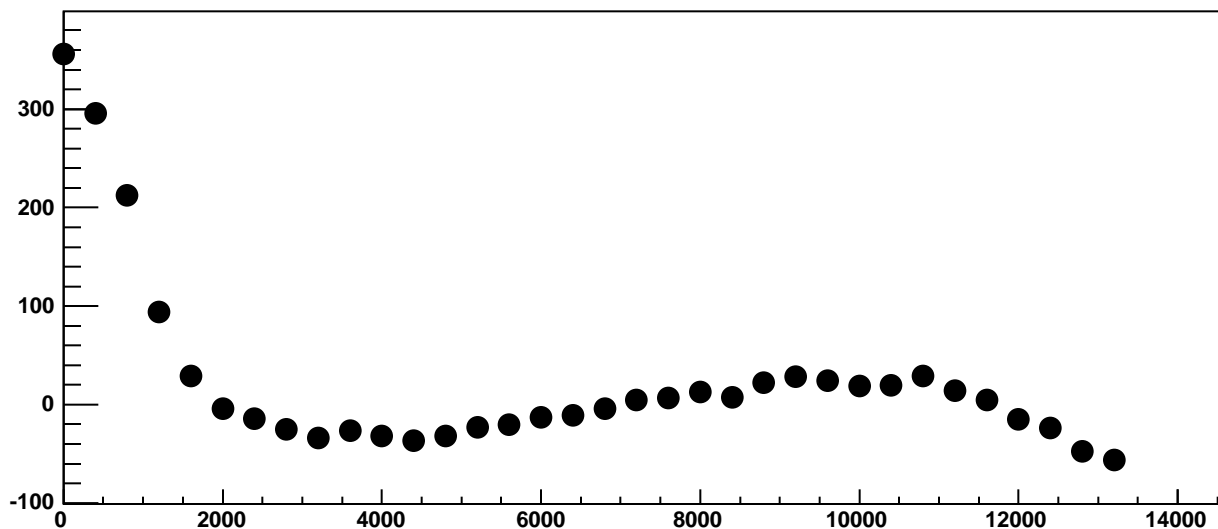
Chip 10, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



Chip 10, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

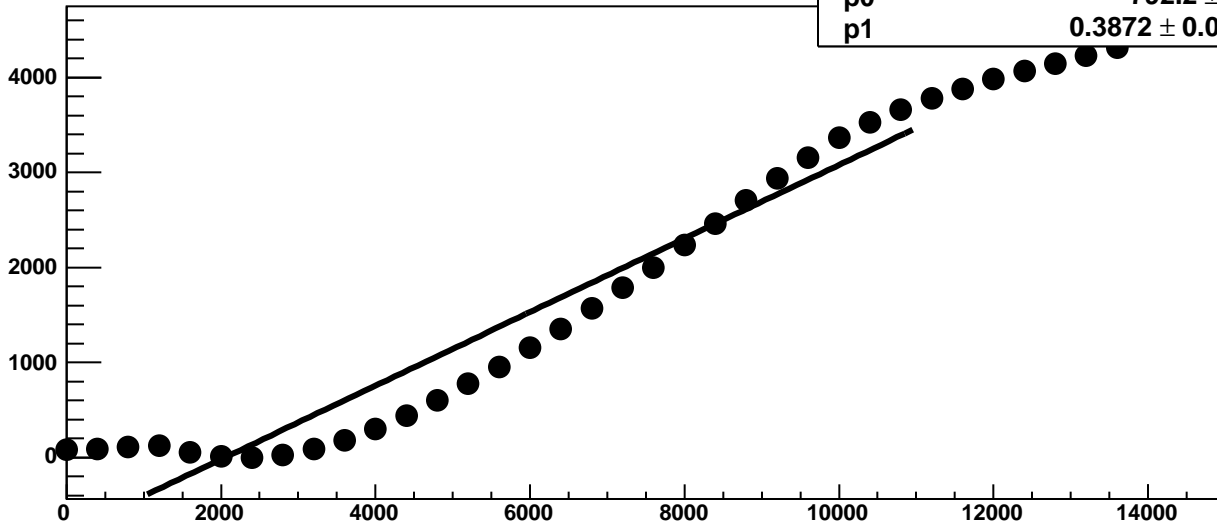


Chip 10, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

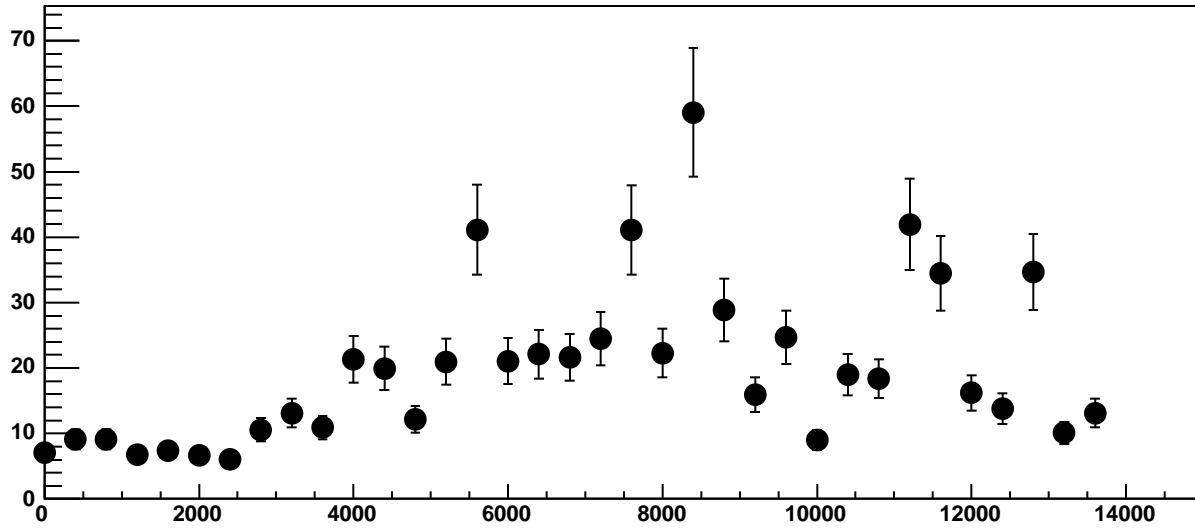


Chip 10, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

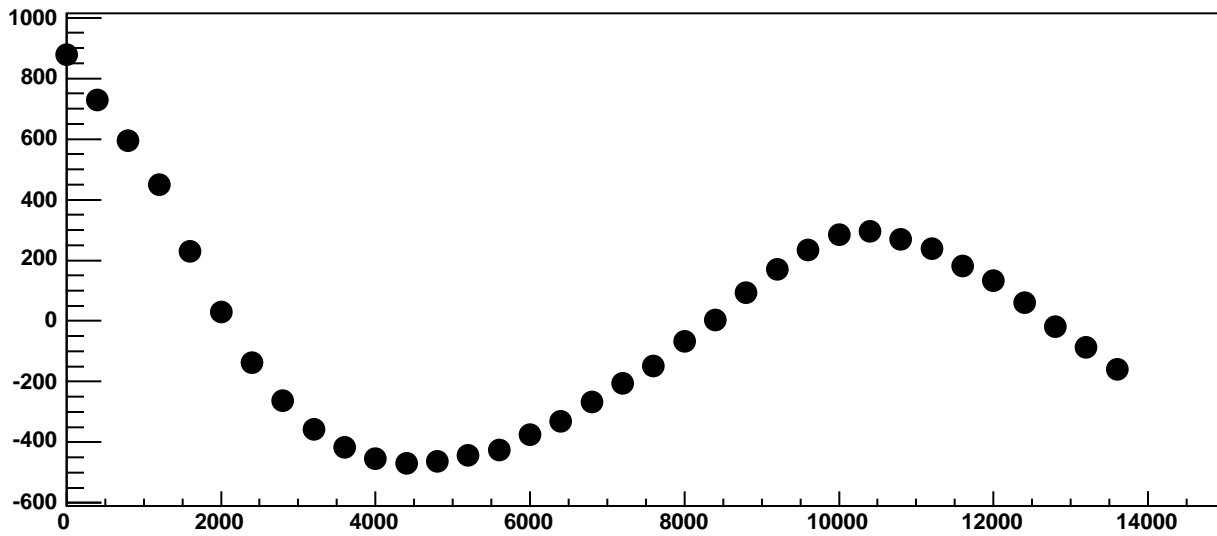
χ^2 / ndf 2.692e+05 / 23
p0 -792.2 ± 0.9312
p1 0.3872 ± 0.0001907



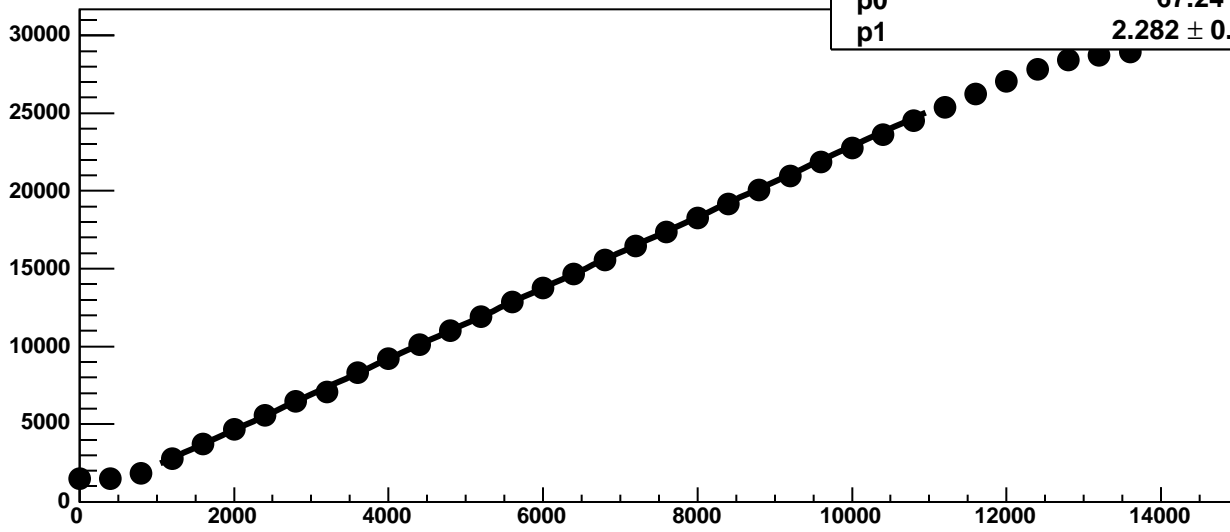
Chip 10, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



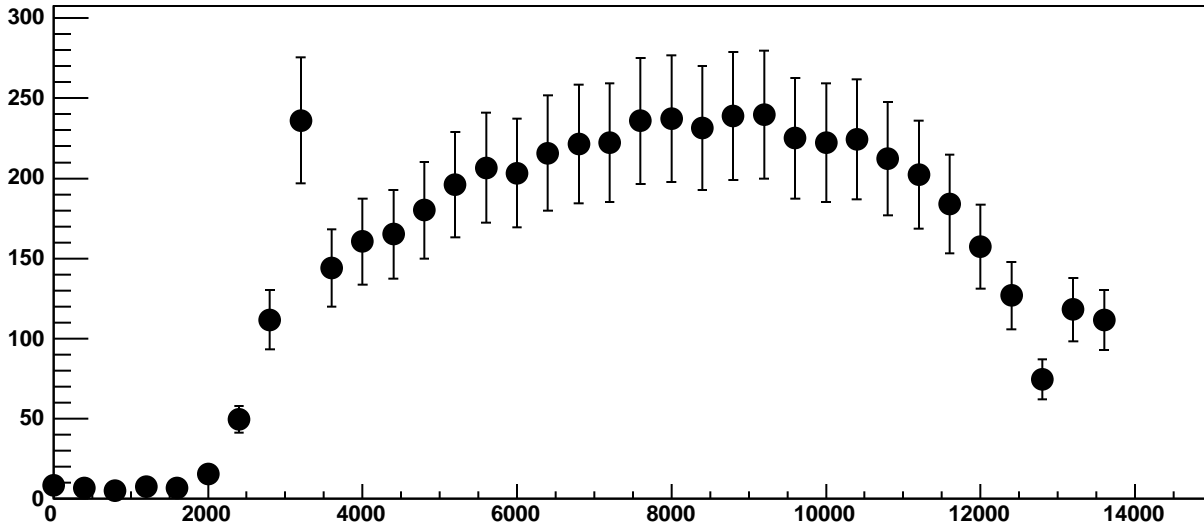
Chip 10, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



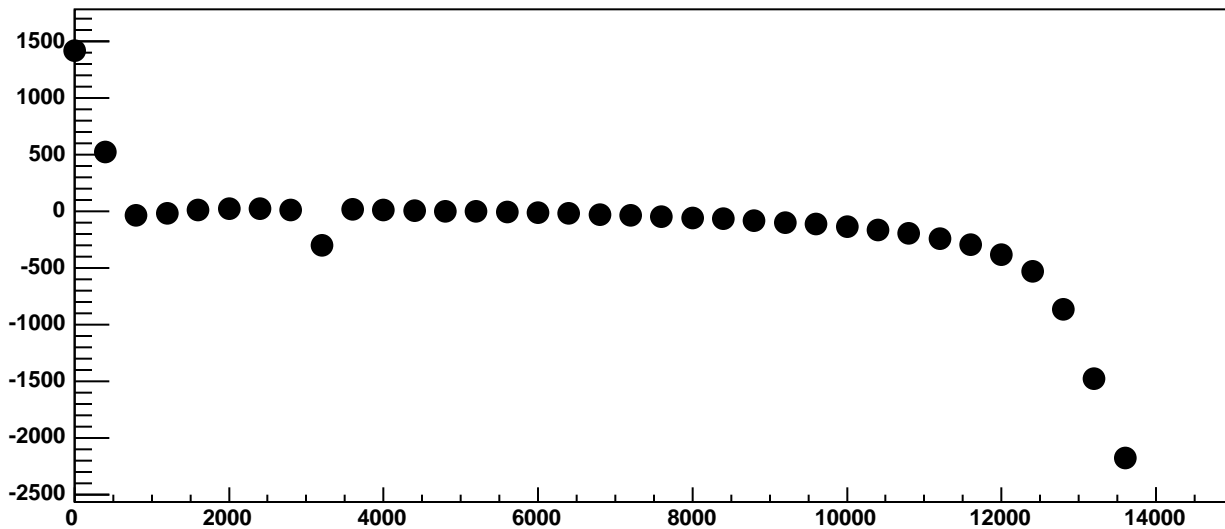
Chip 10, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC



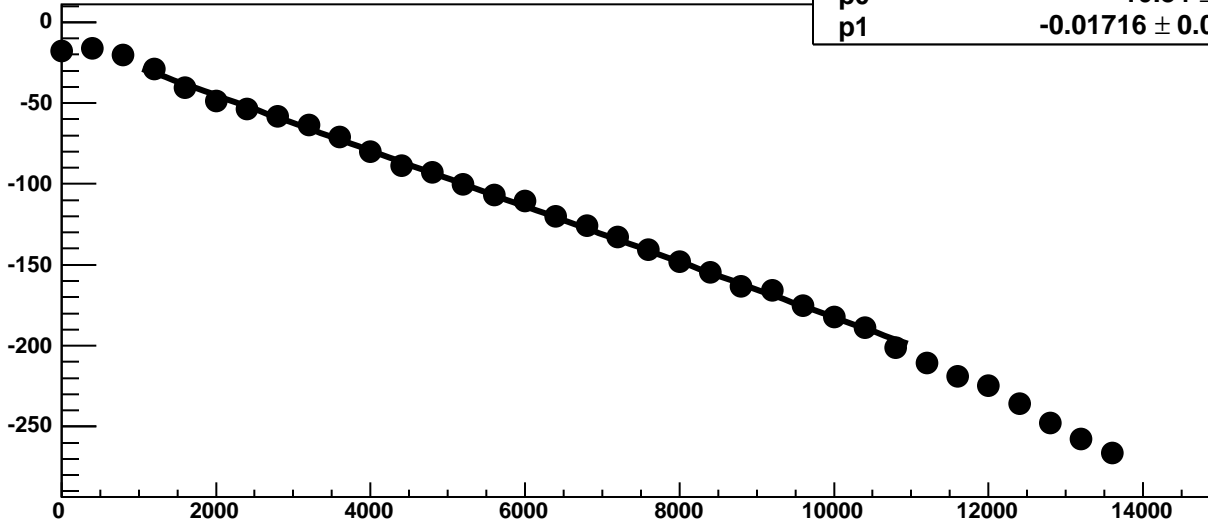
Chip 10, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

21.84 / 23

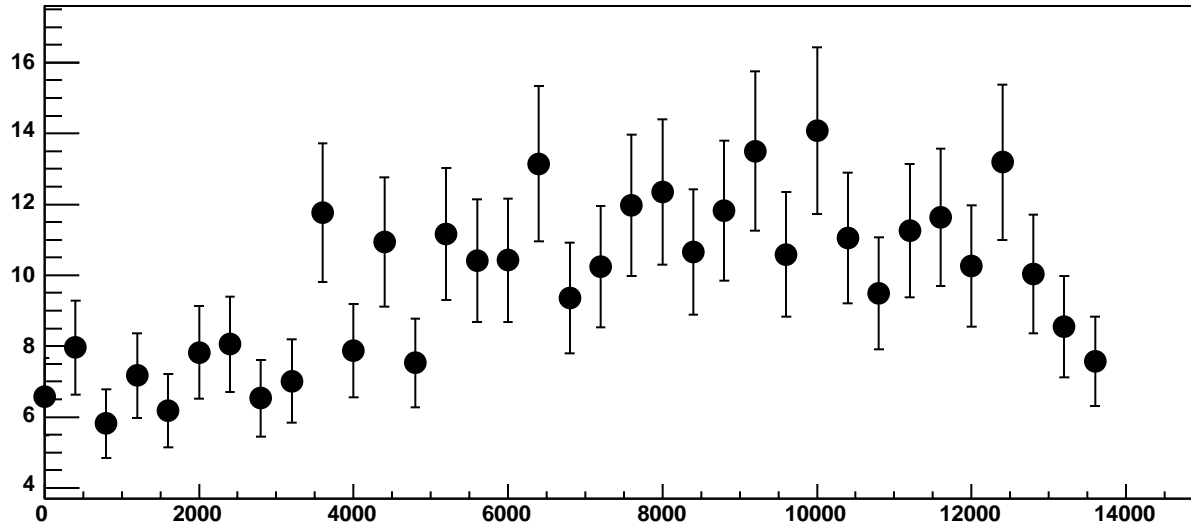
p0

-10.84 ± 0.8347

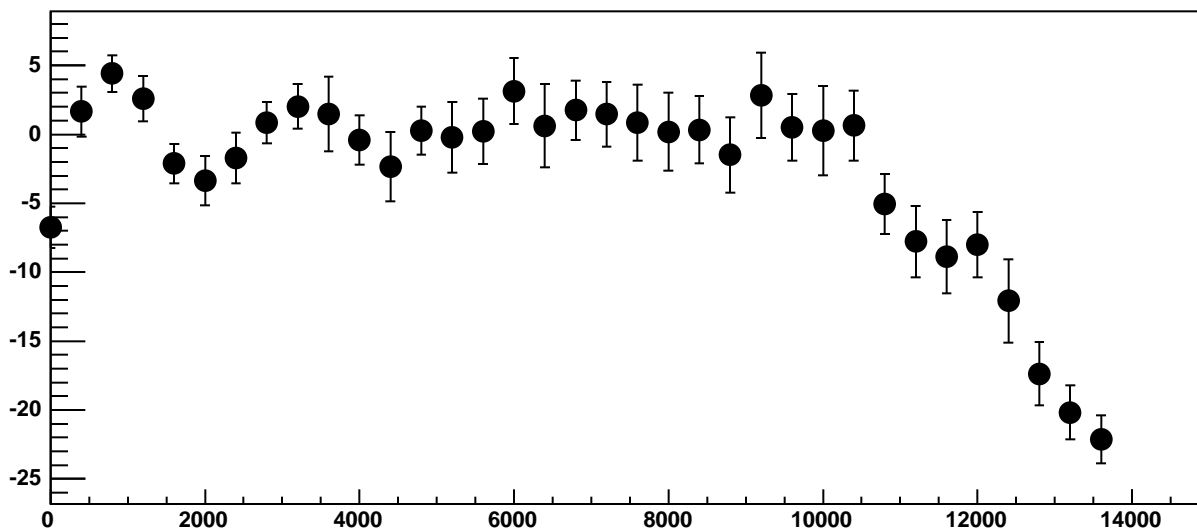
p1

-0.01716 ± 0.0001454

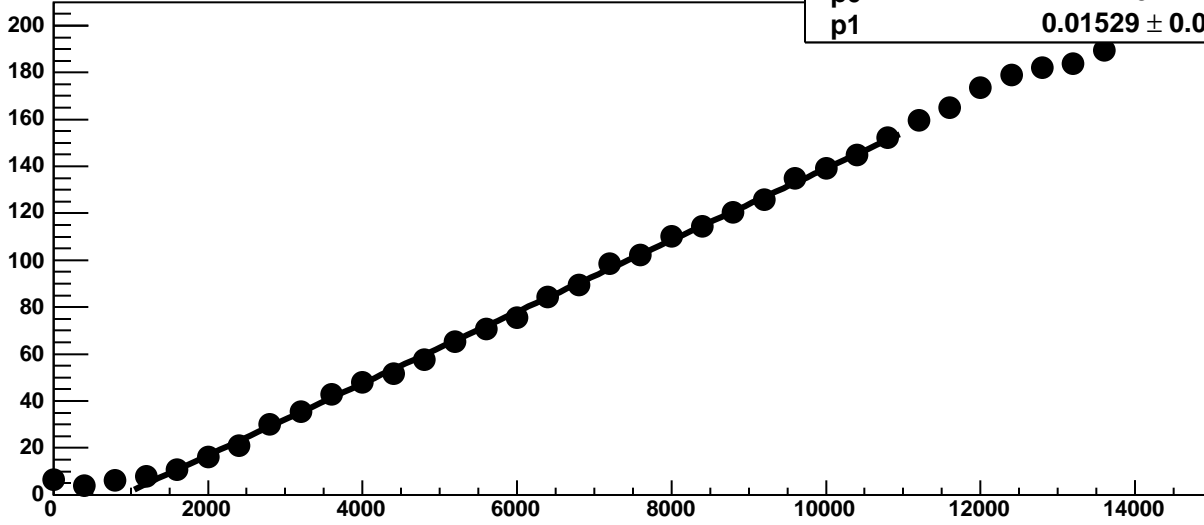
Chip 10, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

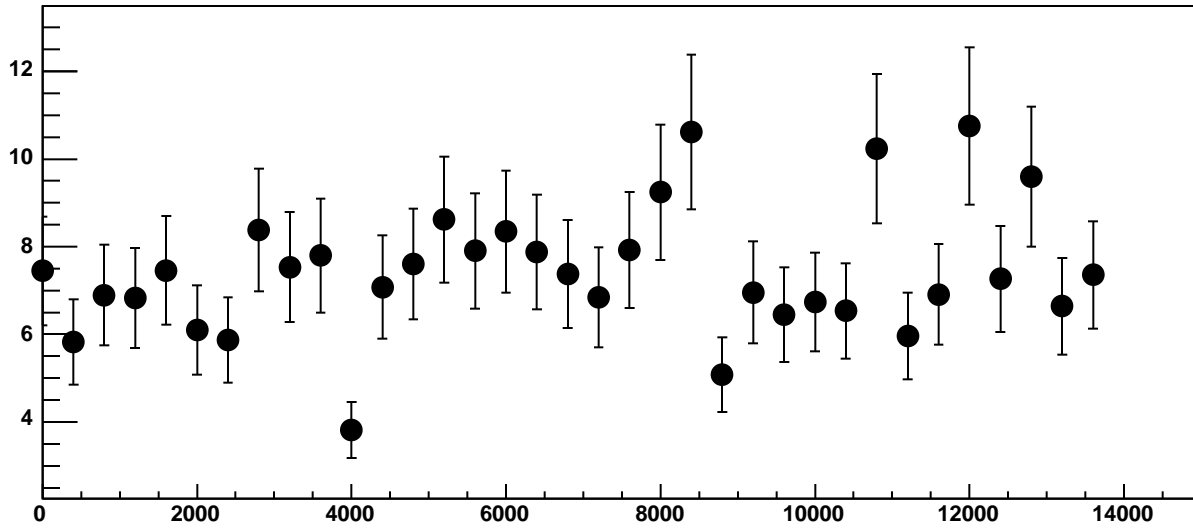


Chip 10, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

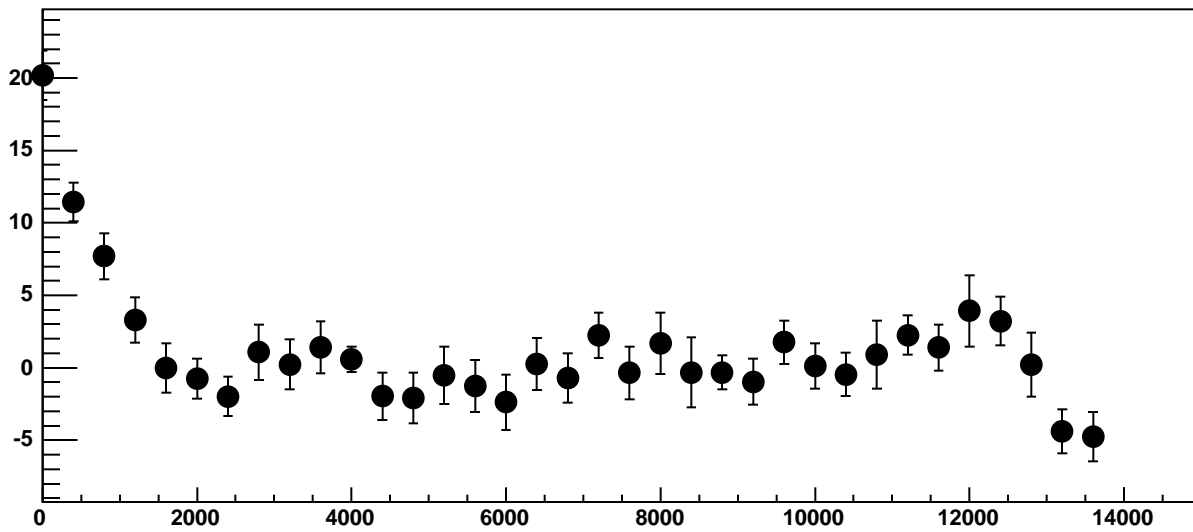


χ^2 / ndf 18.28 / 23
p0 -13.71 ± 0.7063
p1 0.01529 ± 0.0001094

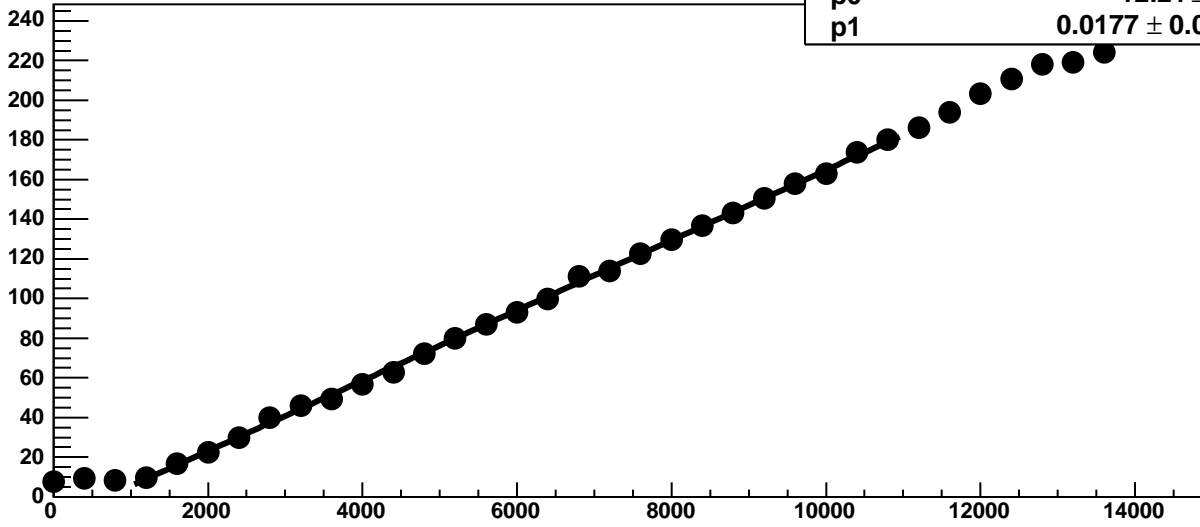
Chip 10, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

14.22 / 23

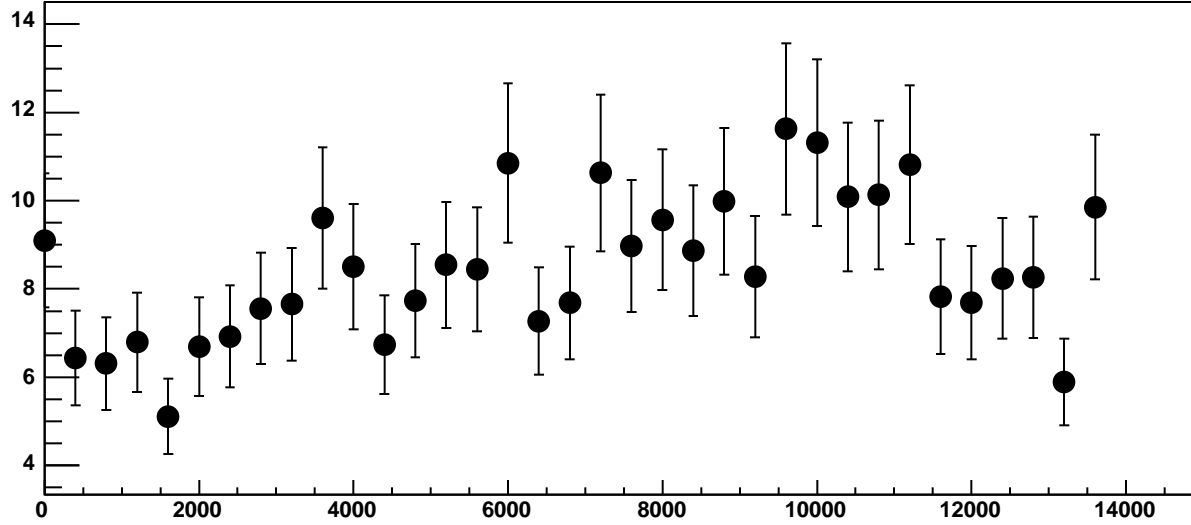
p0

-12.21 ± 0.7602

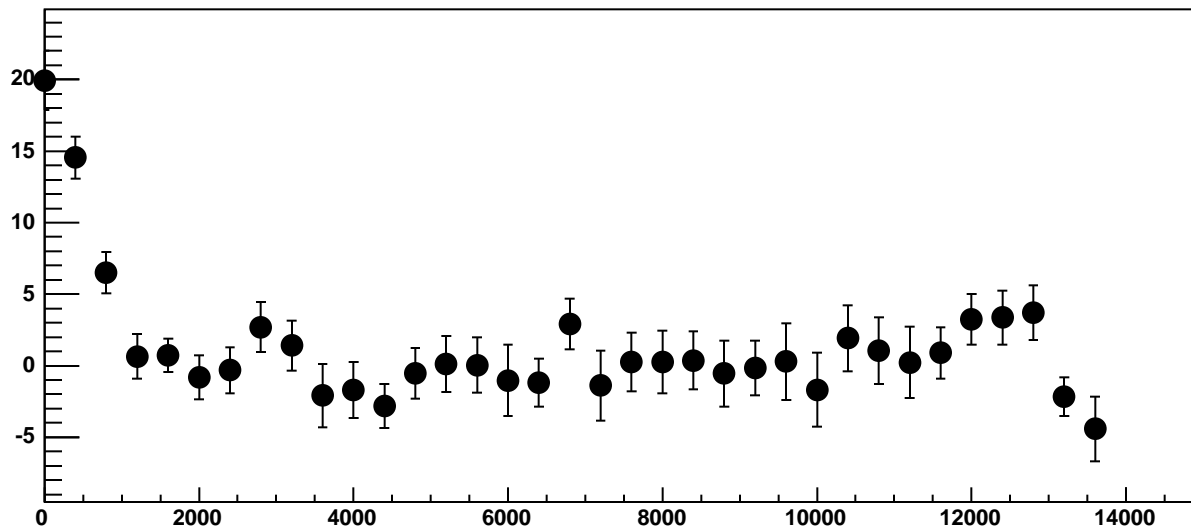
p1

0.0177 ± 0.0001298

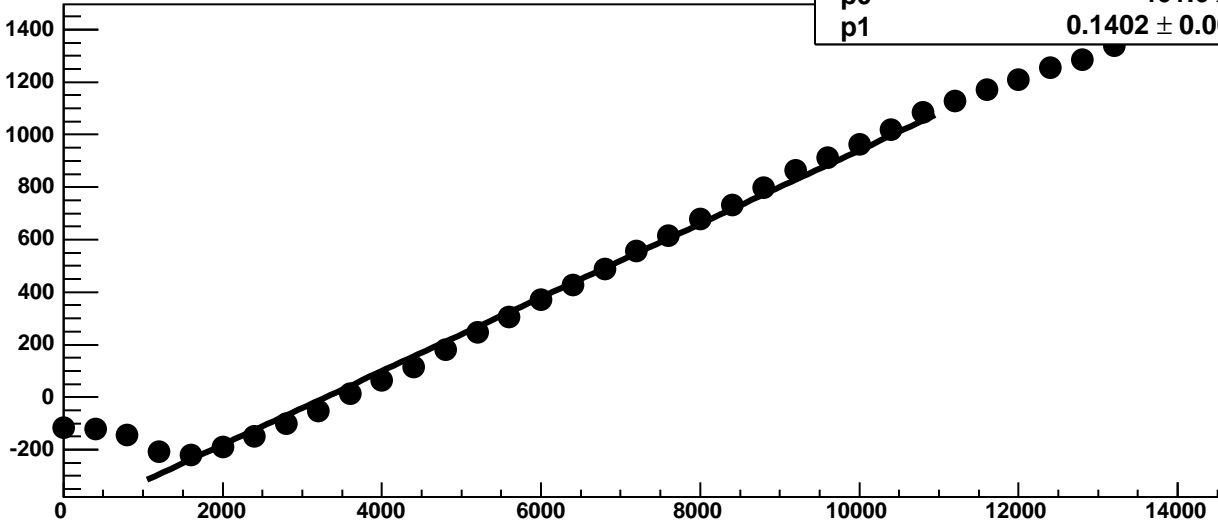
Chip 10, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



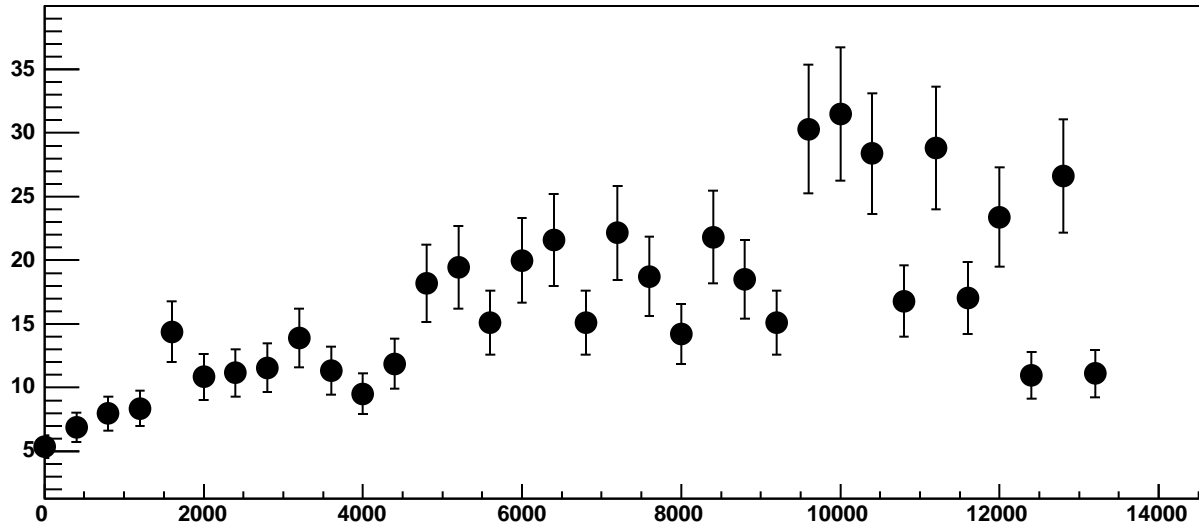
Chip 10, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC



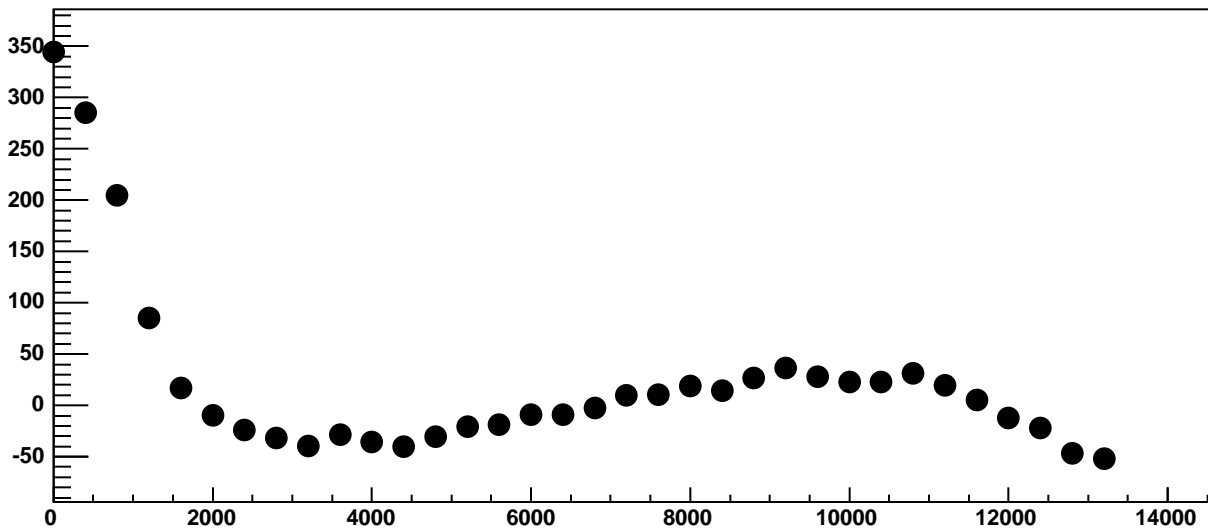
Chip 10, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



Chip 10, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

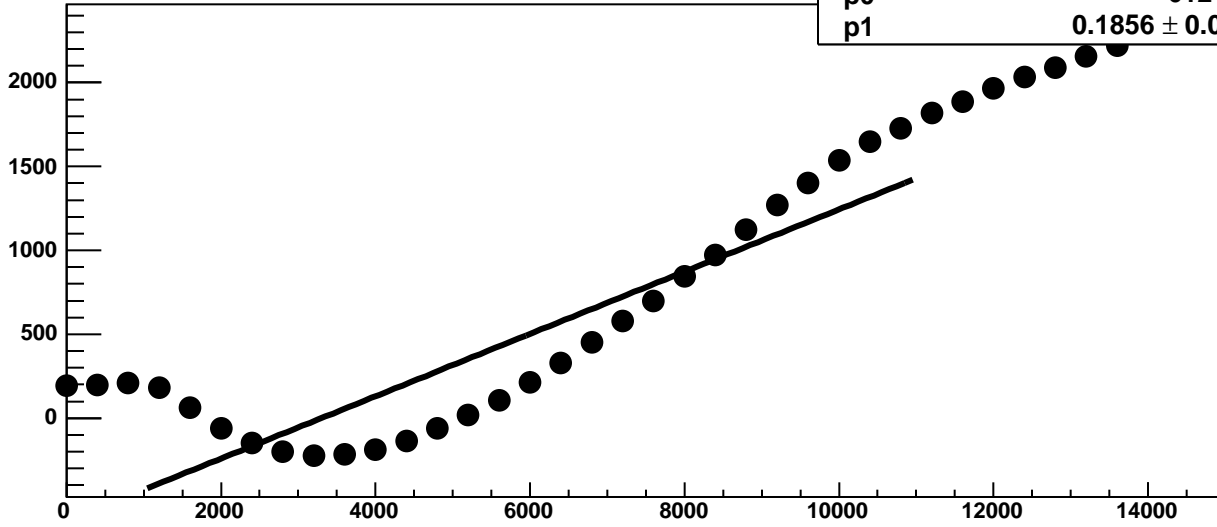


Chip 10, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

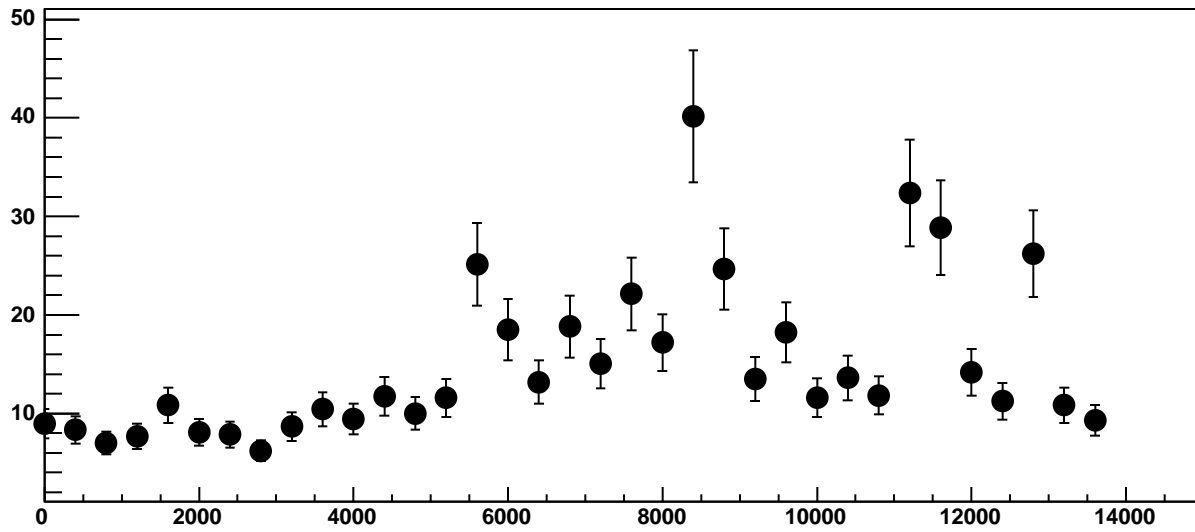


Chip 10, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

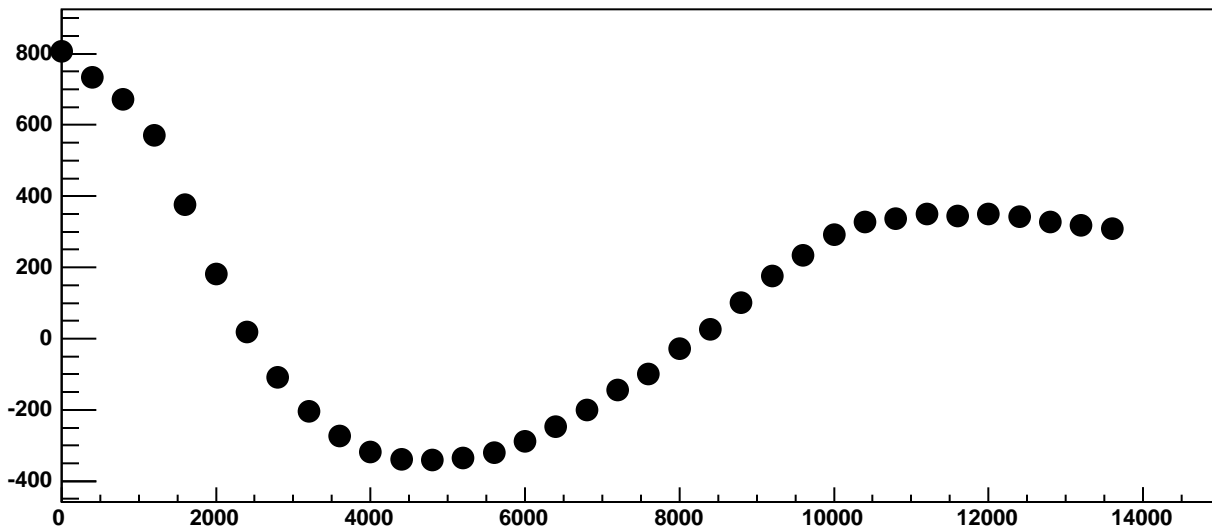
χ^2 / ndf 3.061e+05 / 23
p0 -612 ± 0.966
p1 0.1856 ± 0.0001801



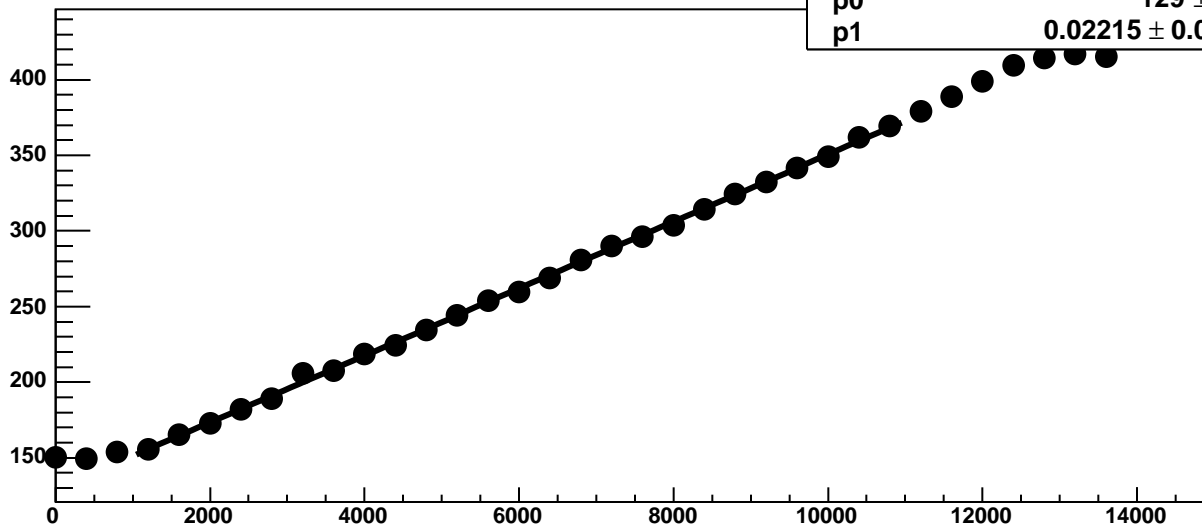
Chip 10, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

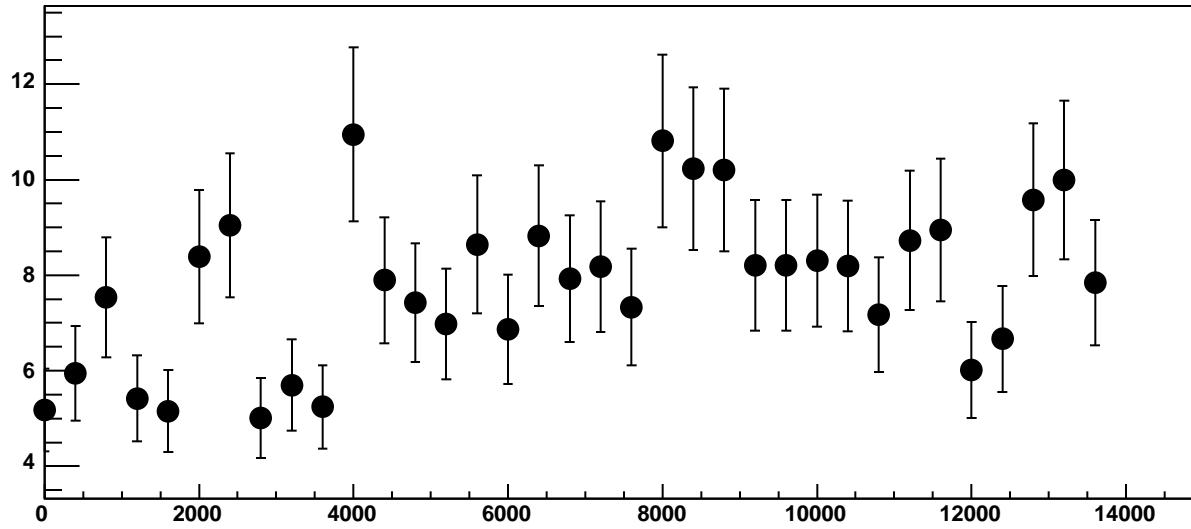


Chip 10, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

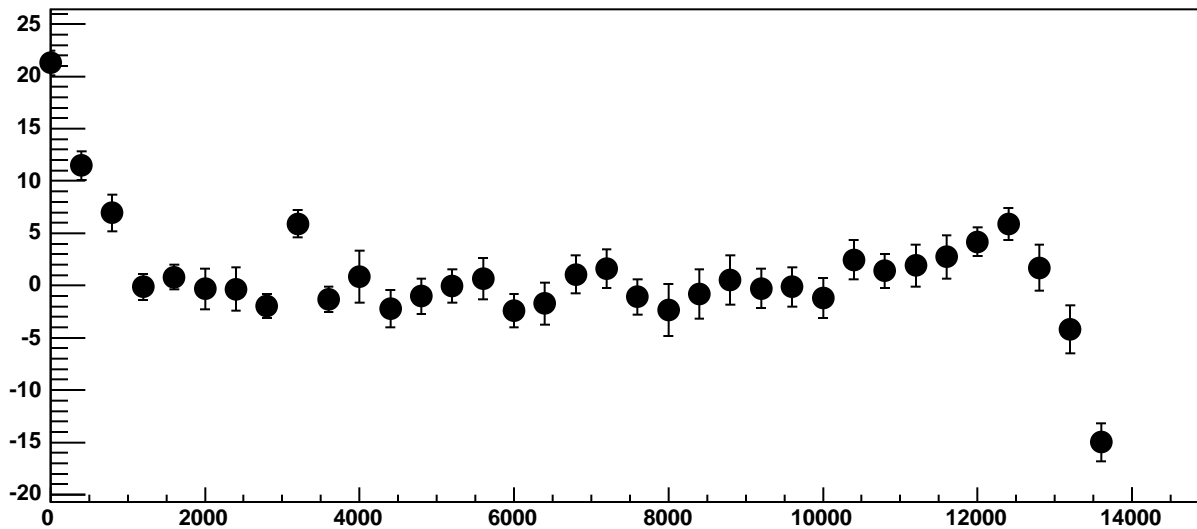


χ^2 / ndf 35.4 / 23
p0 129 ± 0.6765
p1 0.02215 ± 0.0001127

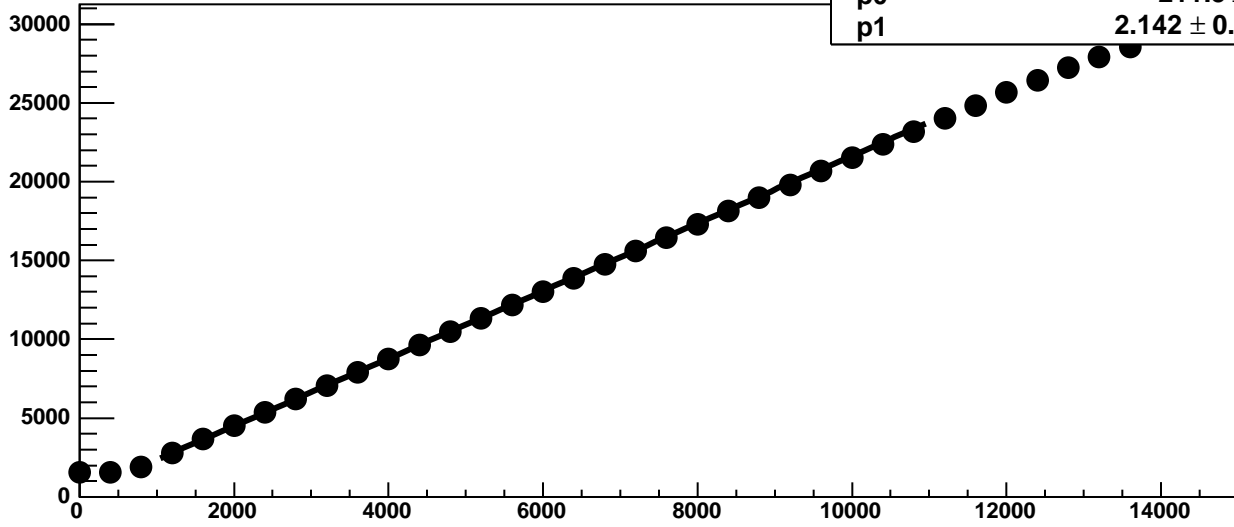
Chip 10, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



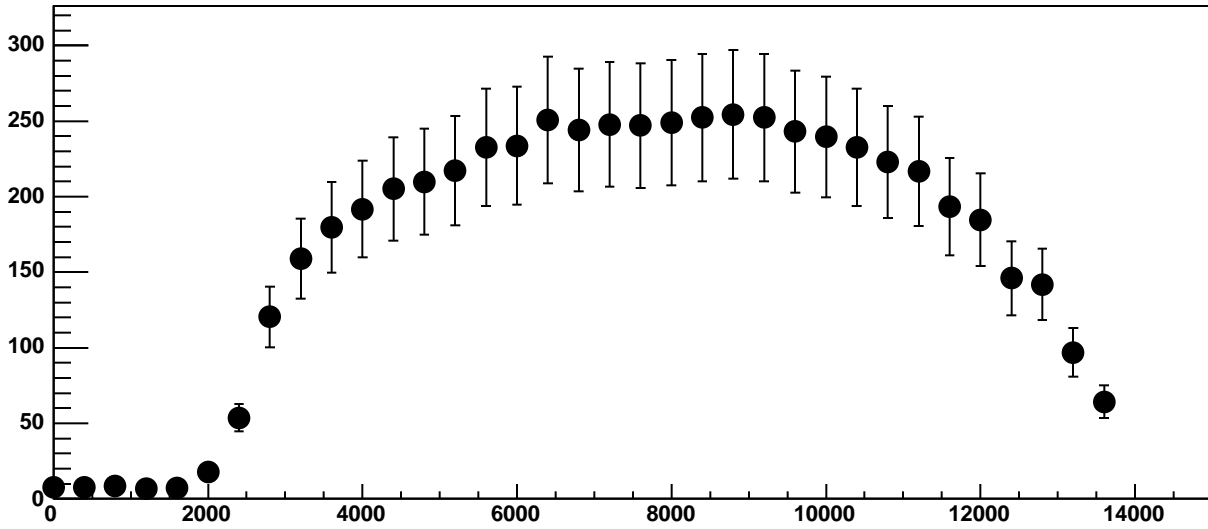
Chip 10, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



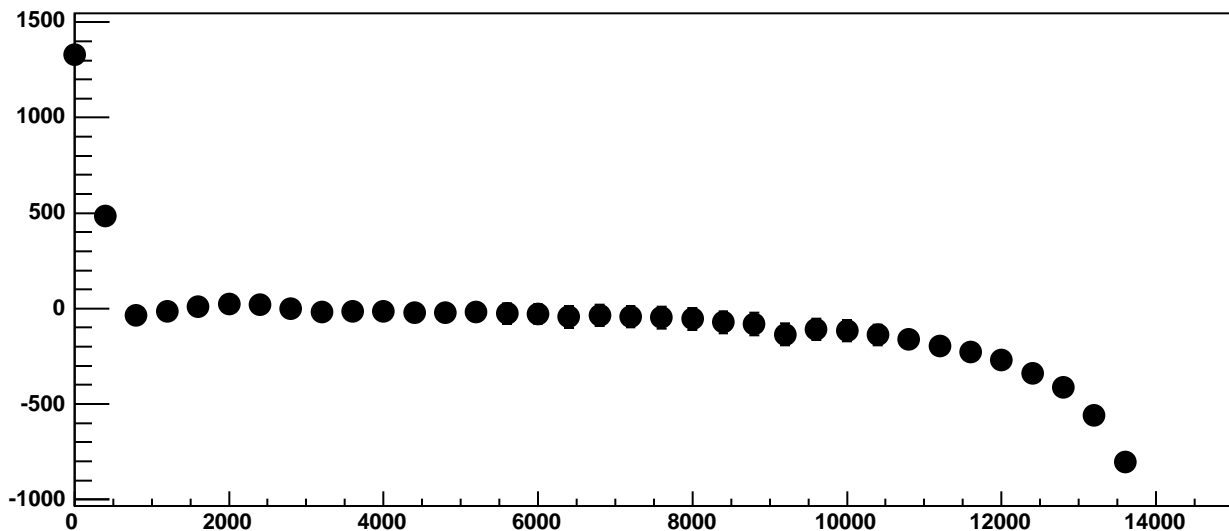
Chip 10, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC



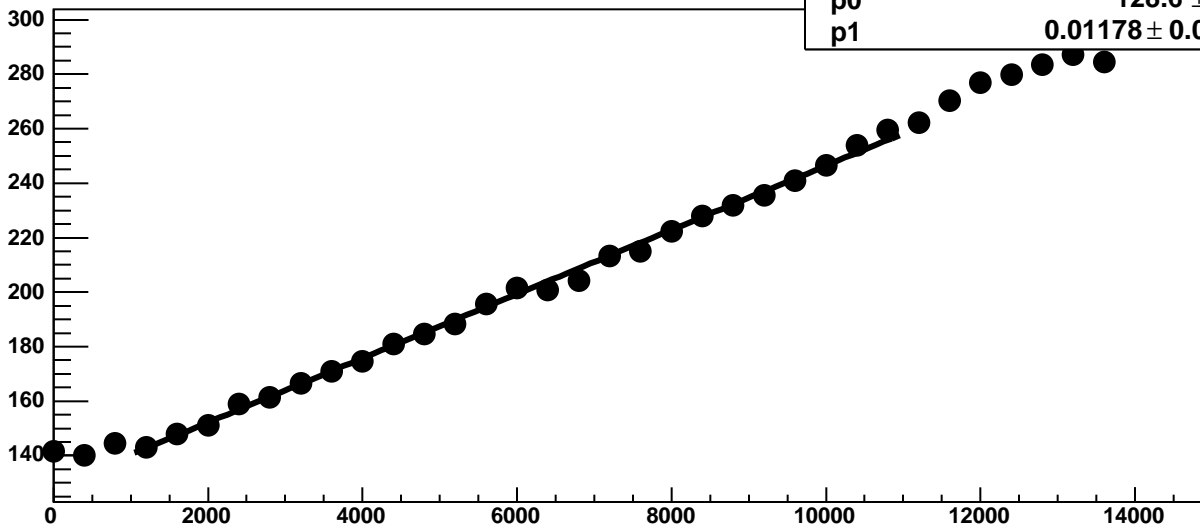
Chip 10, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



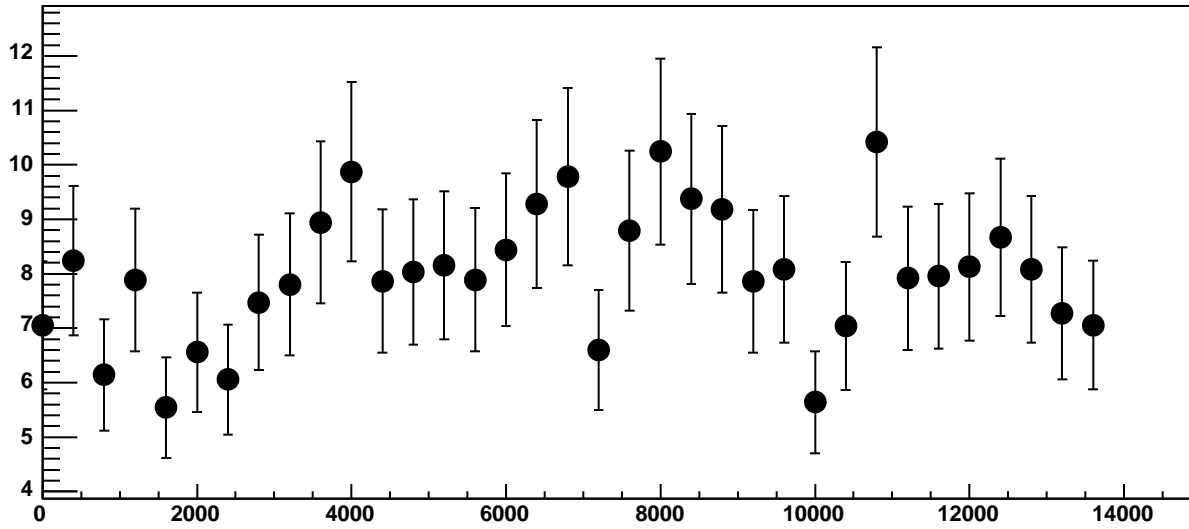
Chip 10, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC



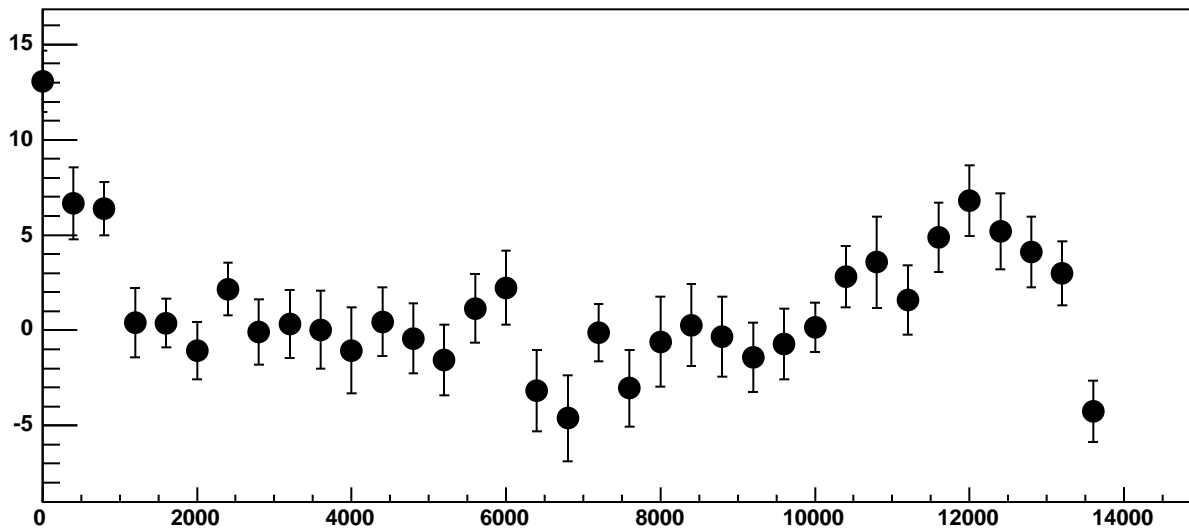
Chip 10, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



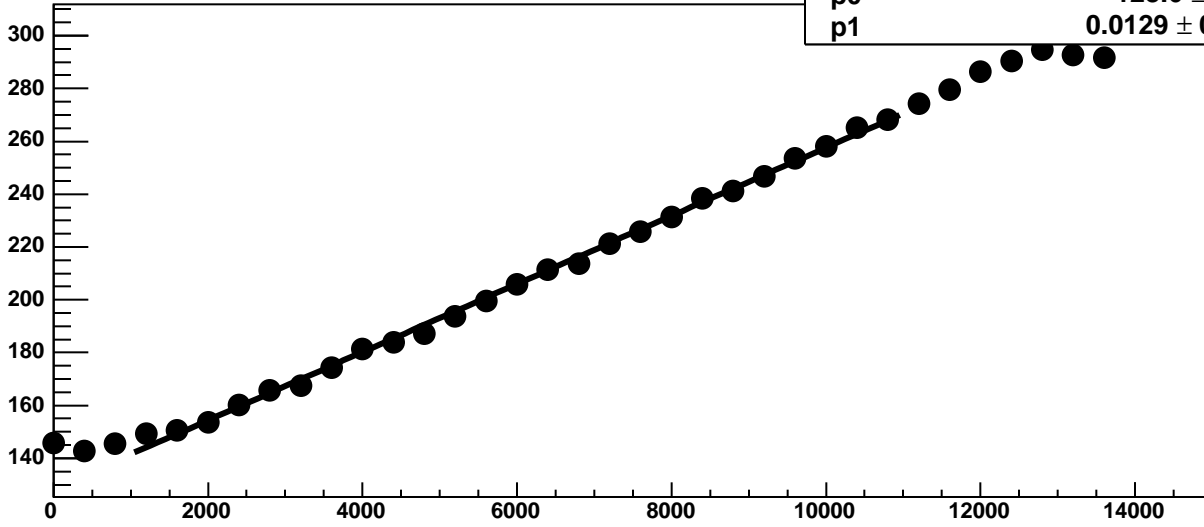
Chip 10, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

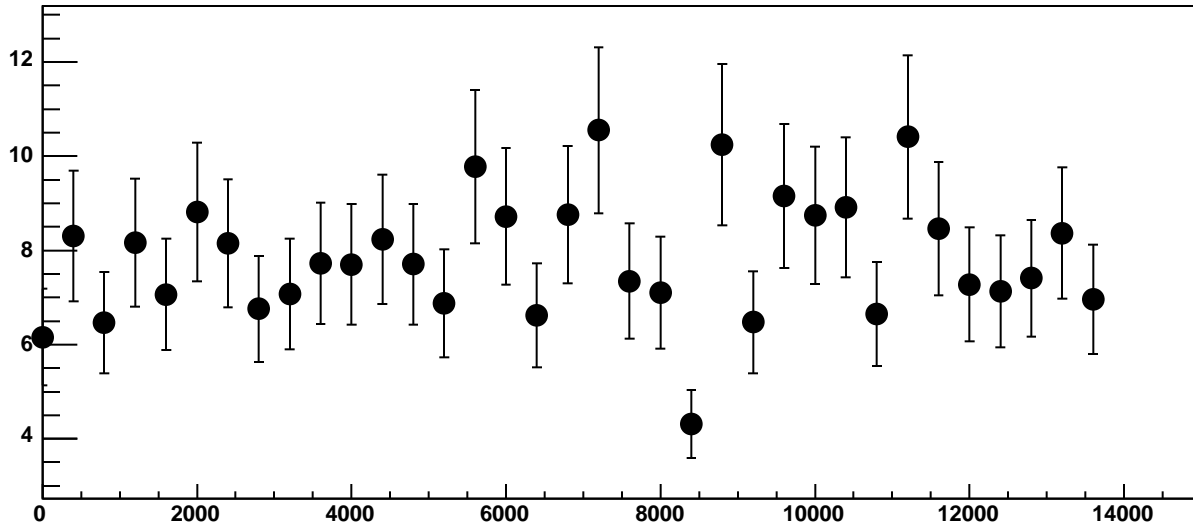


Chip 10, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

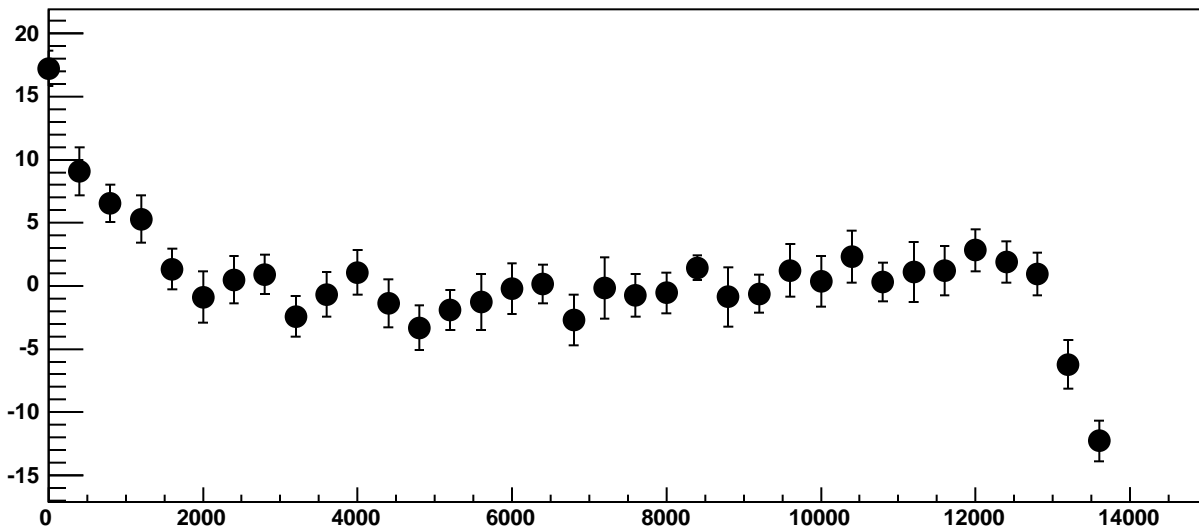


χ^2 / ndf 24.09 / 23
p0 128.6 ± 0.8123
p1 0.0129 ± 0.00012

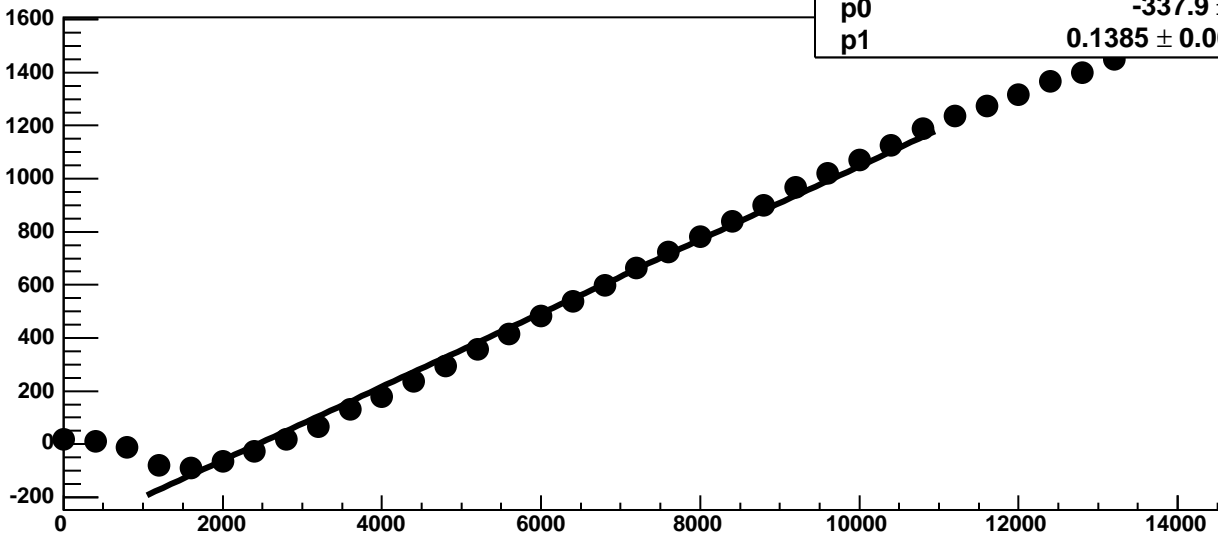
Chip 10, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



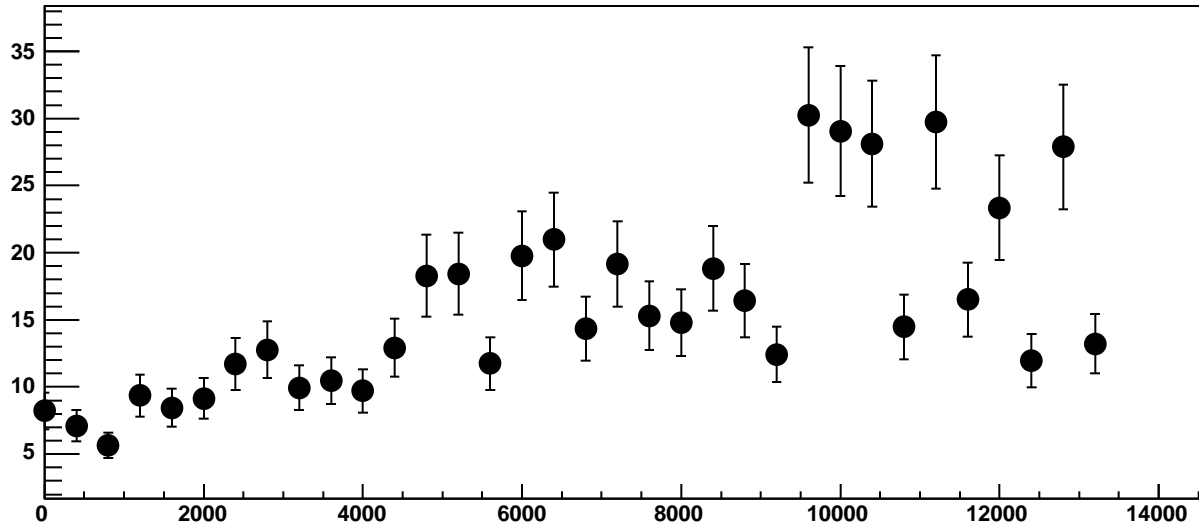
Chip 10, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



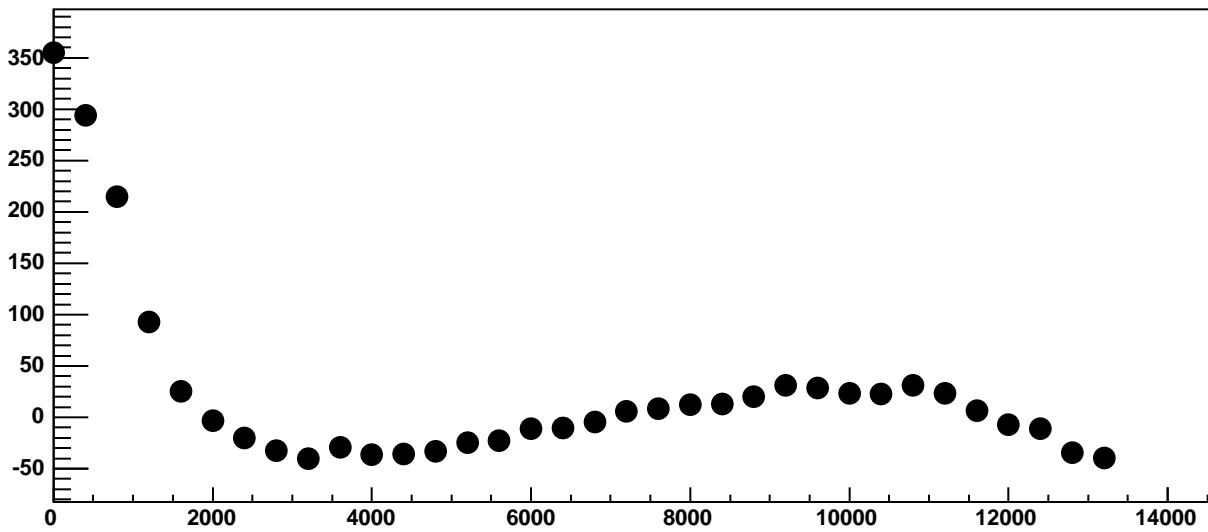
Chip 10, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC



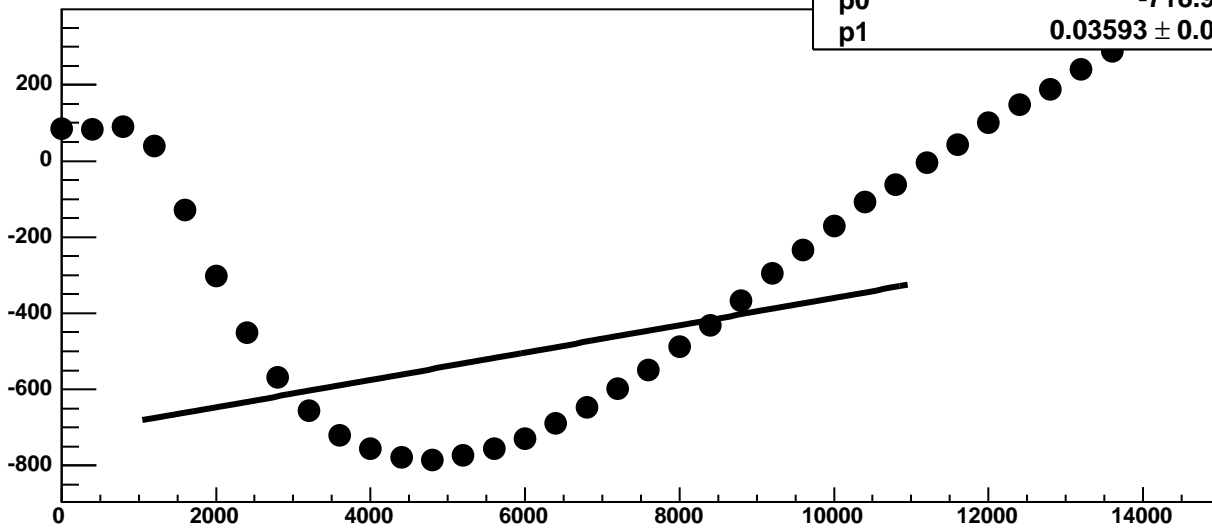
Chip 10, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



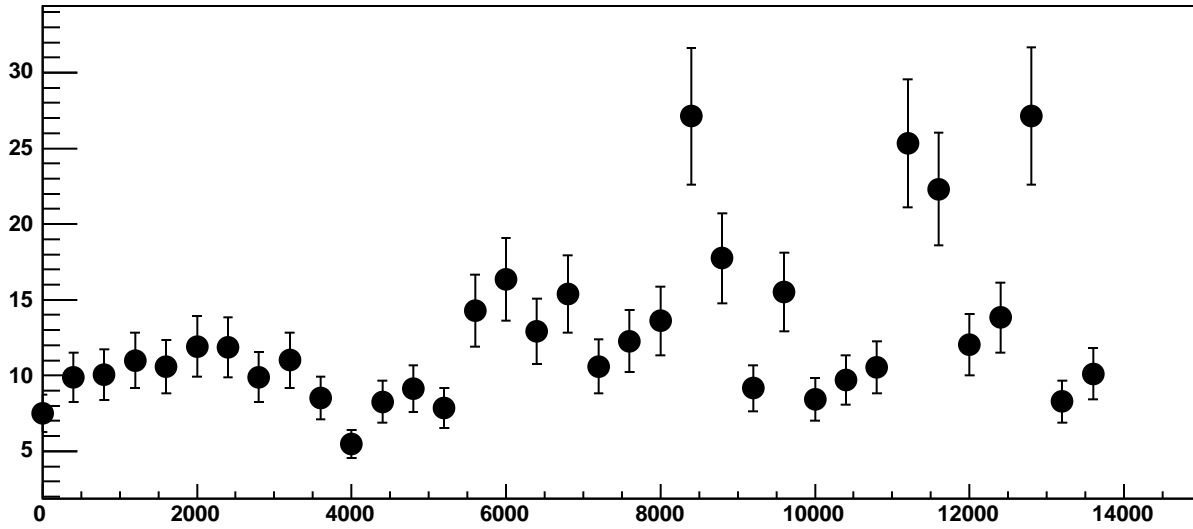
Chip 10, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC



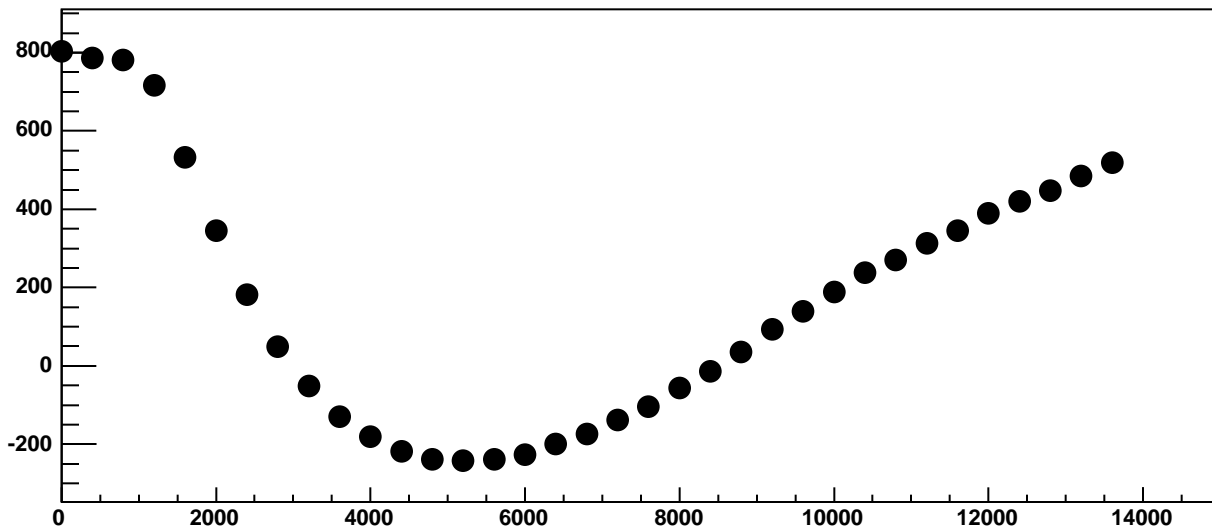
Chip 10, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



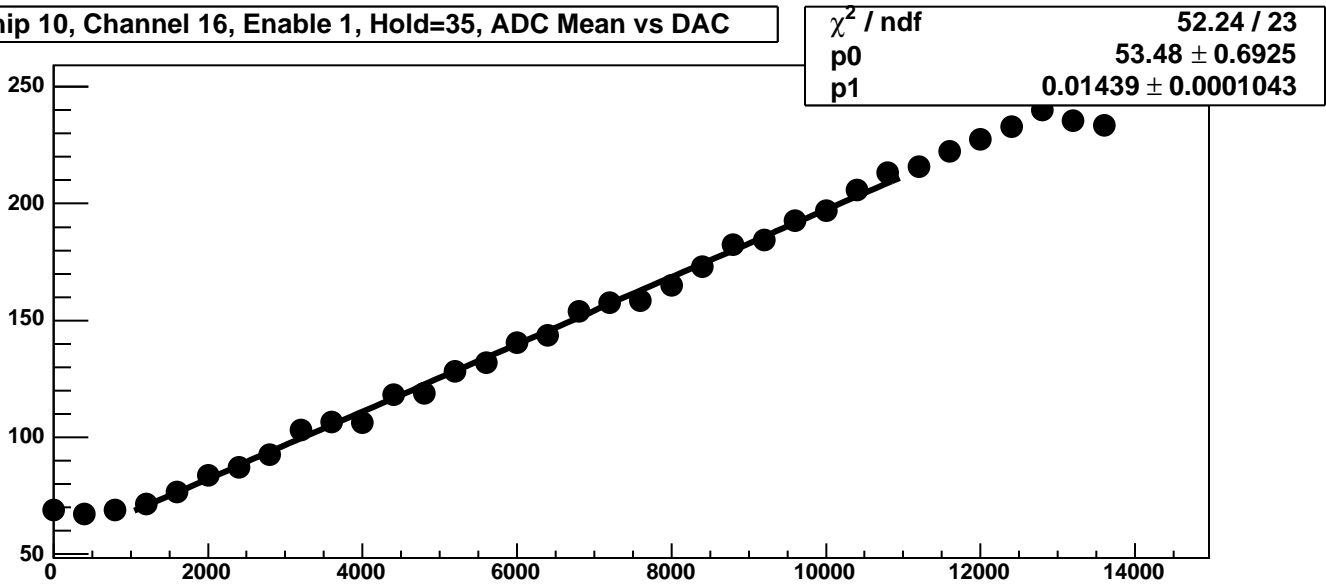
Chip 10, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



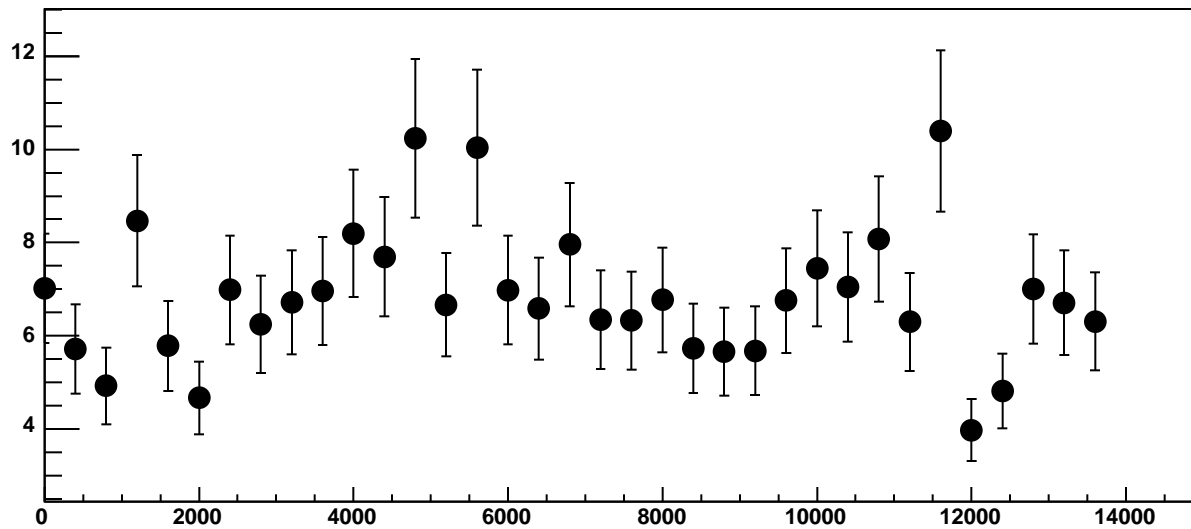
Chip 10, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



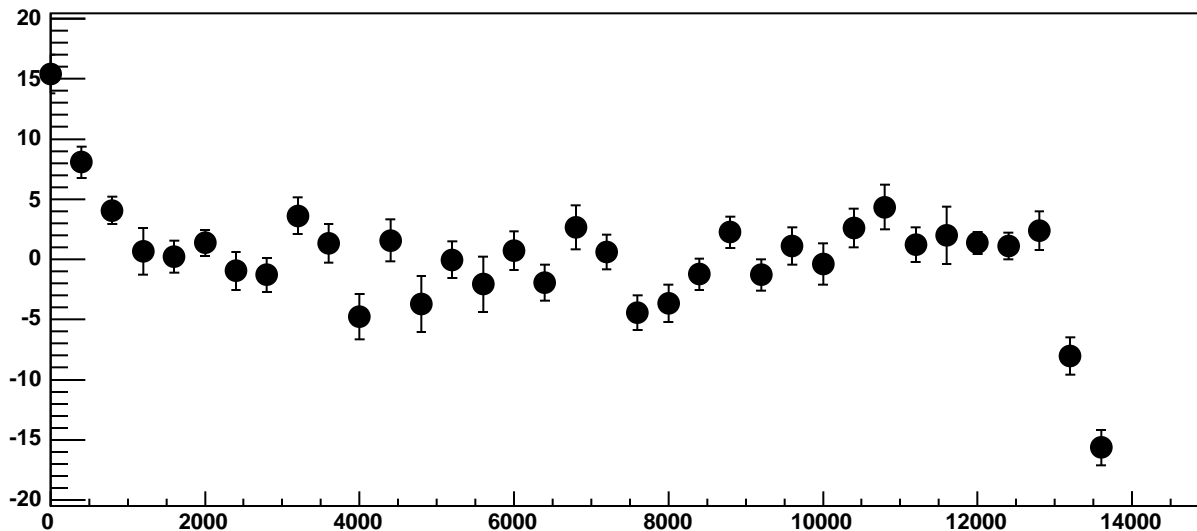
Chip 10, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



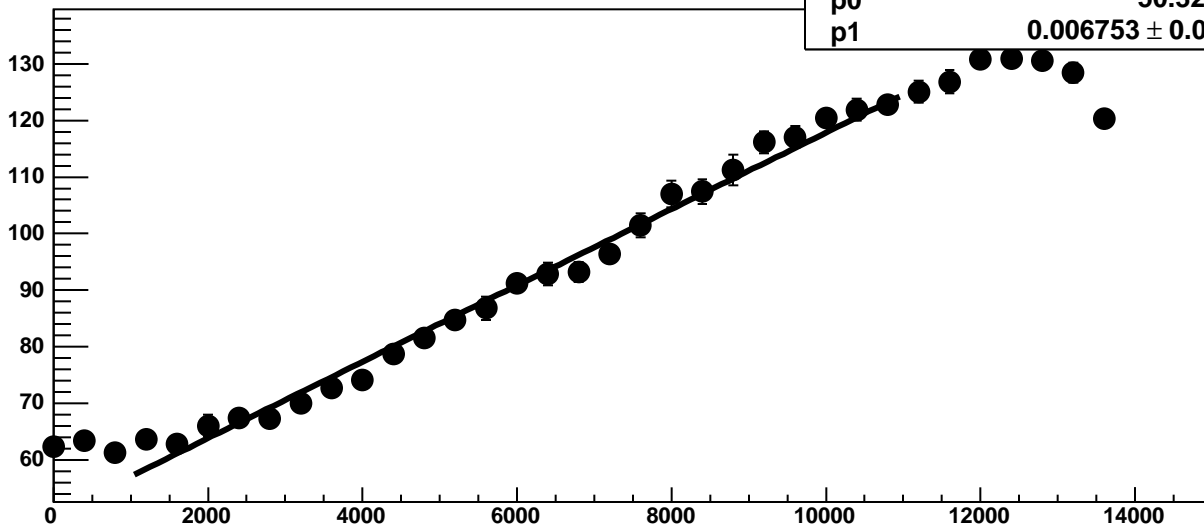
Chip 10, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



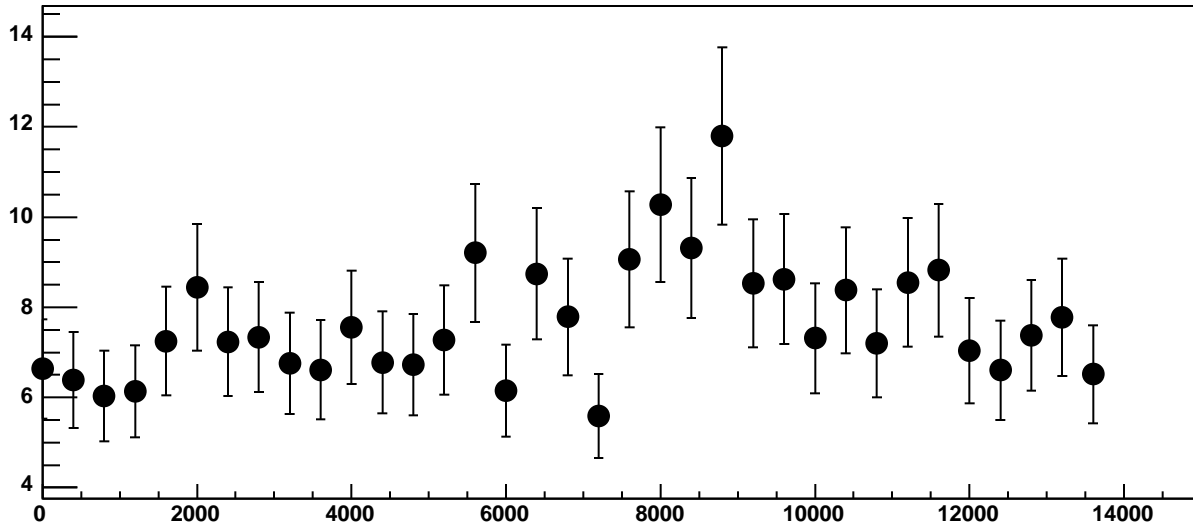
Chip 10, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



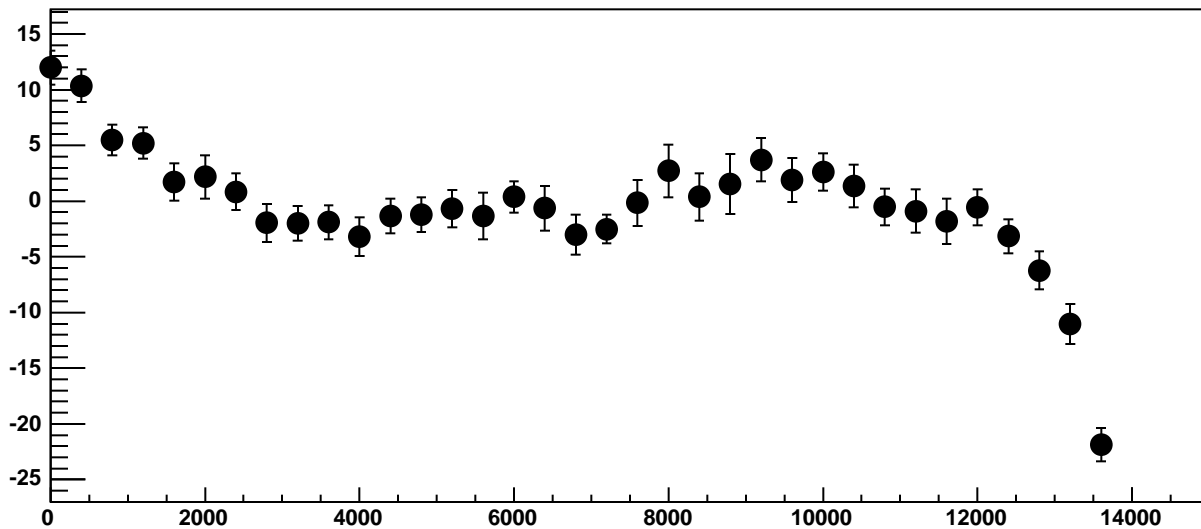
Chip 10, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC



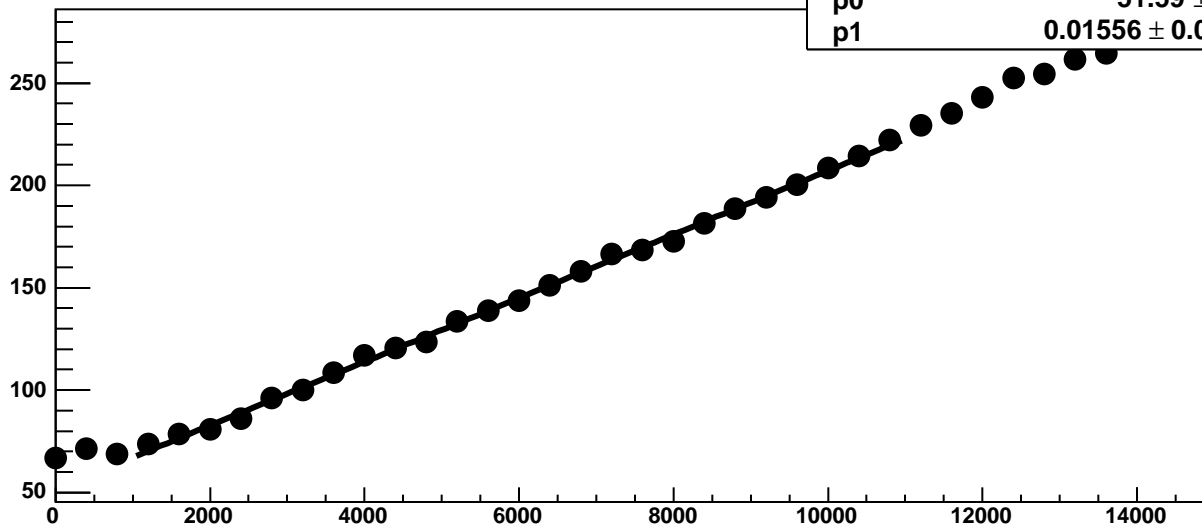
Chip 10, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

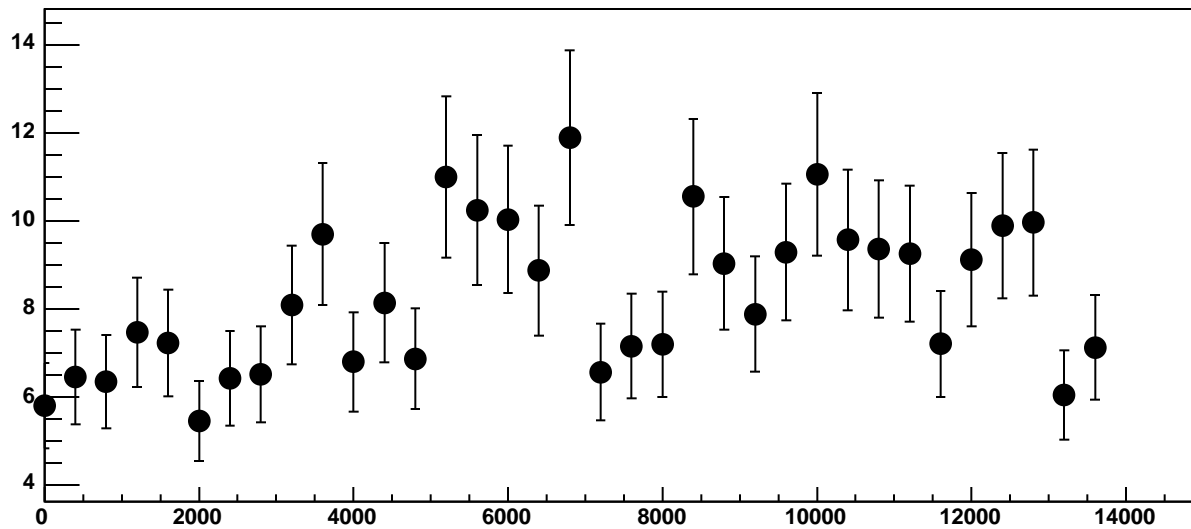


Chip 10, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

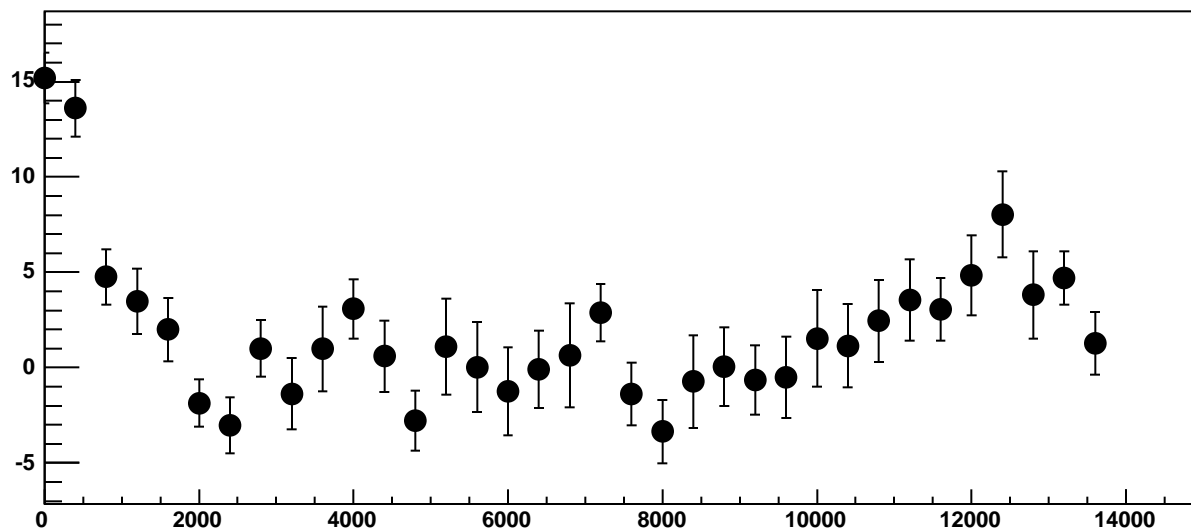


χ^2 / ndf 31.59 / 23
p0 51.59 ± 0.7714
p1 0.01556 ± 0.0001263

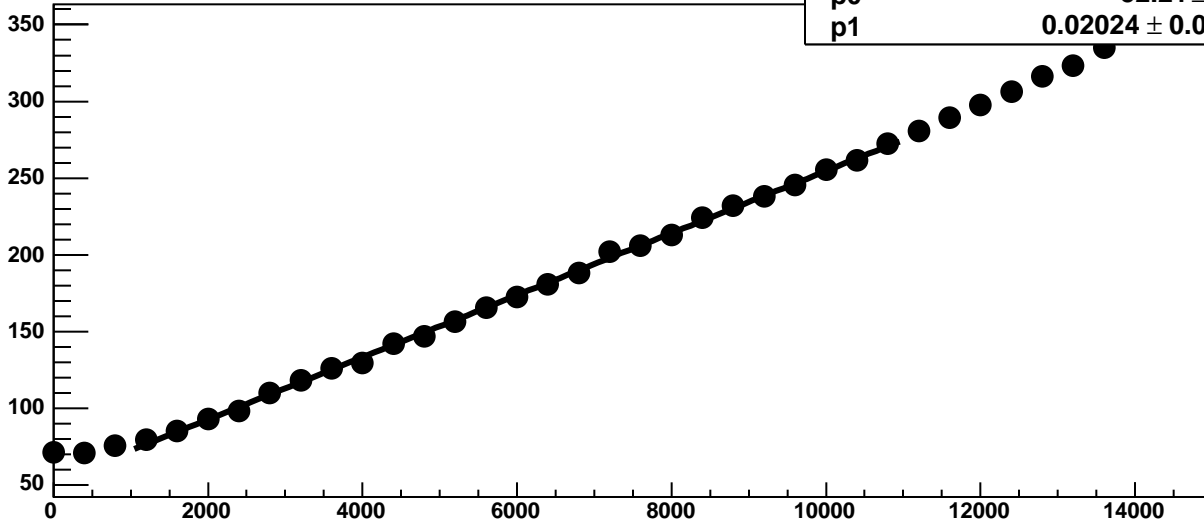
Chip 10, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

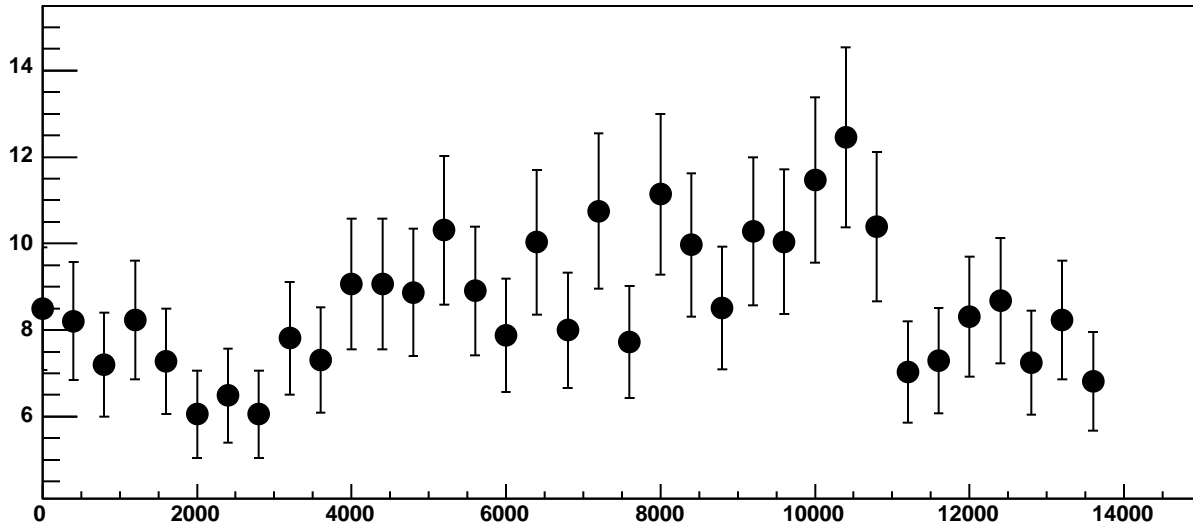


Chip 10, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

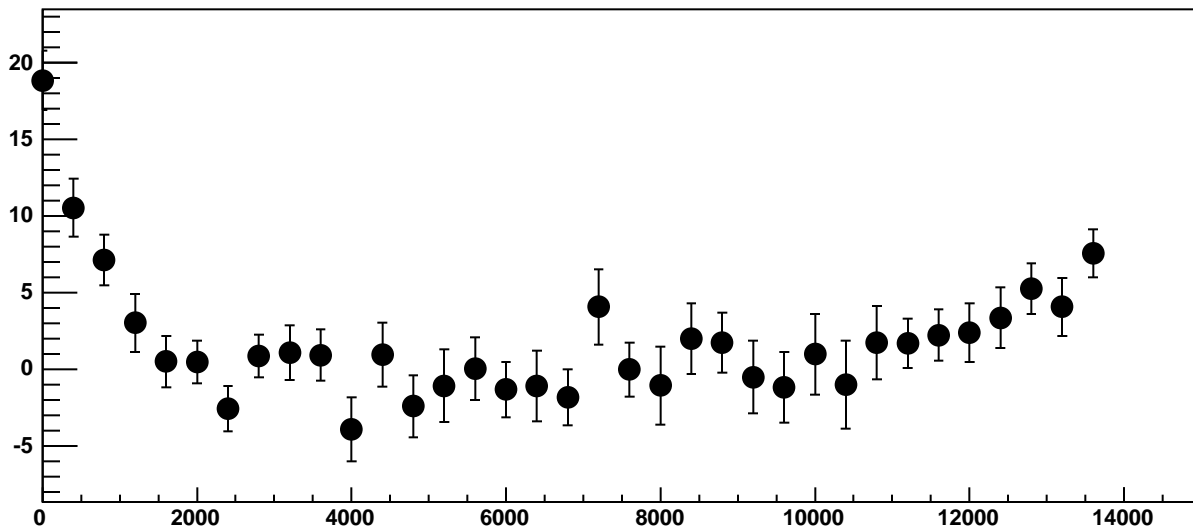


χ^2 / ndf 19.49 / 23
p0 52.21 ± 0.8093
p1 0.02024 ± 0.0001379

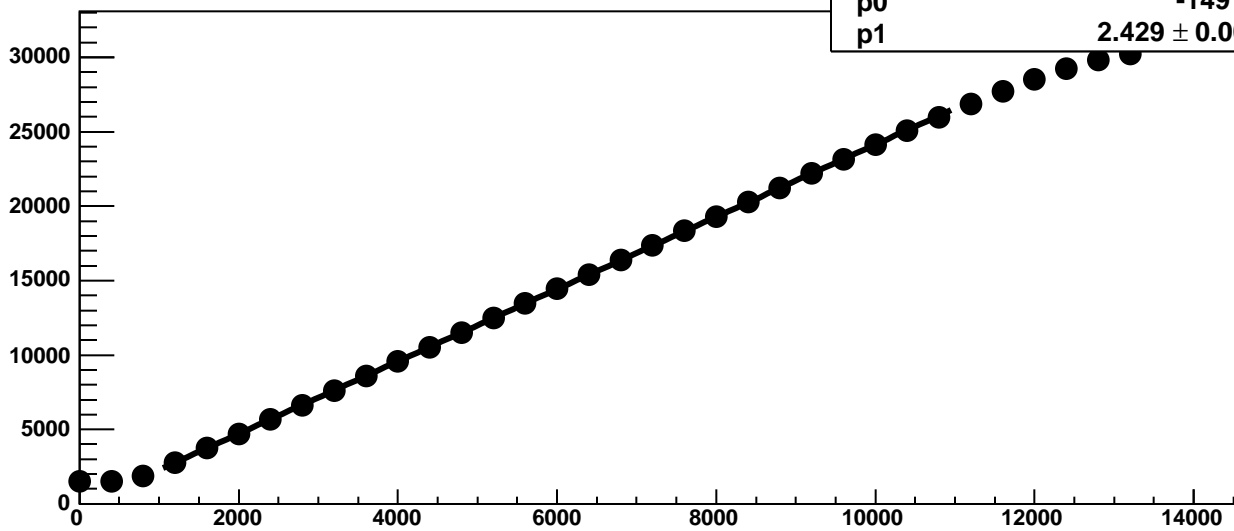
Chip 10, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



Chip 10, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

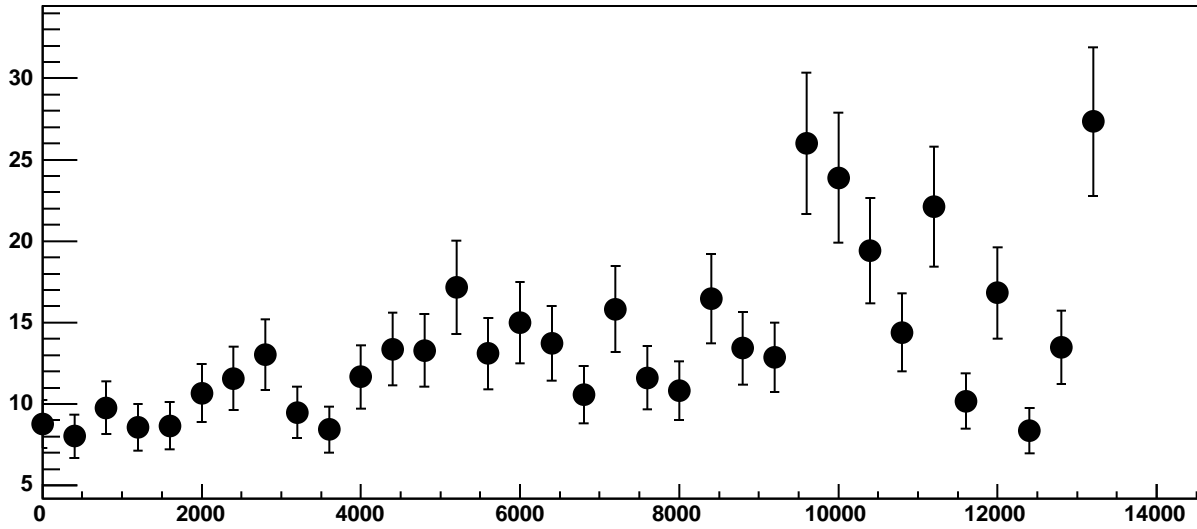


Chip 10, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC

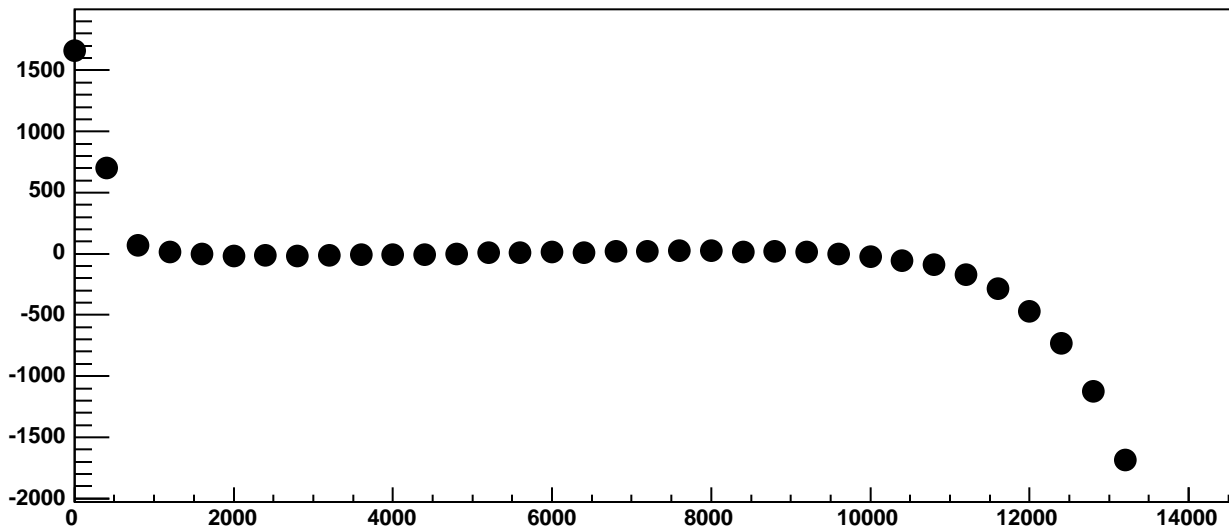


χ^2 / ndf 1598 / 23
p0 -149 ± 1.141
p1 2.429 ± 0.0001998

Chip 10, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC

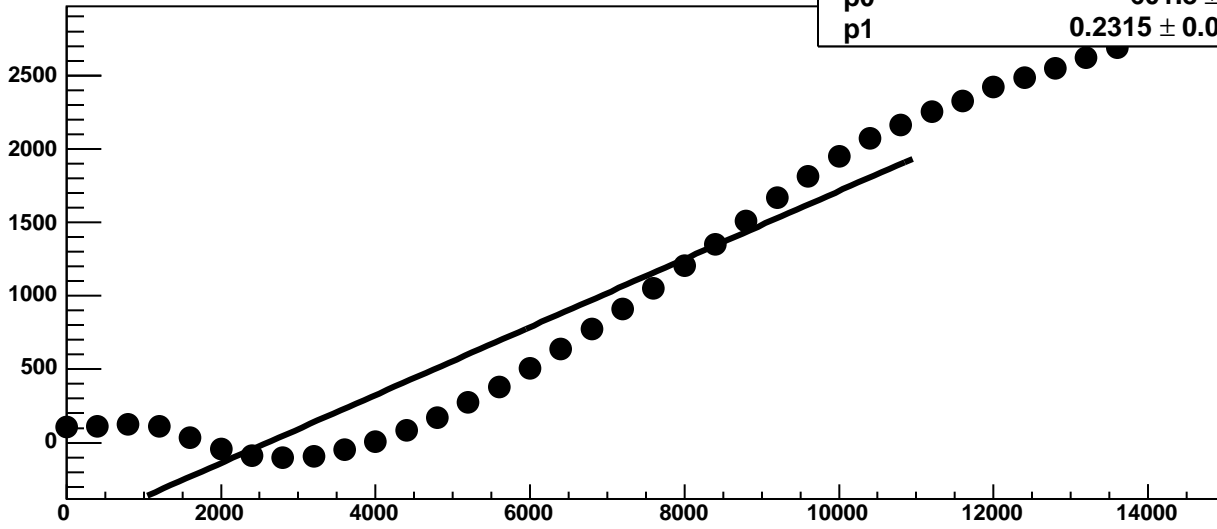


Chip 10, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC

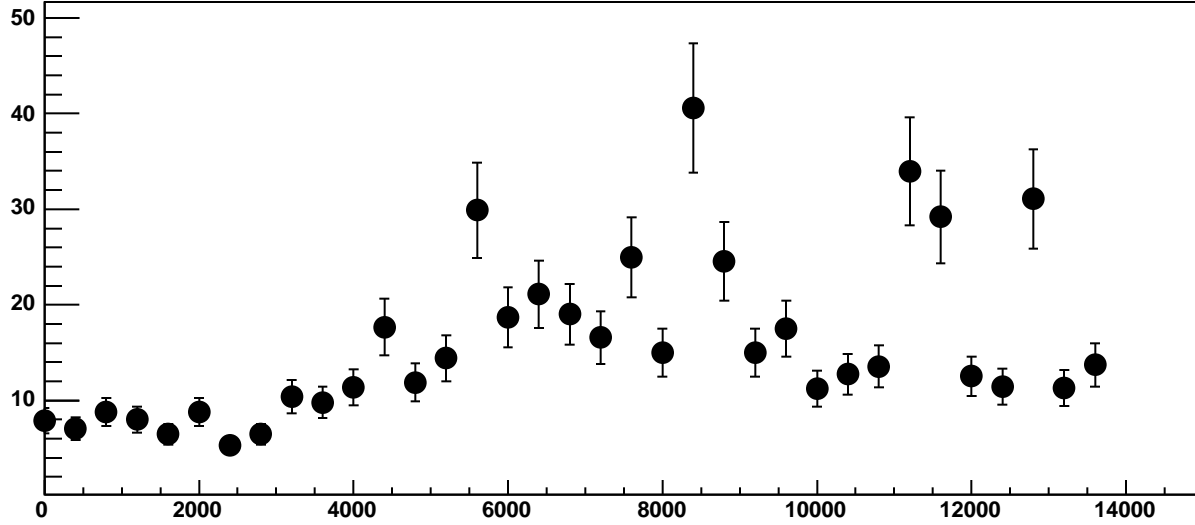


Chip 10, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

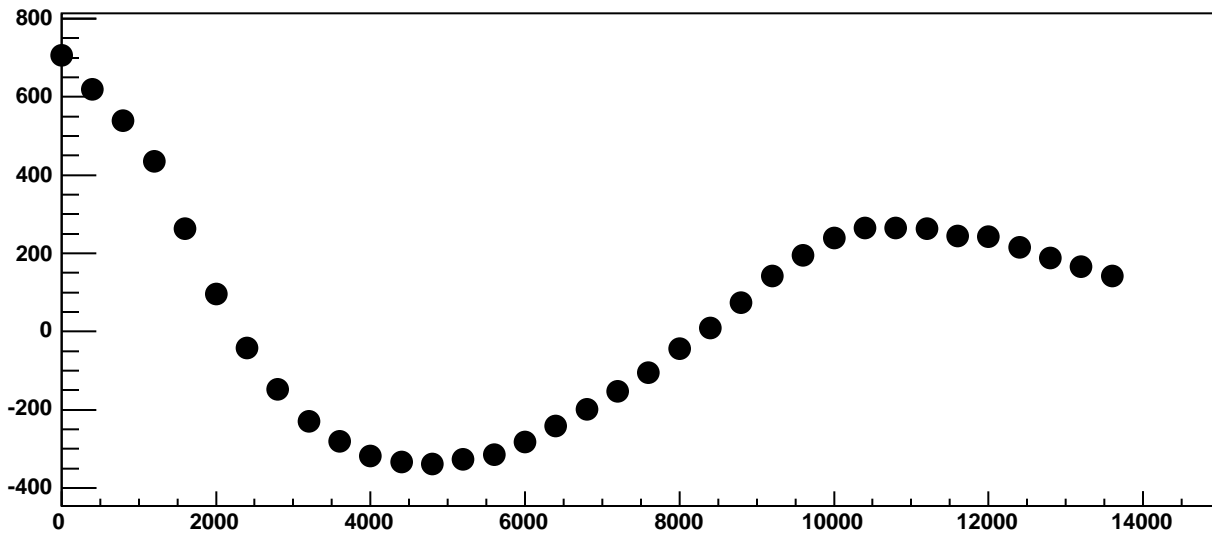
χ^2 / ndf 2.152e+05 / 23
p0 -601.8 ± 0.8824
p1 0.2315 ± 0.0001755



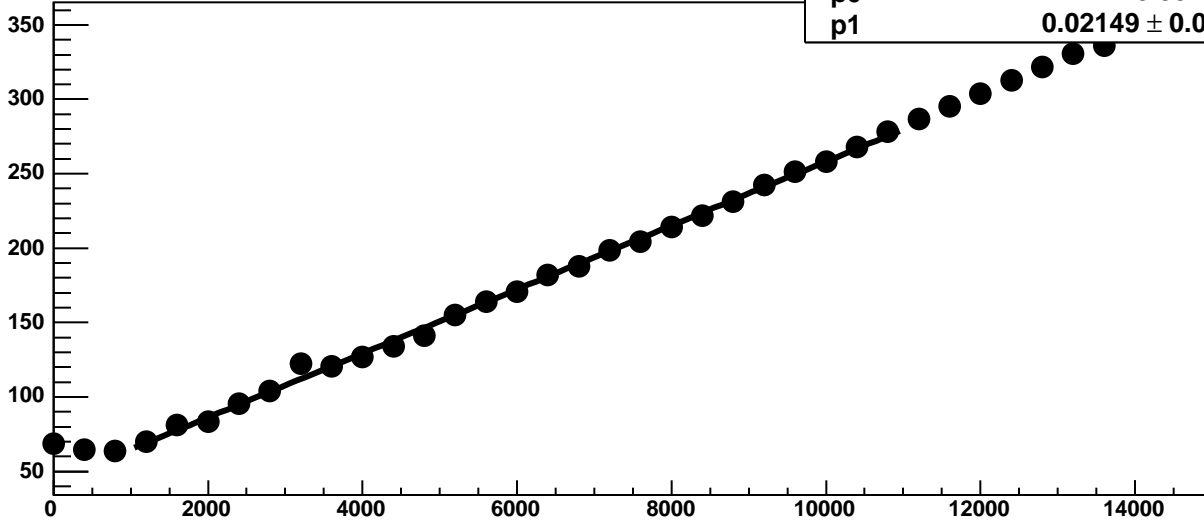
Chip 10, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

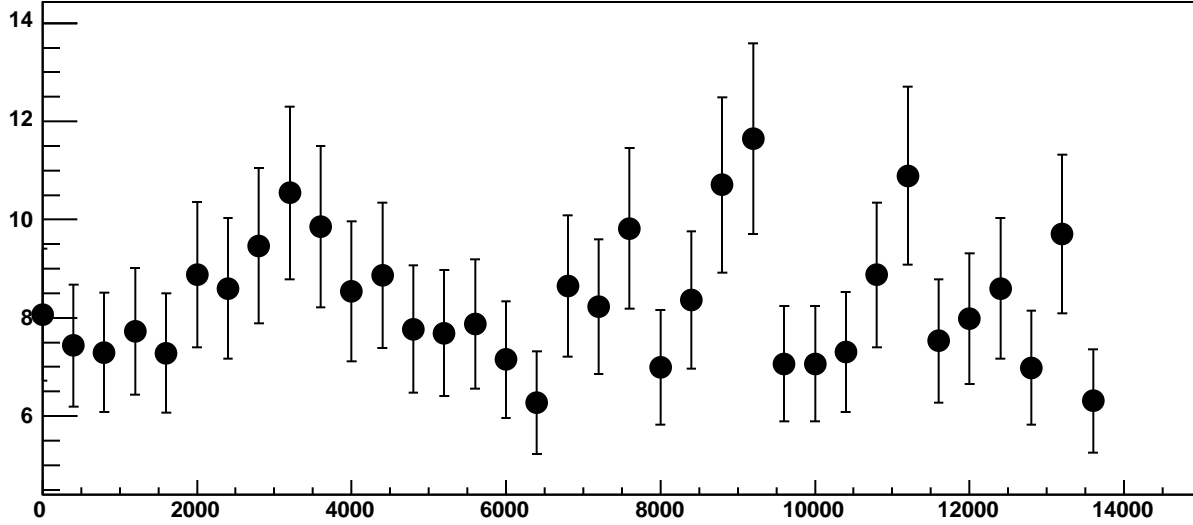


Chip 10, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

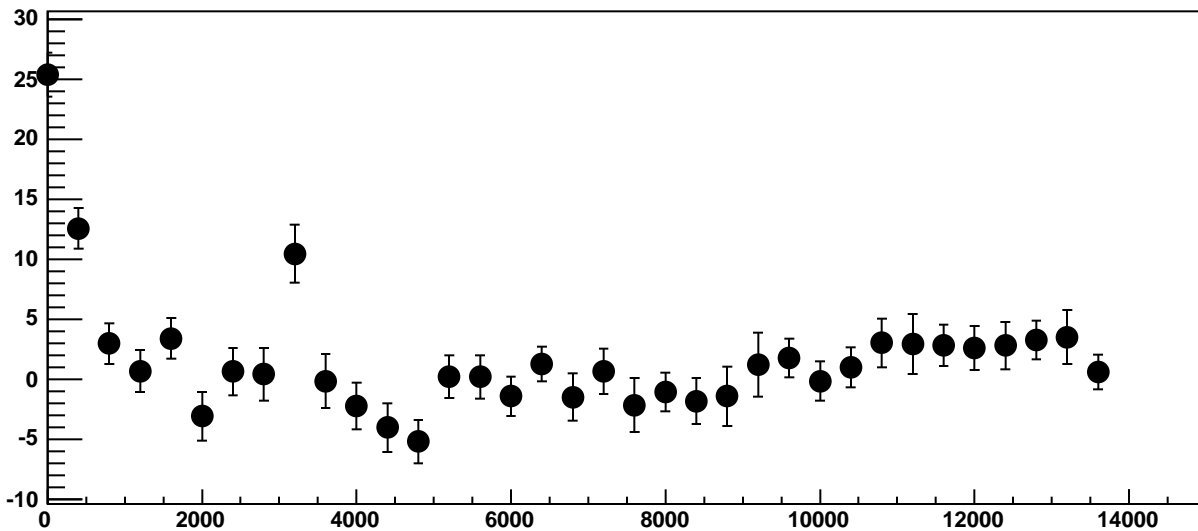


χ^2 / ndf 47.87 / 23
p0 43.35 ± 0.8791
p1 0.02149 ± 0.0001303

Chip 10, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

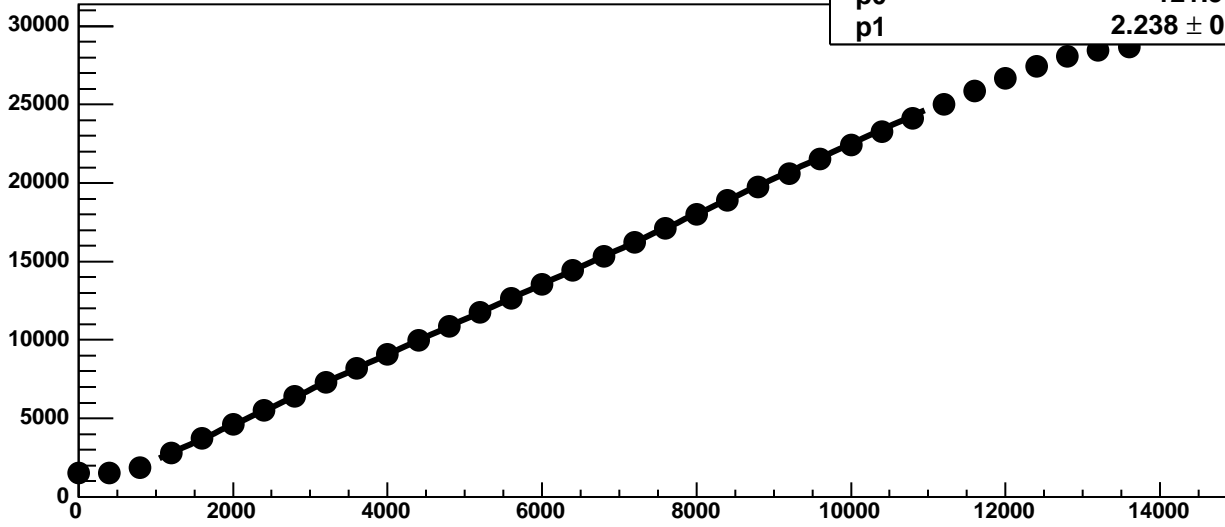


Chip 10, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

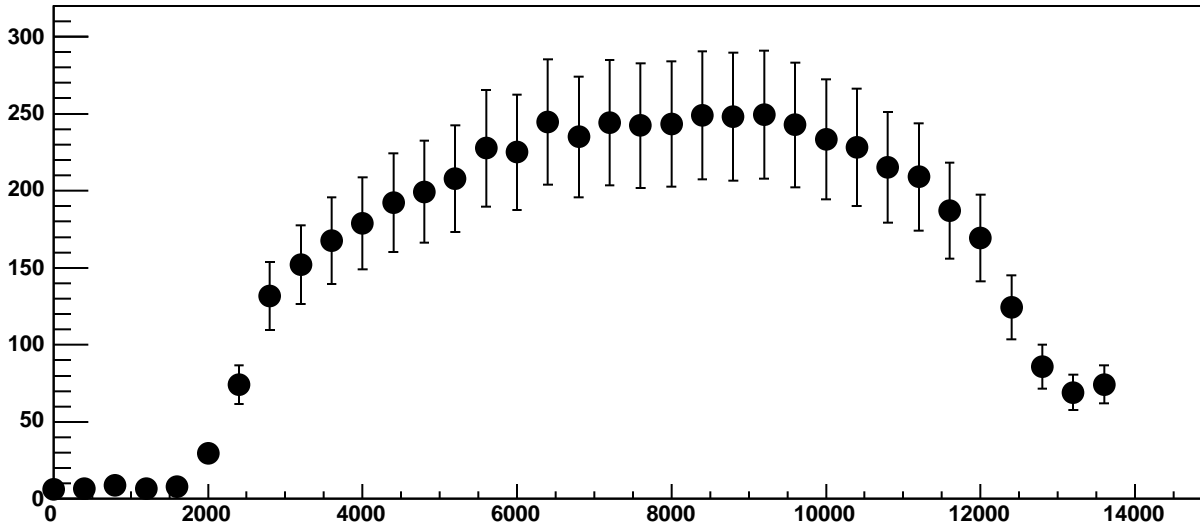


Chip 10, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC

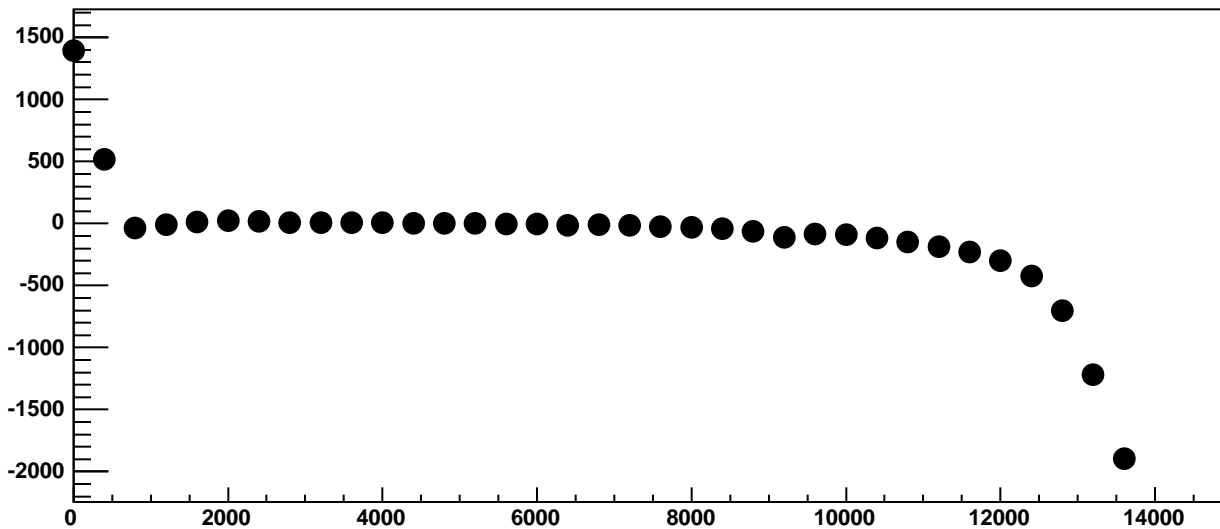
χ^2 / ndf 141.4 / 23
p0 121.5 ± 2.858
p1 2.238 ± 0.001822



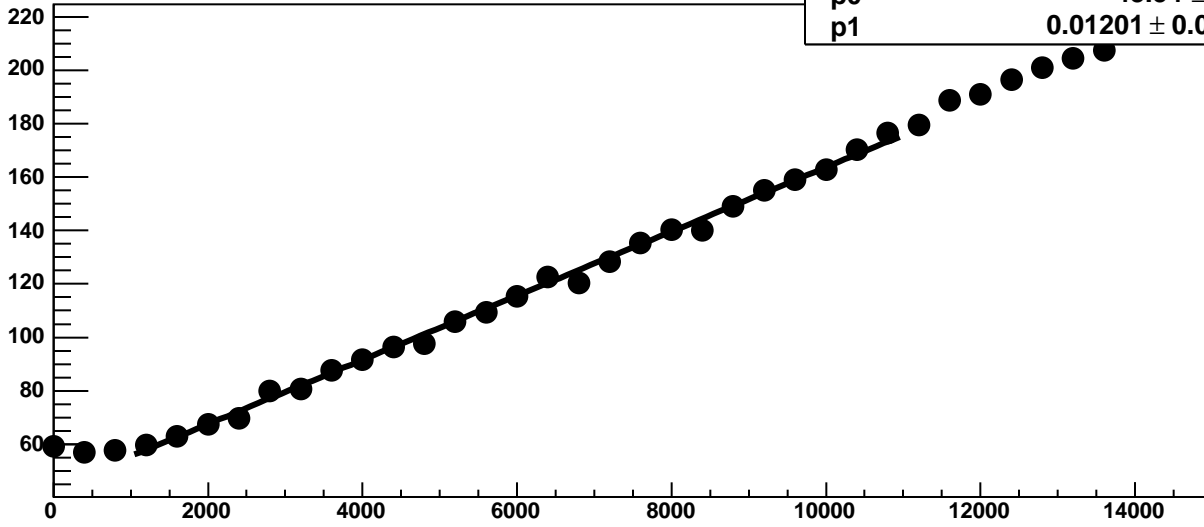
Chip 10, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC

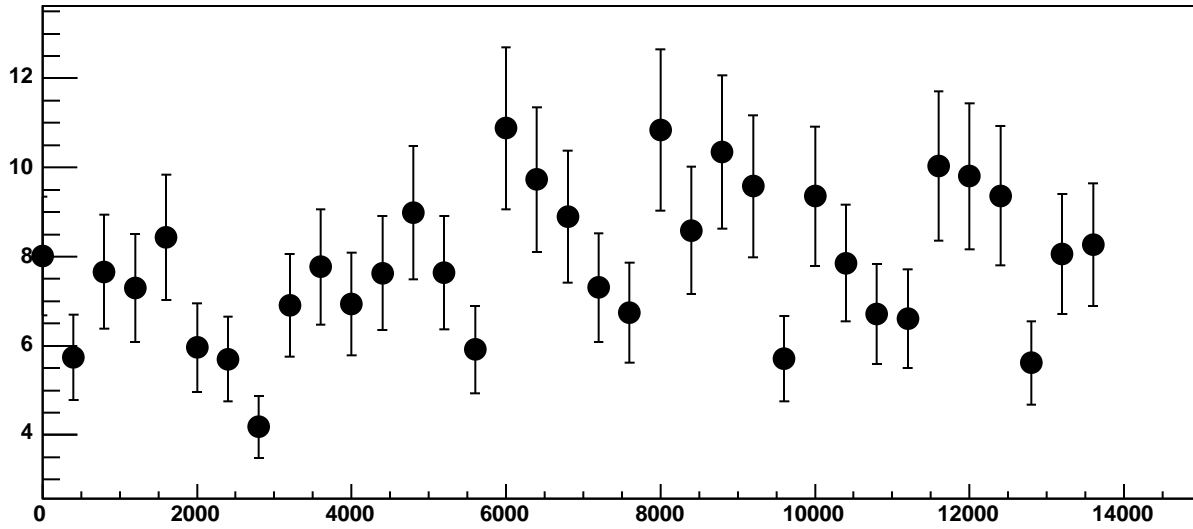


Chip 10, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

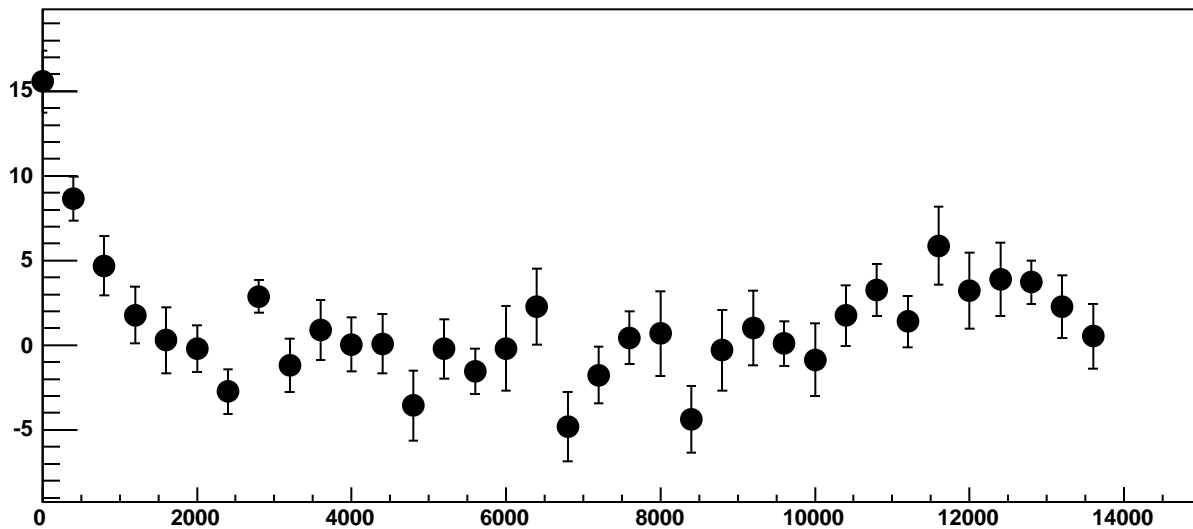


χ^2 / ndf 38.27 / 23
p0 43.54 ± 0.6958
p1 0.01201 ± 0.0001116

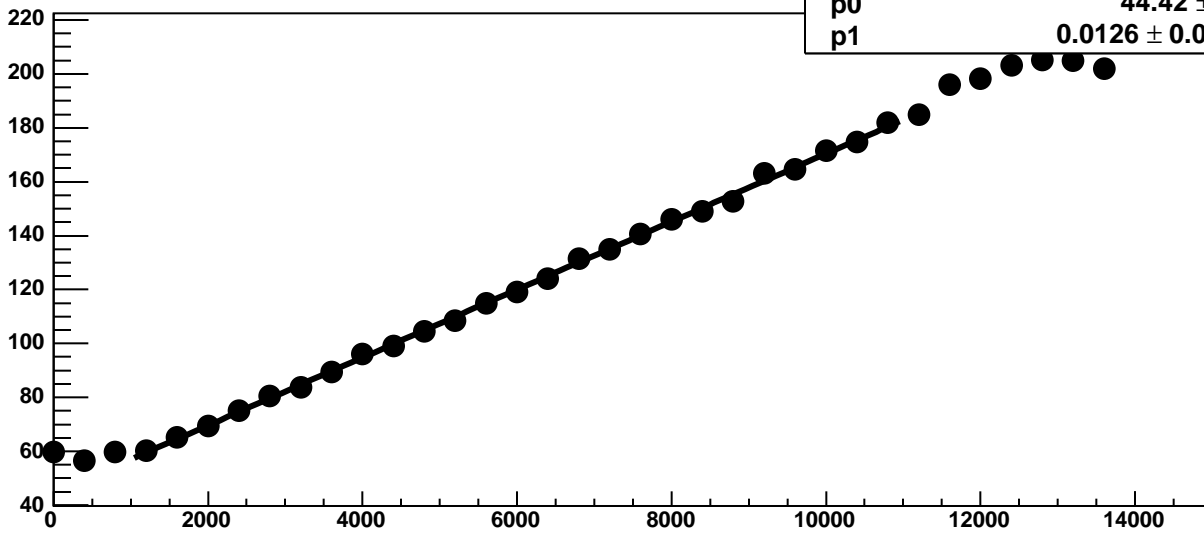
Chip 10, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

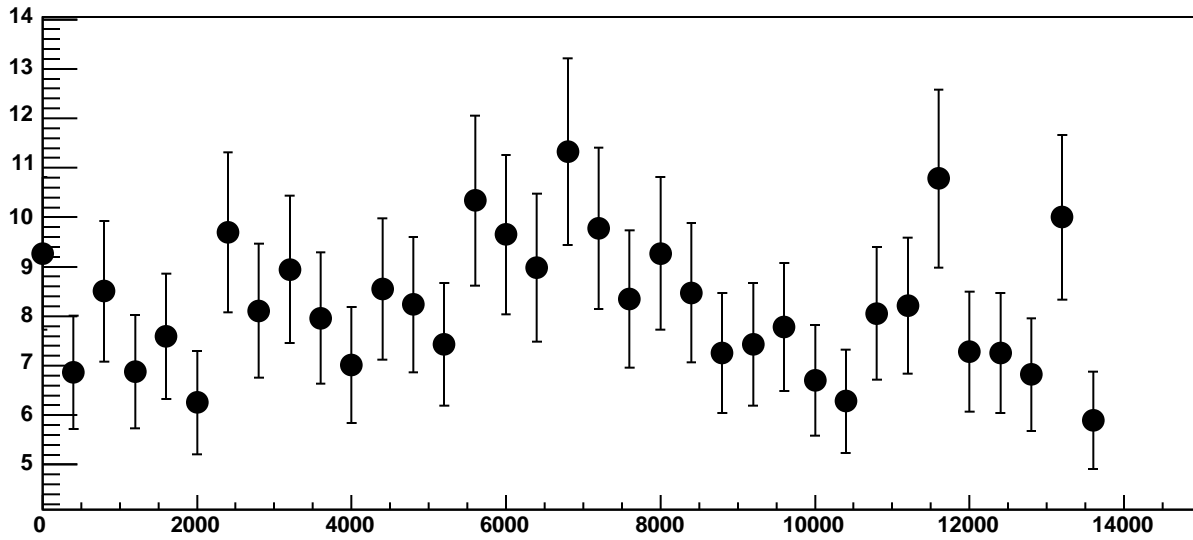


Chip 10, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

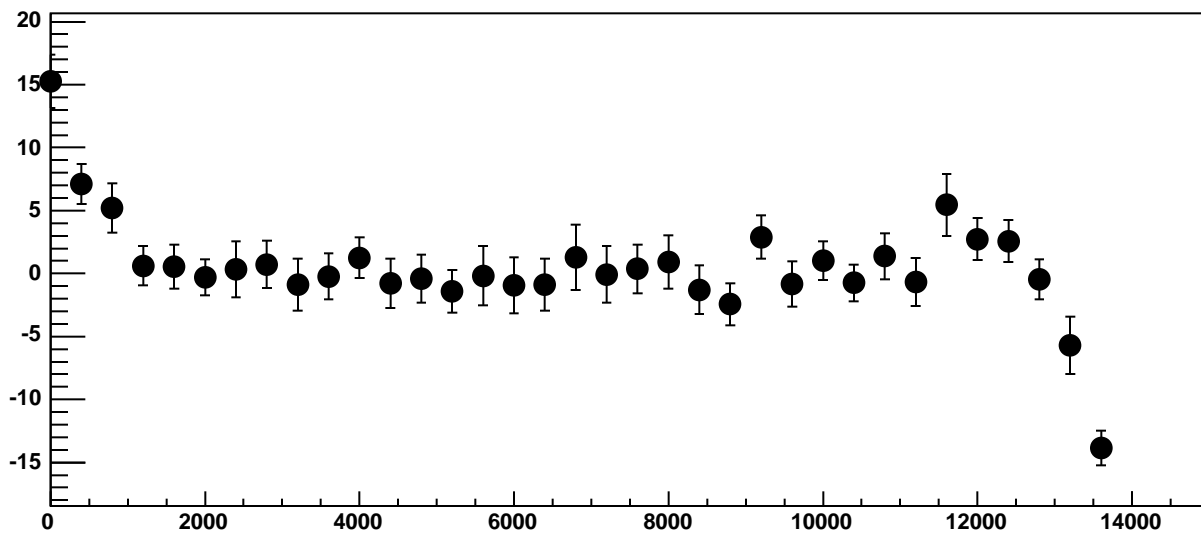


χ^2 / ndf 9.941 / 23
p0 44.42 ± 0.7981
p1 0.0126 ± 0.0001177

Chip 10, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

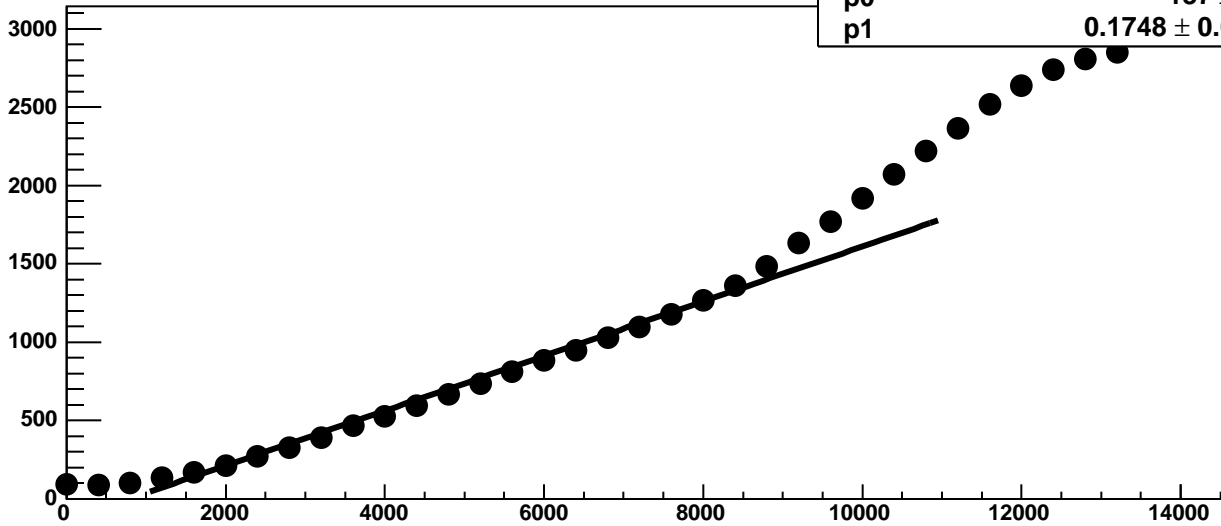


Chip 10, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

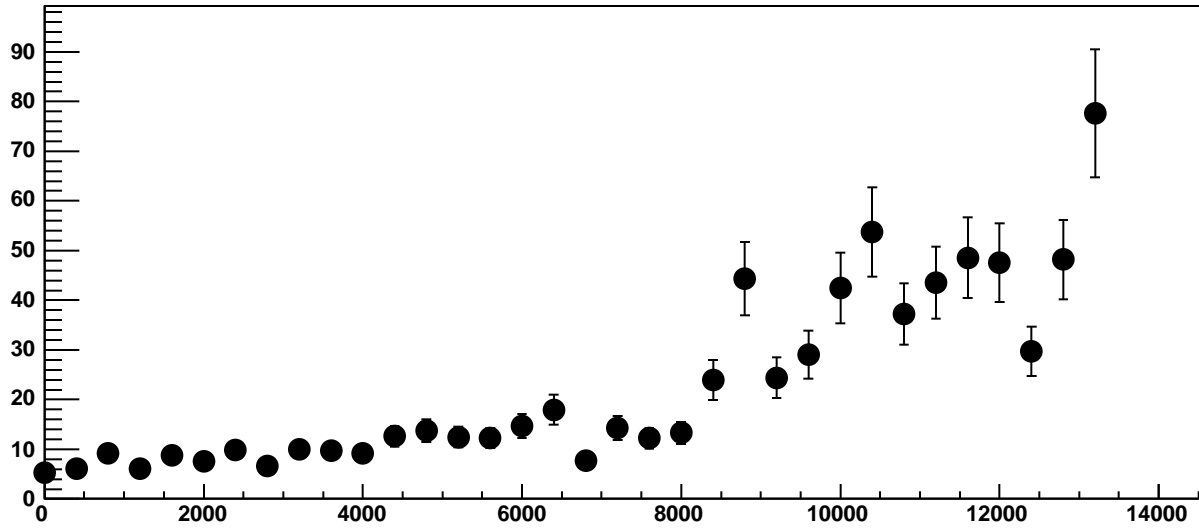


Chip 10, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC

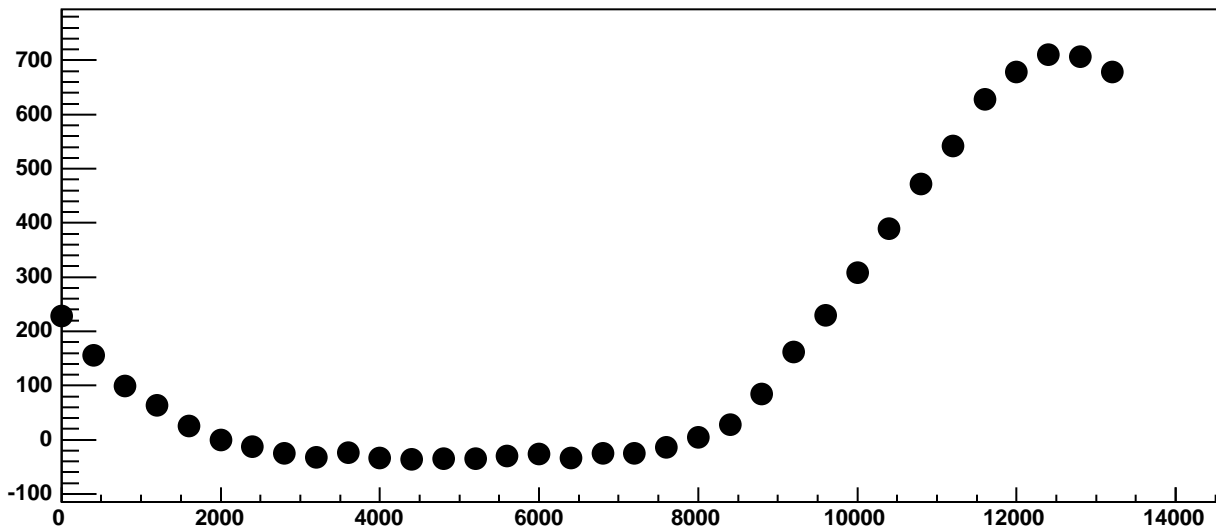
χ^2 / ndf 1.129e+04 / 23
p0 -137 ± 1.024
p1 0.1748 ± 0.000224



Chip 10, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC

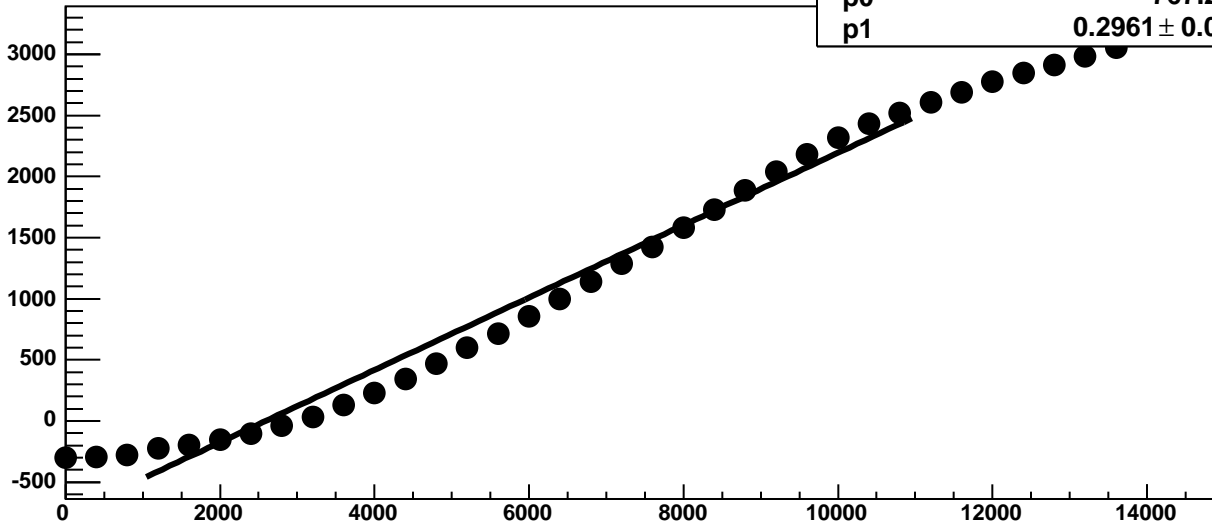


Chip 10, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC

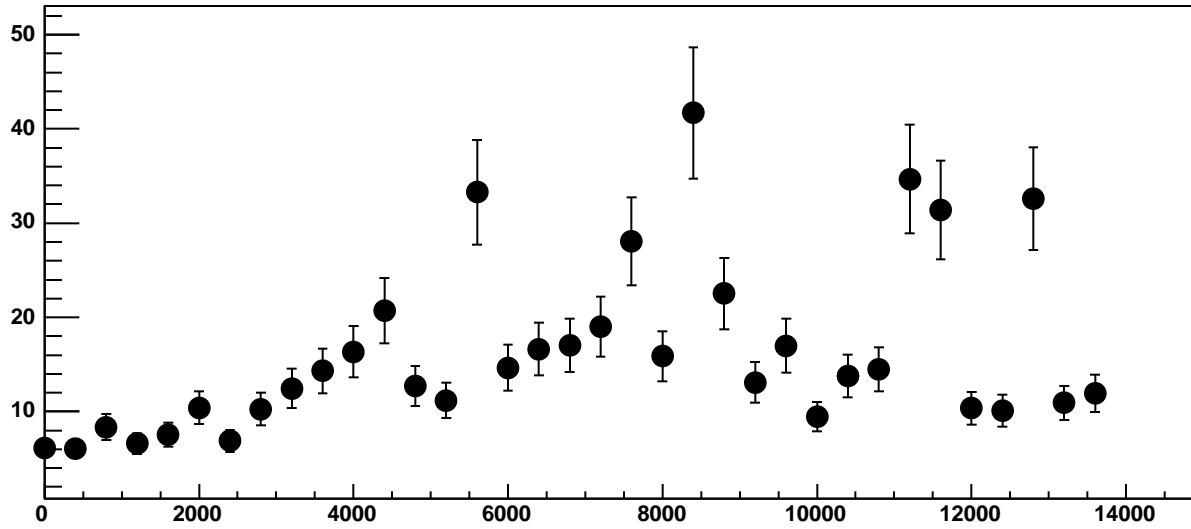


Chip 11, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

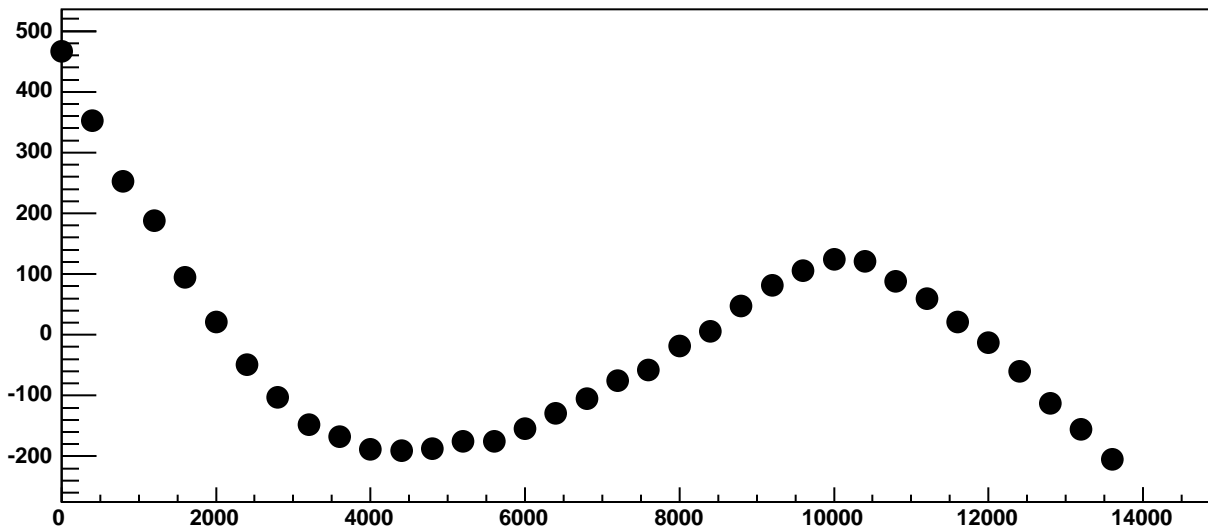
χ^2 / ndf 5.145e+04 / 23
p0 -767.2 ± 0.98
p1 0.2961 ± 0.0001758



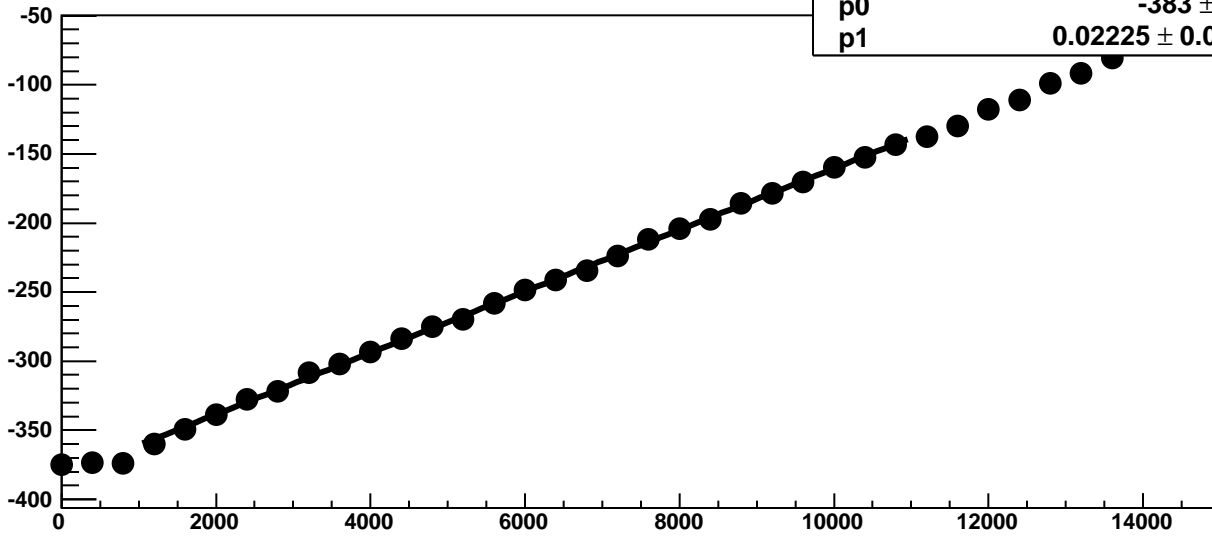
Chip 11, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

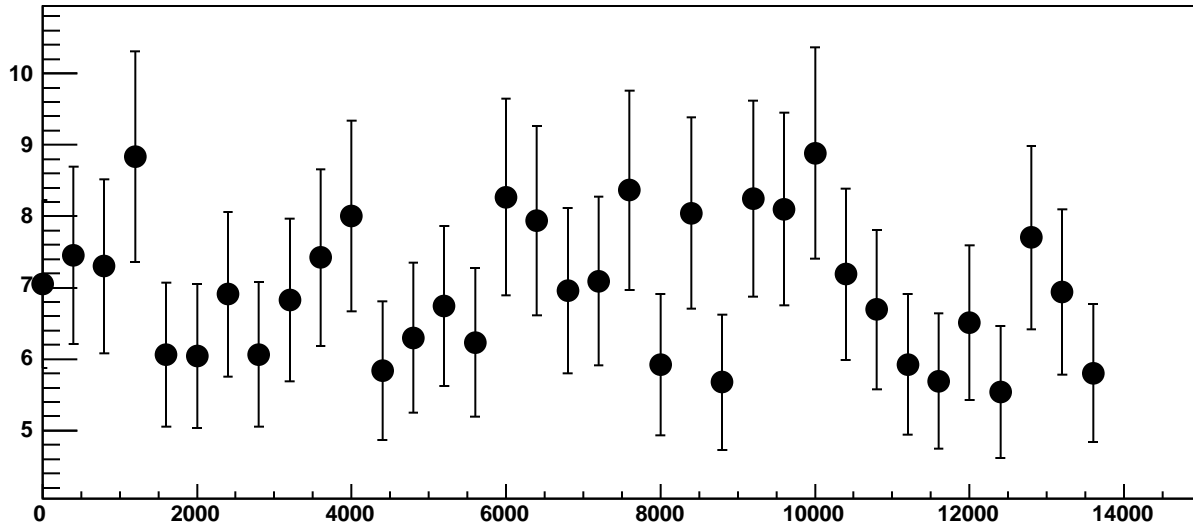


Chip 11, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

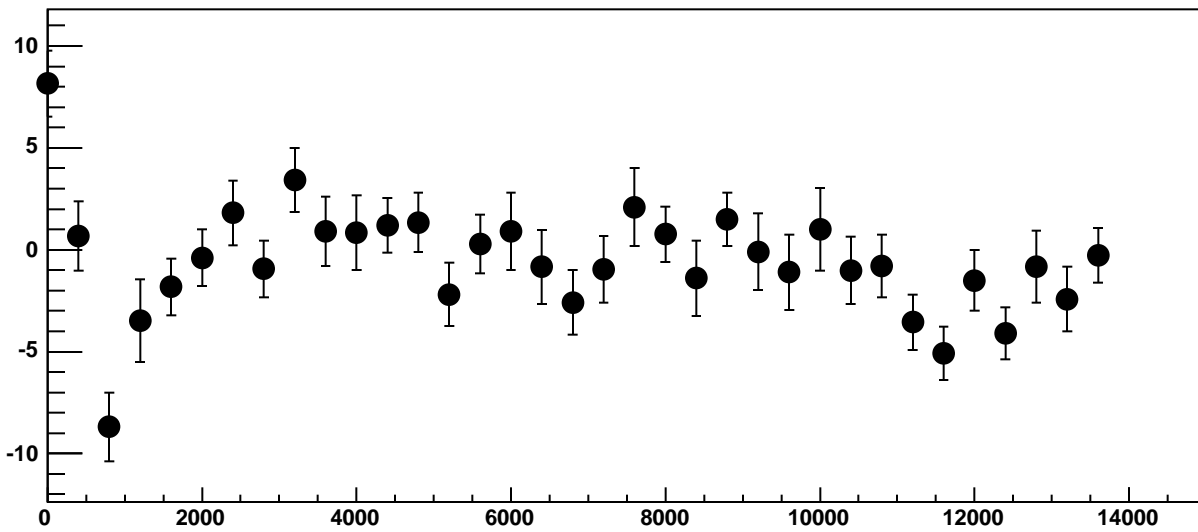


χ^2 / ndf 23.49 / 23
p0 -383 ± 0.7229
p1 0.02225 ± 0.0001116

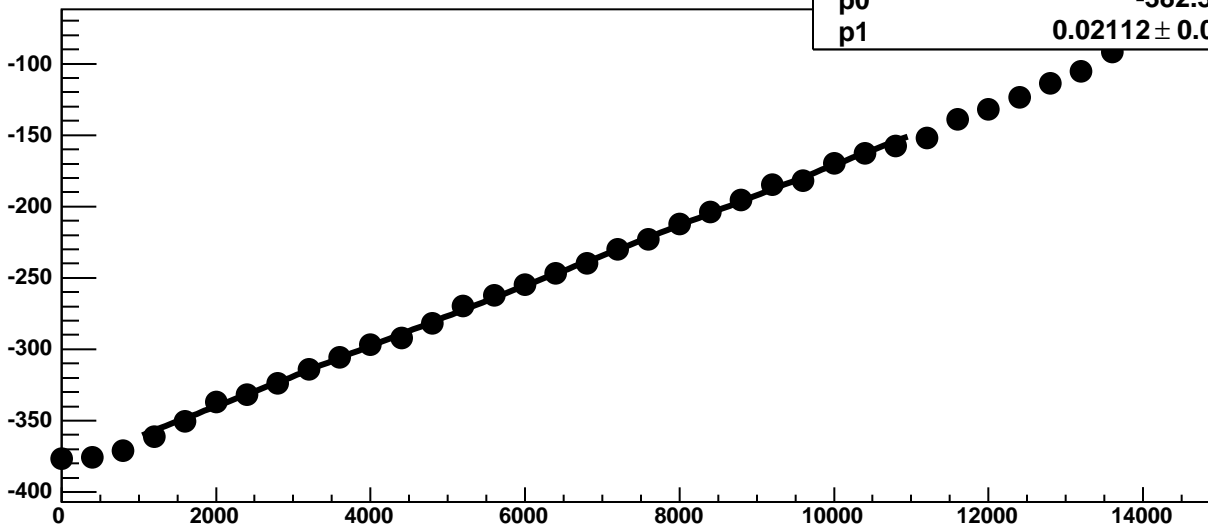
Chip 11, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

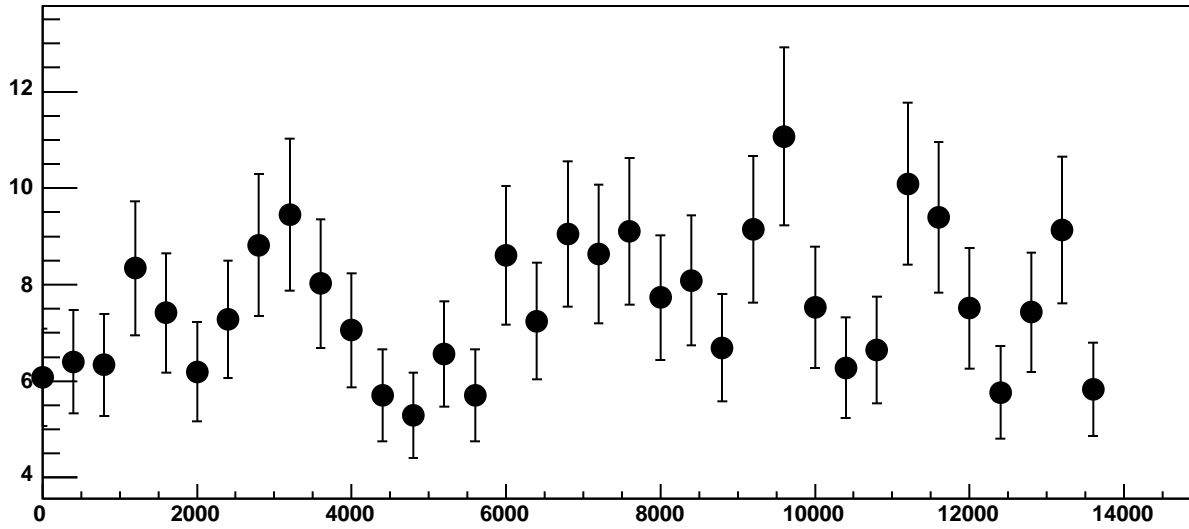


Chip 11, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

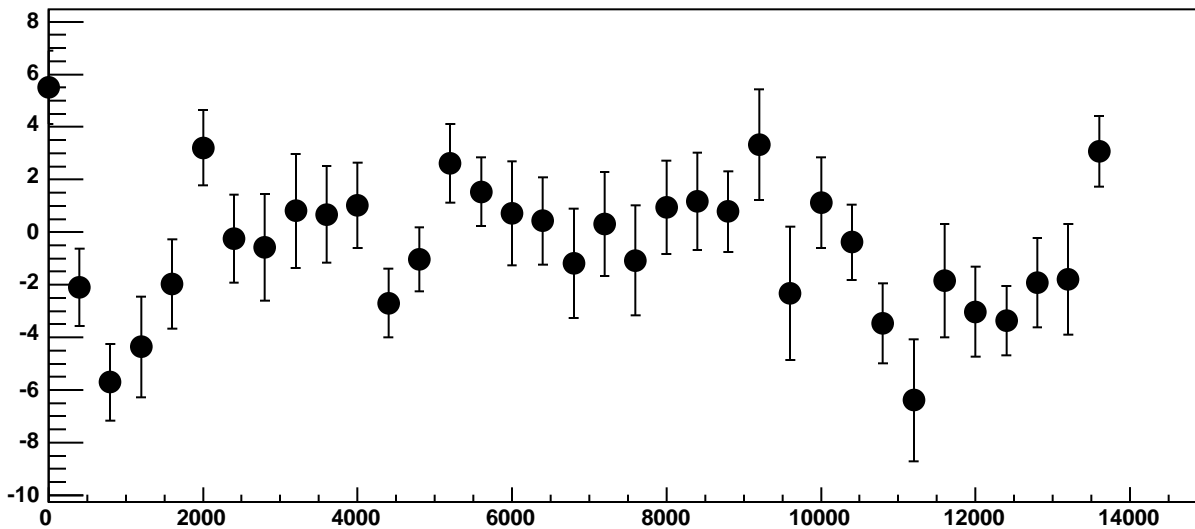


χ^2 / ndf 32.57 / 23
p0 -382.3 ± 0.771
p1 0.02112 ± 0.0001178

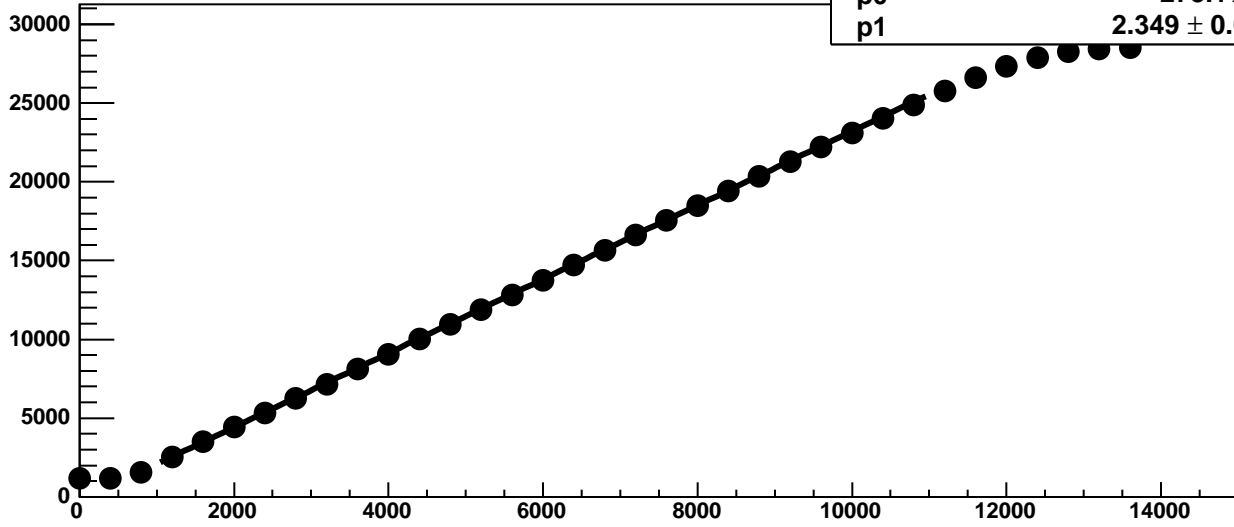
Chip 11, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



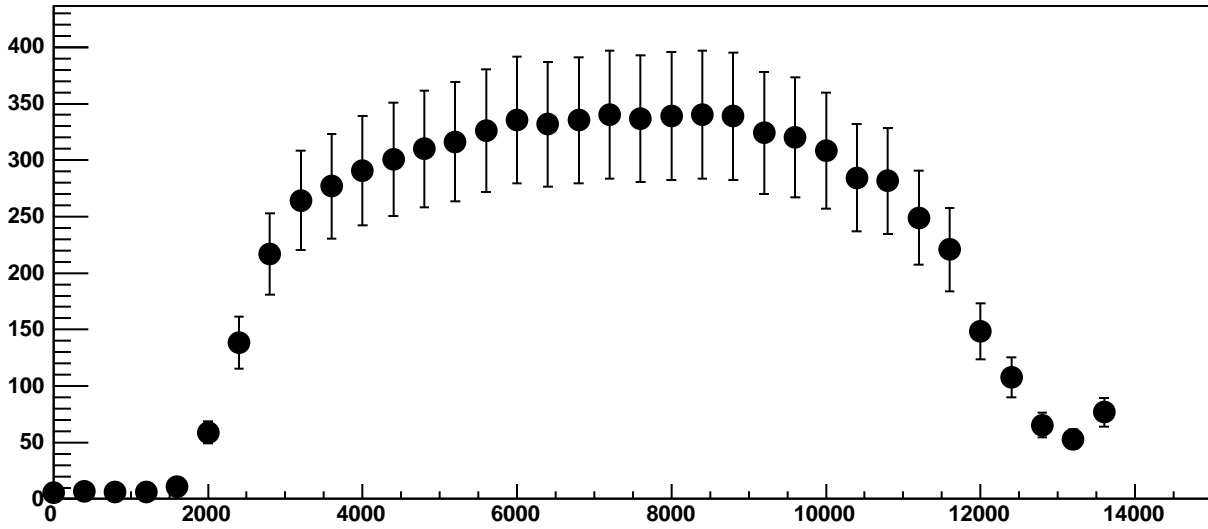
Chip 11, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



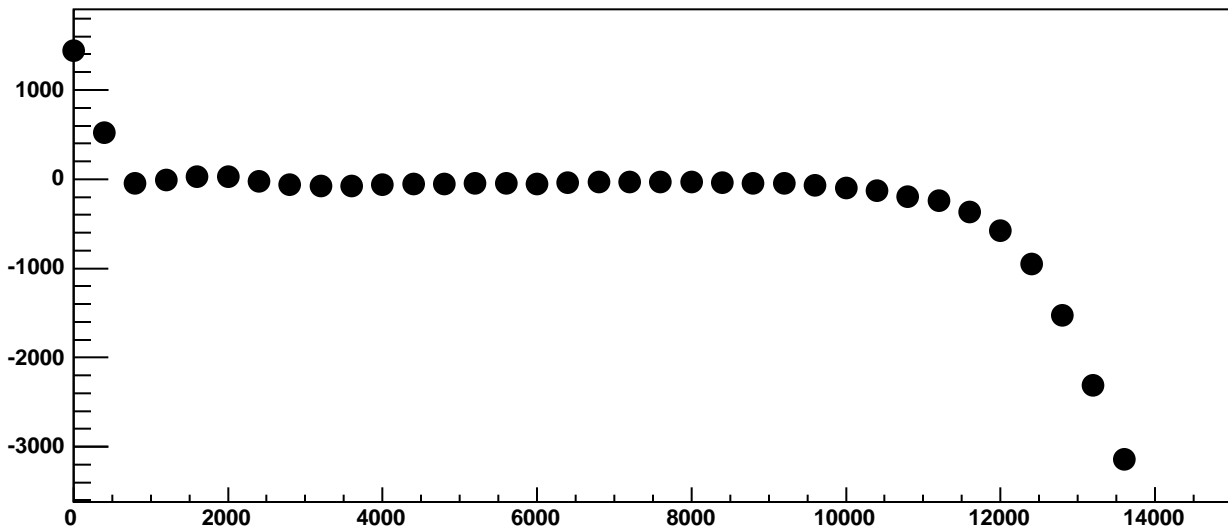
Chip 11, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC



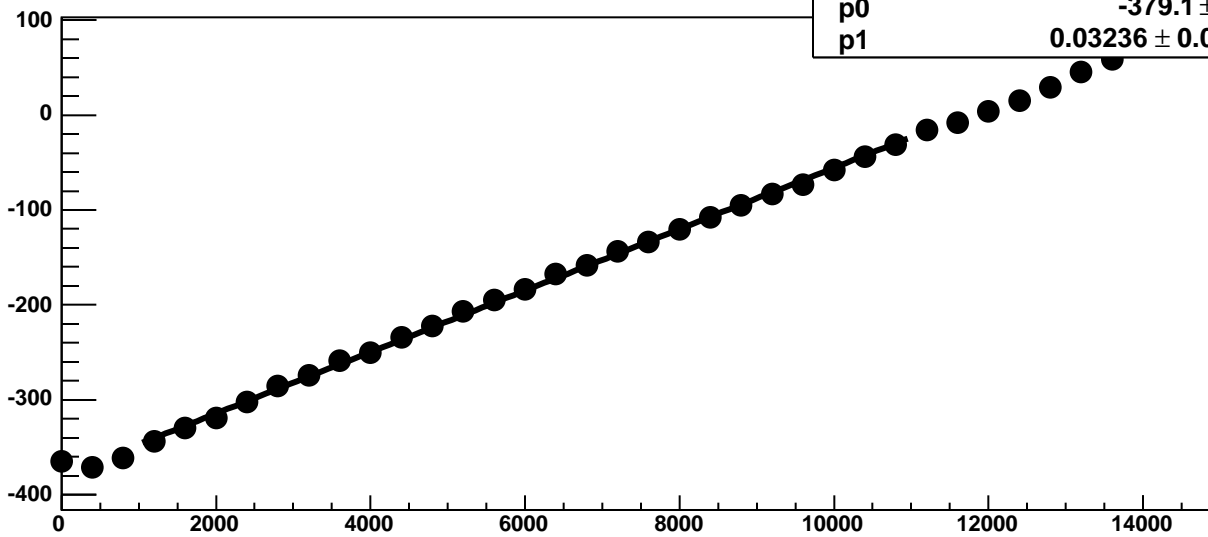
Chip 11, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC

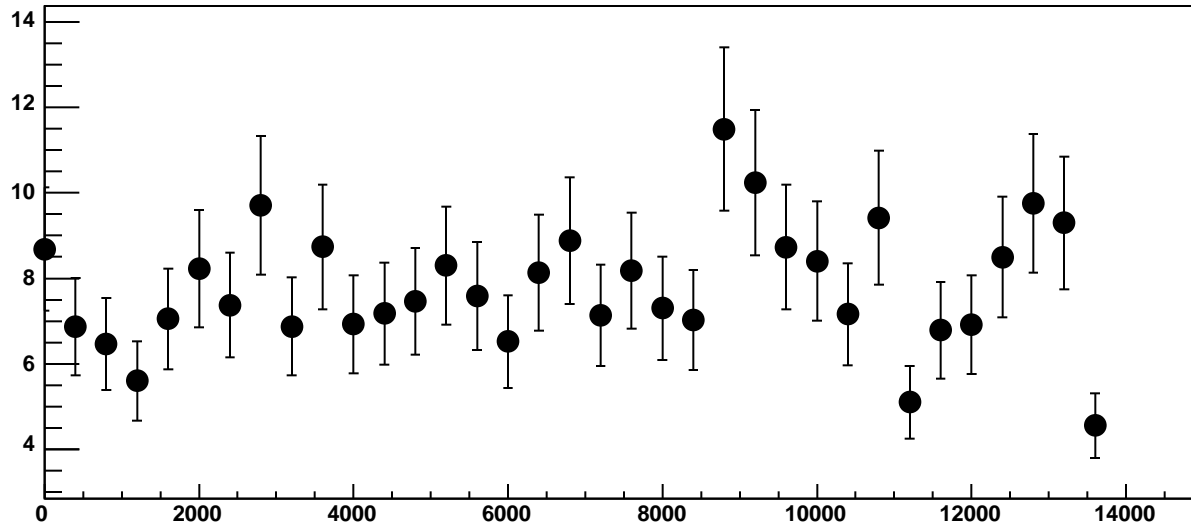


Chip 11, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

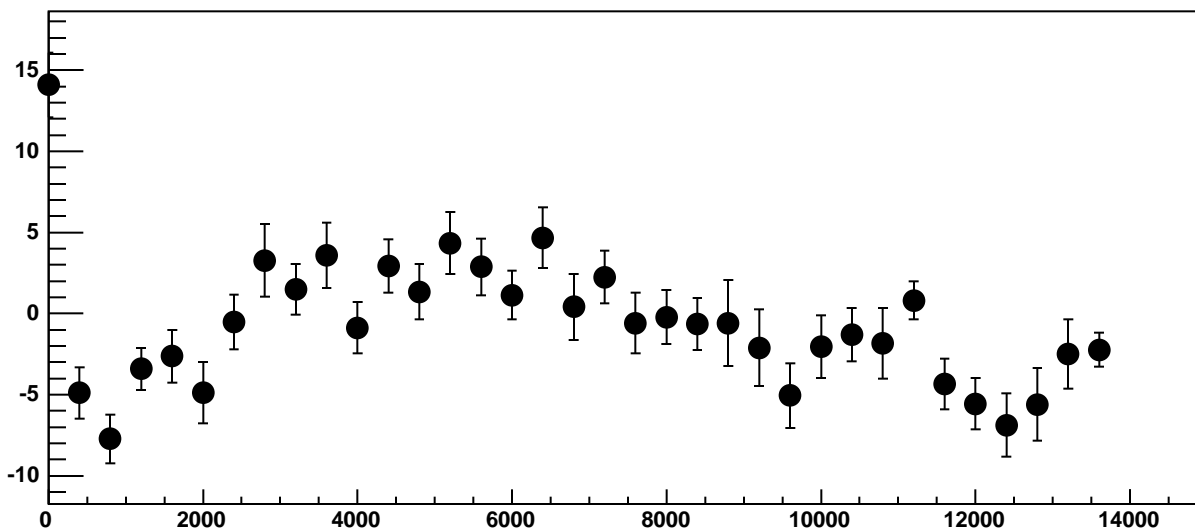


χ^2 / ndf 53.49 / 23
p0 -379.1 ± 0.7732
p1 0.03236 ± 0.0001223

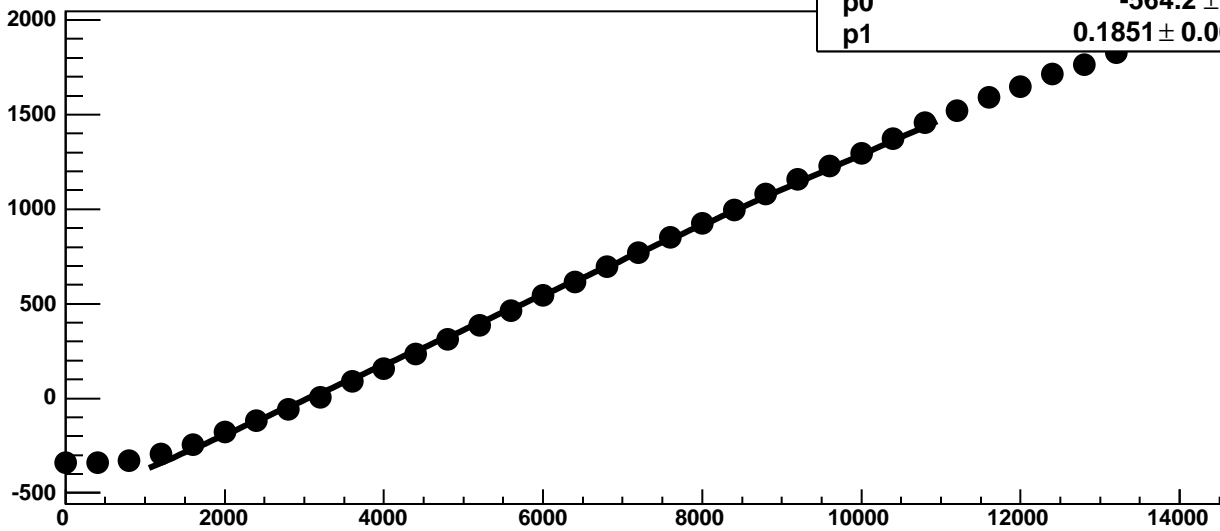
Chip 11, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



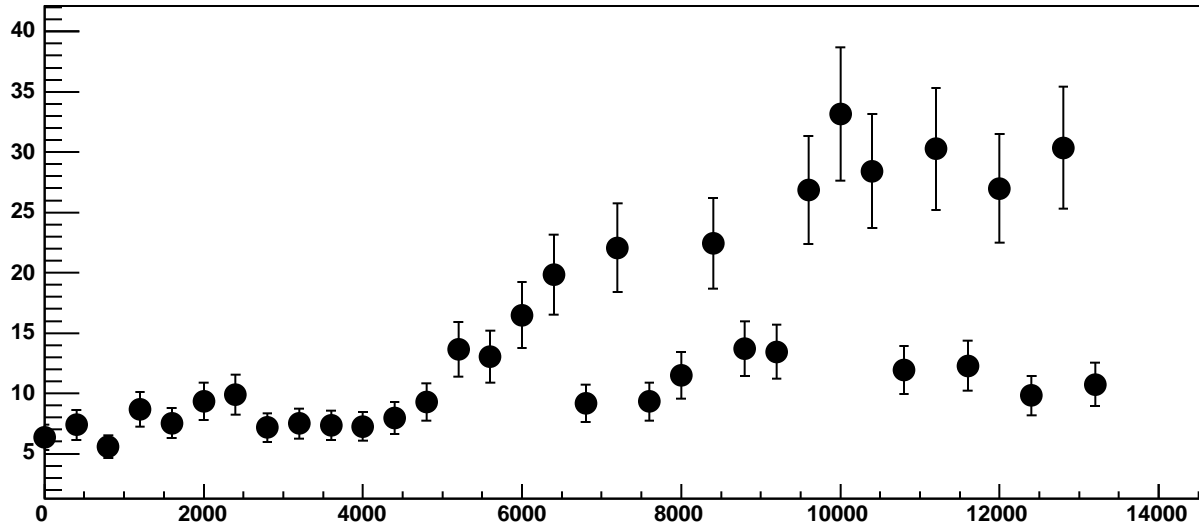
Chip 11, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



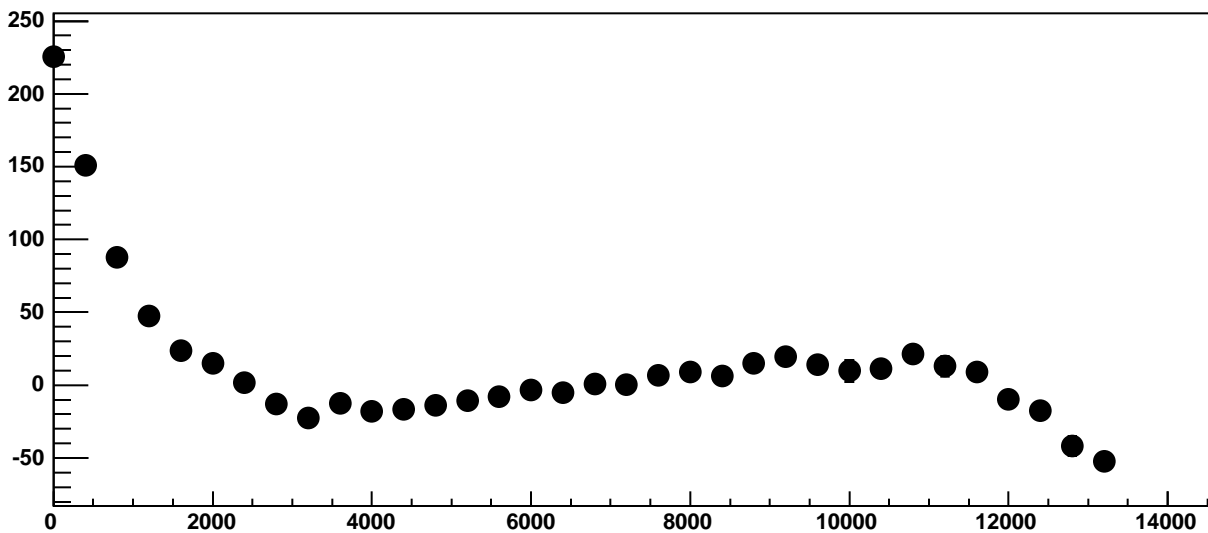
Chip 11, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



Chip 11, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



Chip 11, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

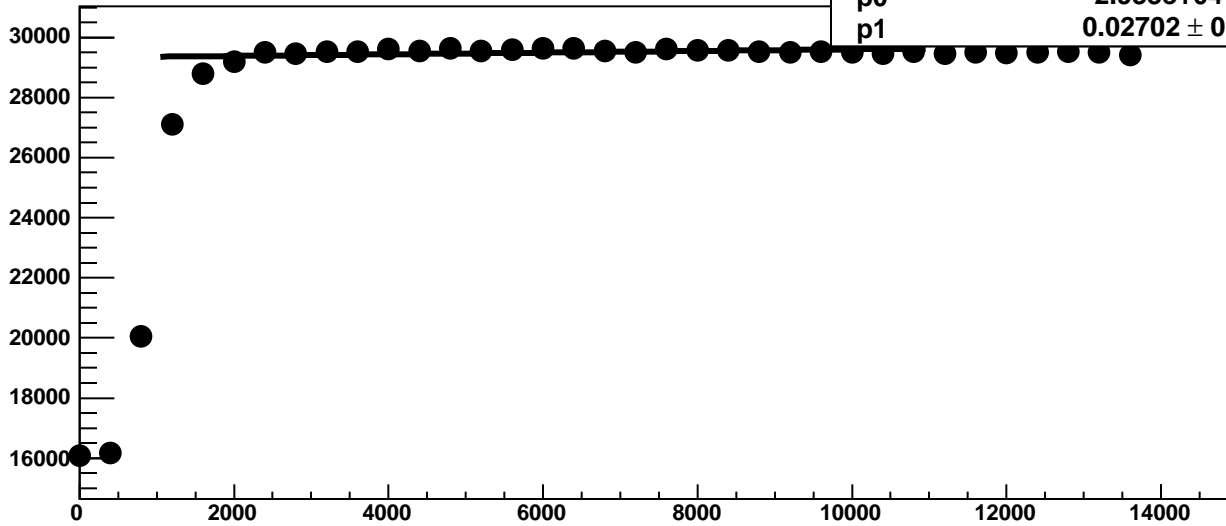
860.7 / 23

p0

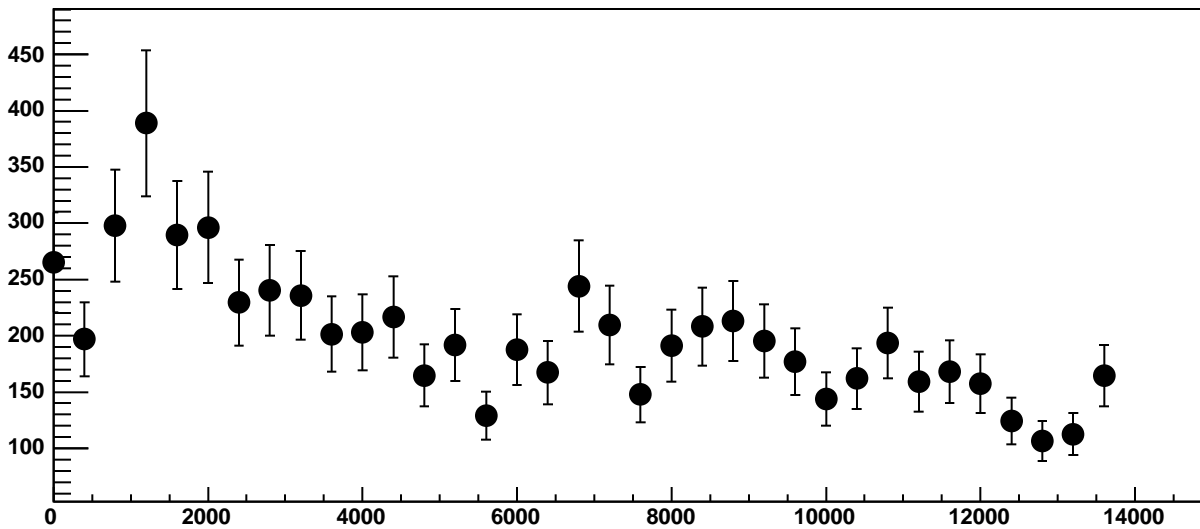
$2.933\text{e}+04 \pm 24.24$

p1

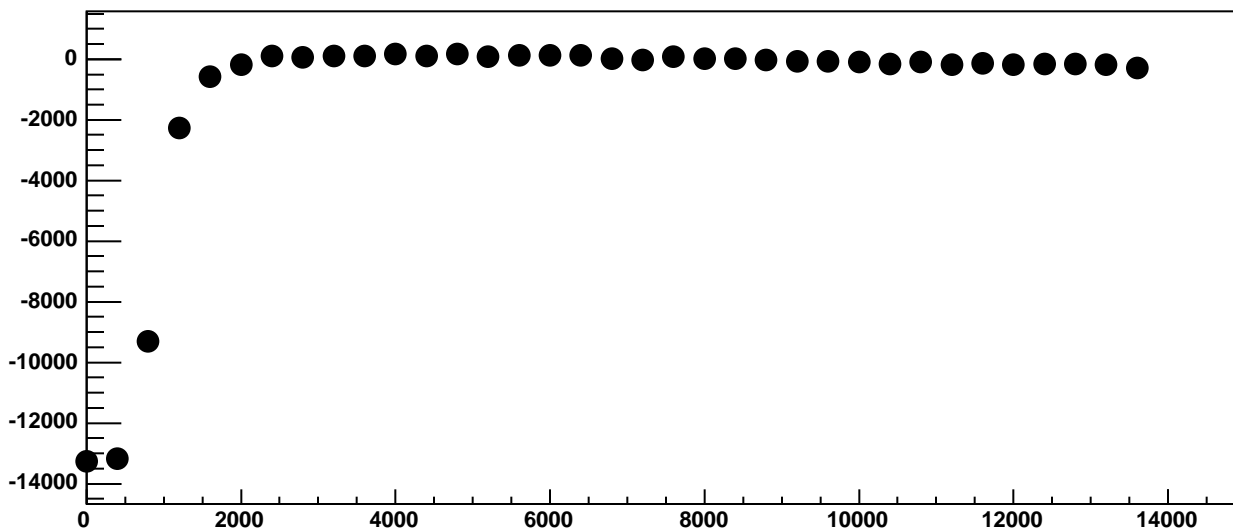
0.02702 ± 0.003378



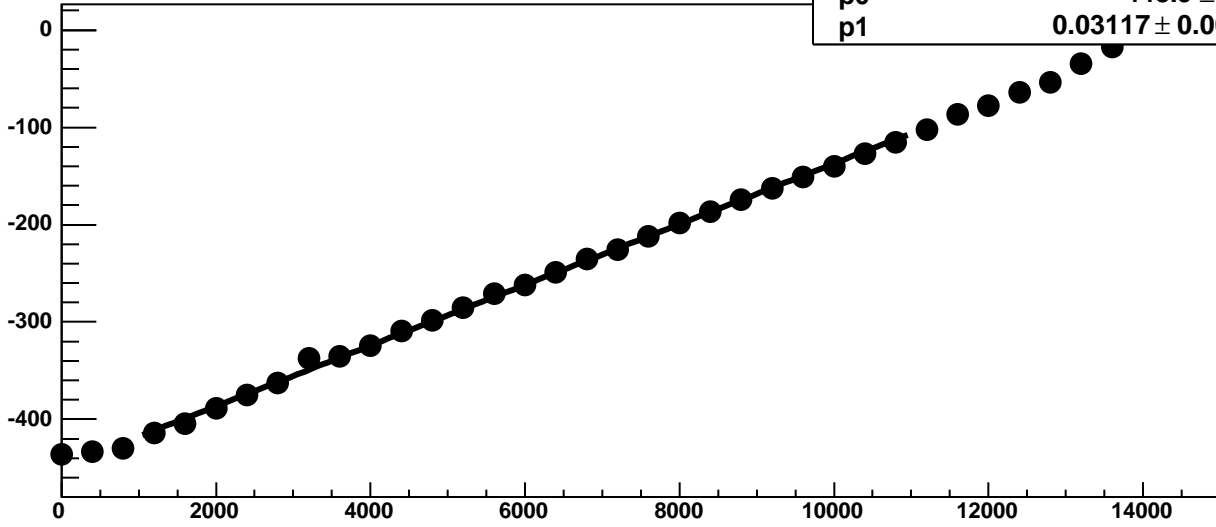
Chip 11, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC

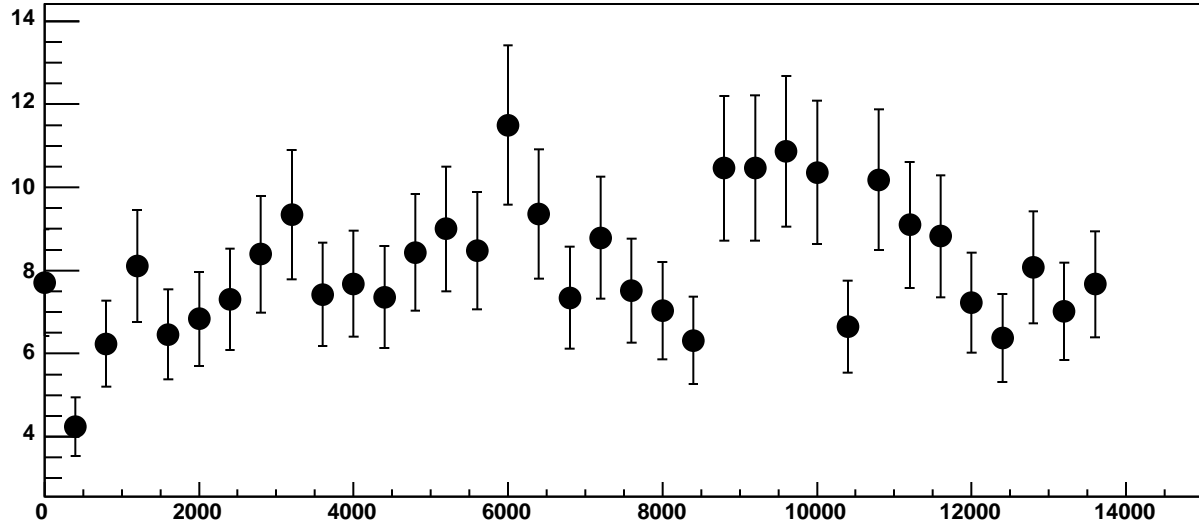


Chip 11, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

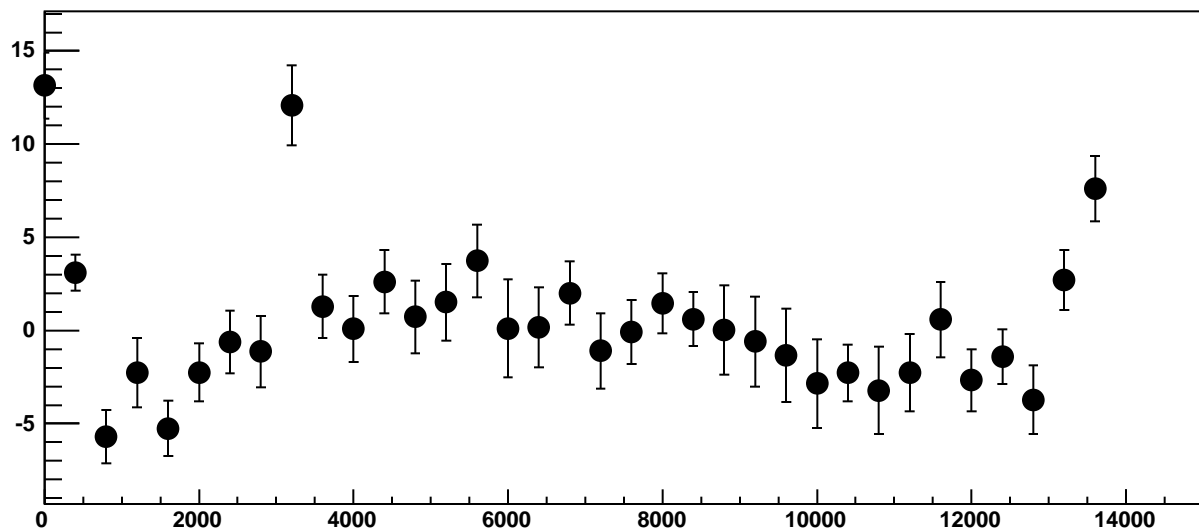


χ^2 / ndf 64.26 / 23
p0 -448.9 ± 0.8159
p1 0.03117 ± 0.0001275

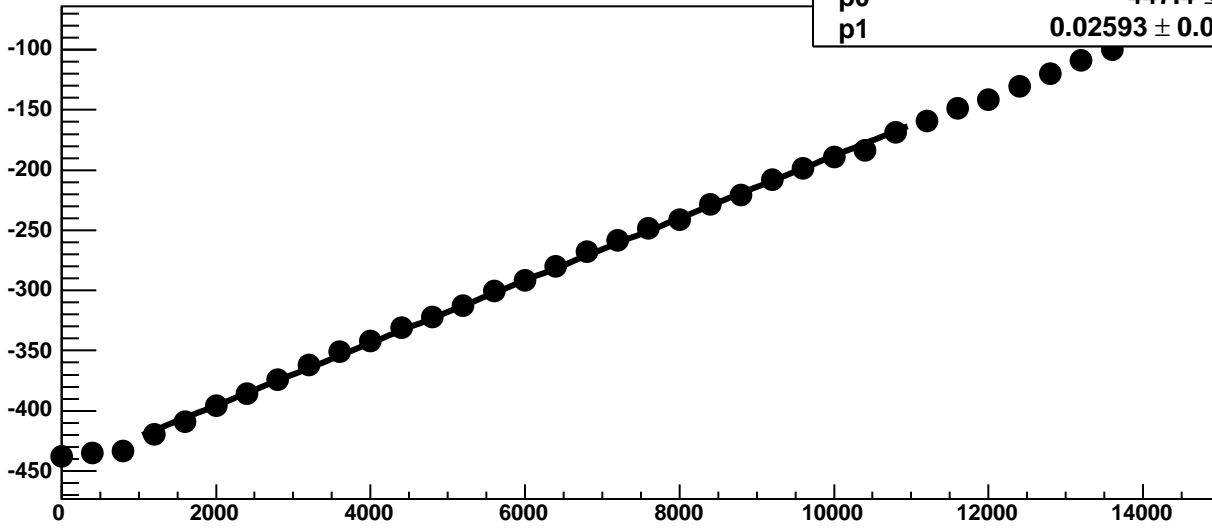
Chip 11, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

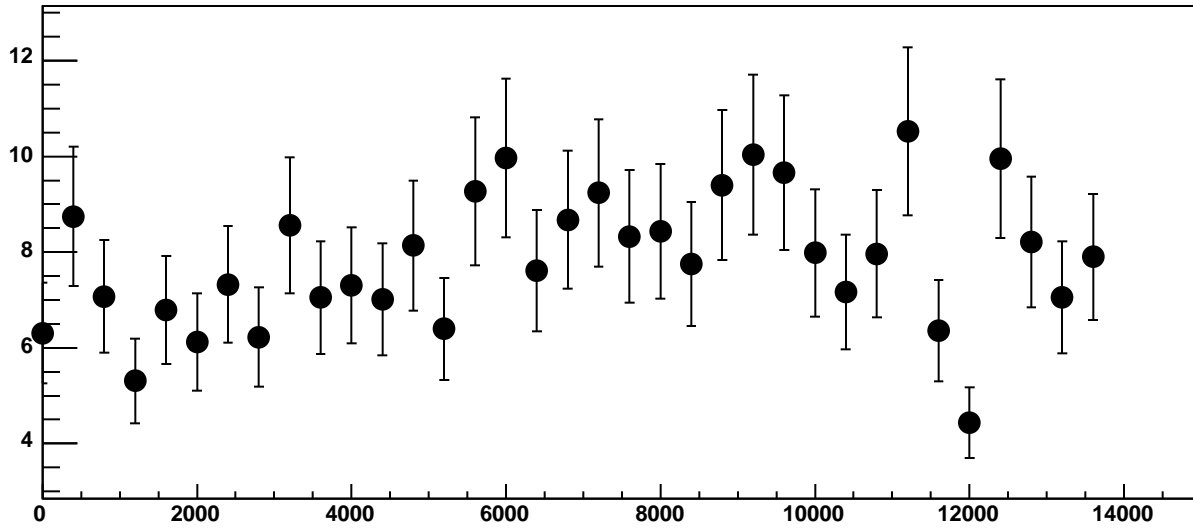


Chip 11, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

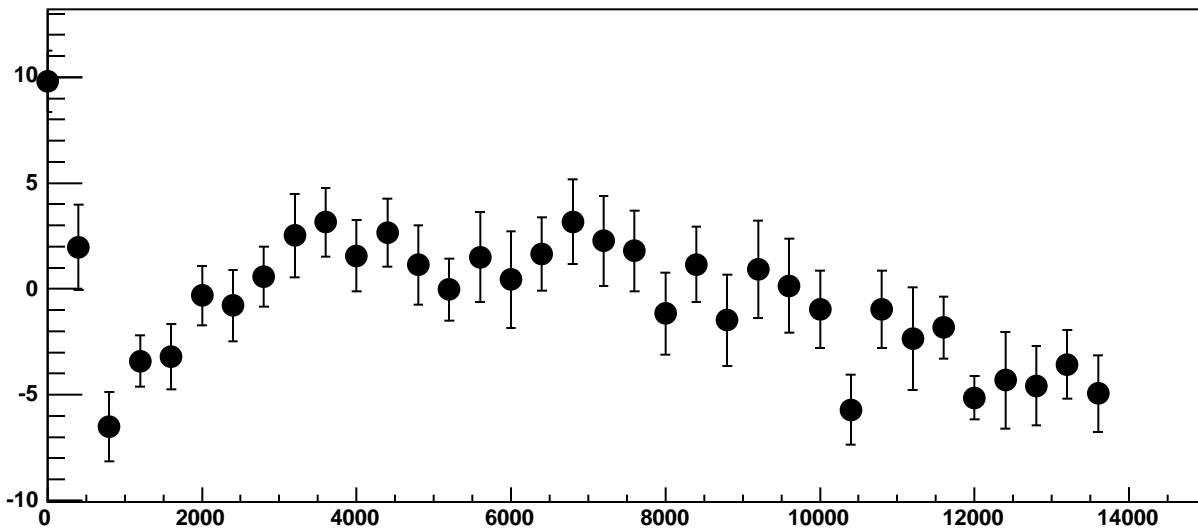


χ^2 / ndf 41.91 / 23
p0 -447.4 ± 0.7131
p1 0.02593 ± 0.0001158

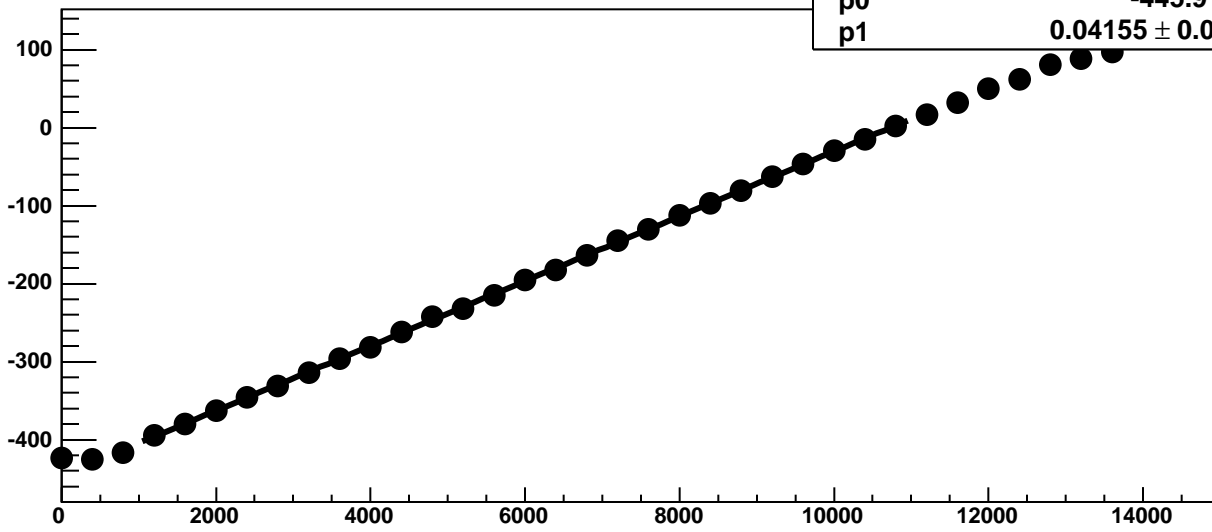
Chip 11, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

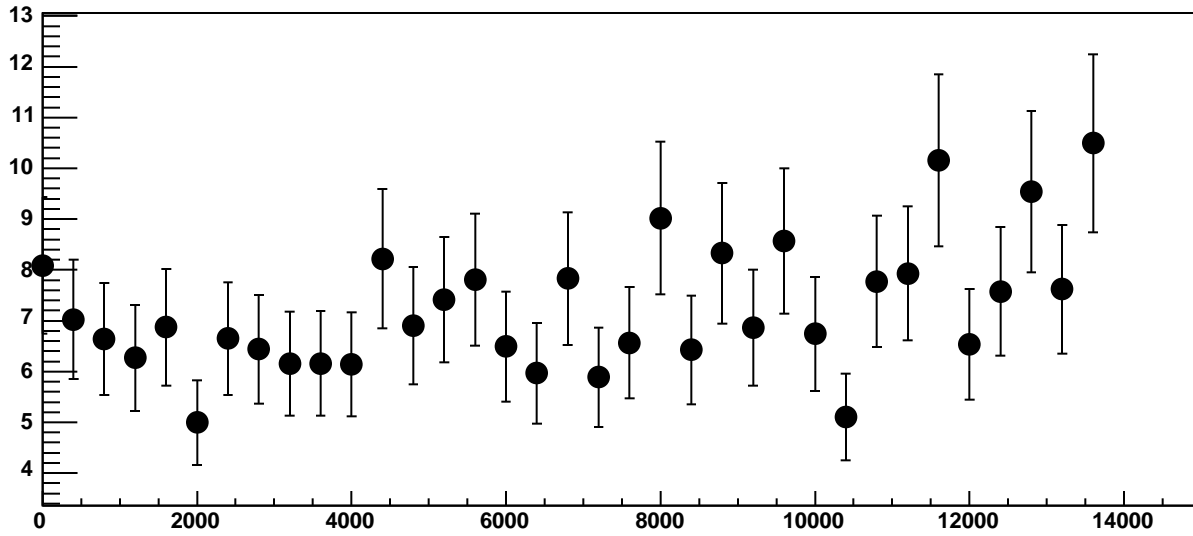


Chip 11, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC

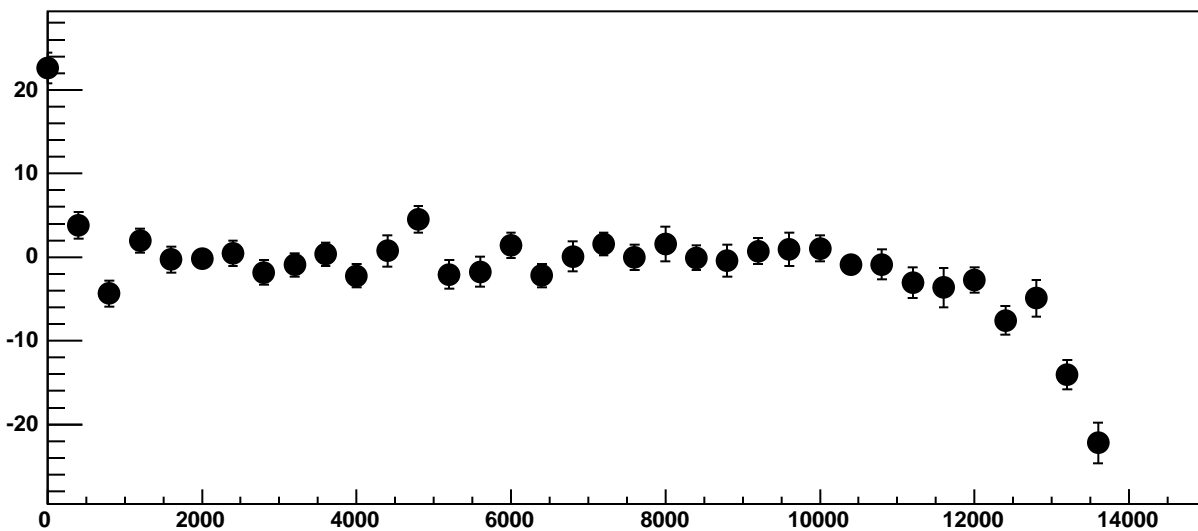


χ^2 / ndf 24.68 / 23
p0 -445.9 ± 0.668
p1 0.04155 ± 0.0001028

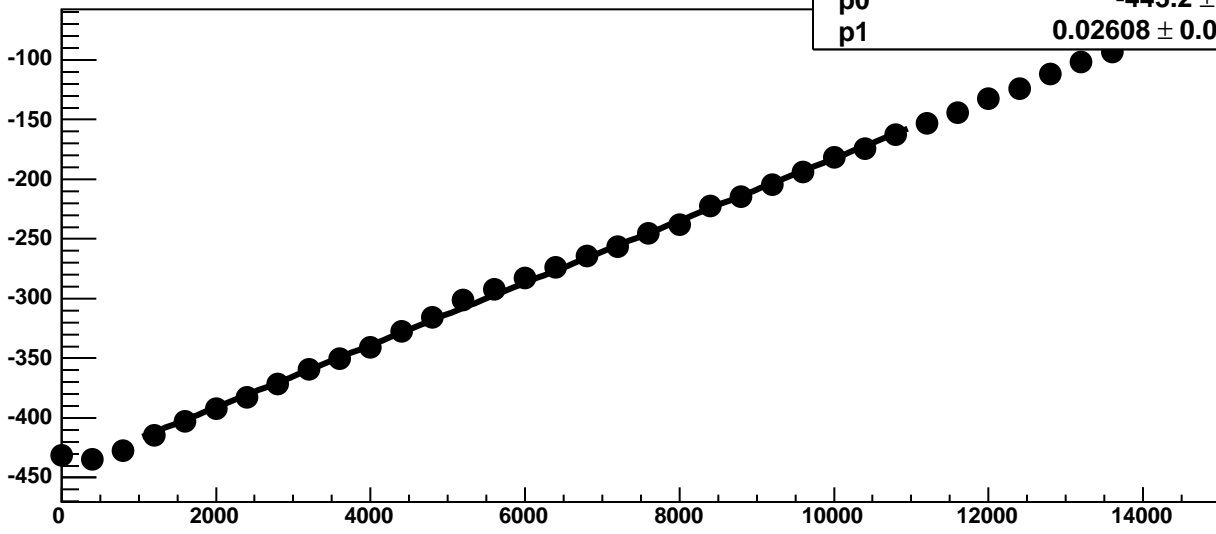
Chip 11, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



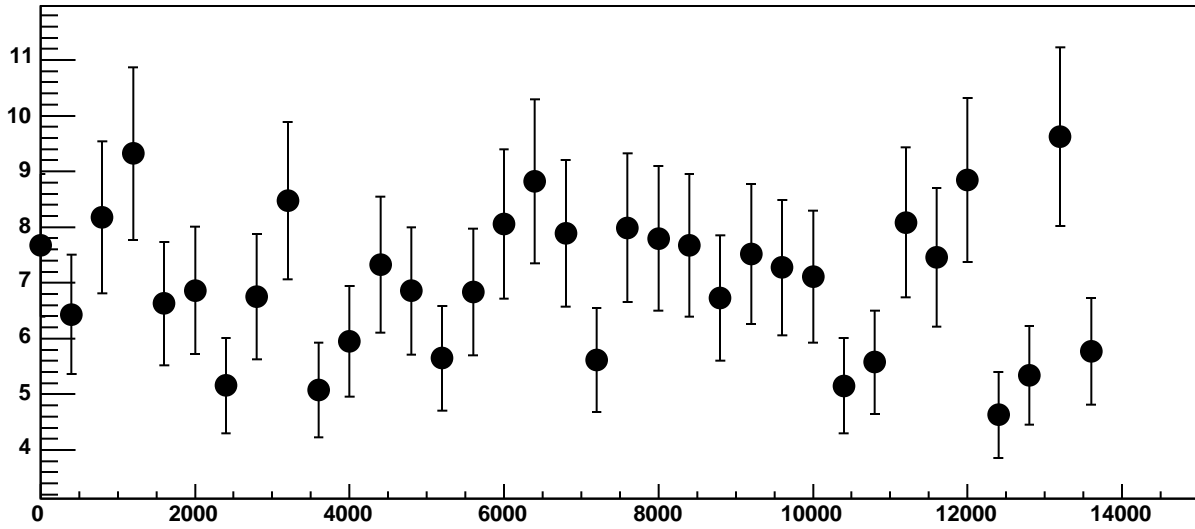
Chip 11, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC



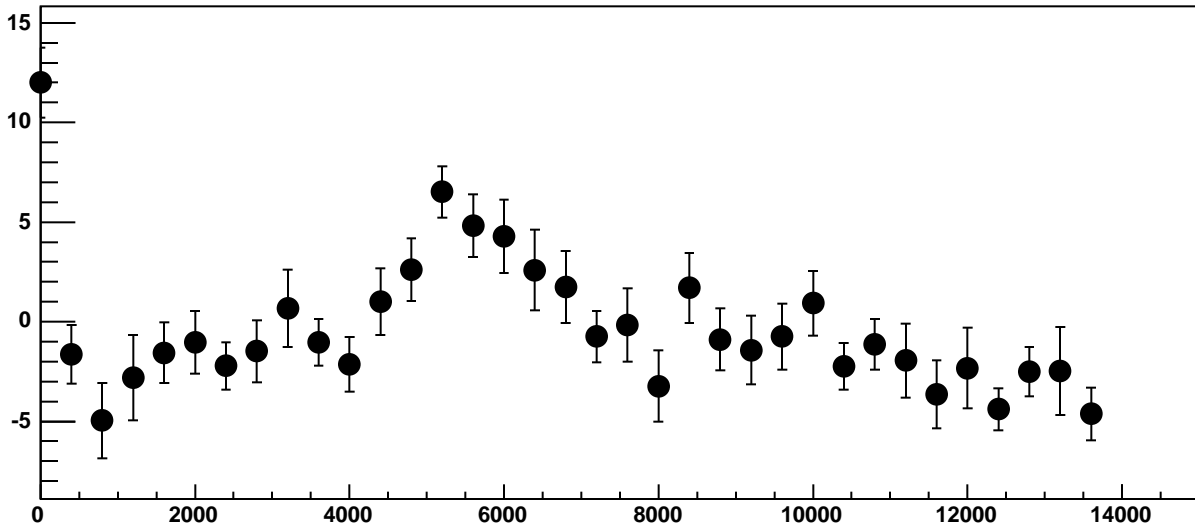
Chip 11, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



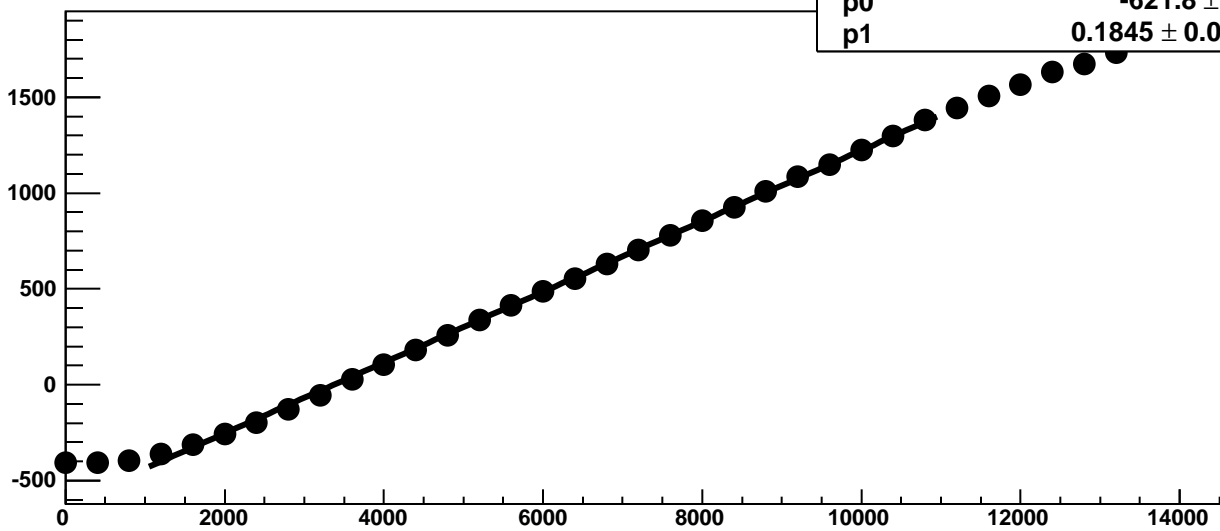
Chip 11, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



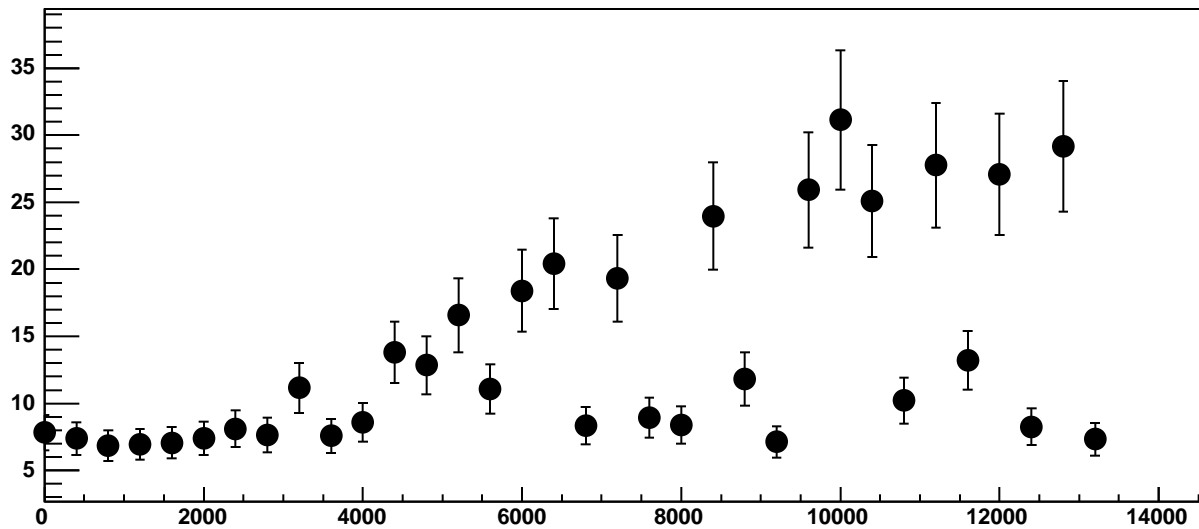
Chip 11, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



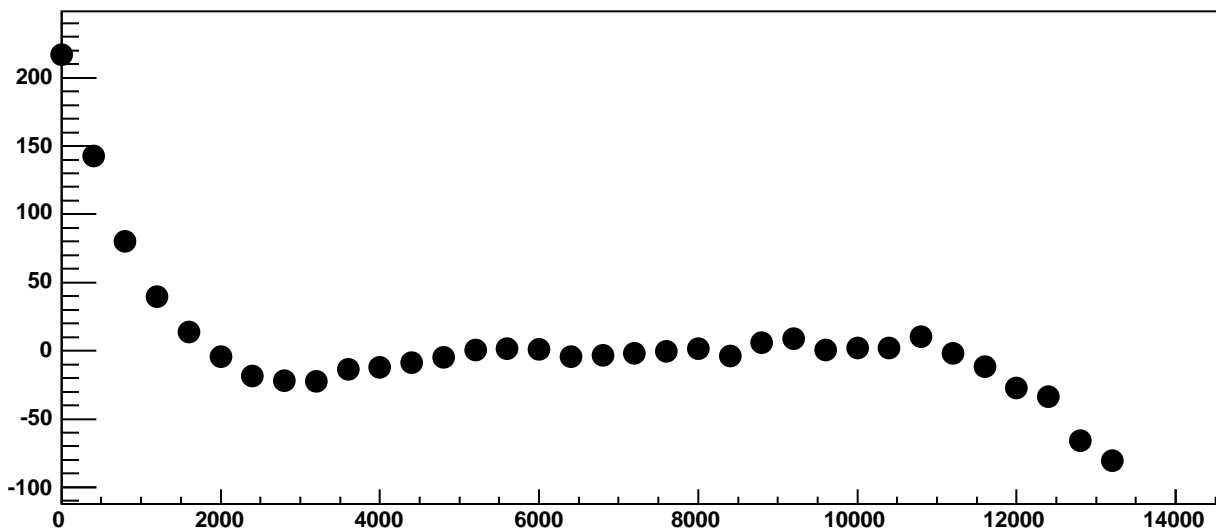
Chip 11, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



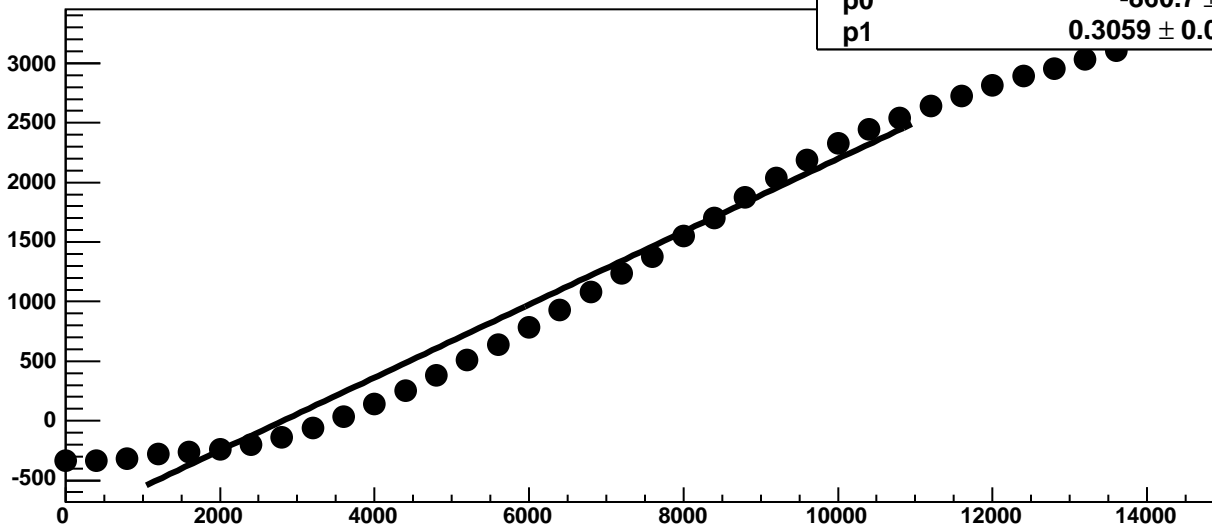
Chip 11, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



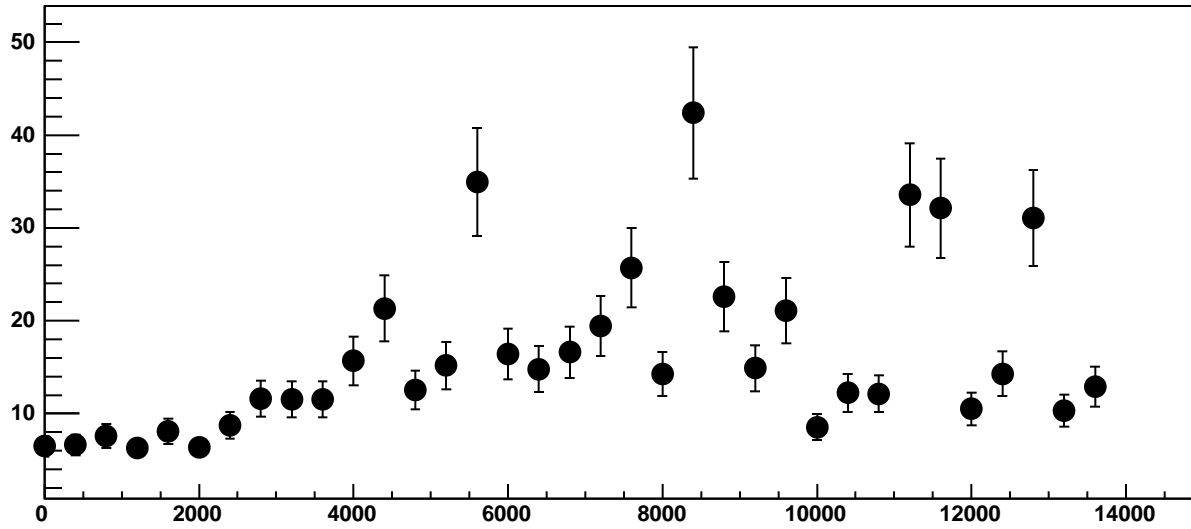
Chip 11, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



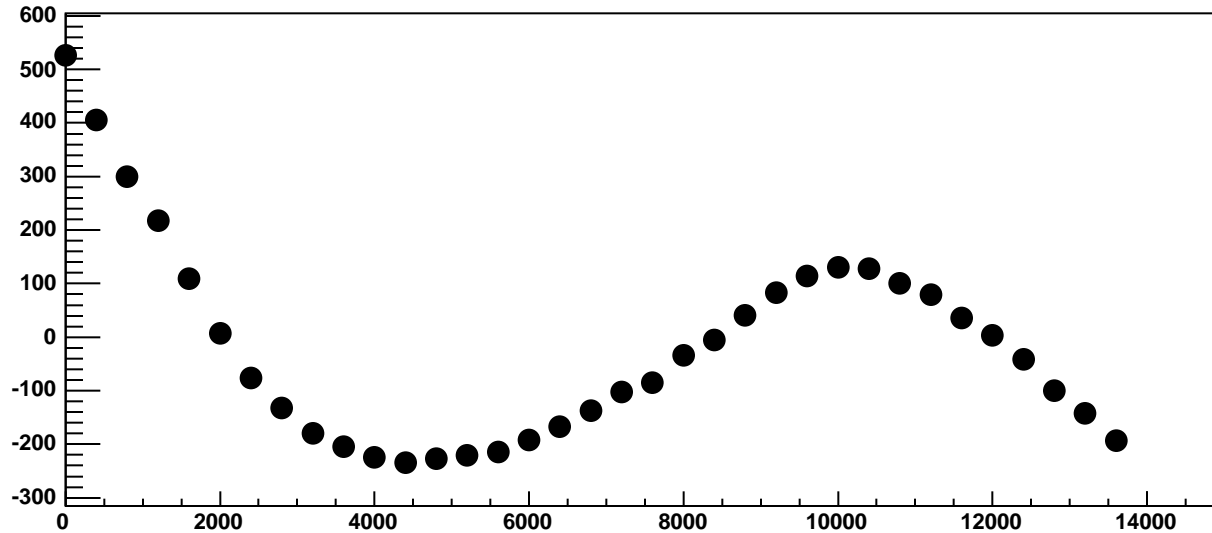
Chip 11, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC



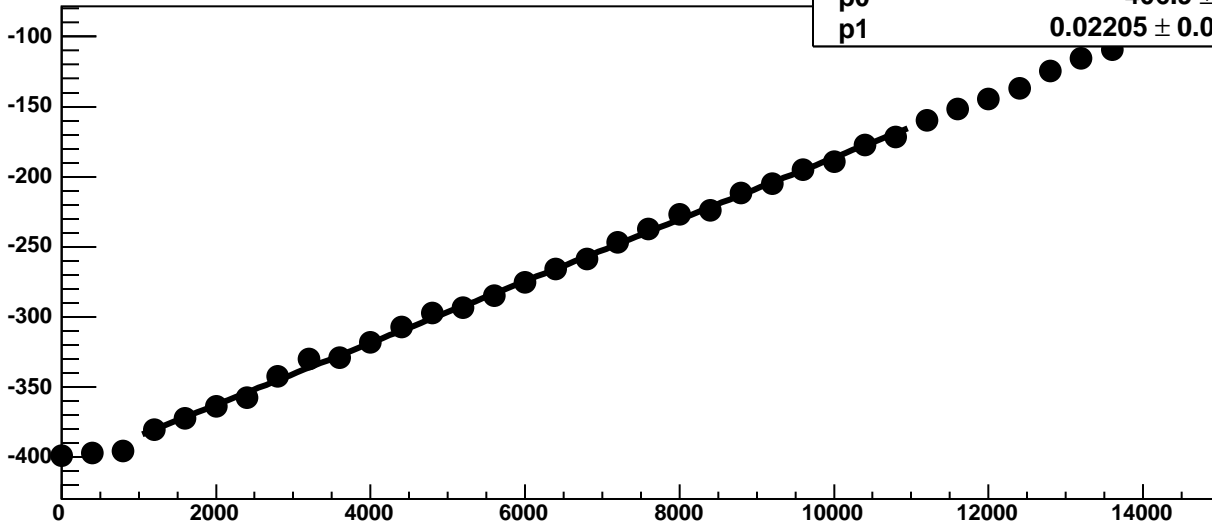
Chip 11, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

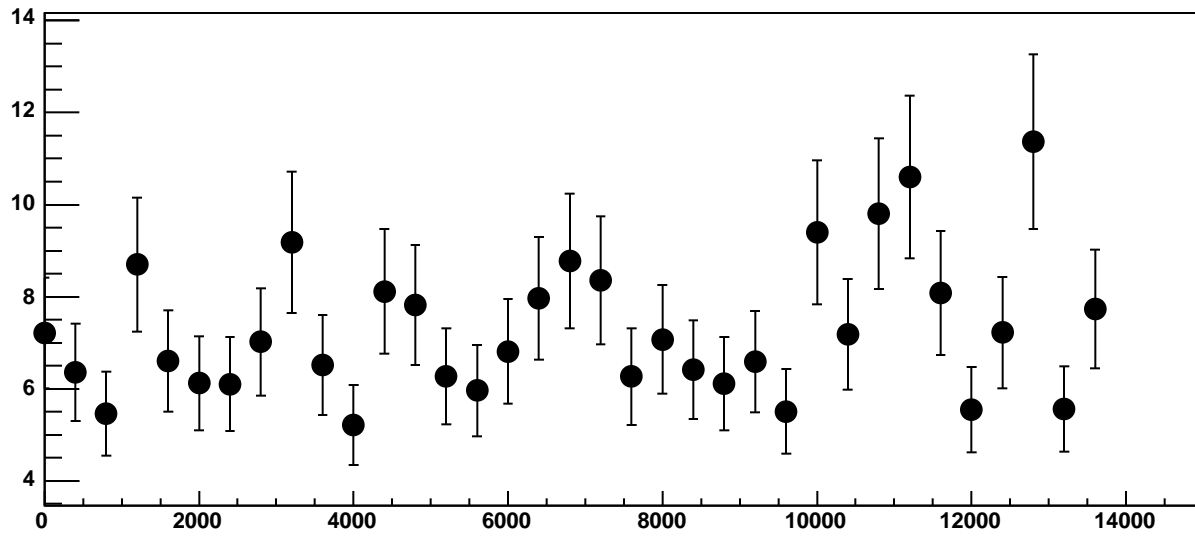


Chip 11, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

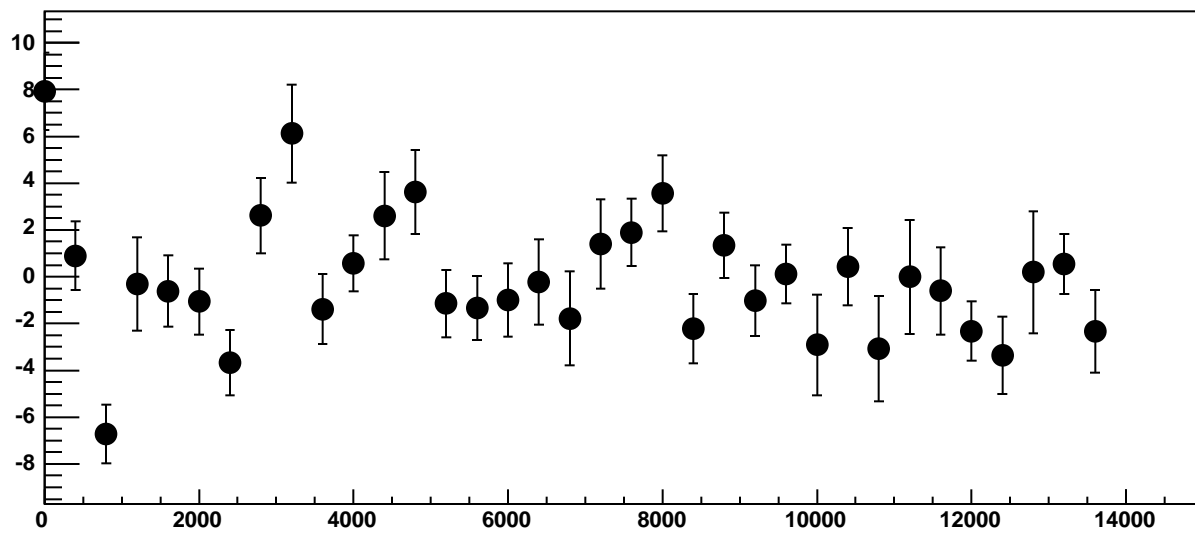


χ^2 / ndf 43.05 / 23
p0 -406.9 ± 0.7349
p1 0.02205 ± 0.0001123

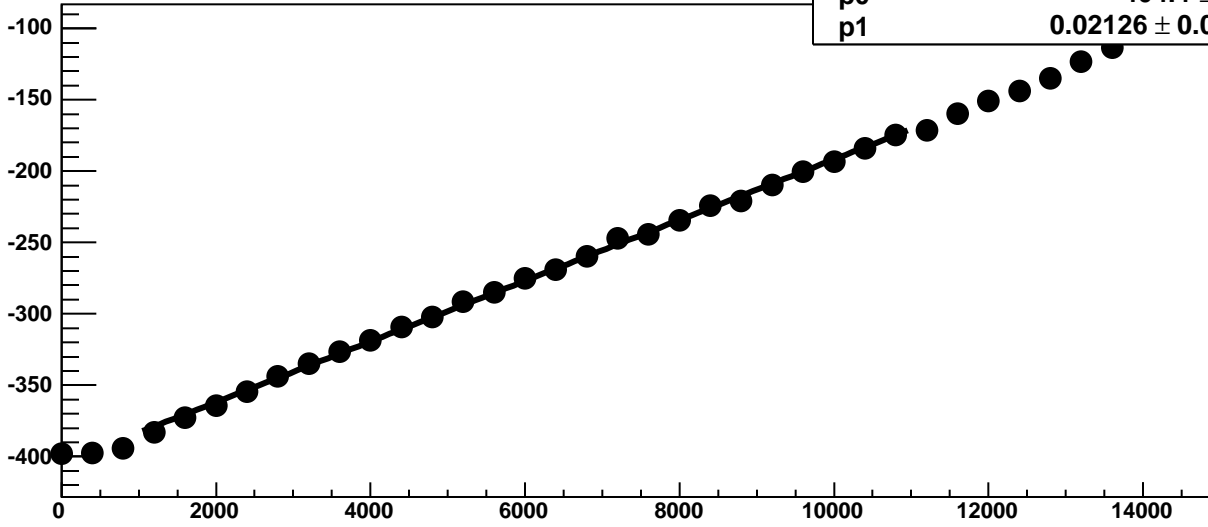
Chip 11, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

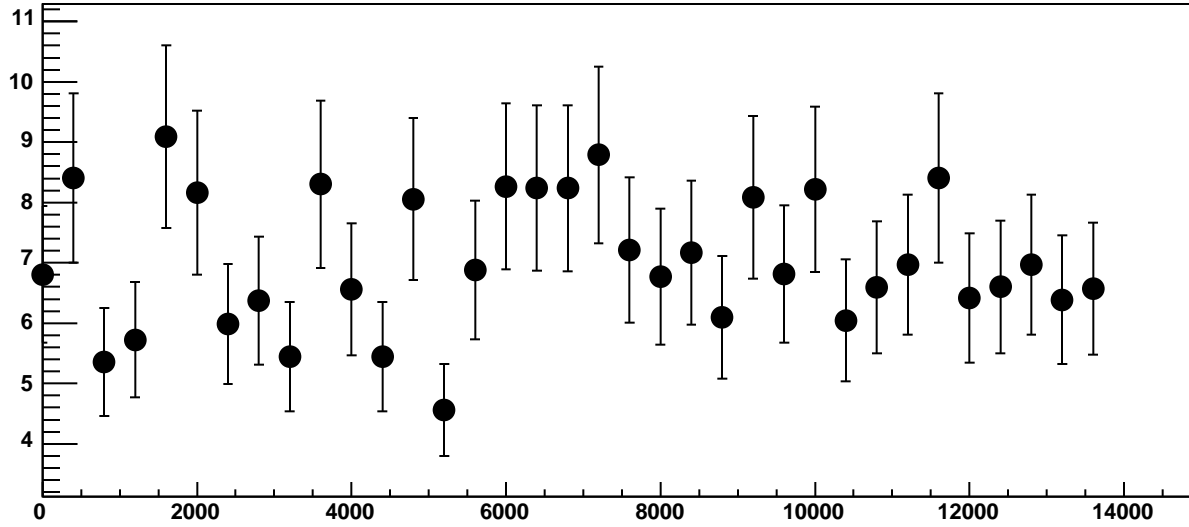


Chip 11, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

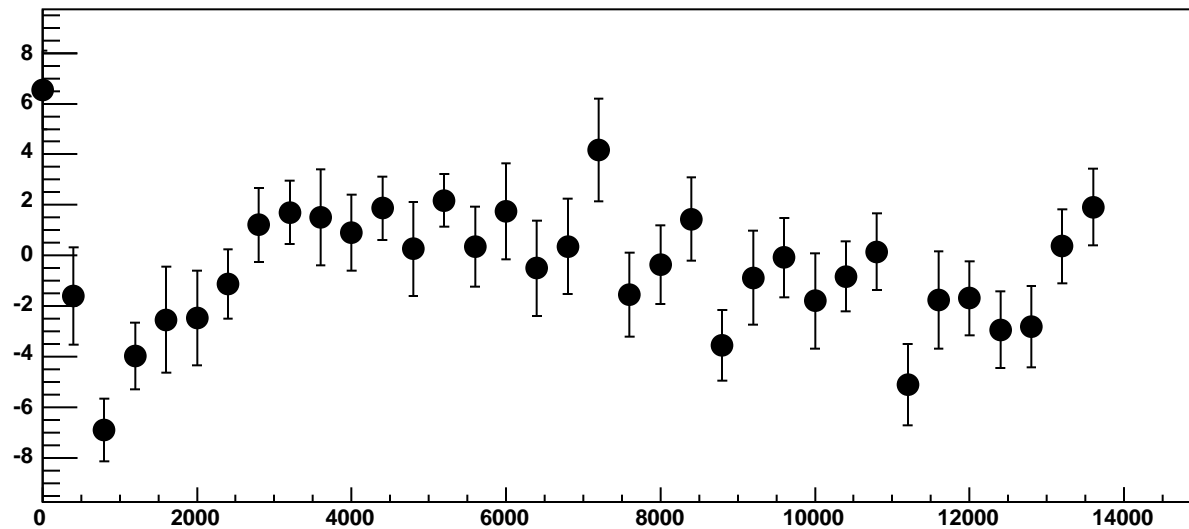


χ^2 / ndf 38.04 / 23
p0 -404.4 ± 0.6984
p1 0.02126 ± 0.0001072

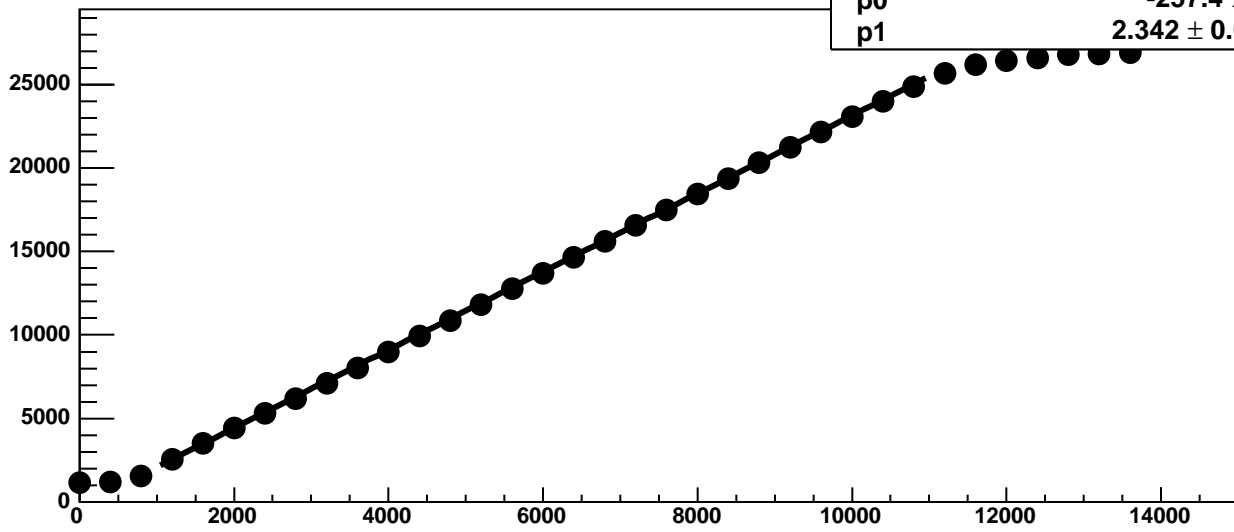
Chip 11, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



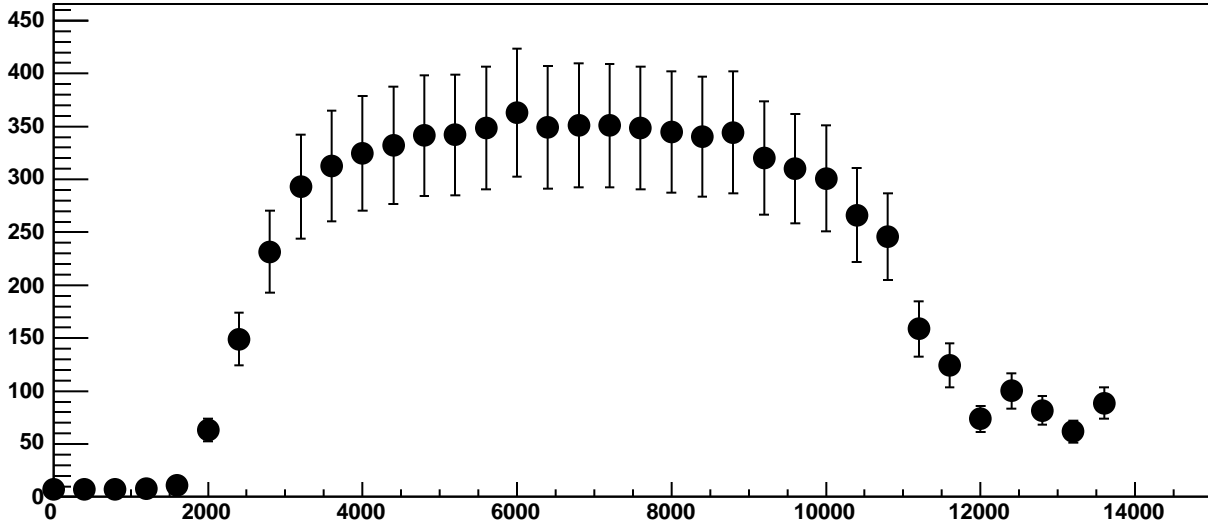
Chip 11, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



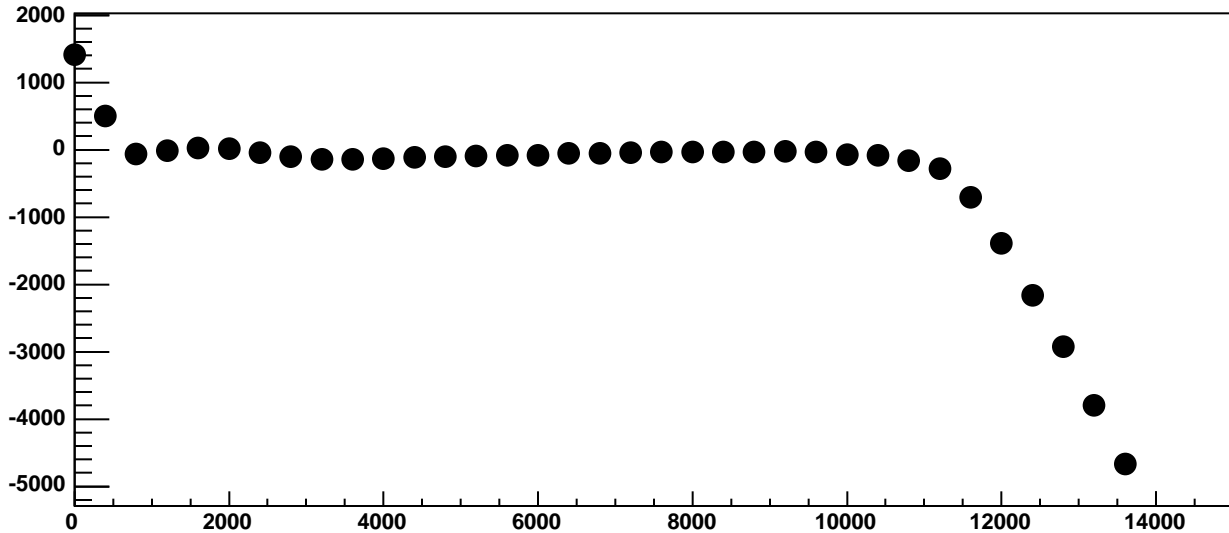
Chip 11, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC



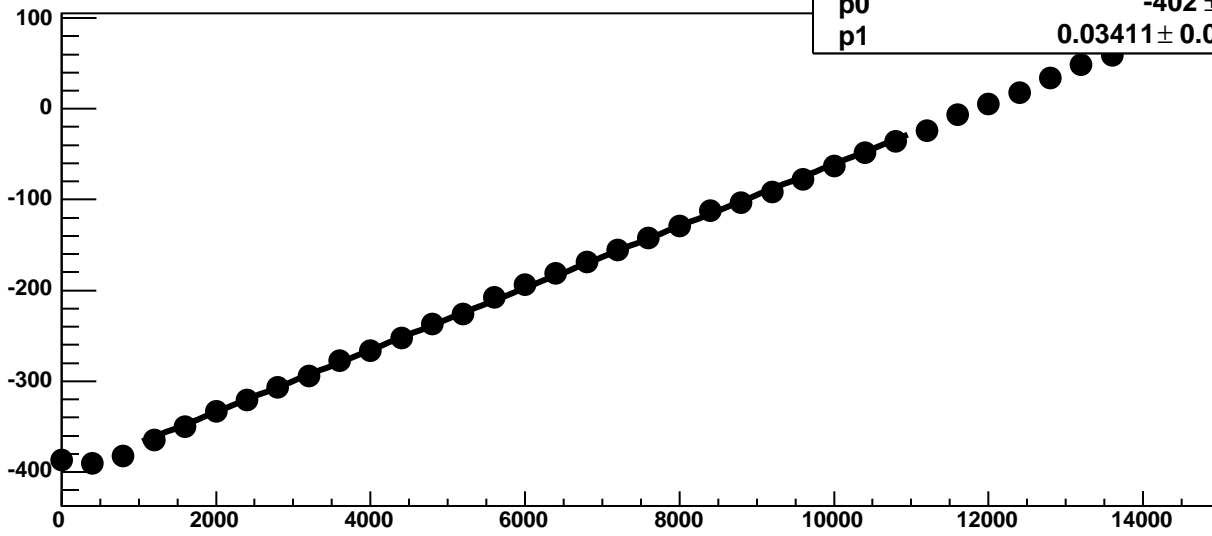
Chip 11, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



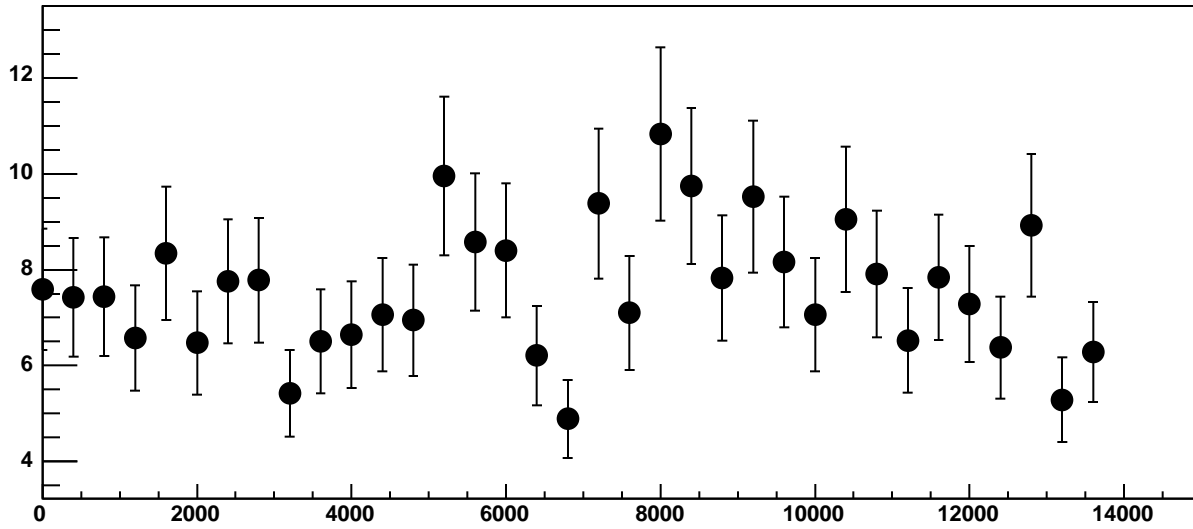
Chip 11, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC



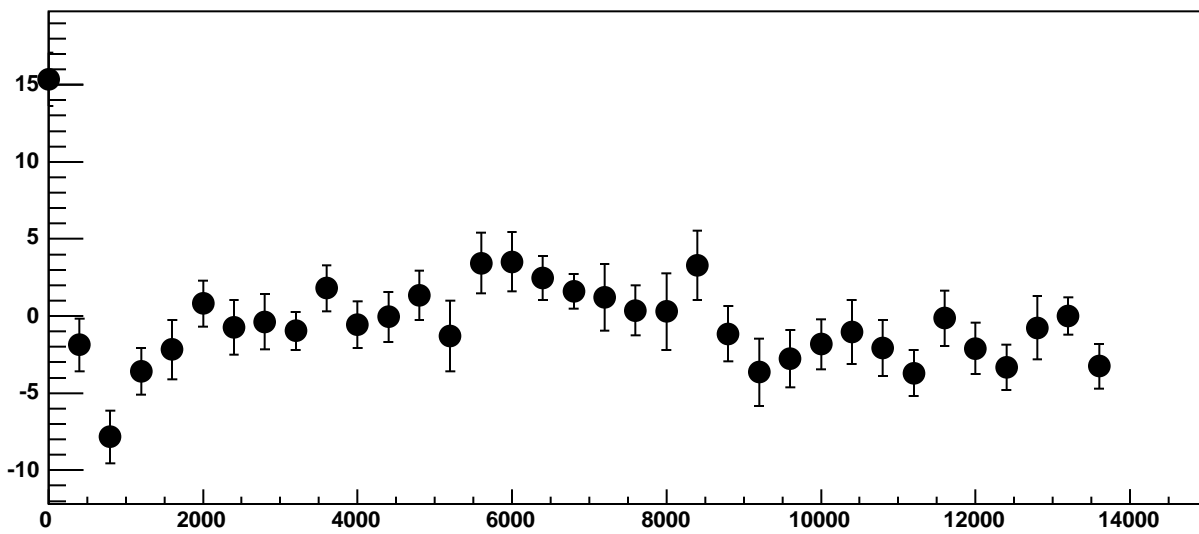
Chip 11, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC



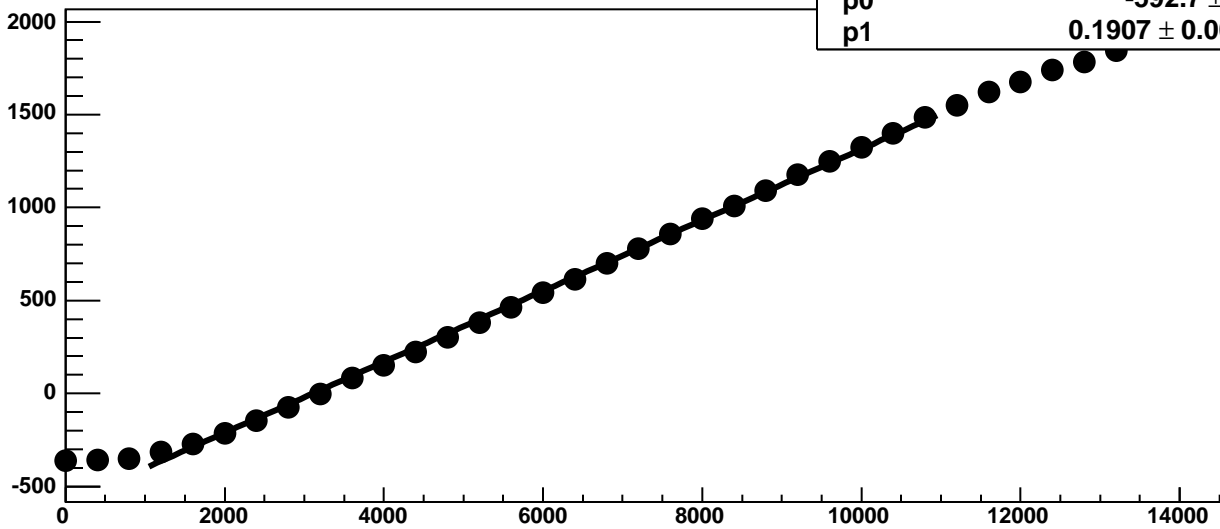
Chip 11, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



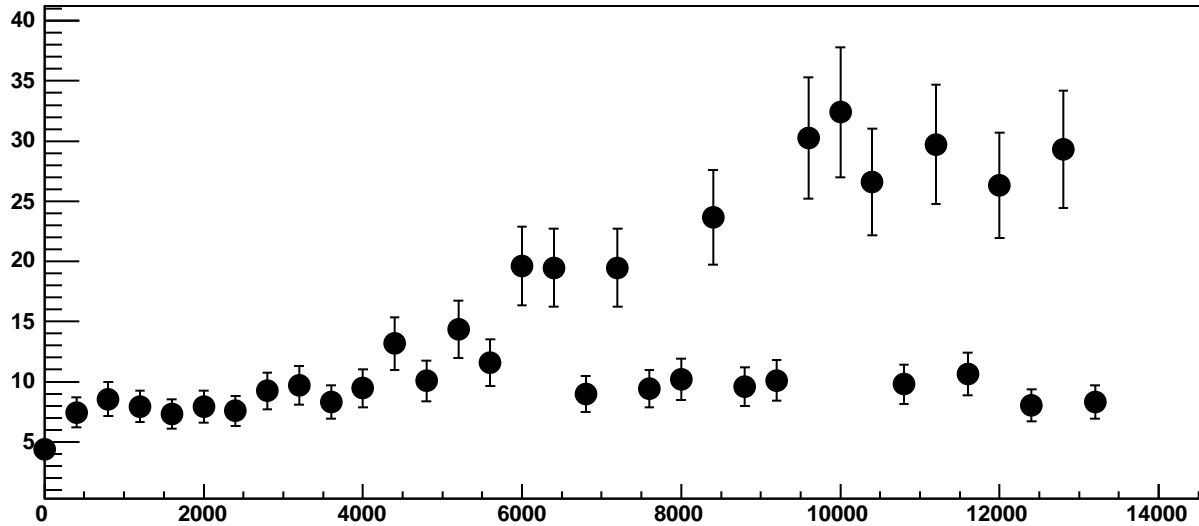
Chip 11, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



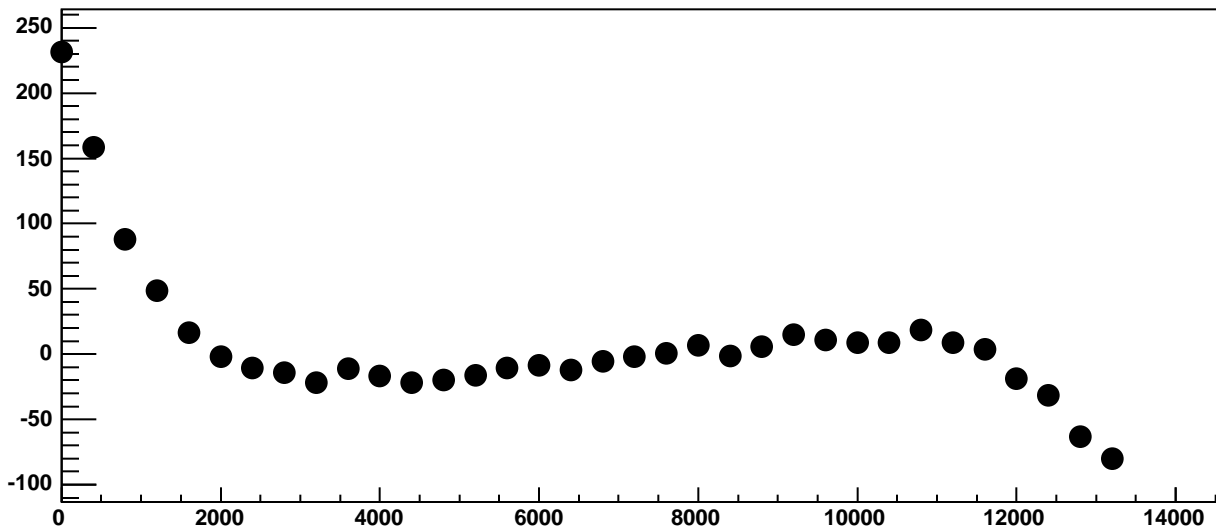
Chip 11, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



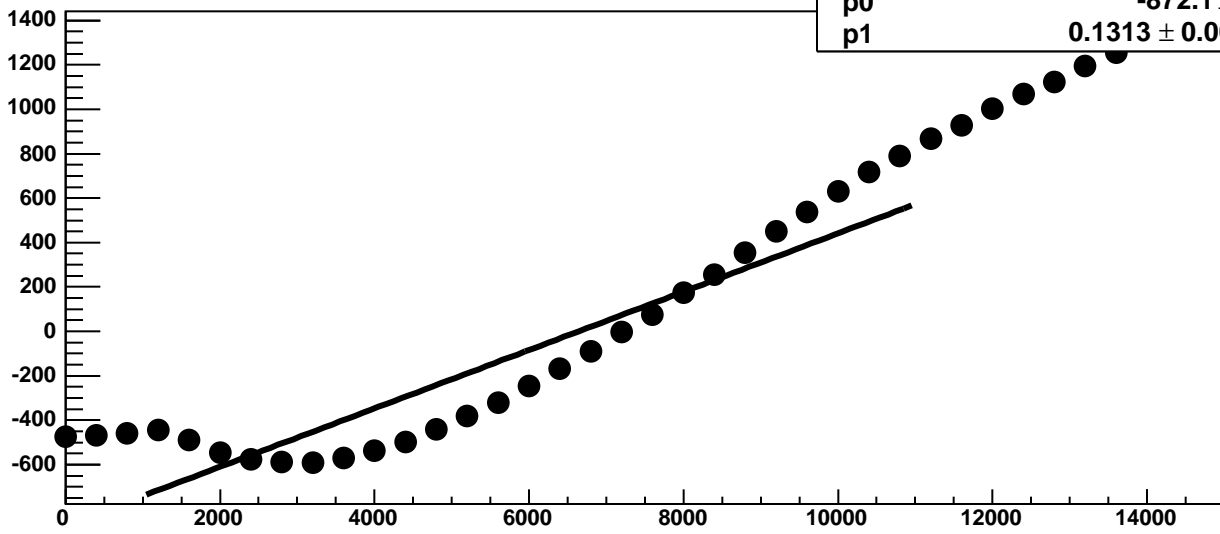
Chip 11, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



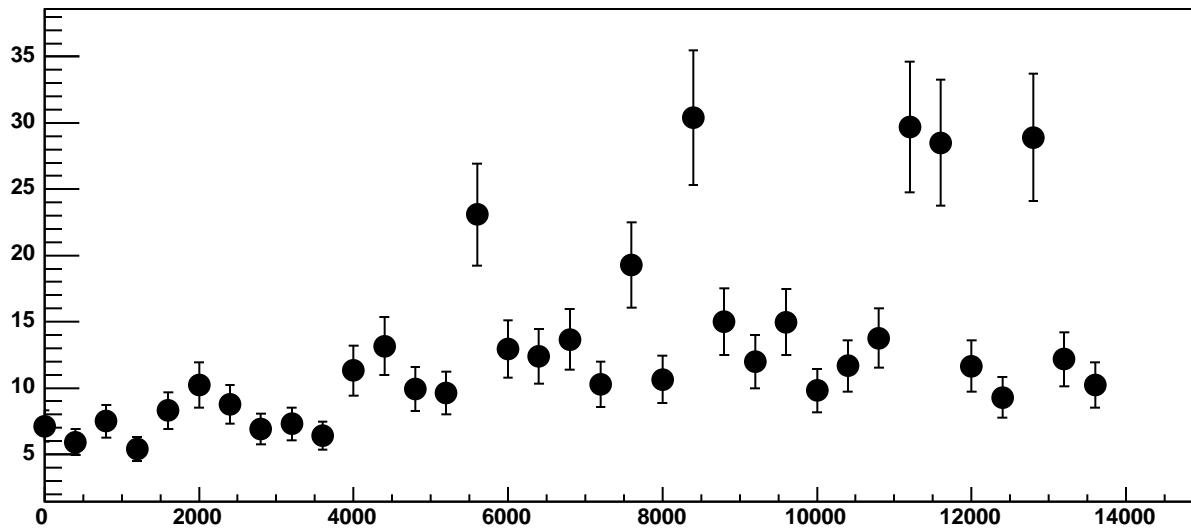
Chip 11, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



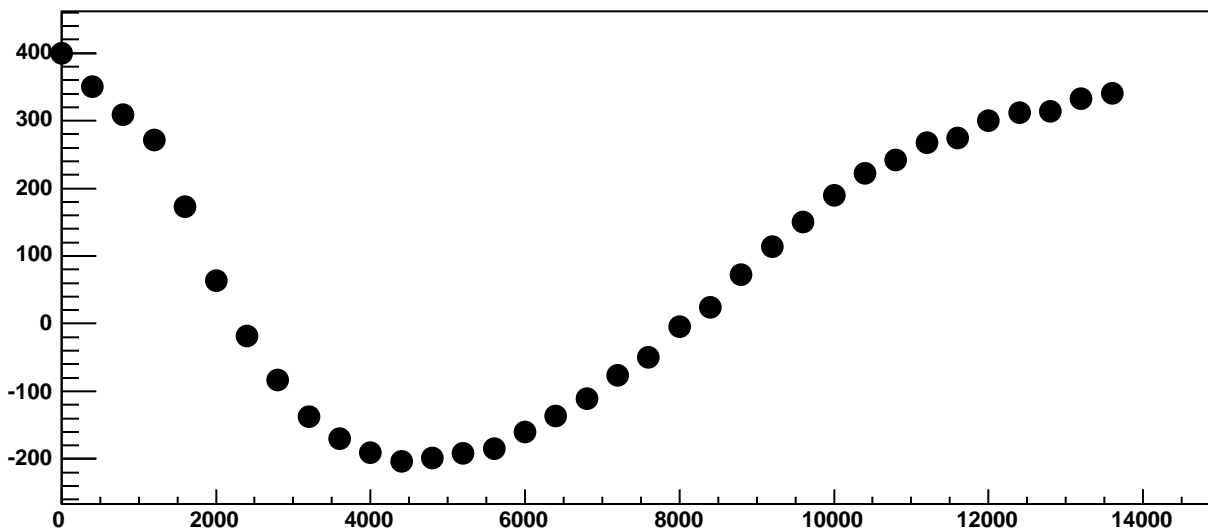
Chip 11, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



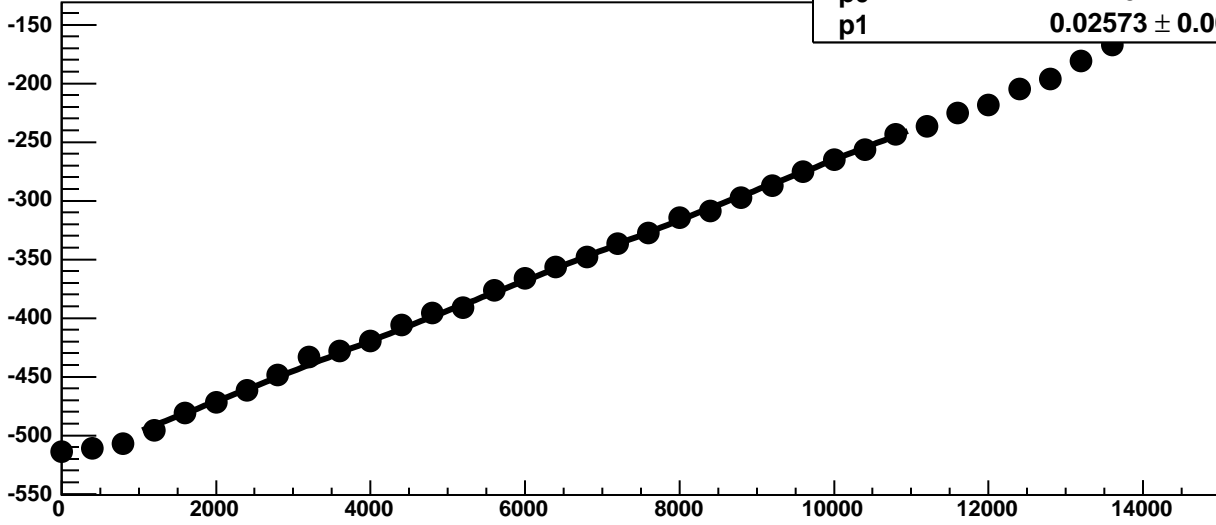
Chip 11, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

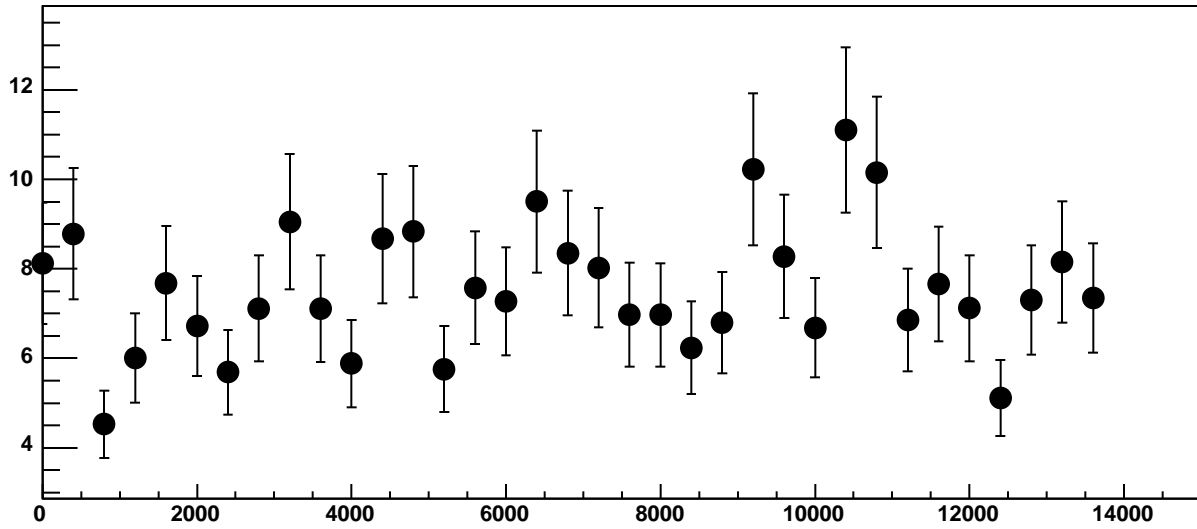


Chip 11, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC

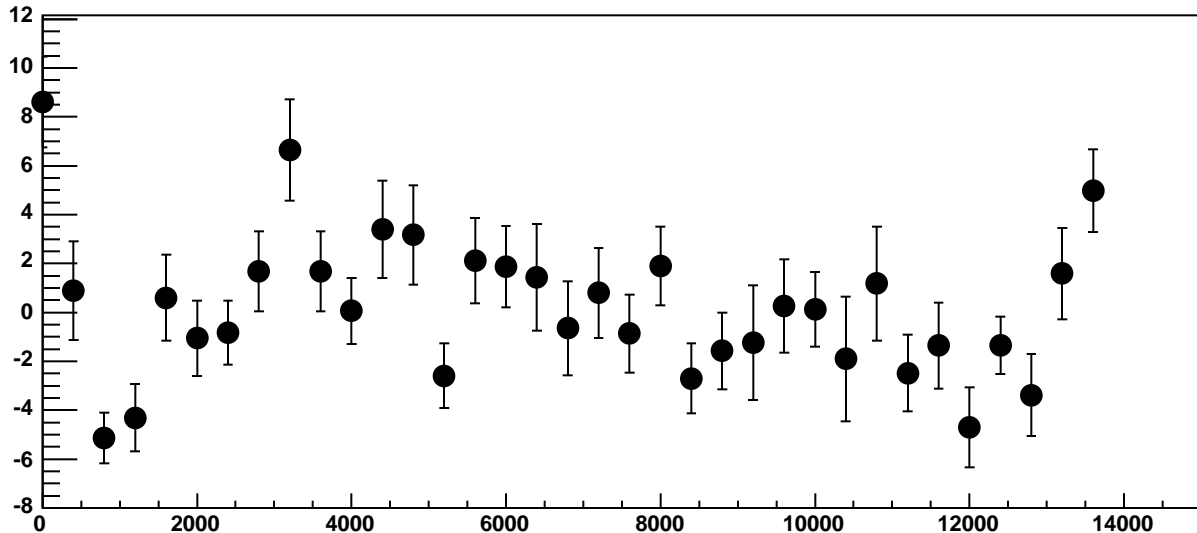


χ^2 / ndf 43.33 / 23
p0 -522.4 ± 0.7418
p1 0.02573 ± 0.0001183

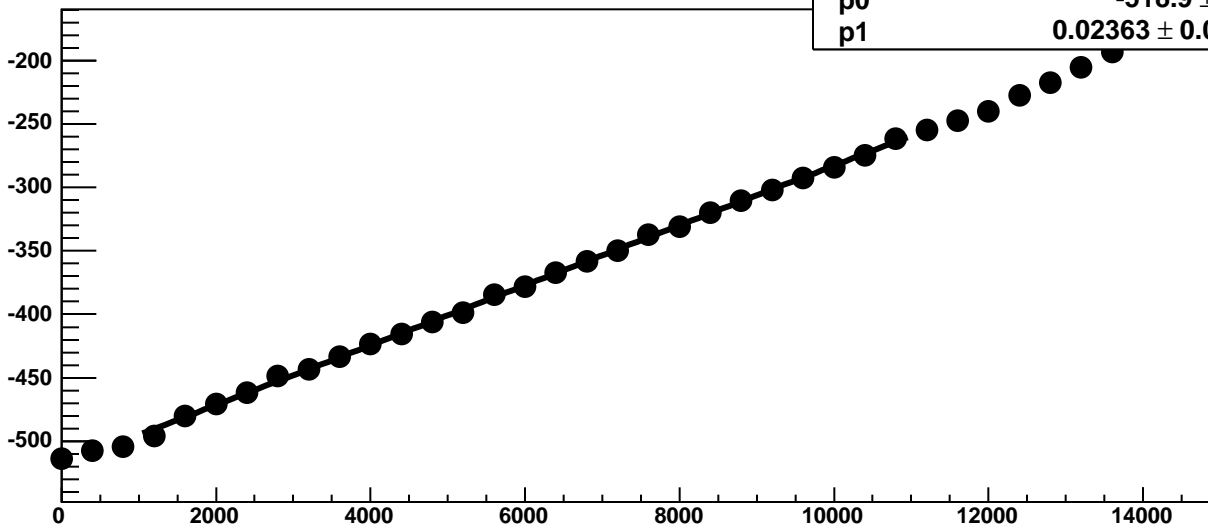
Chip 11, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC

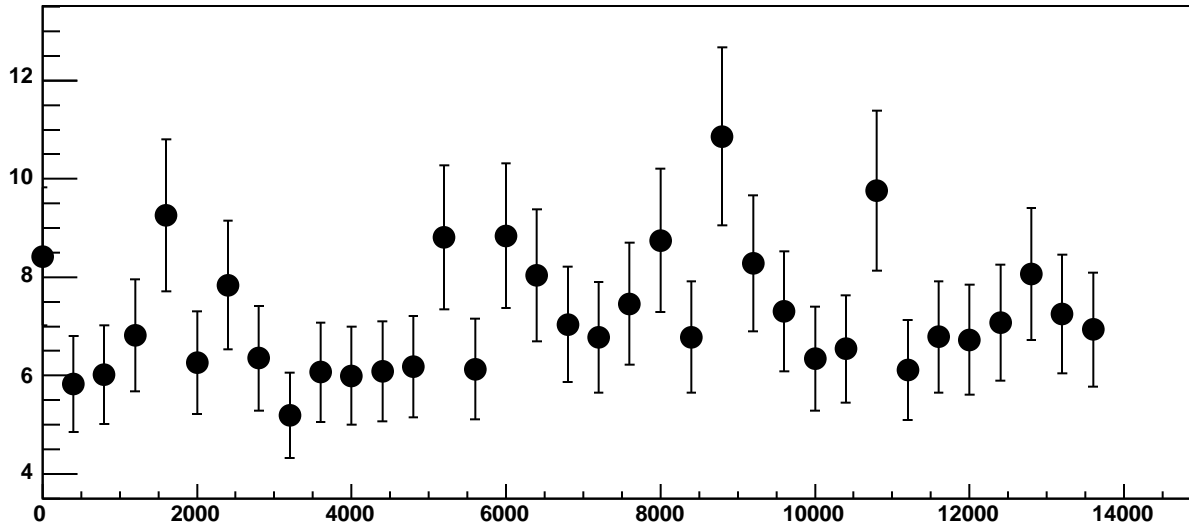


Chip 11, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

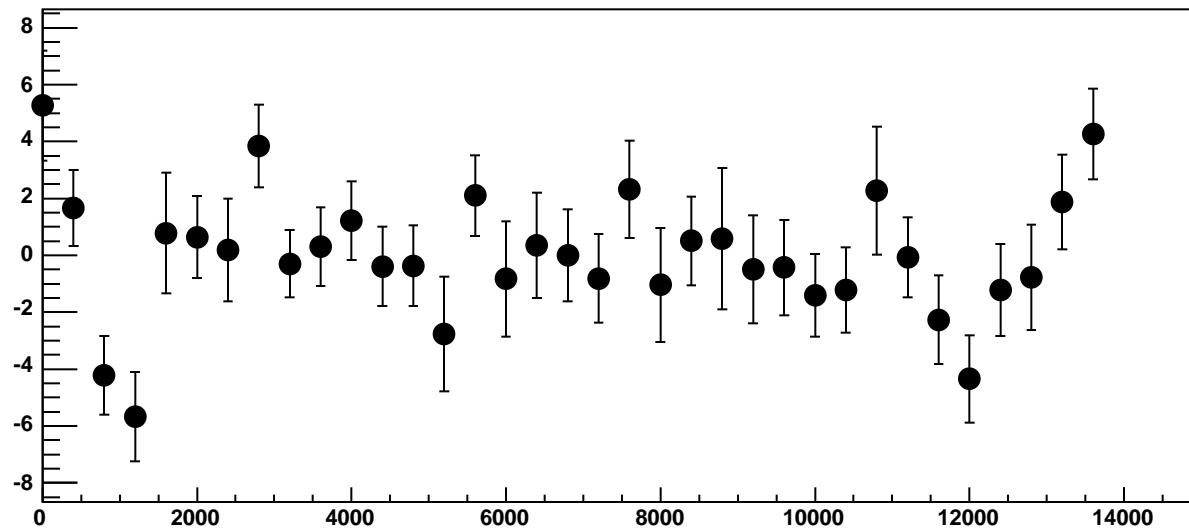


χ^2 / ndf 31.04 / 23
p0 -518.9 ± 0.7227
p1 0.02363 ± 0.0001142

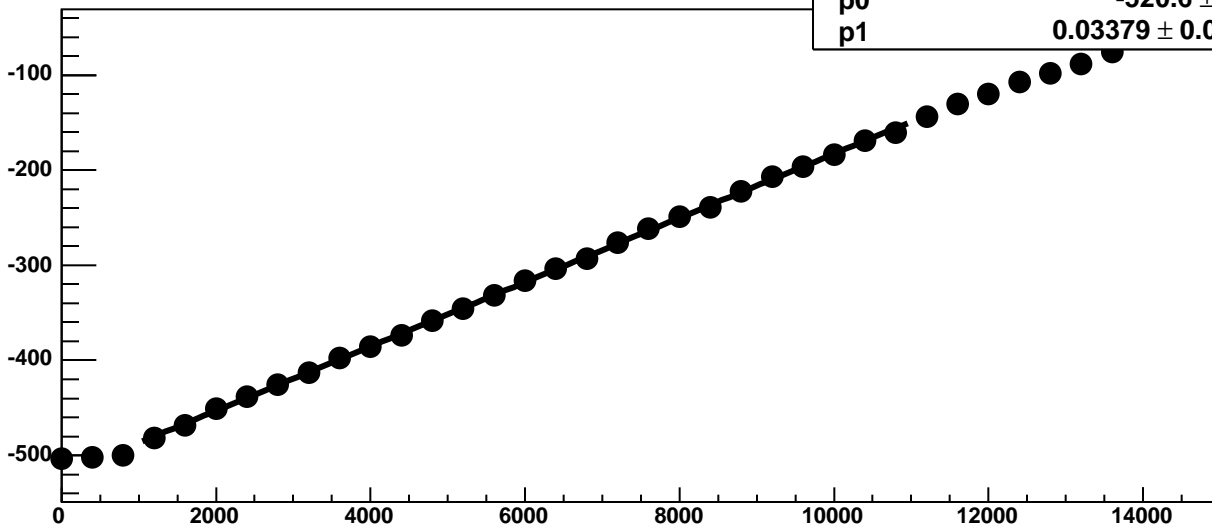
Chip 11, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



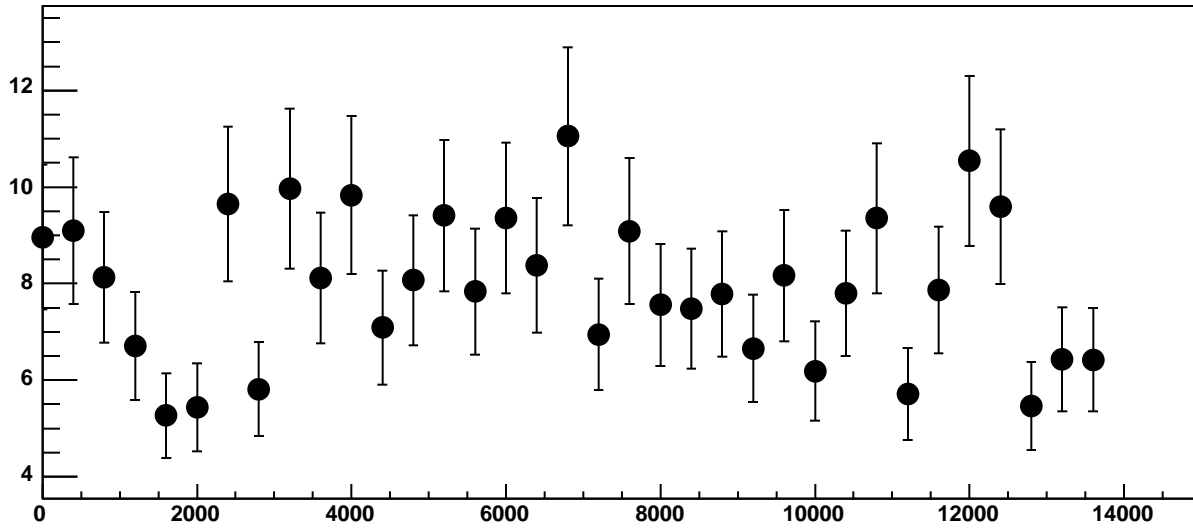
Chip 11, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC



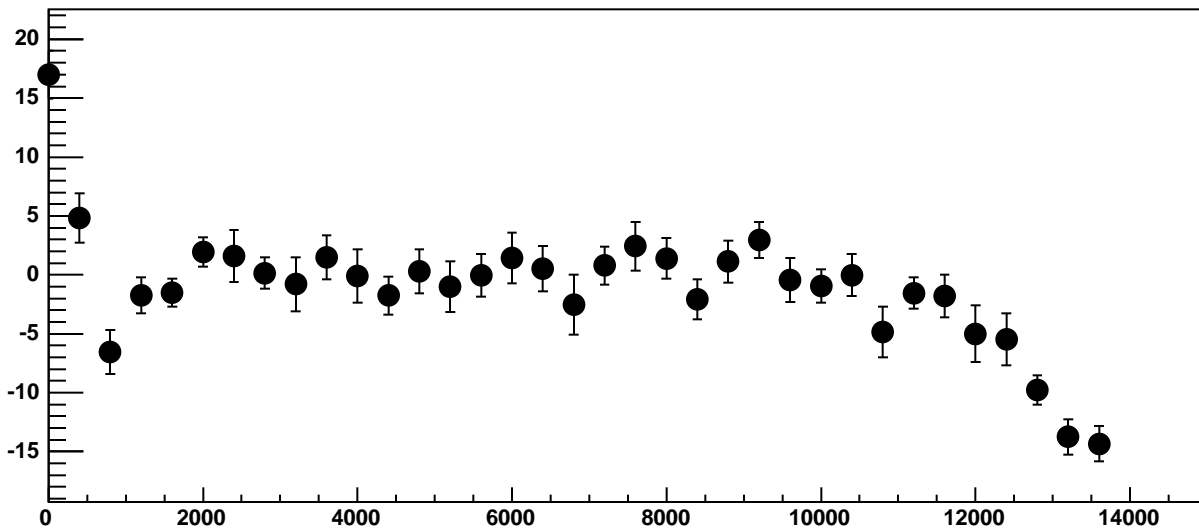
Chip 11, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC



Chip 11, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC

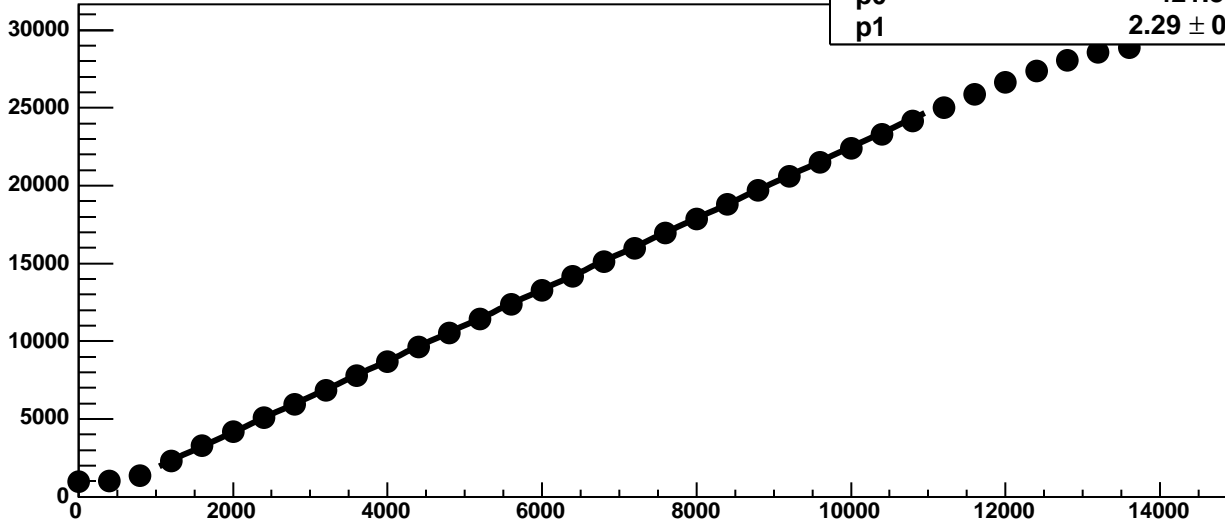


Chip 11, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

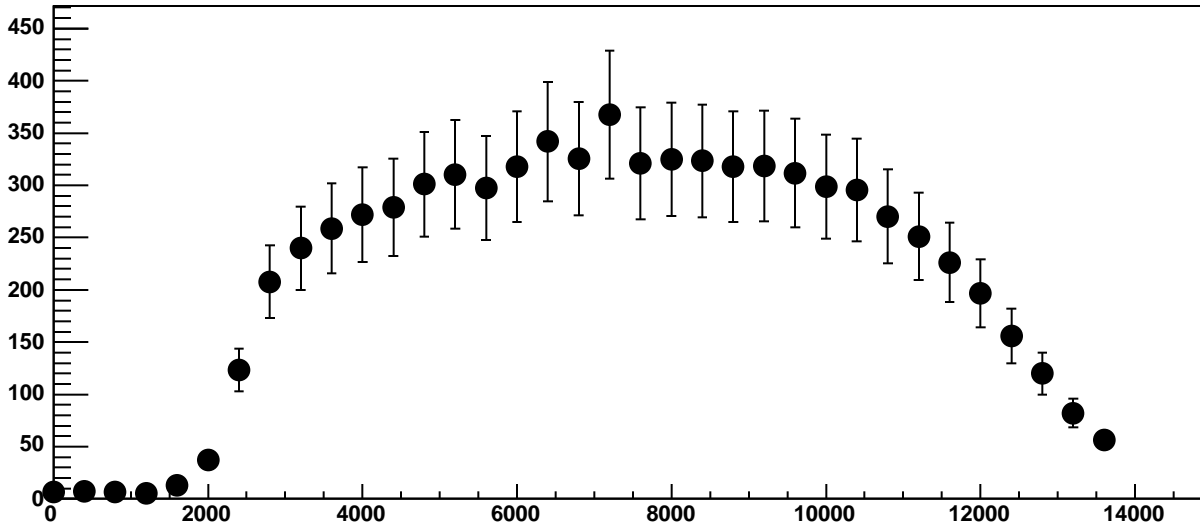


Chip 11, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC

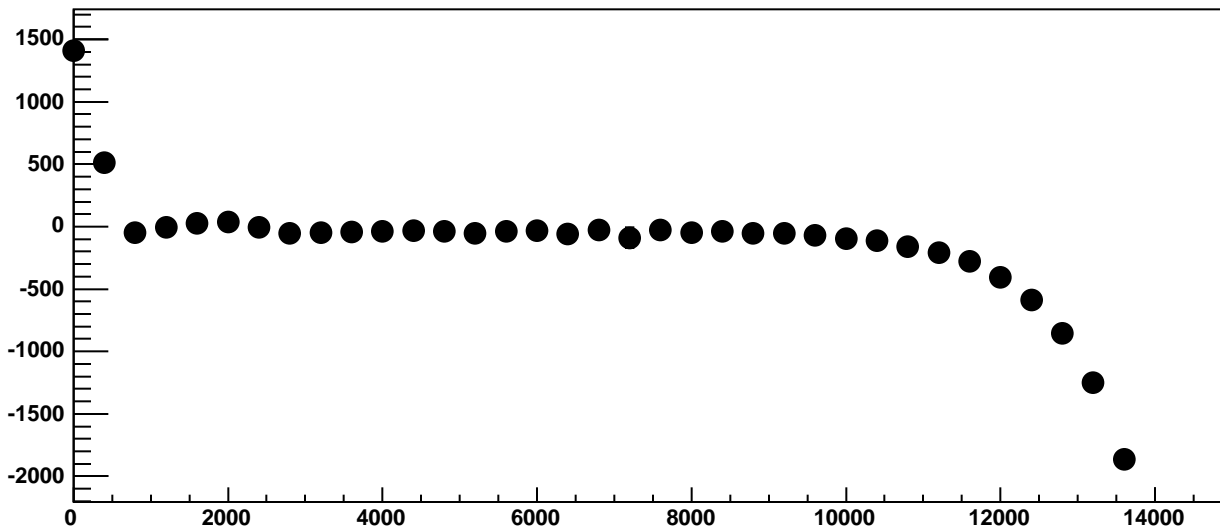
χ^2 / ndf 147.4 / 23
p0 -421.8 ± 3.324
p1 2.29 ± 0.002371



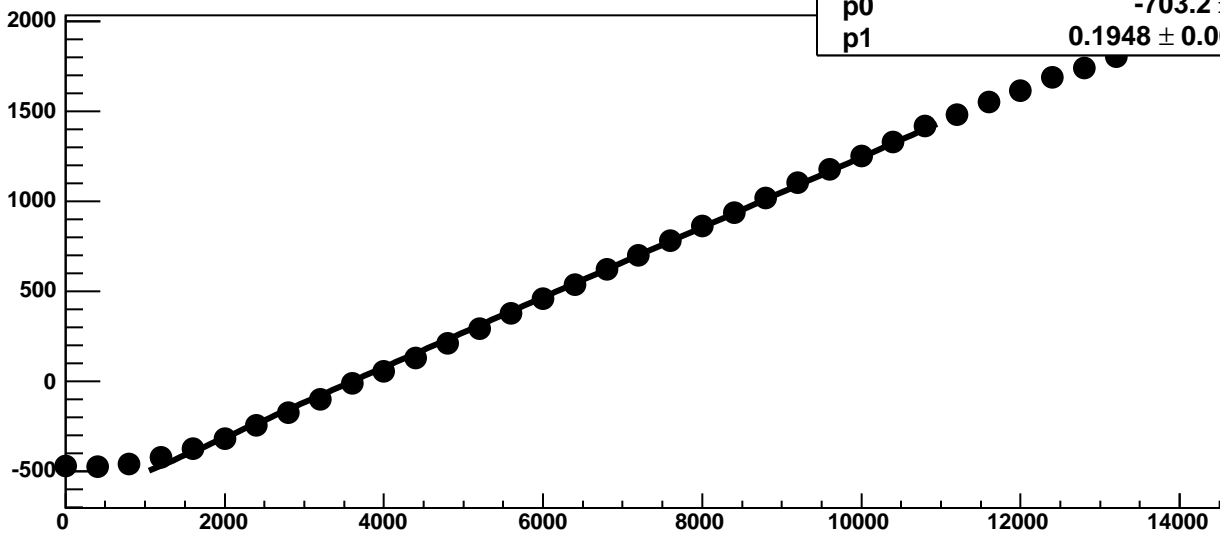
Chip 11, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



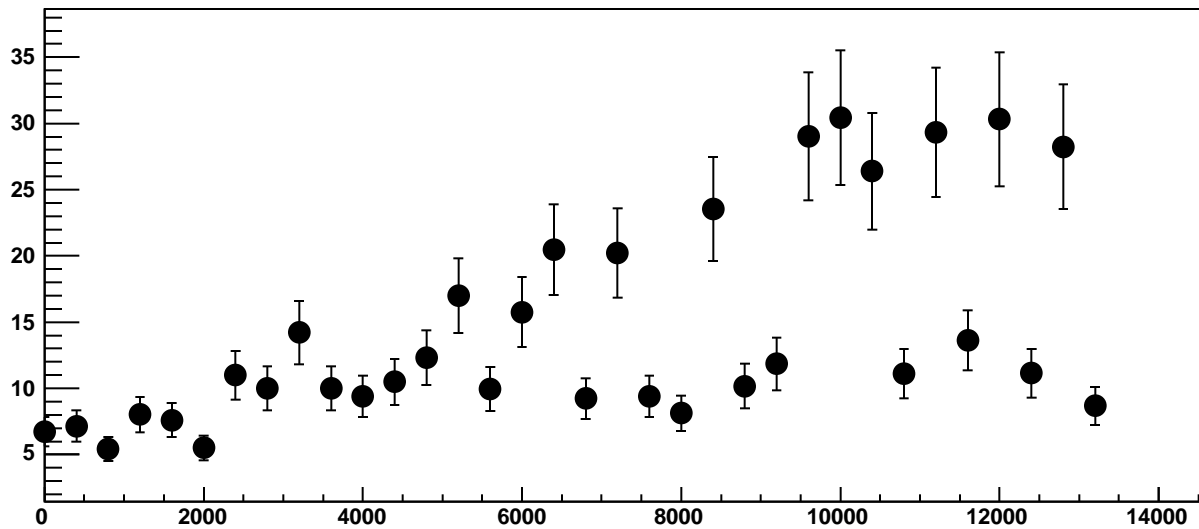
Chip 11, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC



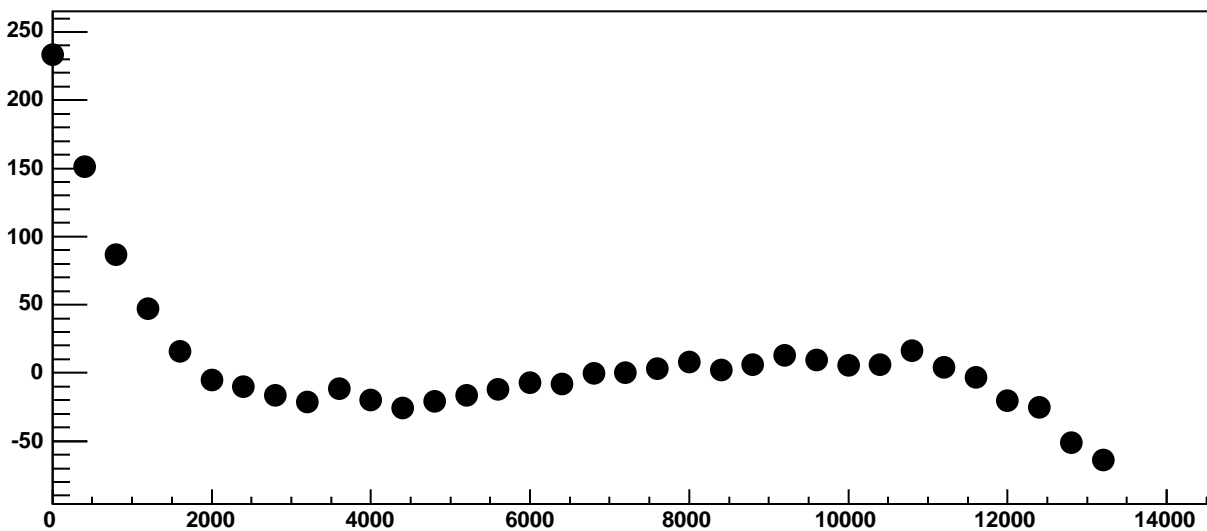
Chip 11, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



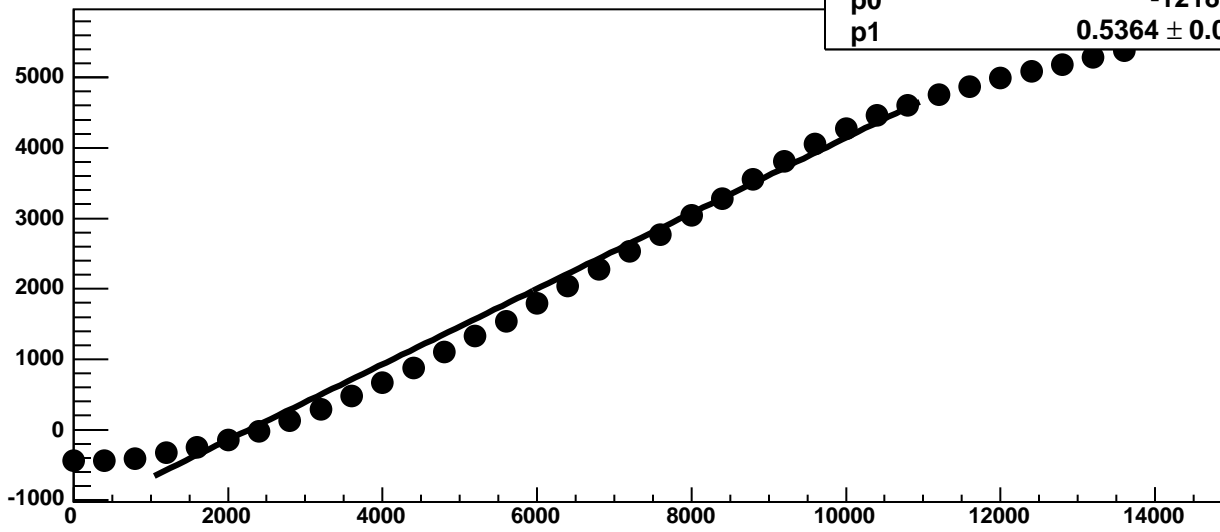
Chip 11, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



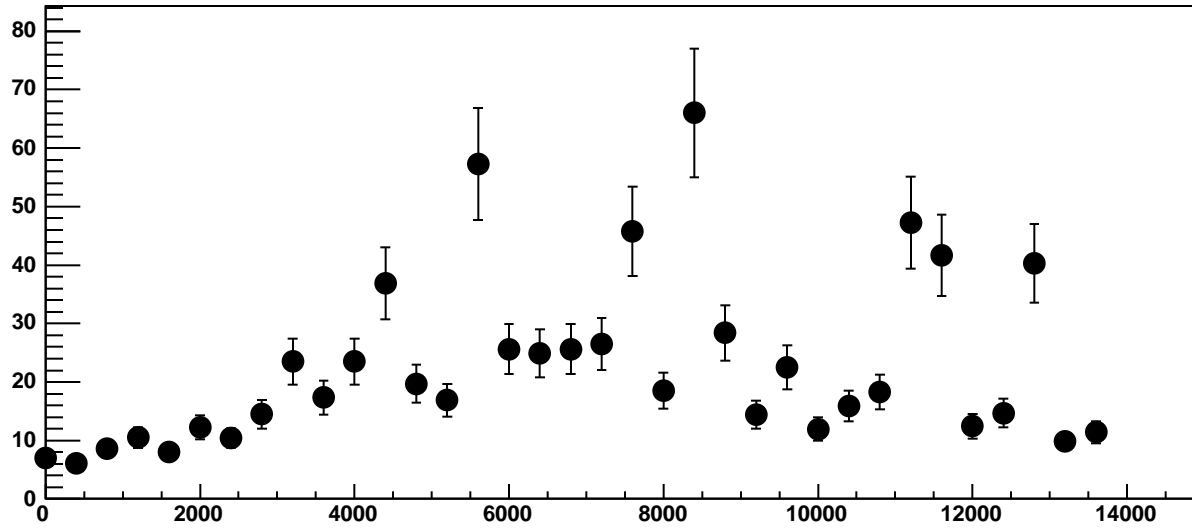
Chip 11, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



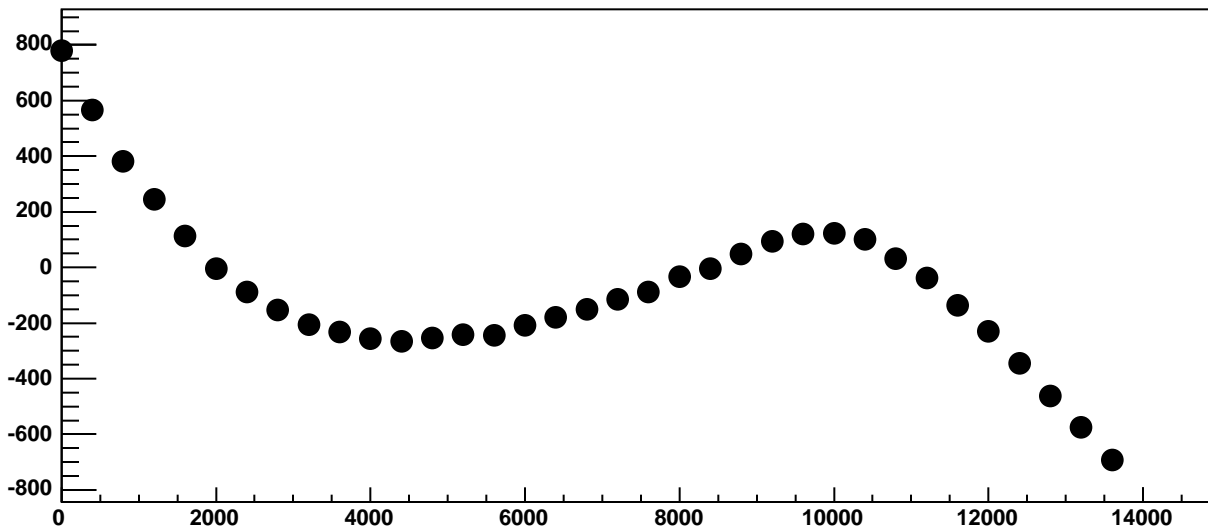
Chip 11, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



Chip 11, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC

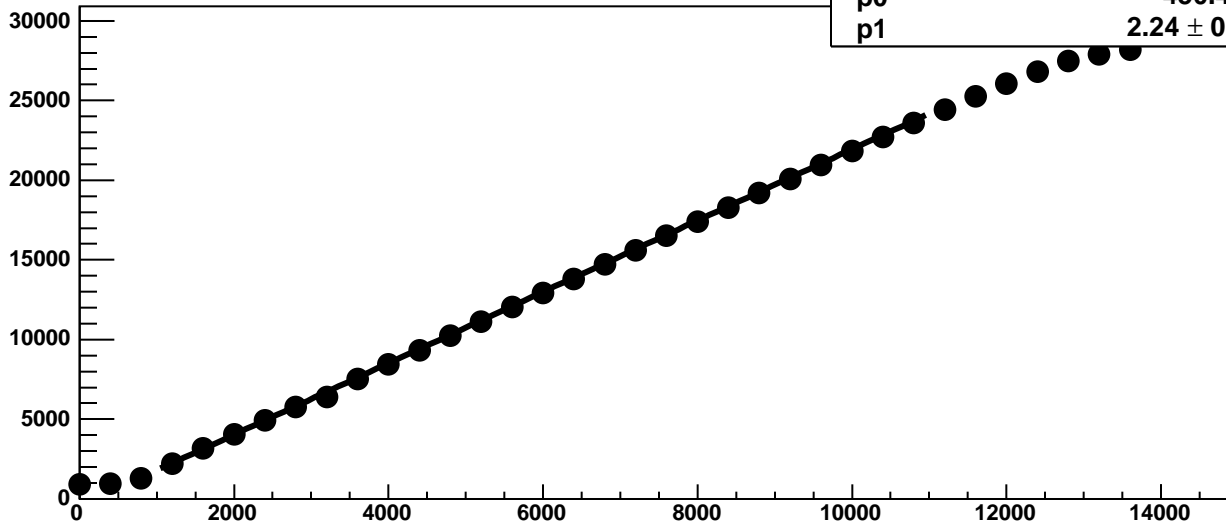


Chip 11, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

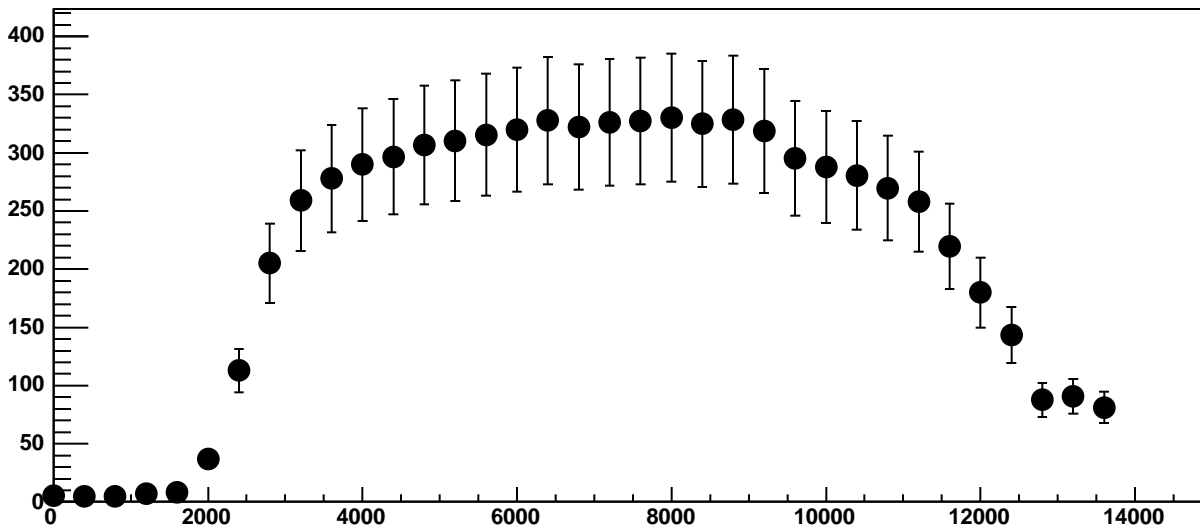


Chip 11, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC

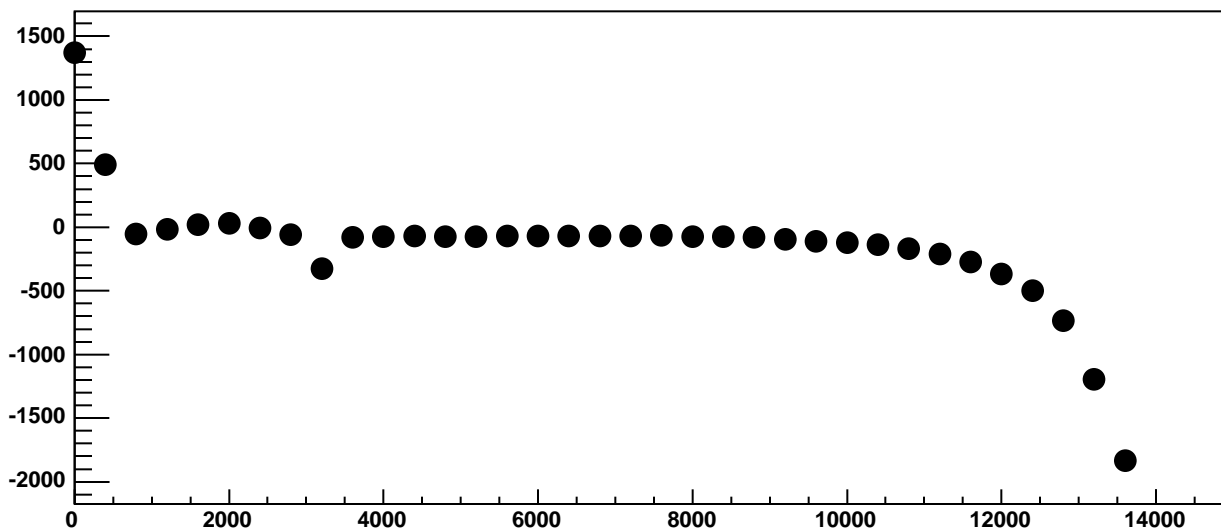
χ^2 / ndf 269.7 / 23
p0 -450.4 ± 3.53
p1 2.24 ± 0.002327



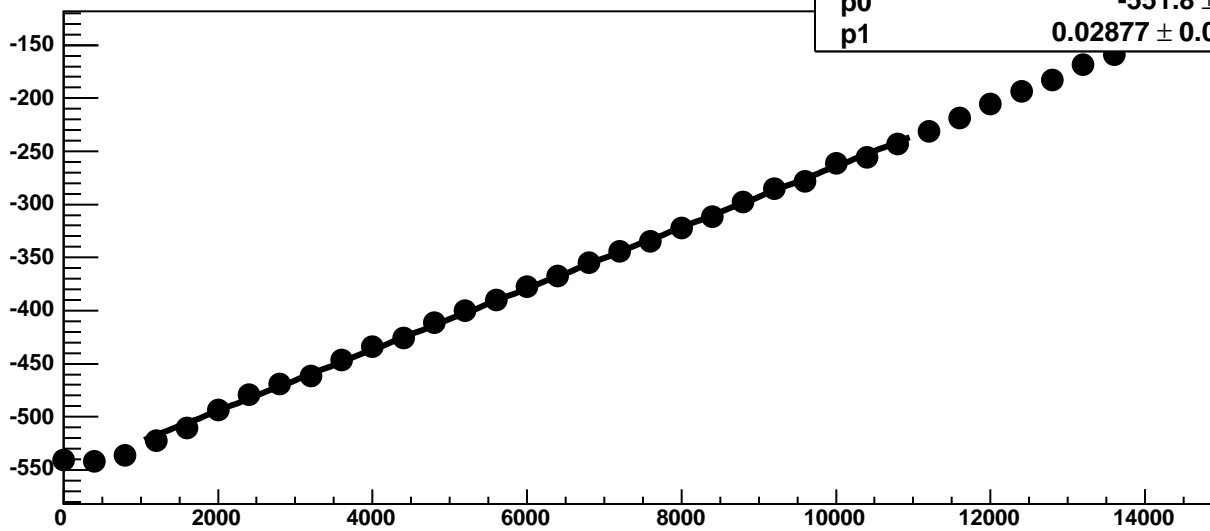
Chip 11, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC

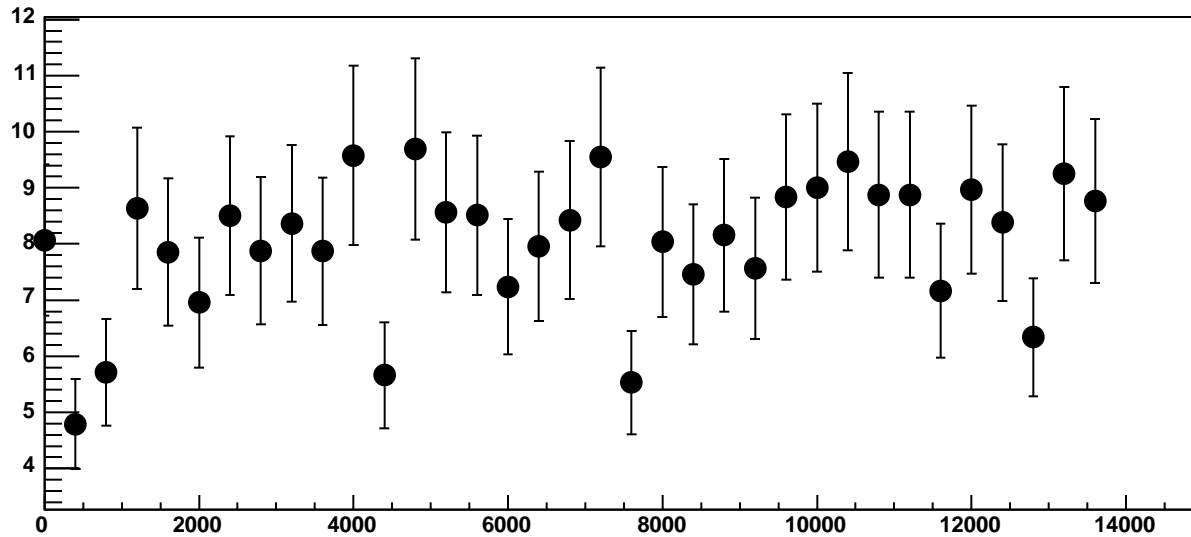


Chip 11, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

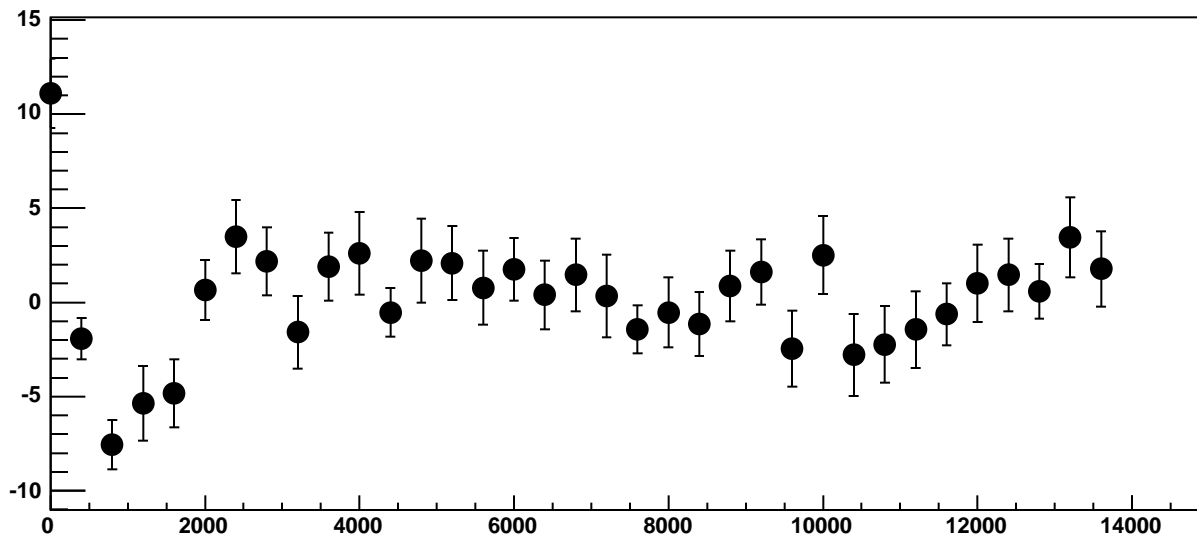


χ^2 / ndf 35.43 / 23
p0 -551.8 ± 0.8514
p1 0.02877 ± 0.0001304

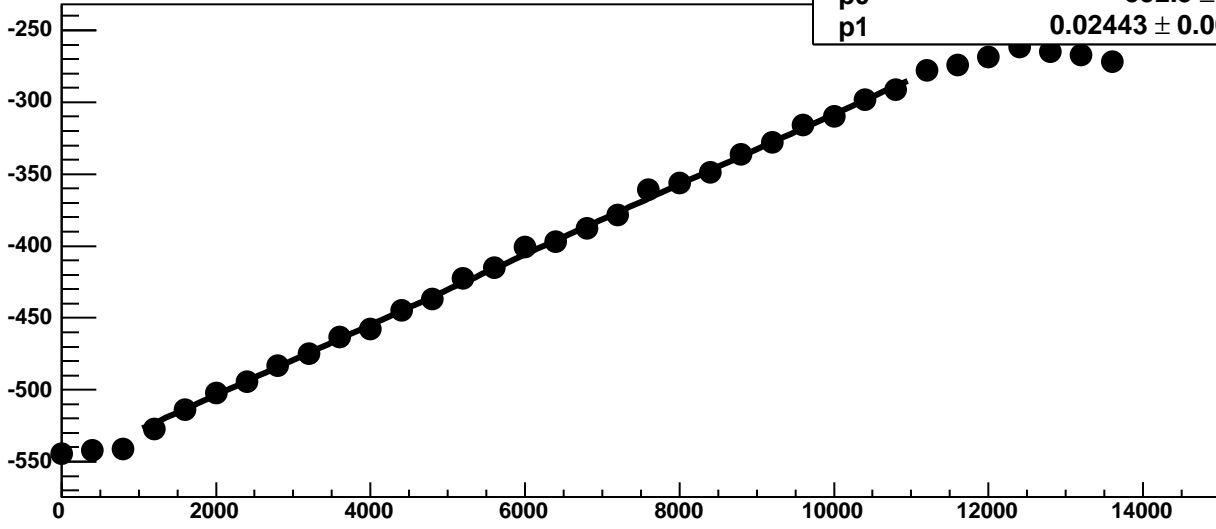
Chip 11, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



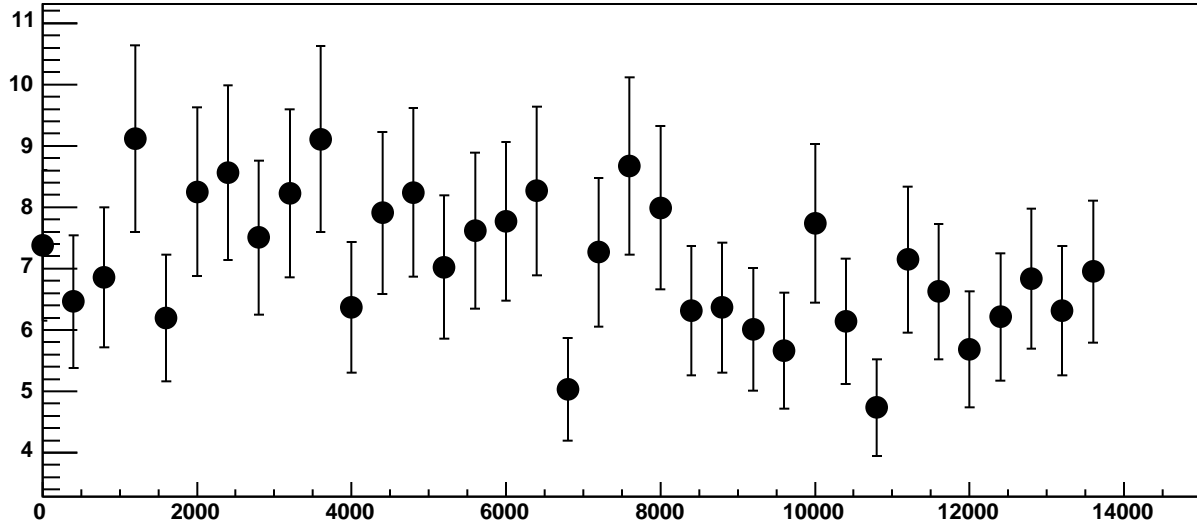
Chip 11, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



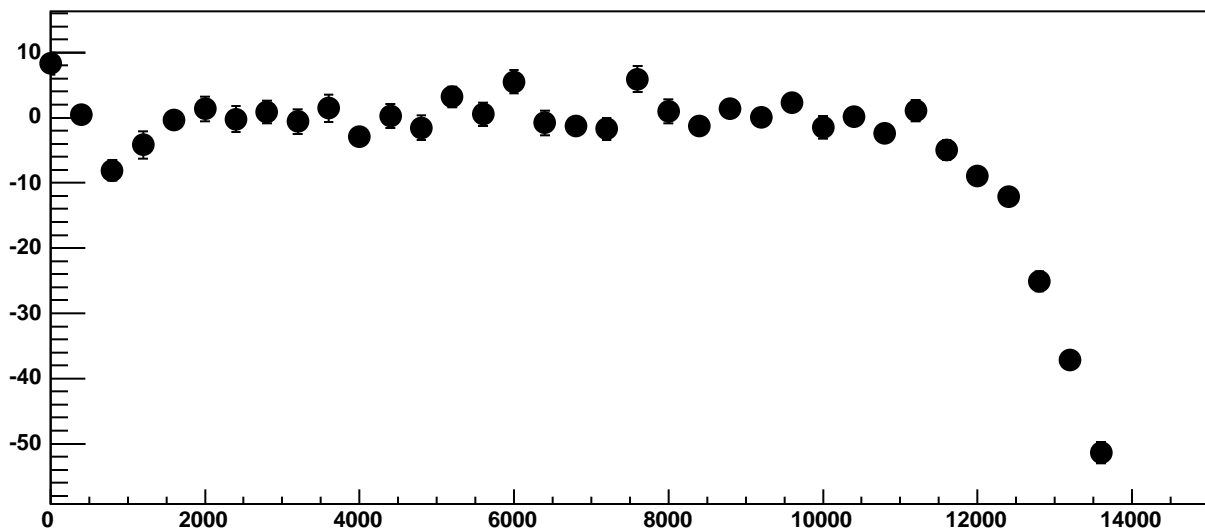
Chip 11, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



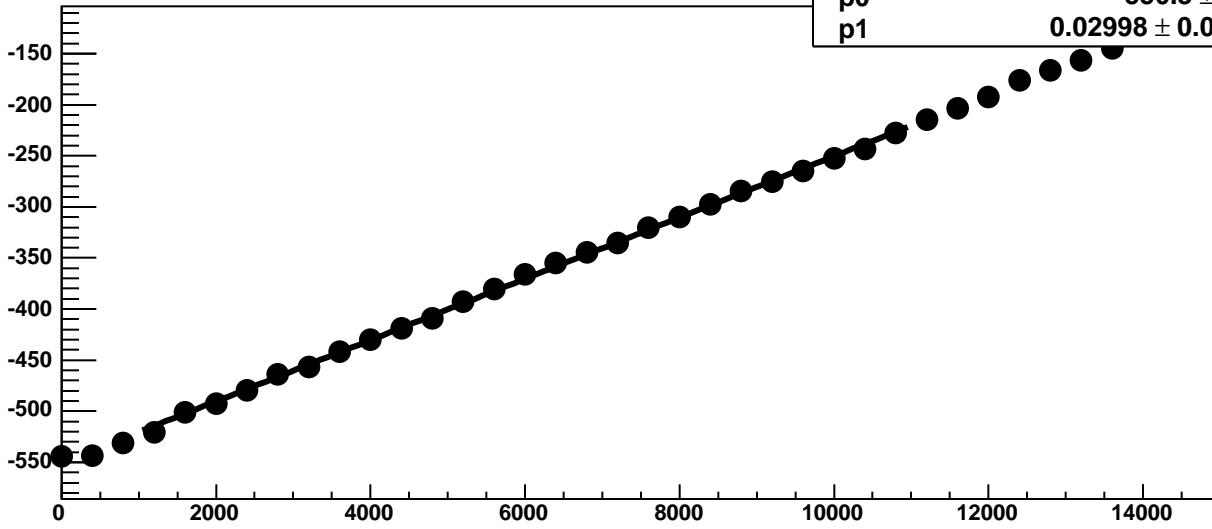
Chip 11, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

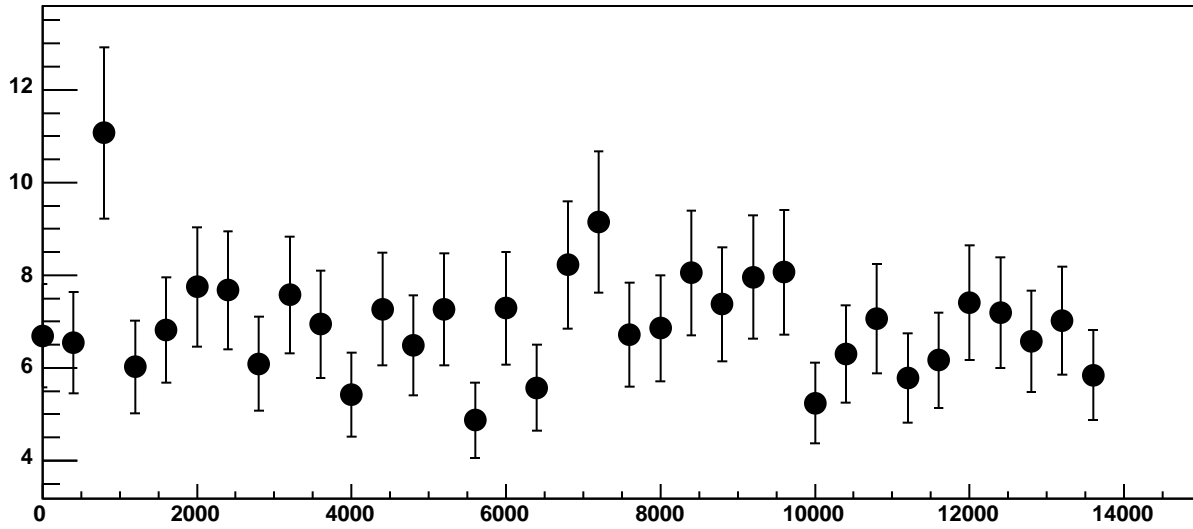


Chip 11, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

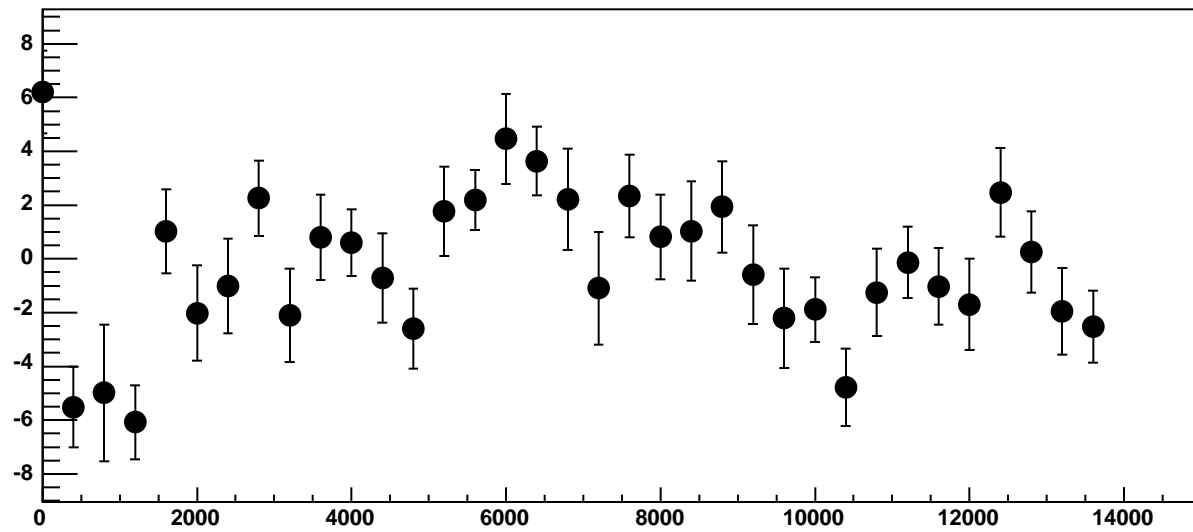


χ^2 / ndf 70.77 / 23
p0 -550.3 ± 0.7059
p1 0.02998 ± 0.0001069

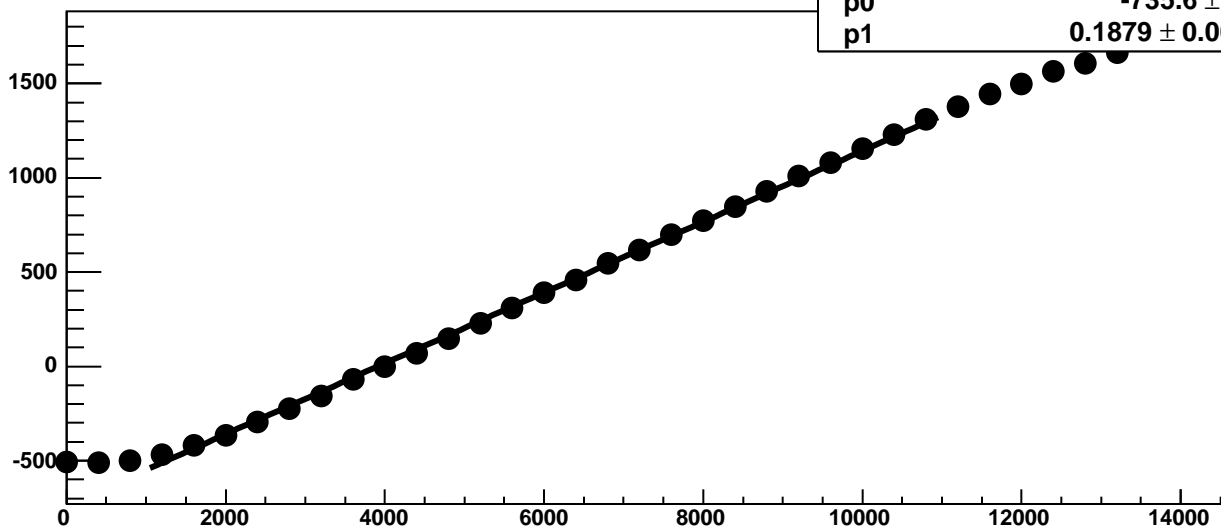
Chip 11, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



Chip 11, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC

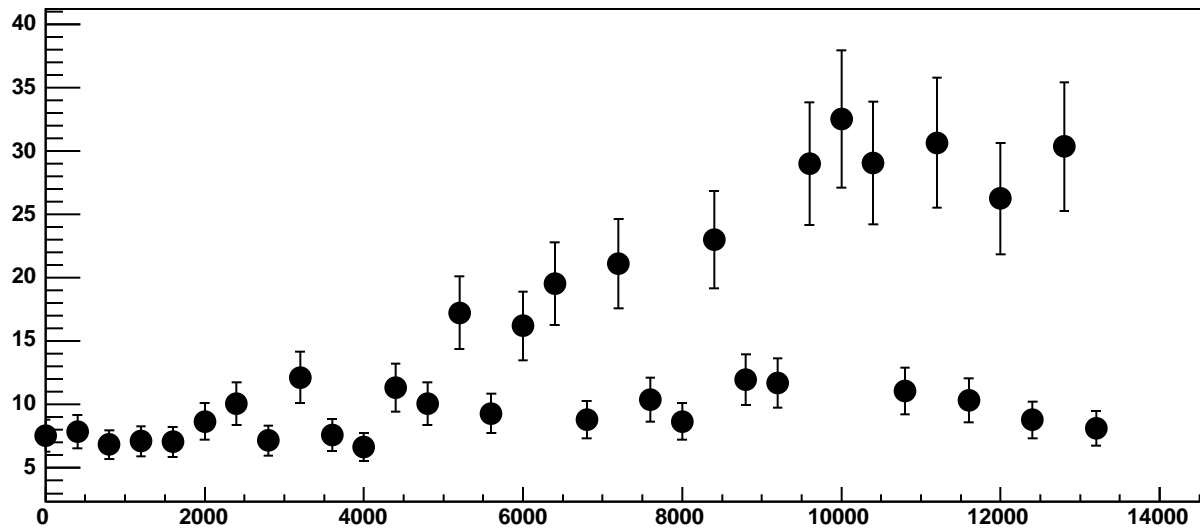


Chip 11, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

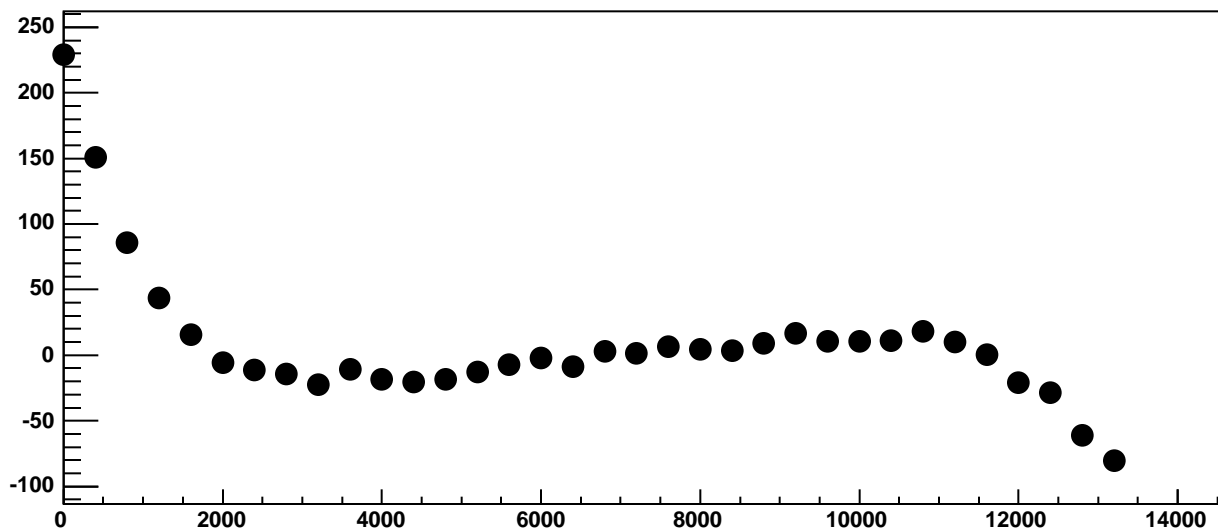


χ^2 / ndf 1440 / 23
p0 -735.6 ± 0.9437
p1 0.1879 ± 0.0001755

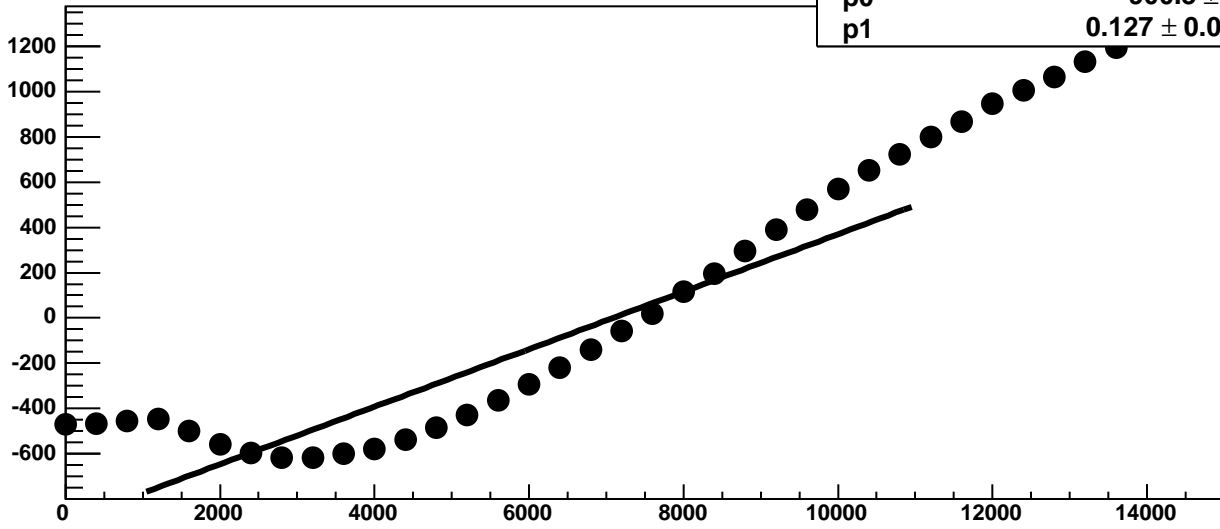
Chip 11, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



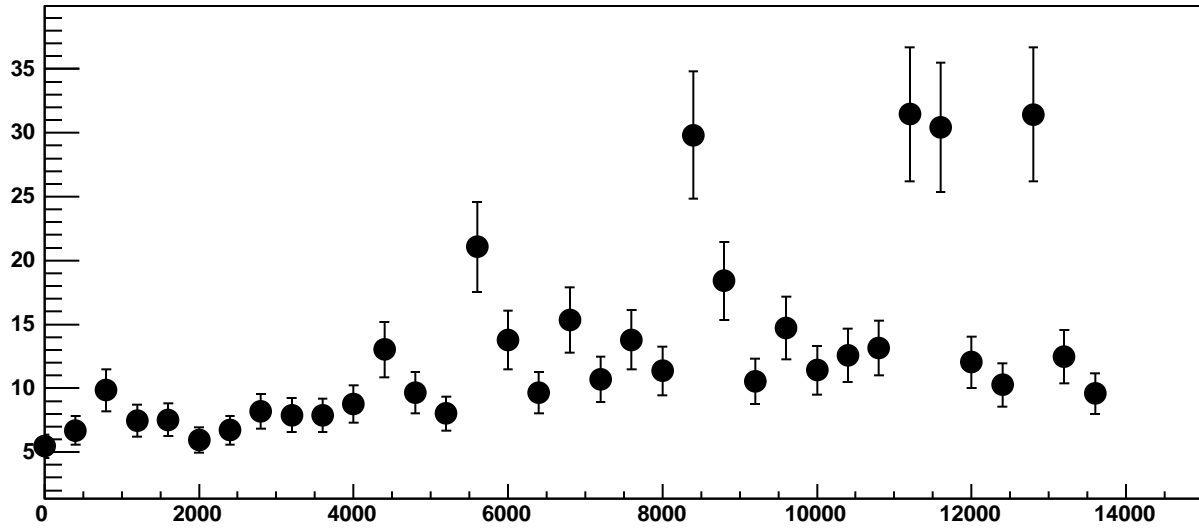
Chip 11, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



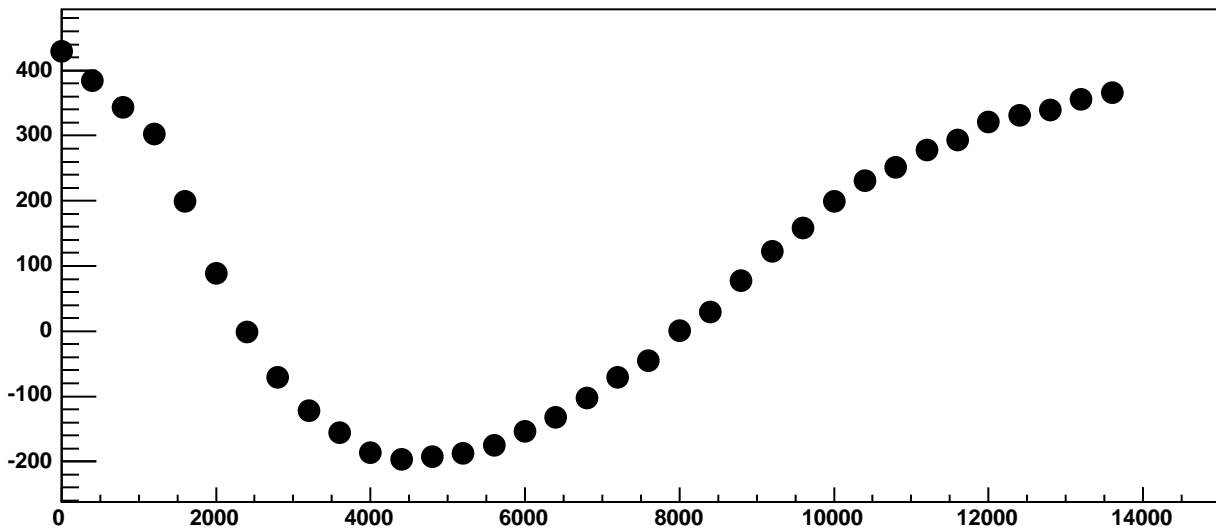
Chip 11, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



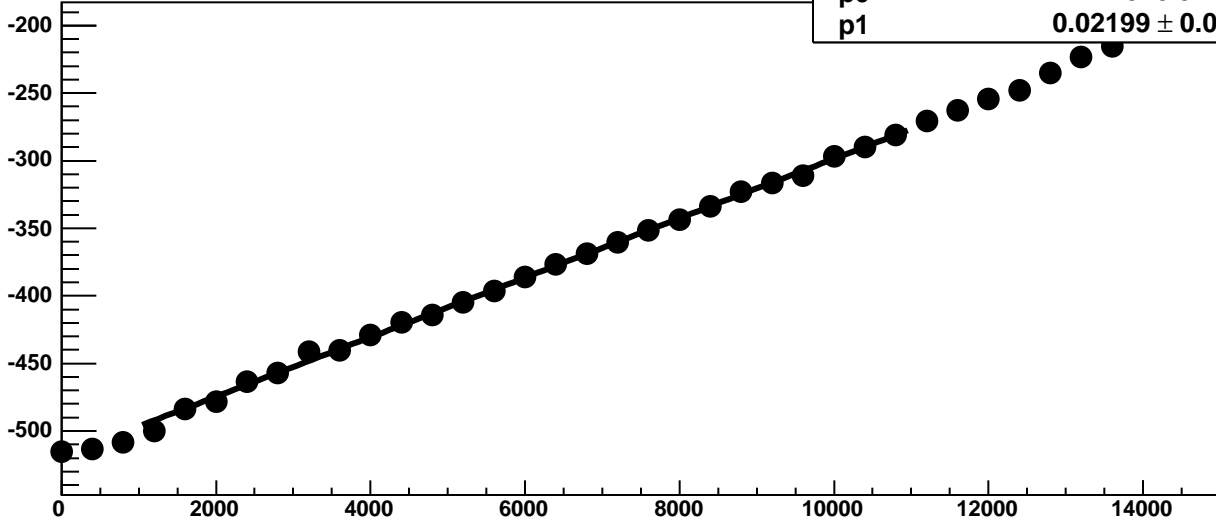
Chip 11, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



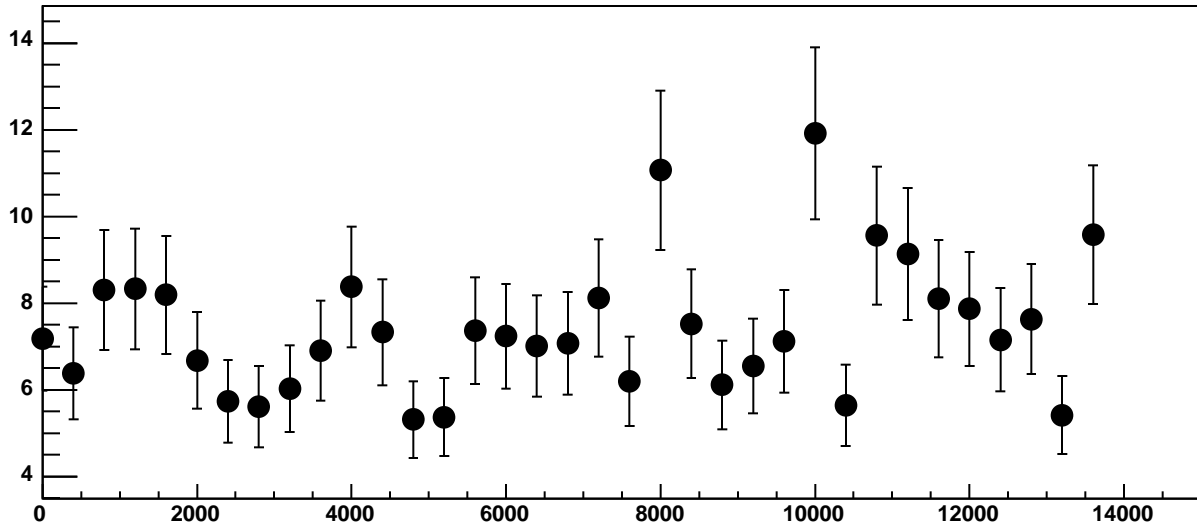
Chip 11, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



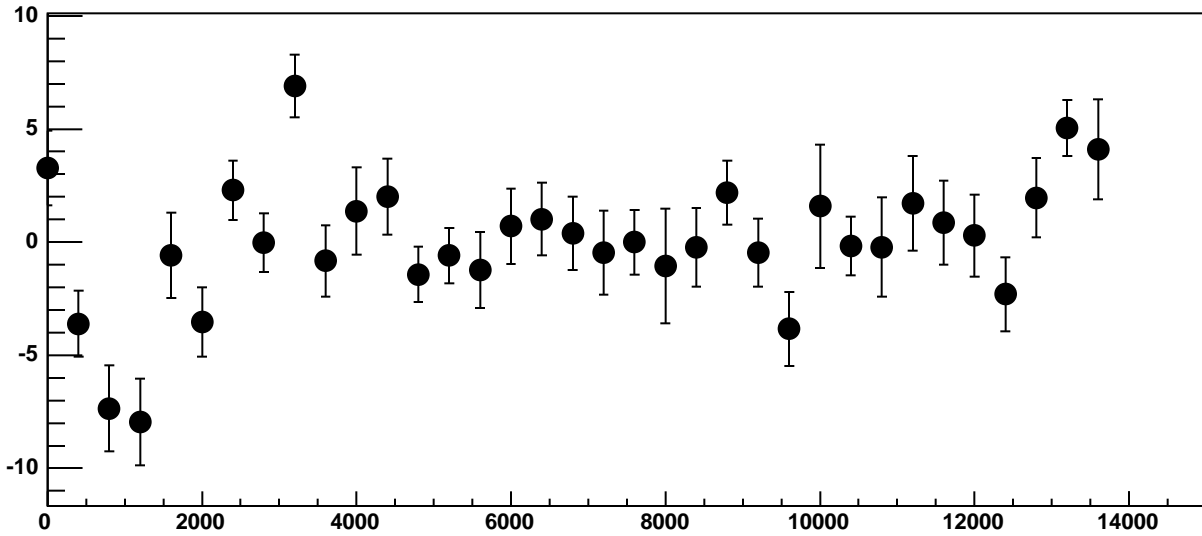
Chip 11, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC



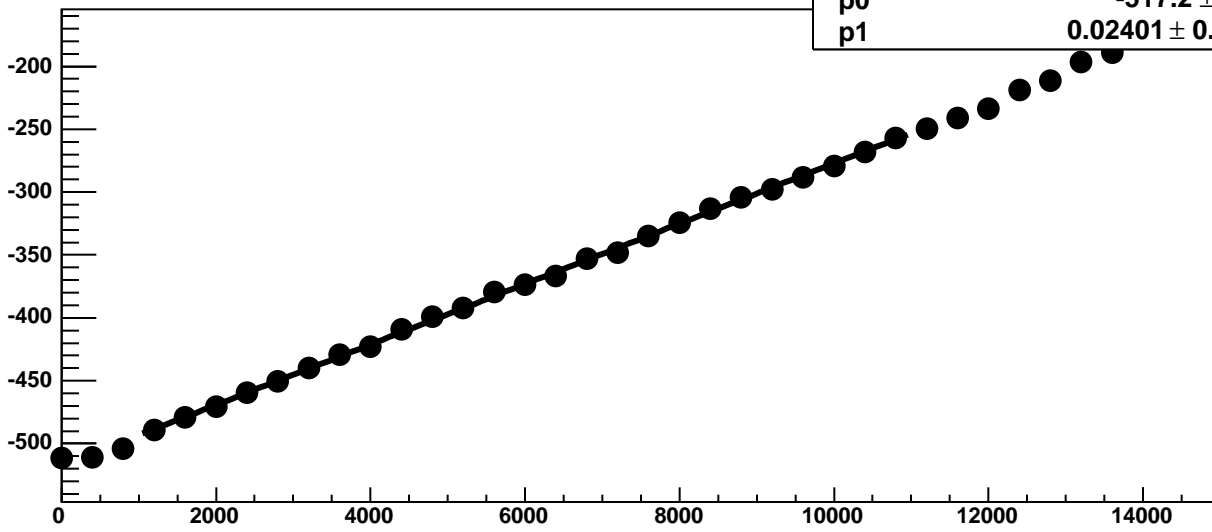
Chip 11, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

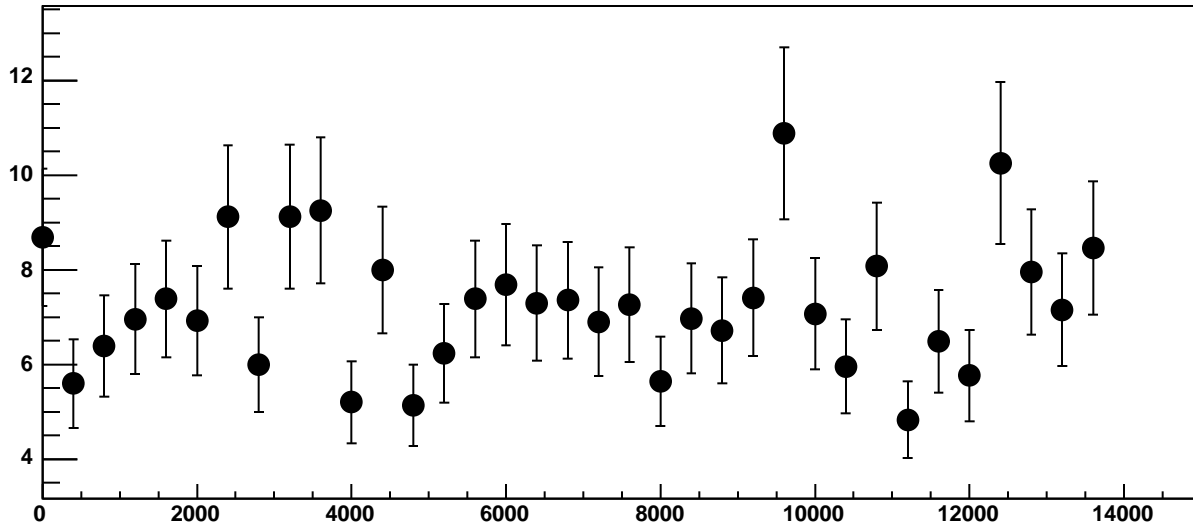


Chip 11, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

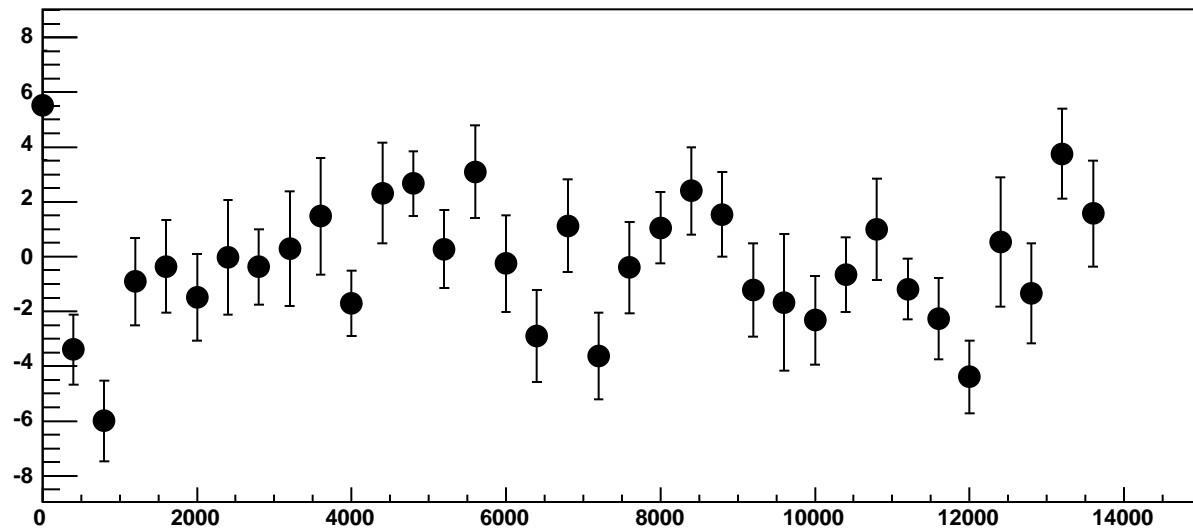


χ^2 / ndf 30.18 / 23
p0 -517.2 ± 0.7526
p1 0.02401 ± 0.000114

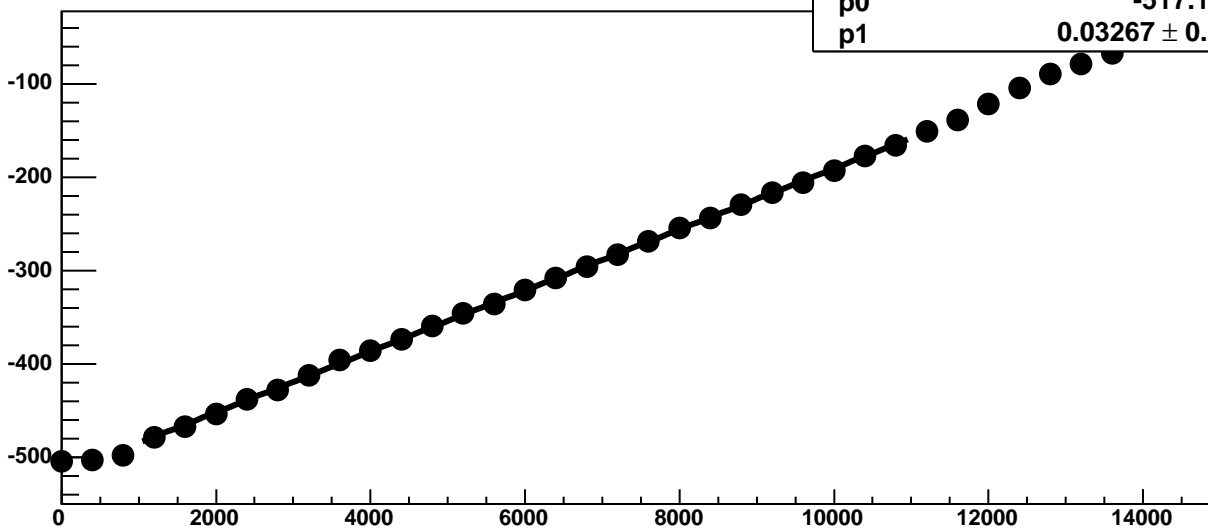
Chip 11, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



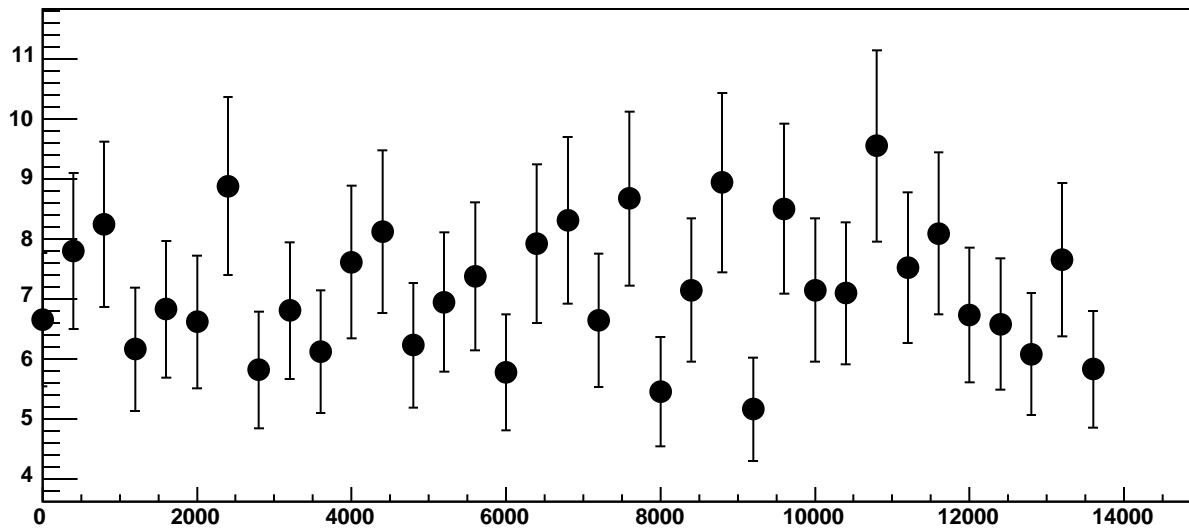
Chip 11, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



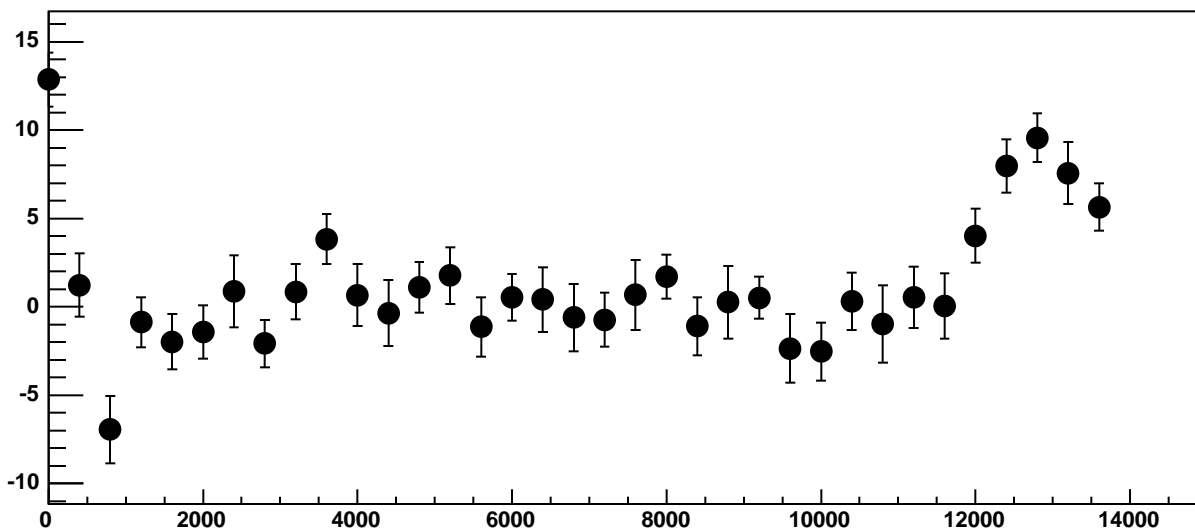
Chip 11, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



Chip 11, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC

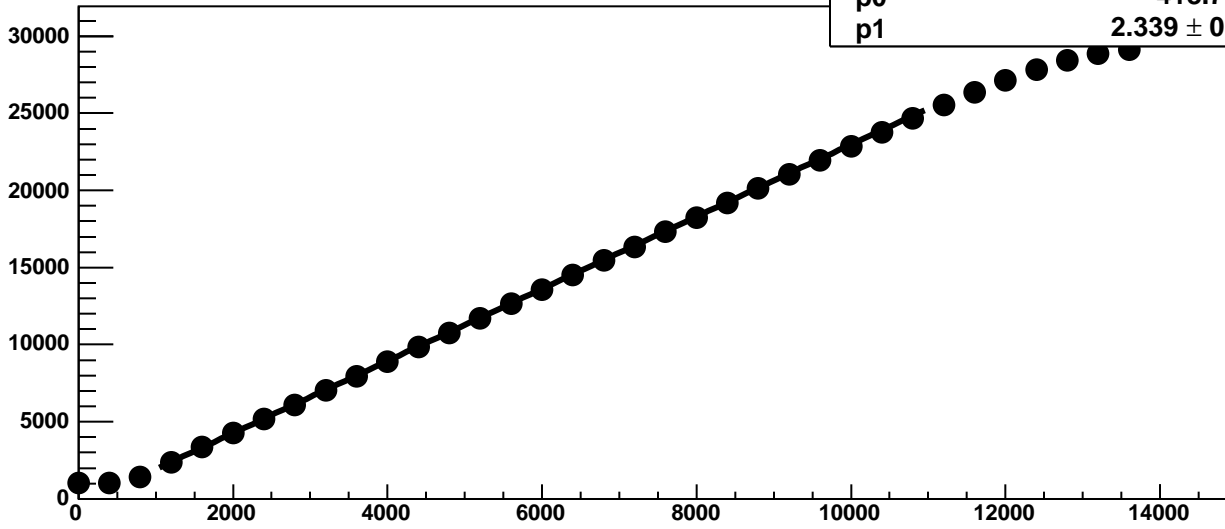


Chip 11, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

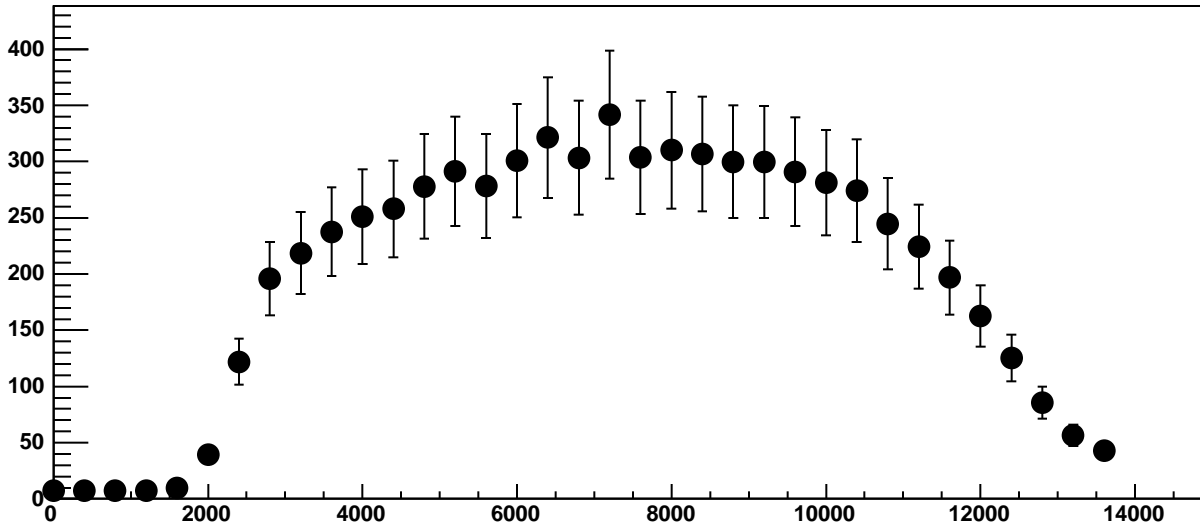


Chip 11, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC

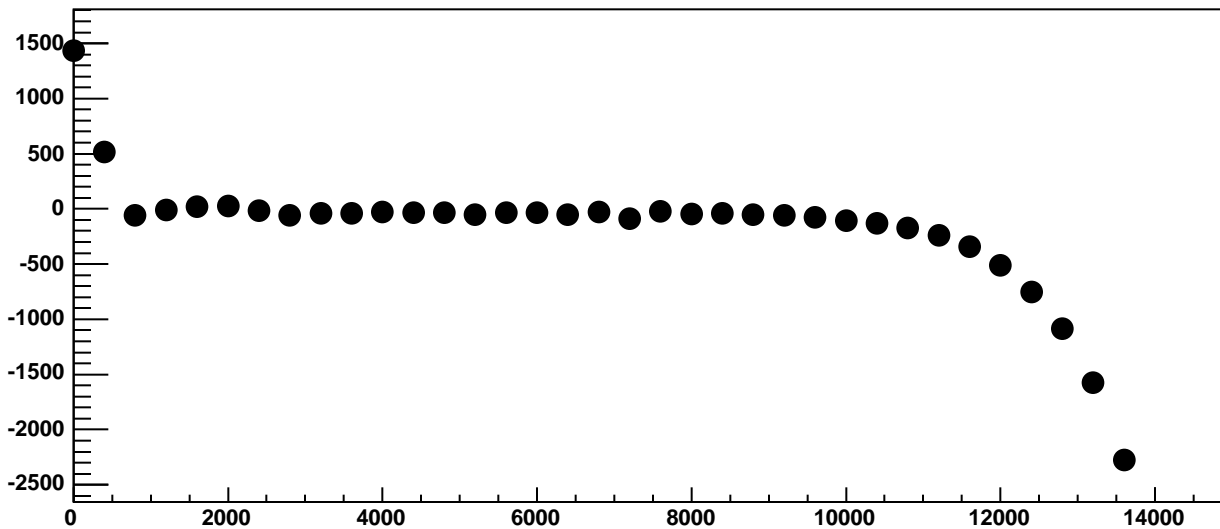
χ^2 / ndf 185.3 / 23
p0 -418.7 ± 3.405
p1 2.339 ± 0.002234



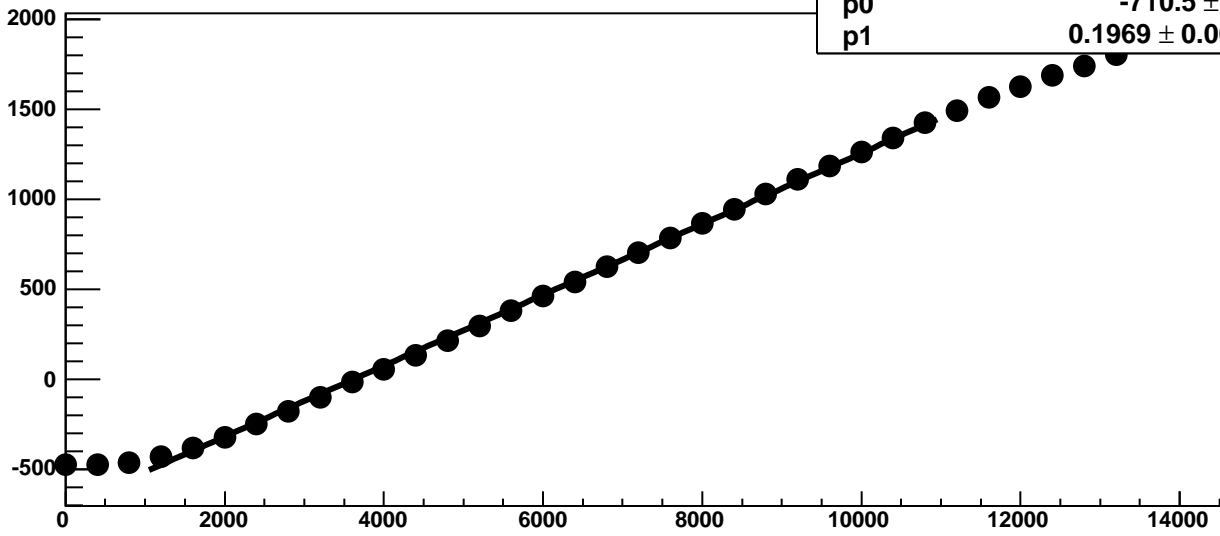
Chip 11, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC



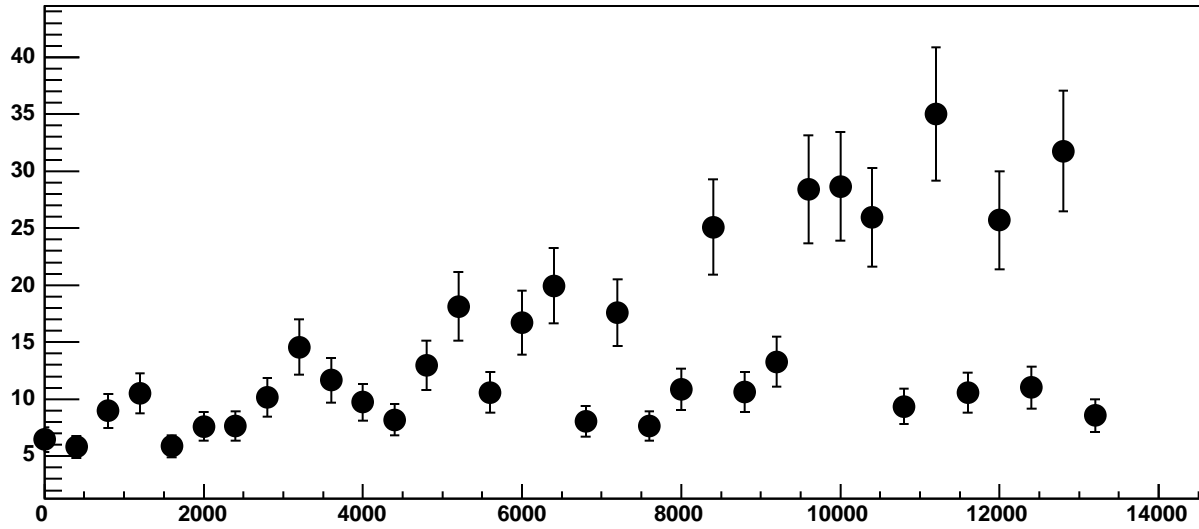
Chip 11, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC



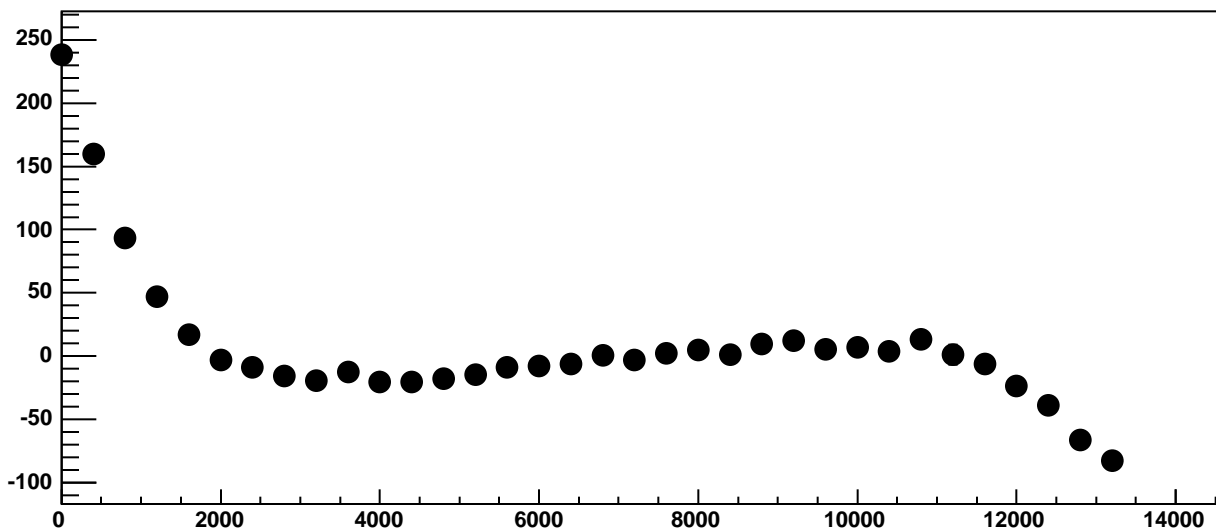
Chip 11, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



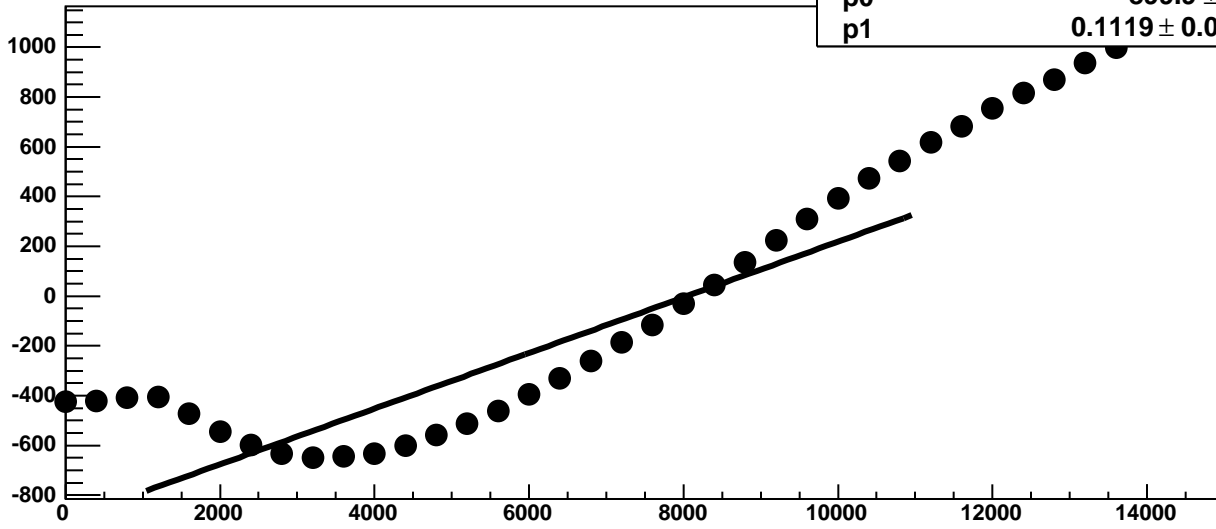
Chip 11, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 11, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

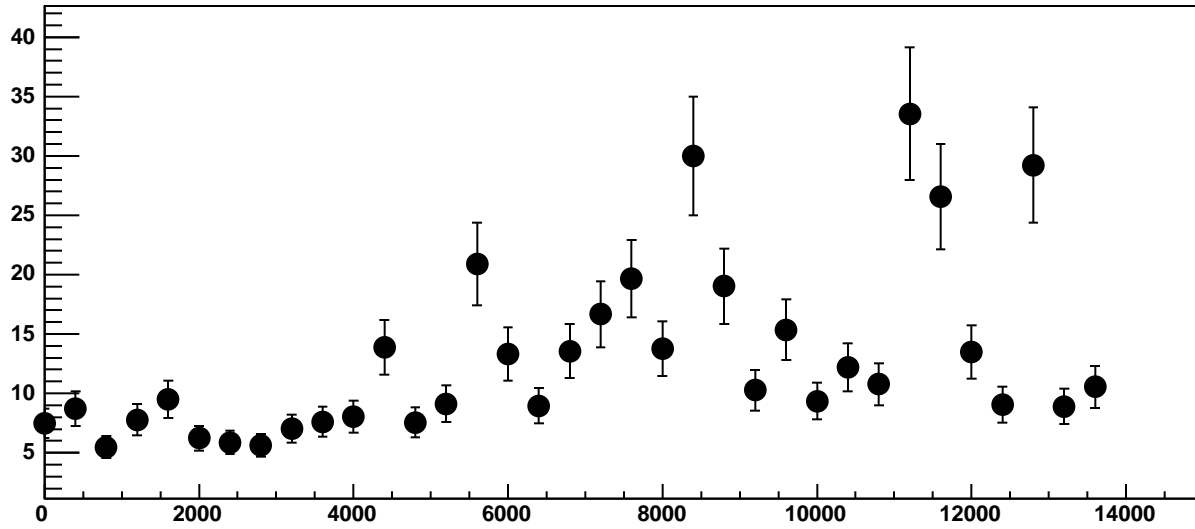


Chip 11, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

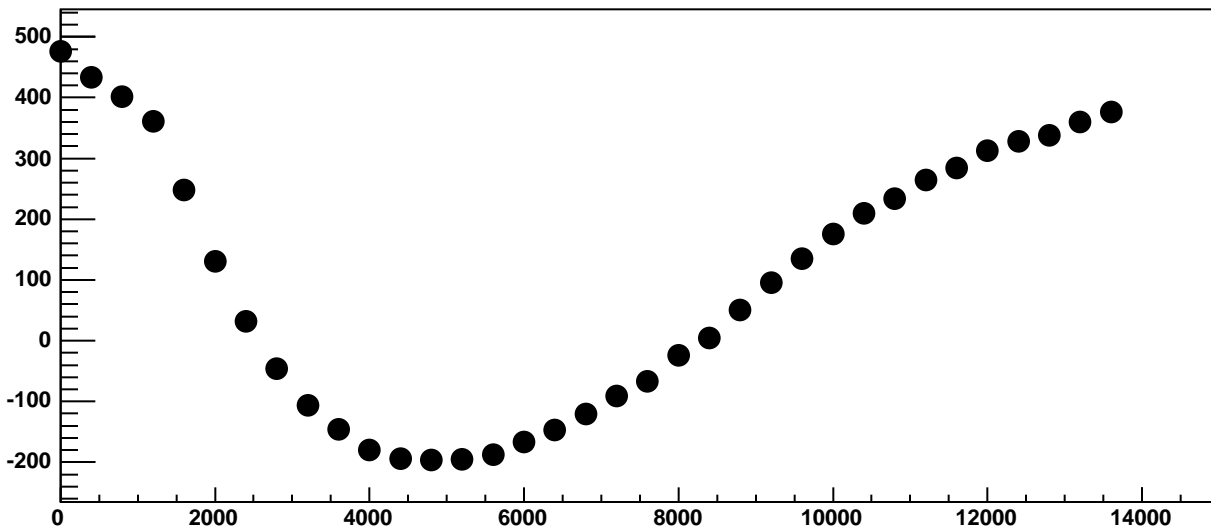


χ^2 / ndf 1.47e+05 / 23
p0 -899.9 ± 0.8238
p1 0.1119 ± 0.0001532

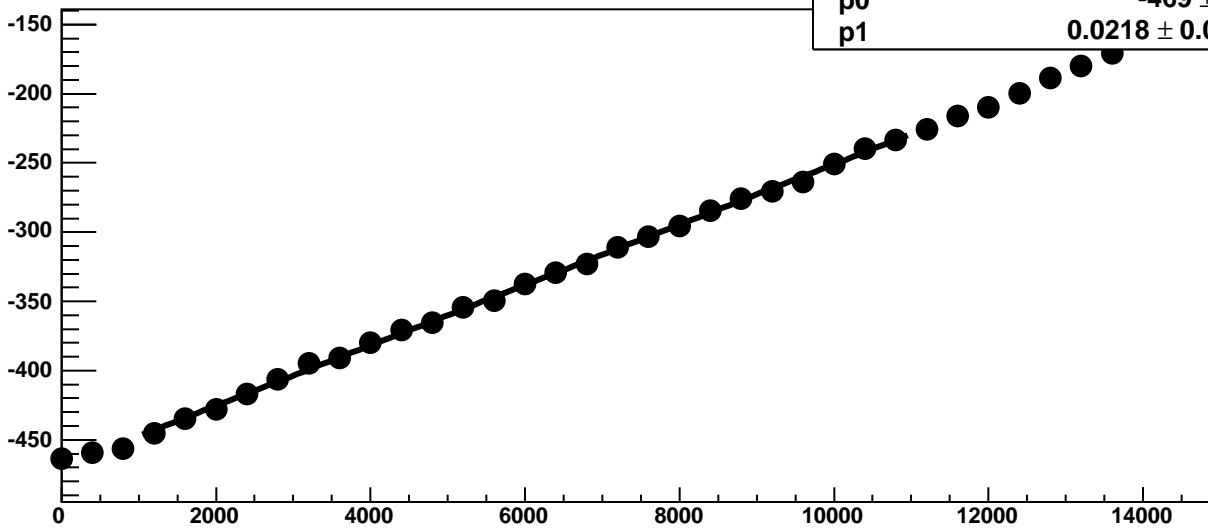
Chip 11, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

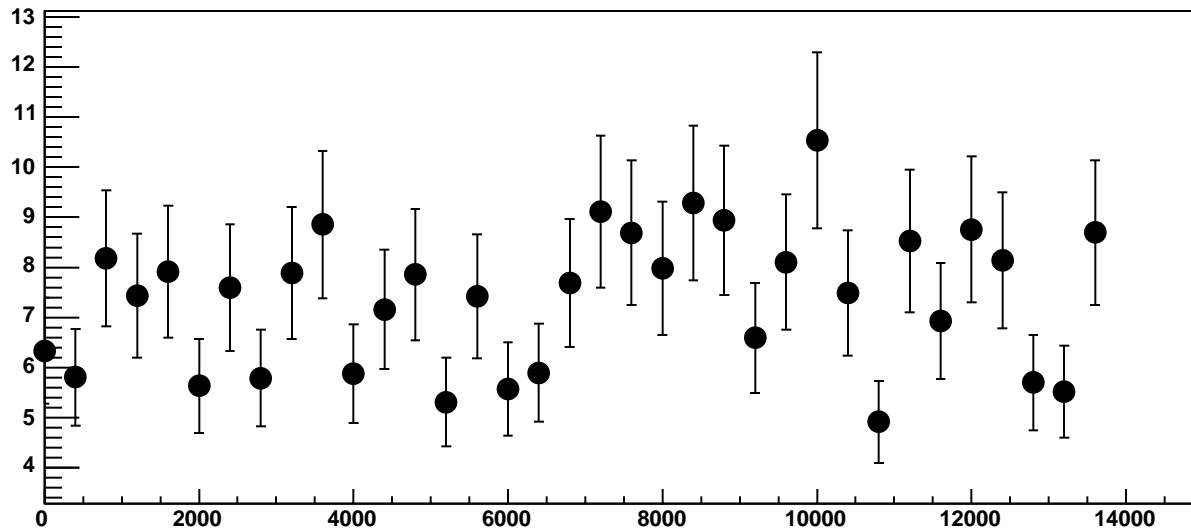


Chip 11, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

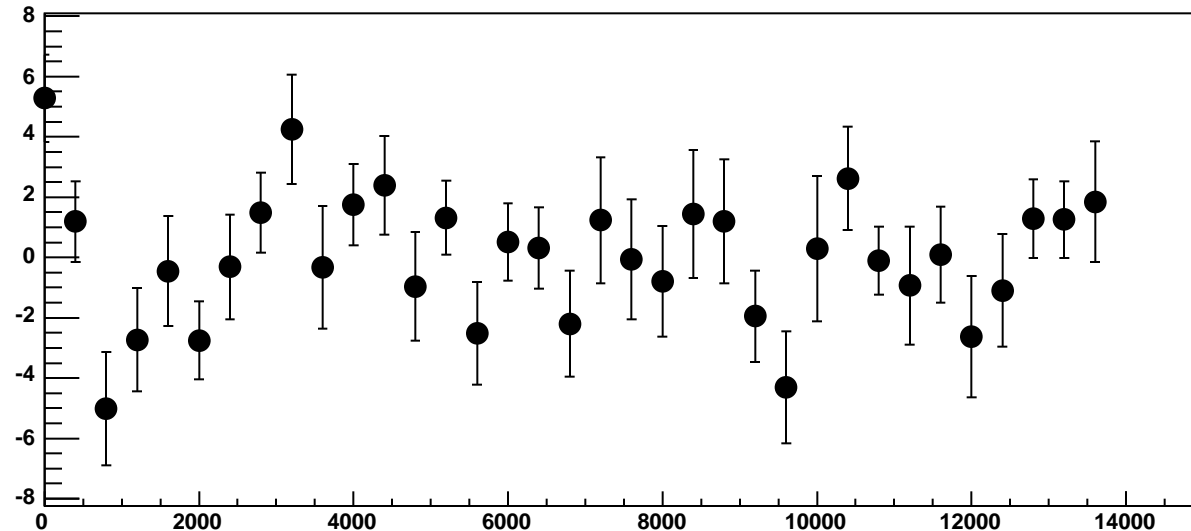


χ^2 / ndf 33.85 / 23
p0 -469 ± 0.7226
p1 0.0218 ± 0.0001102

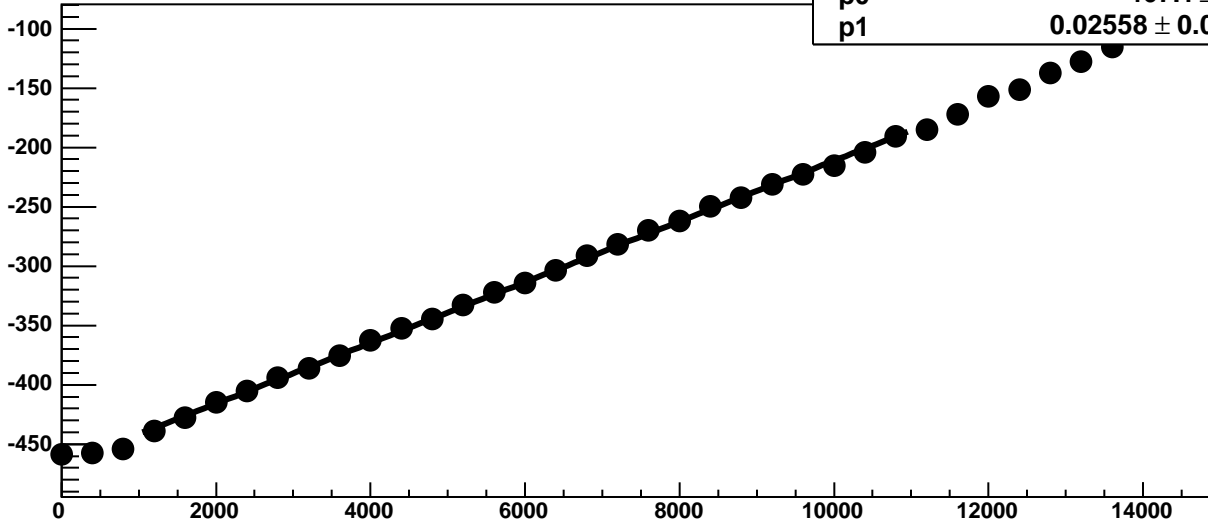
Chip 11, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

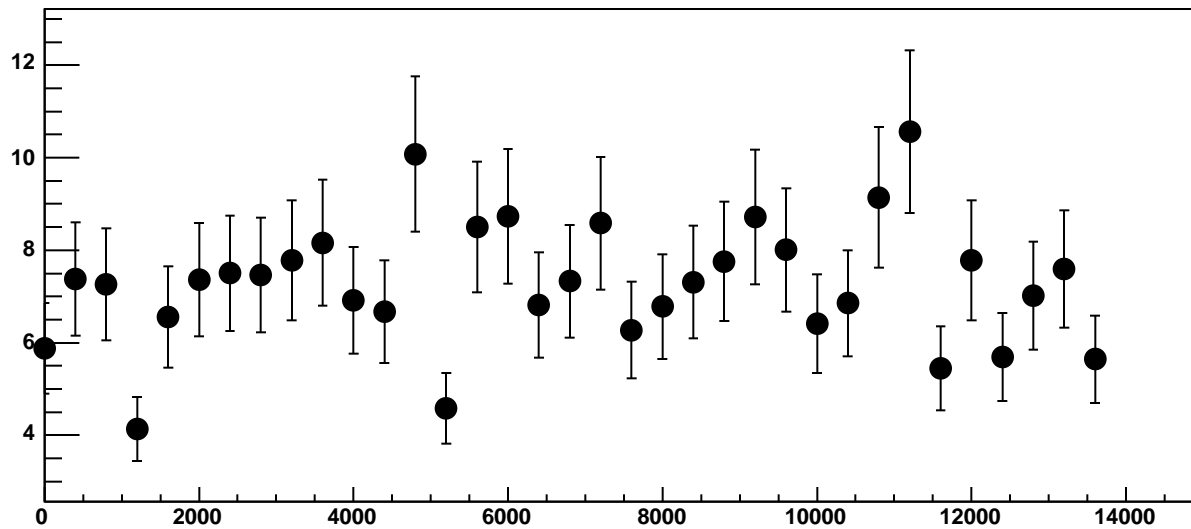


Chip 11, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC

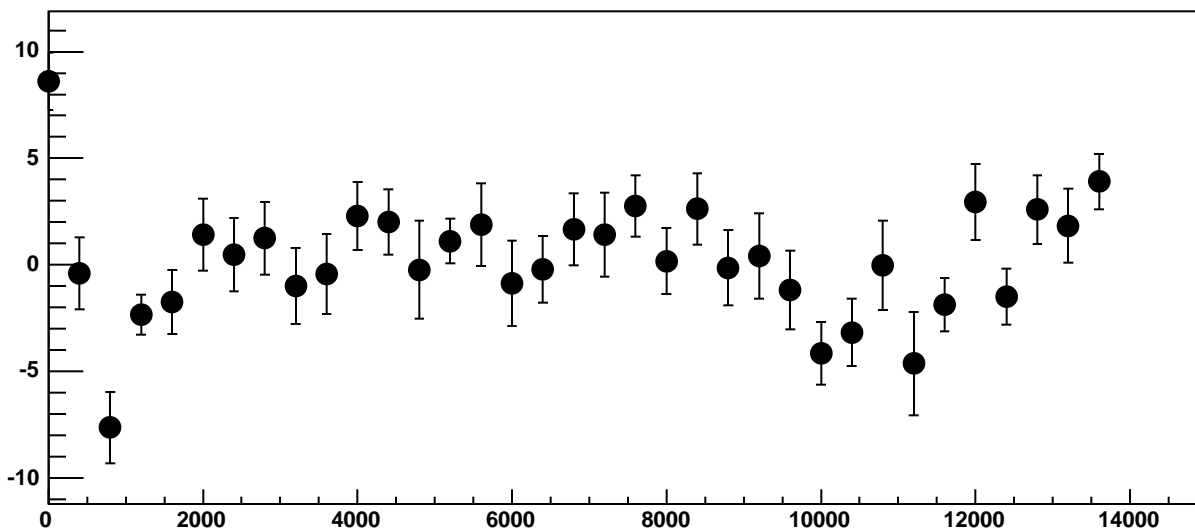


χ^2 / ndf 35.34 / 23
p0 -467.1 ± 0.6714
p1 0.02558 ± 0.0001064

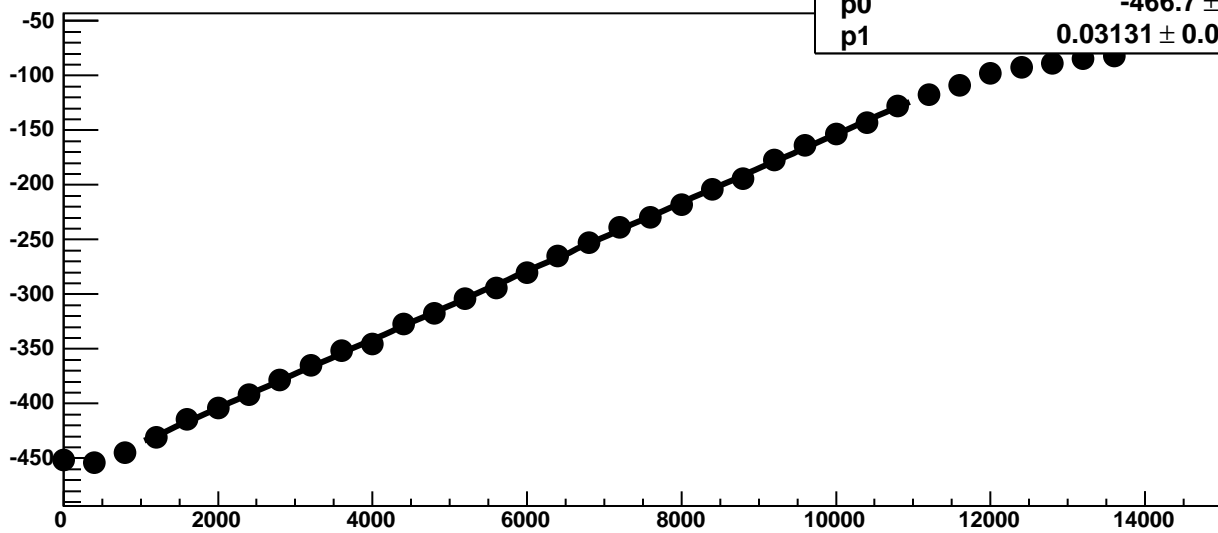
Chip 11, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



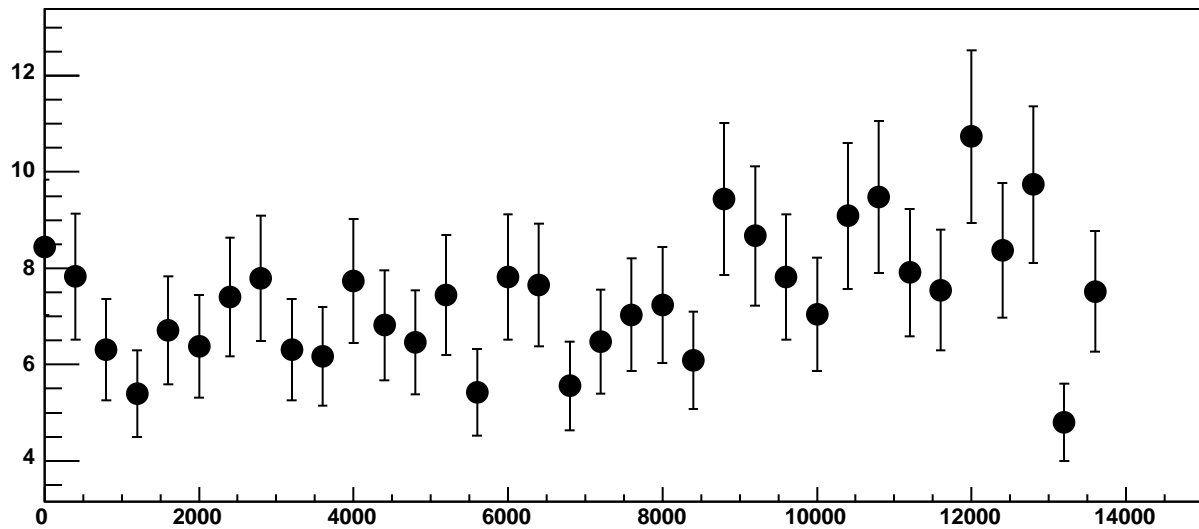
Chip 11, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC



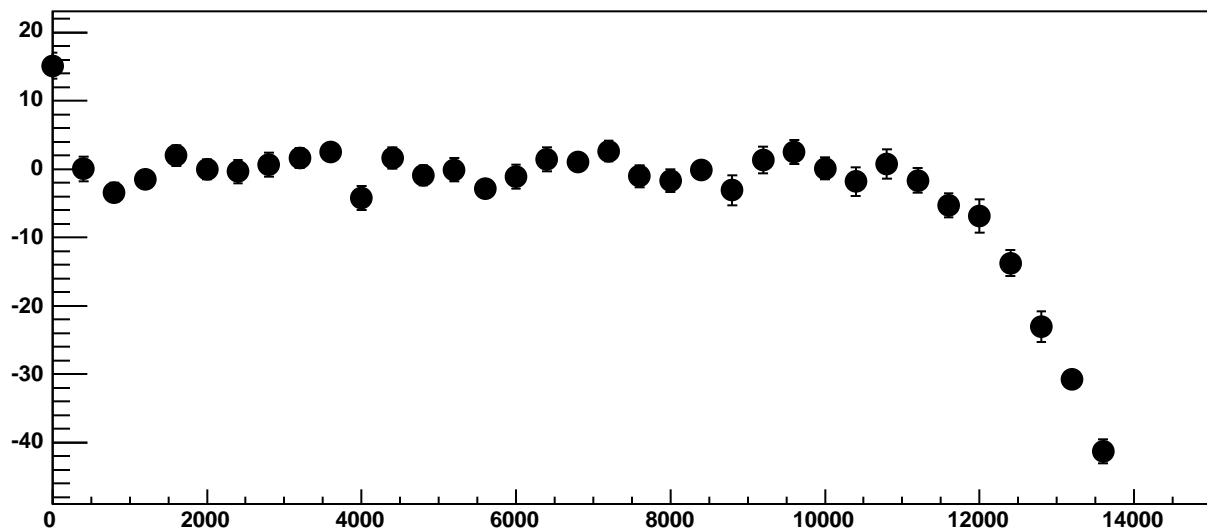
Chip 11, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



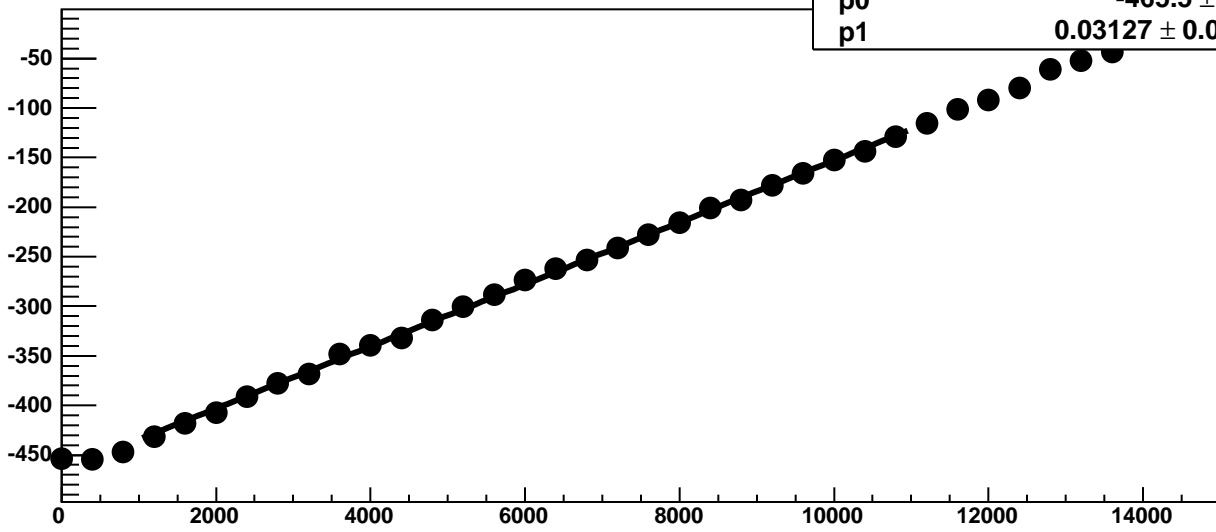
Chip 11, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



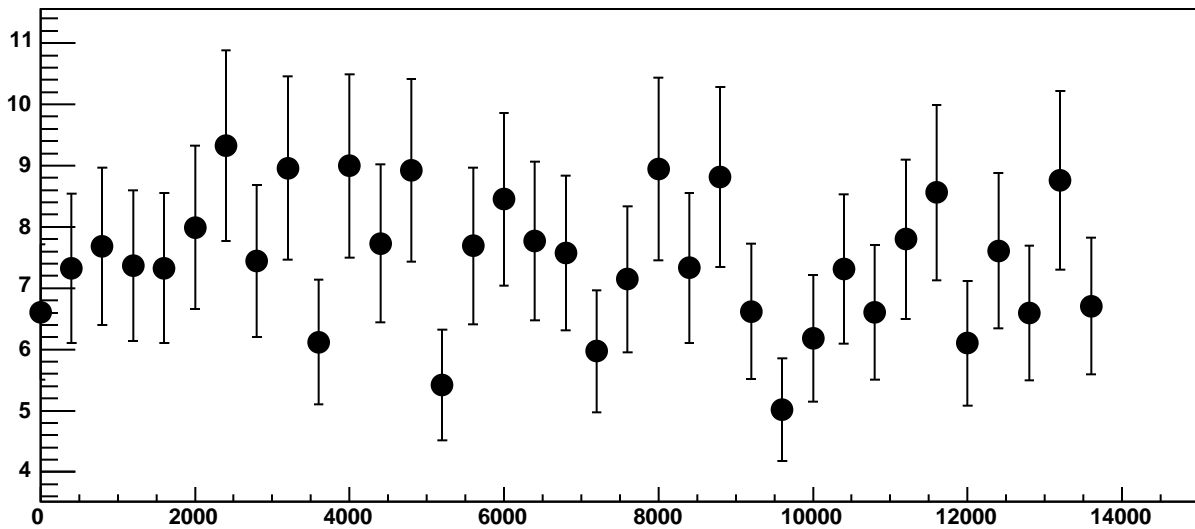
Chip 11, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



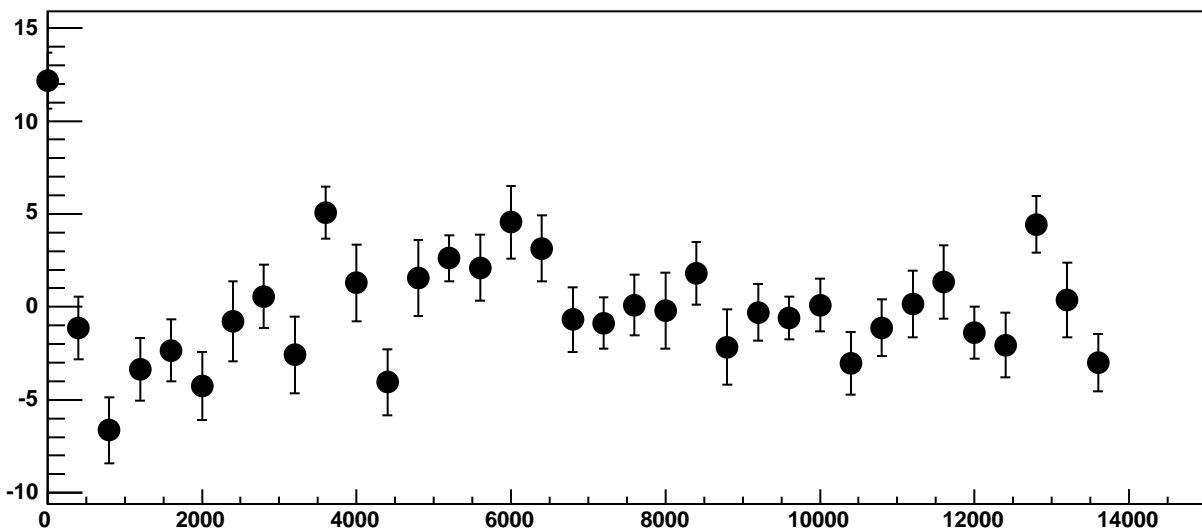
Chip 11, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



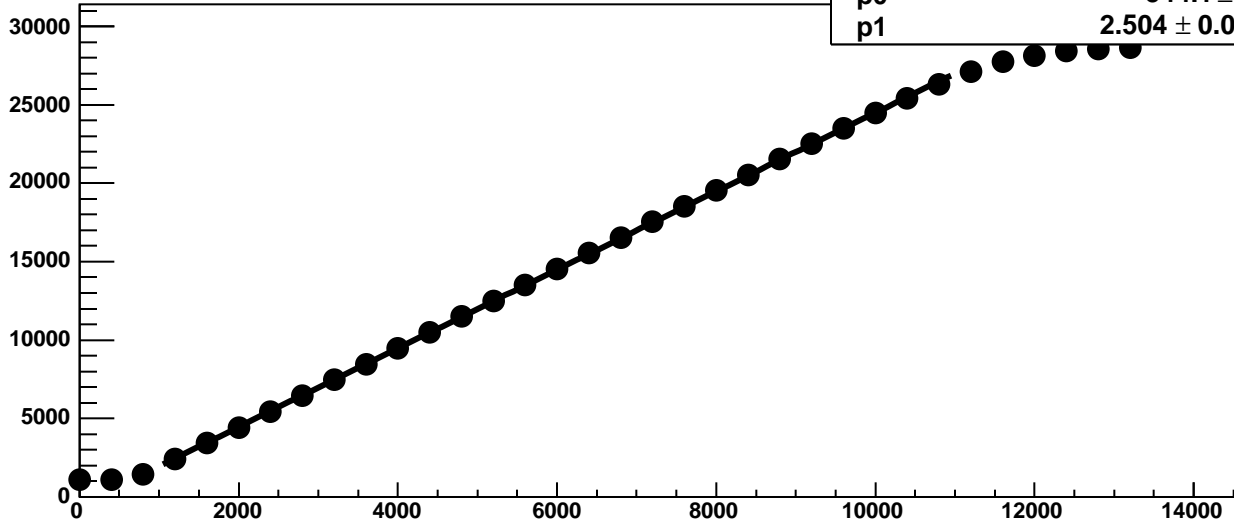
Chip 11, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



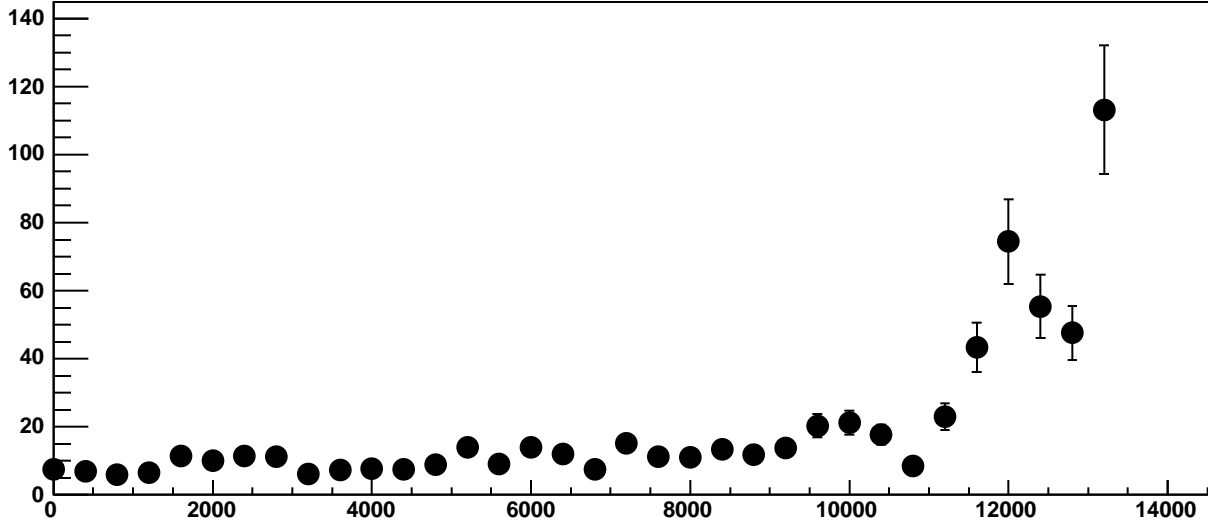
Chip 11, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



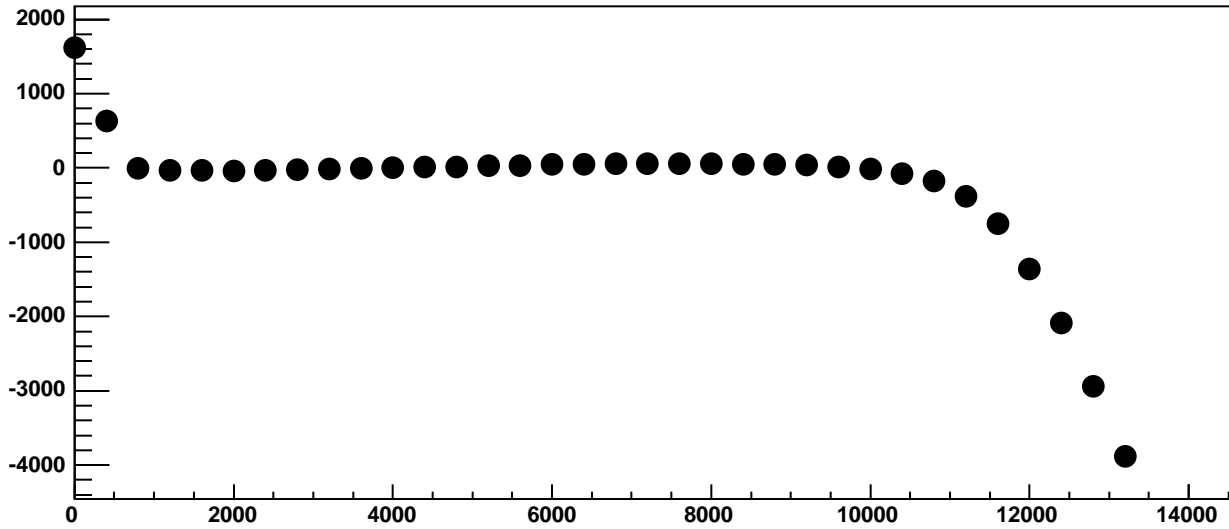
Chip 11, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC



Chip 11, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC

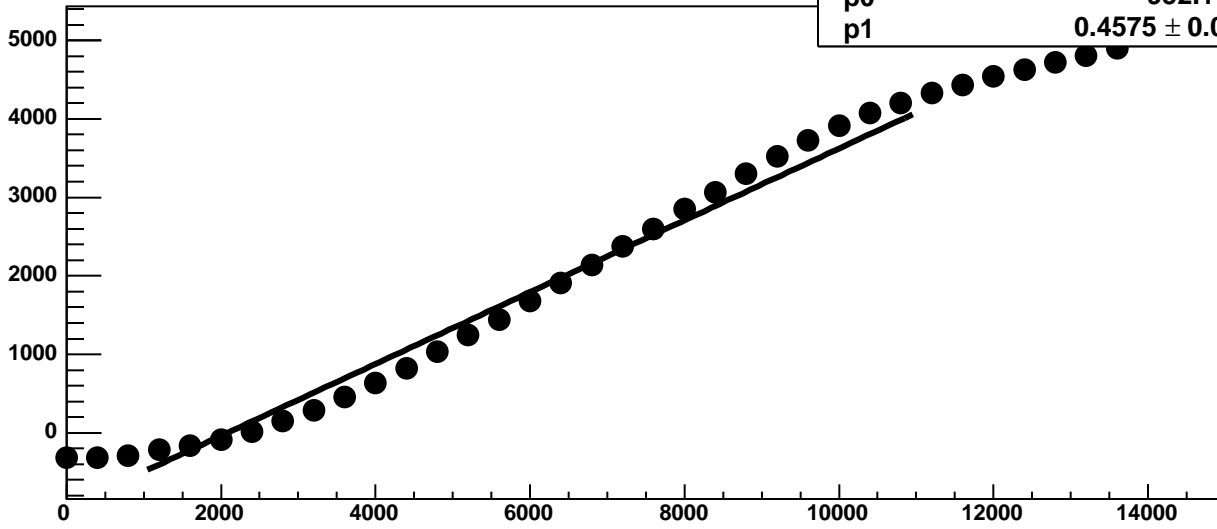


Chip 11, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC

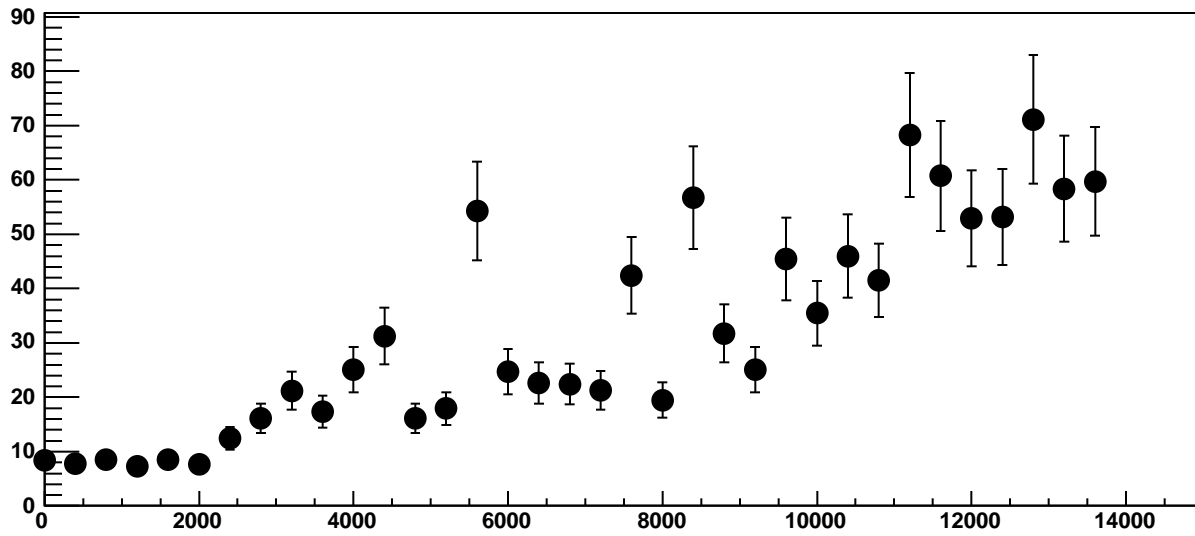


Chip 11, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

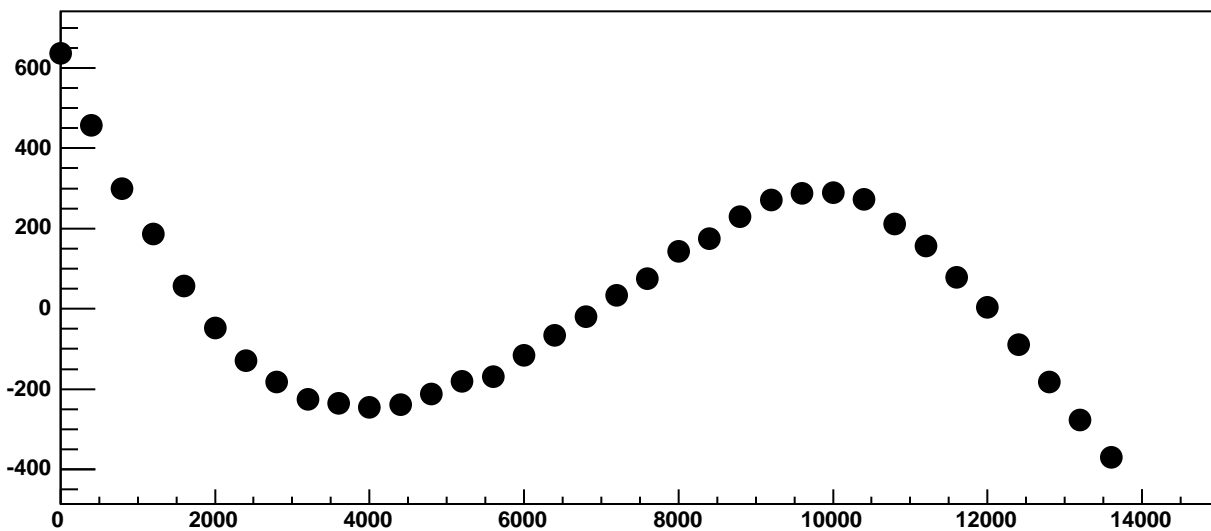
χ^2 / ndf 4.061e+04 / 23
p0 -952.1 ± 1.266
p1 0.4575 ± 0.0003111



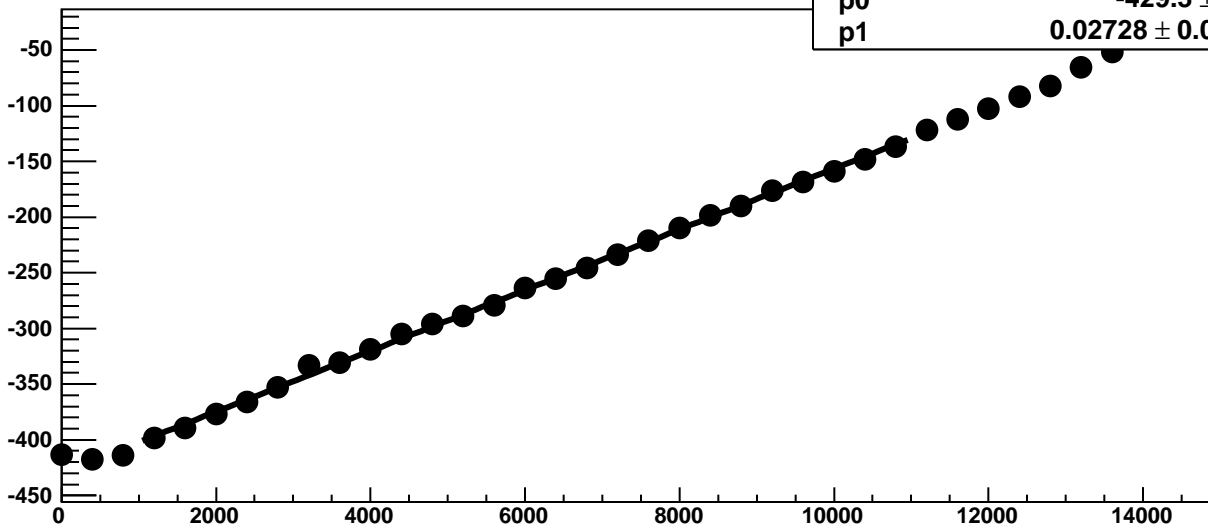
Chip 11, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

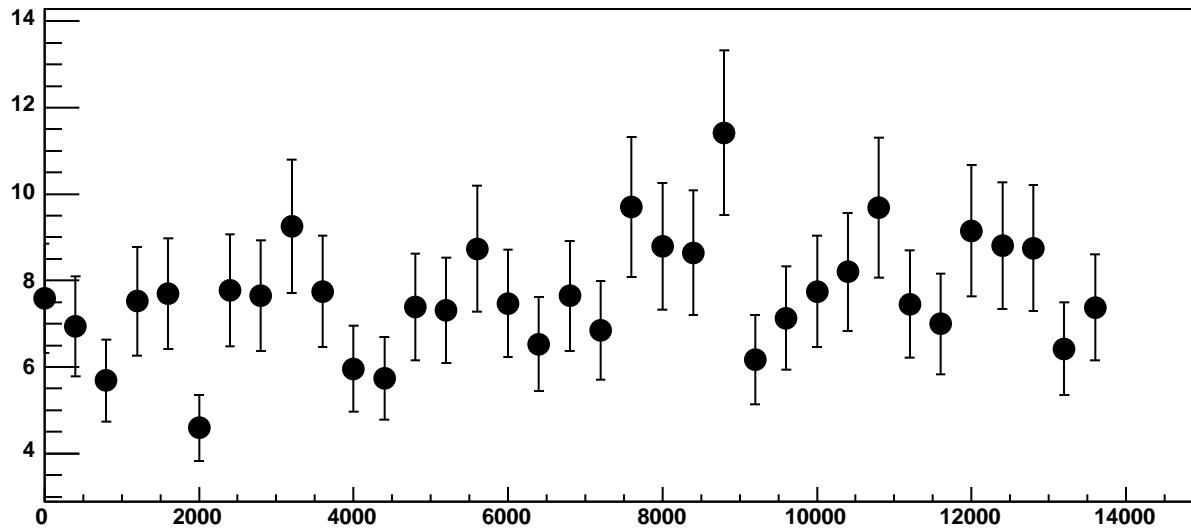


Chip 11, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

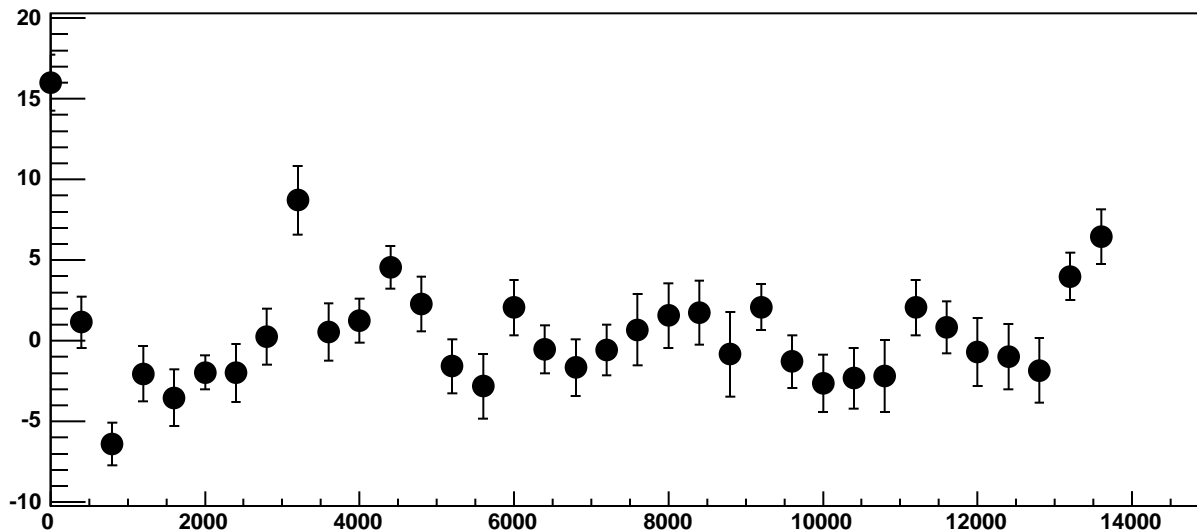


χ^2 / ndf 56.28 / 23
p0 -429.3 ± 0.7369
p1 0.02728 ± 0.0001177

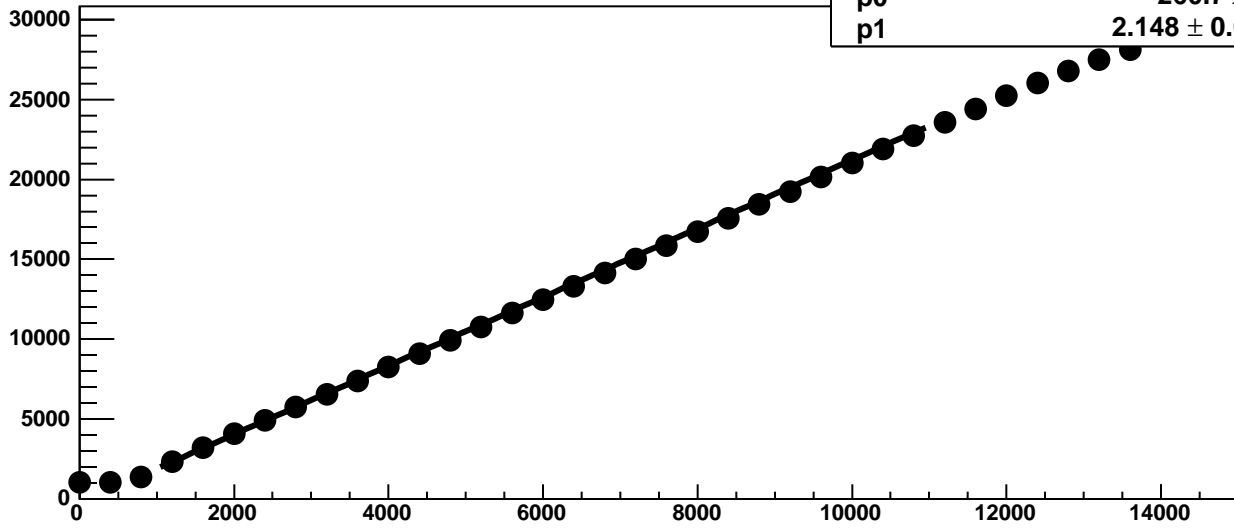
Chip 11, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



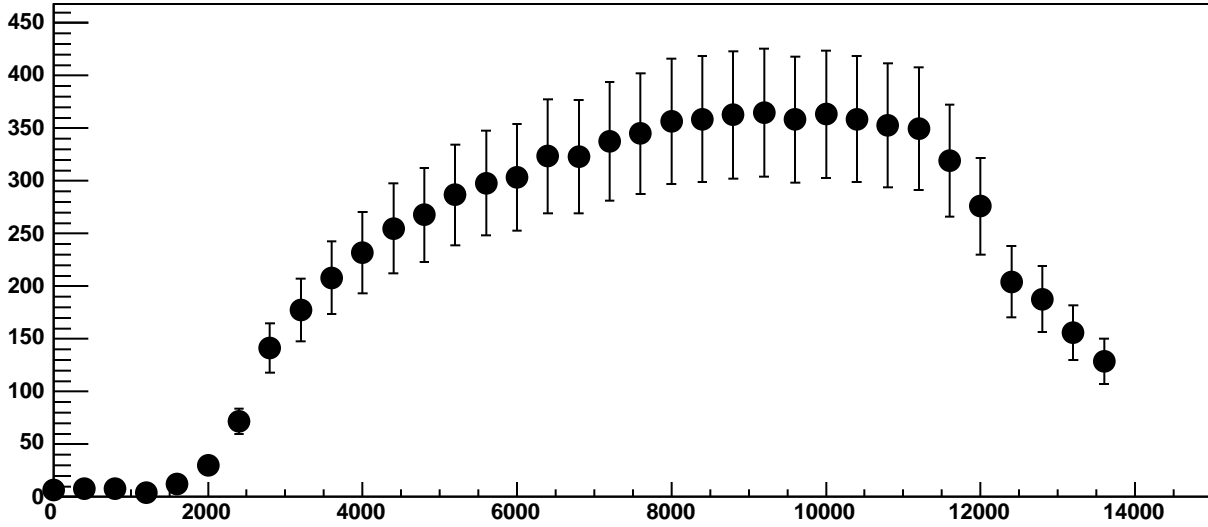
Chip 11, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



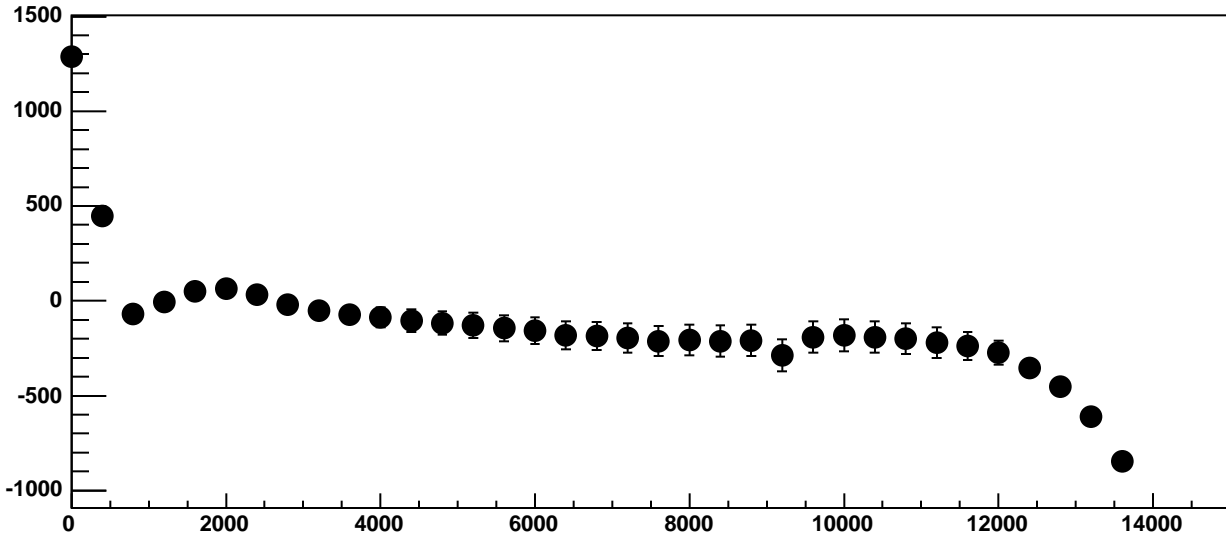
Chip 11, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC



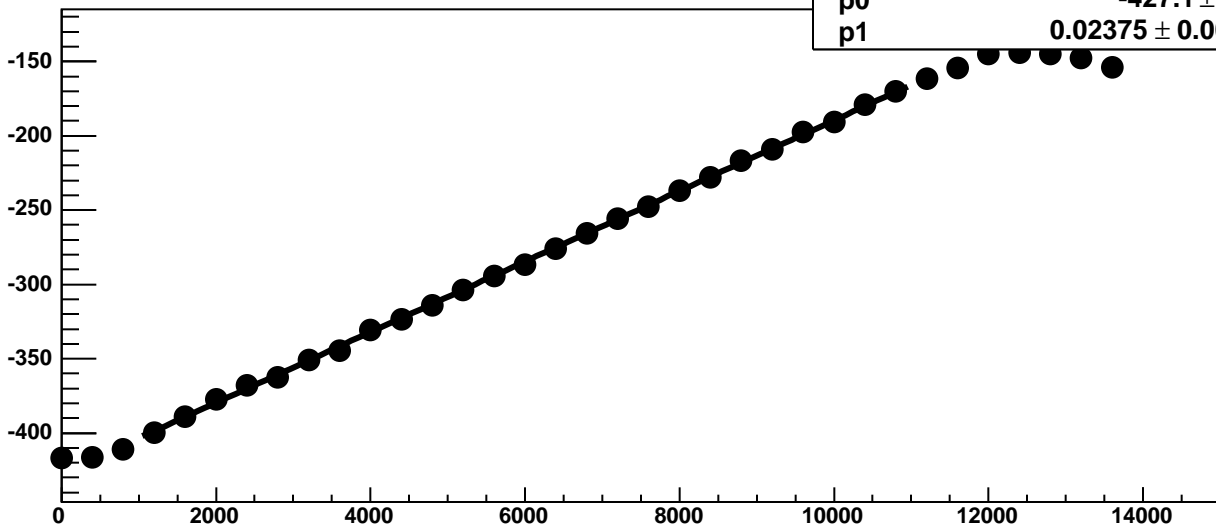
Chip 11, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



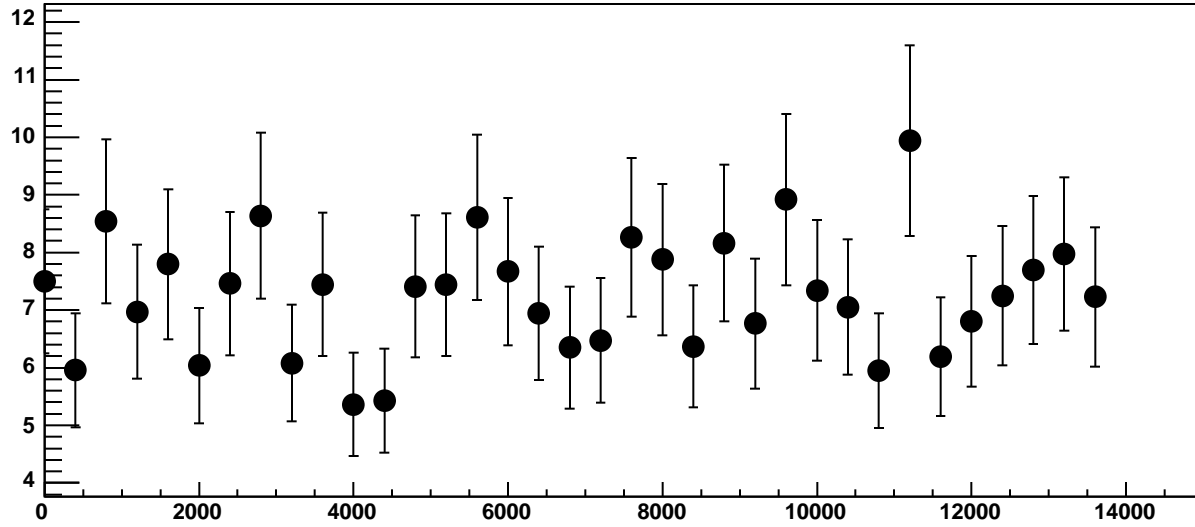
Chip 11, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC



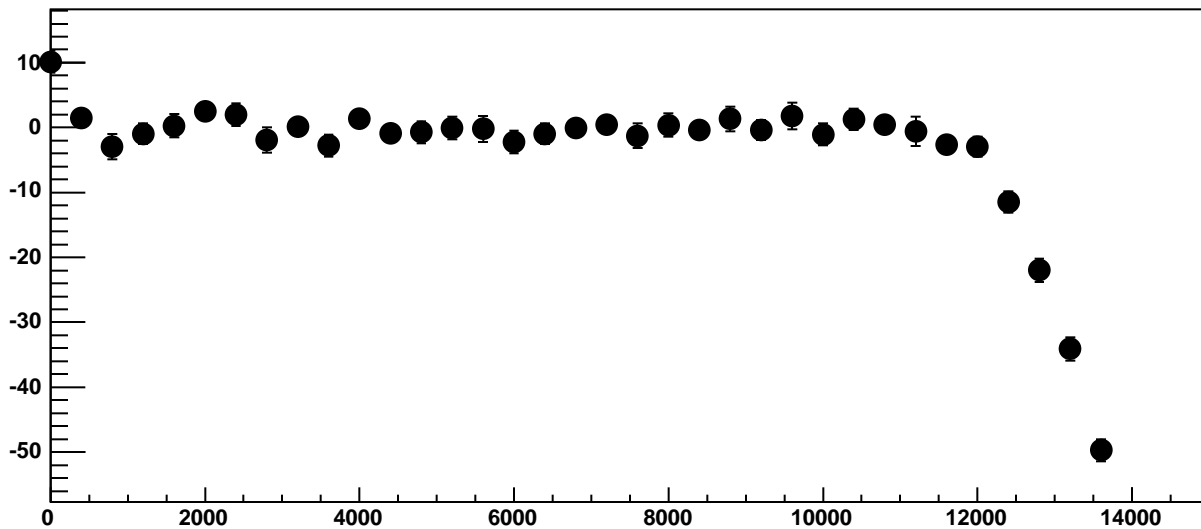
Chip 11, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



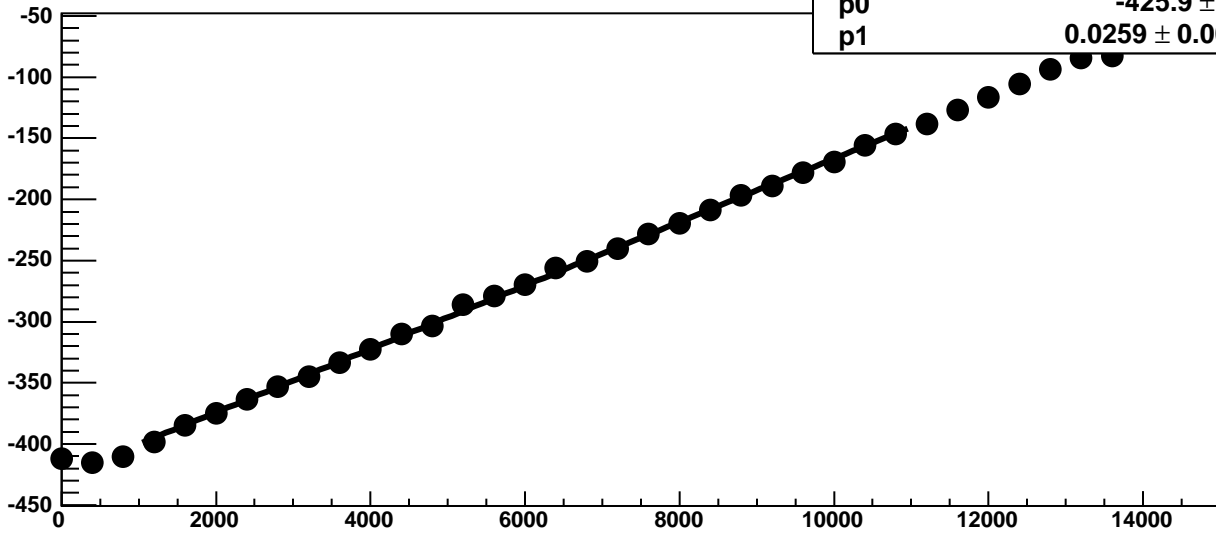
Chip 11, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

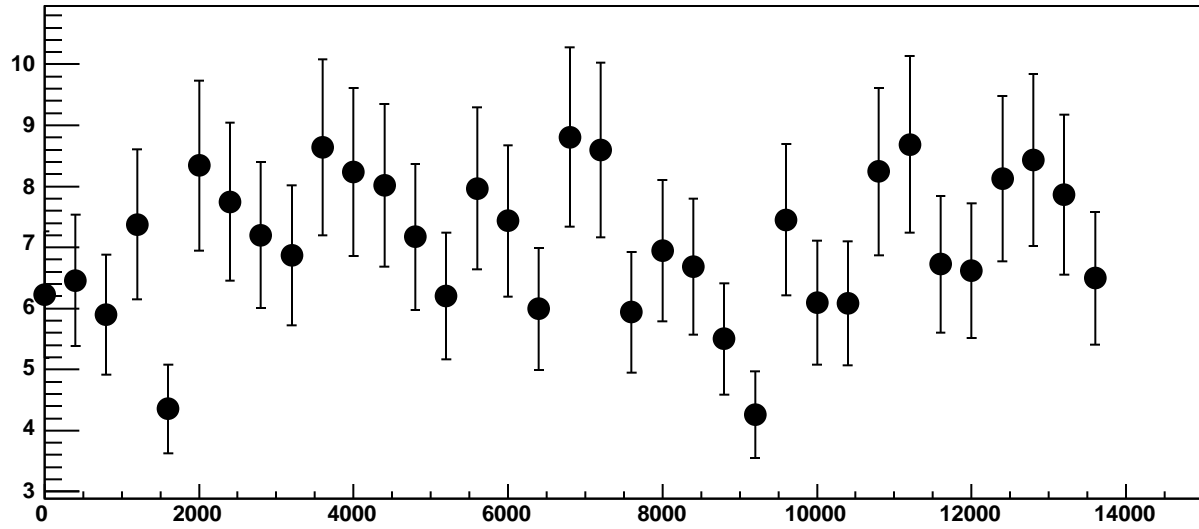


Chip 11, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

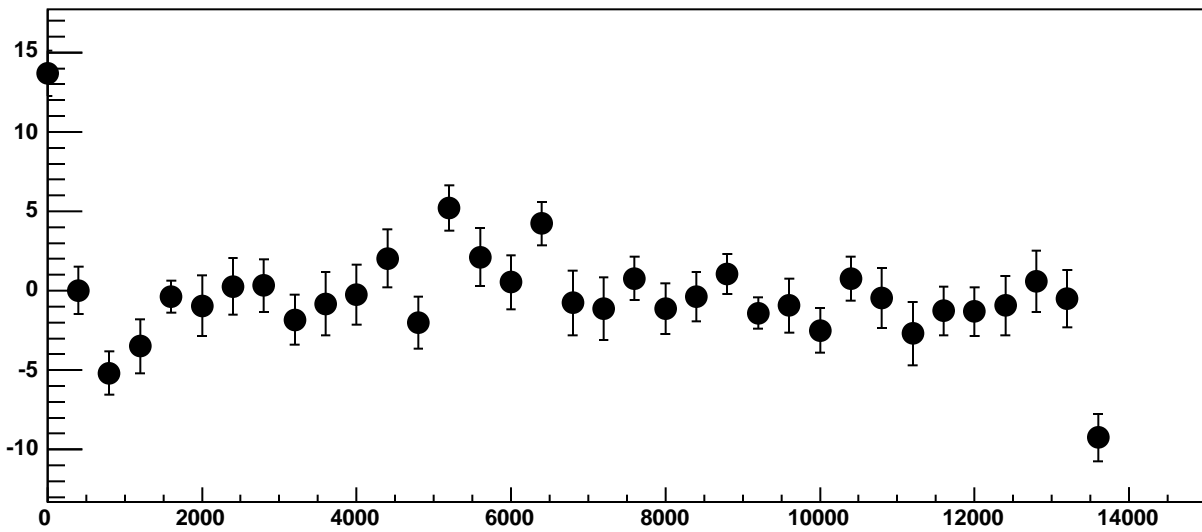


χ^2 / ndf 41.2 / 23
p0 -425.9 ± 0.6986
p1 0.0259 ± 0.0001014

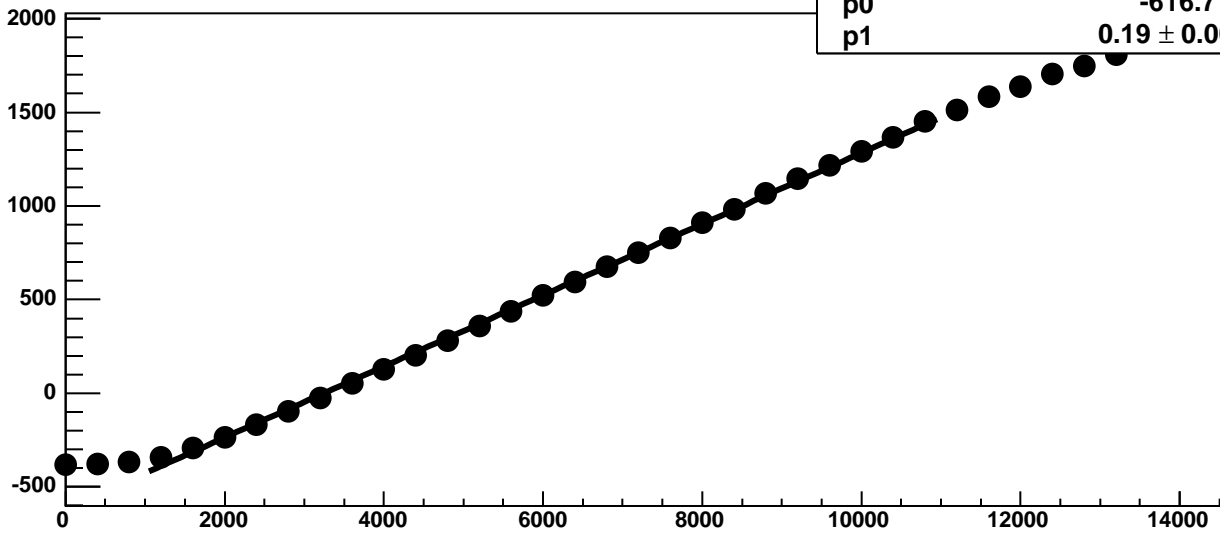
Chip 11, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



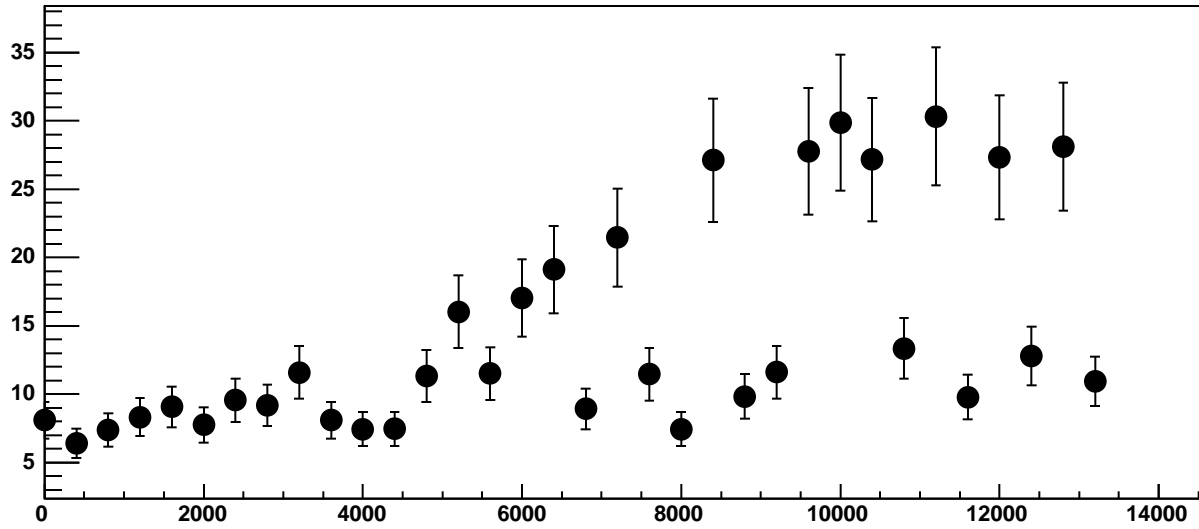
Chip 11, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



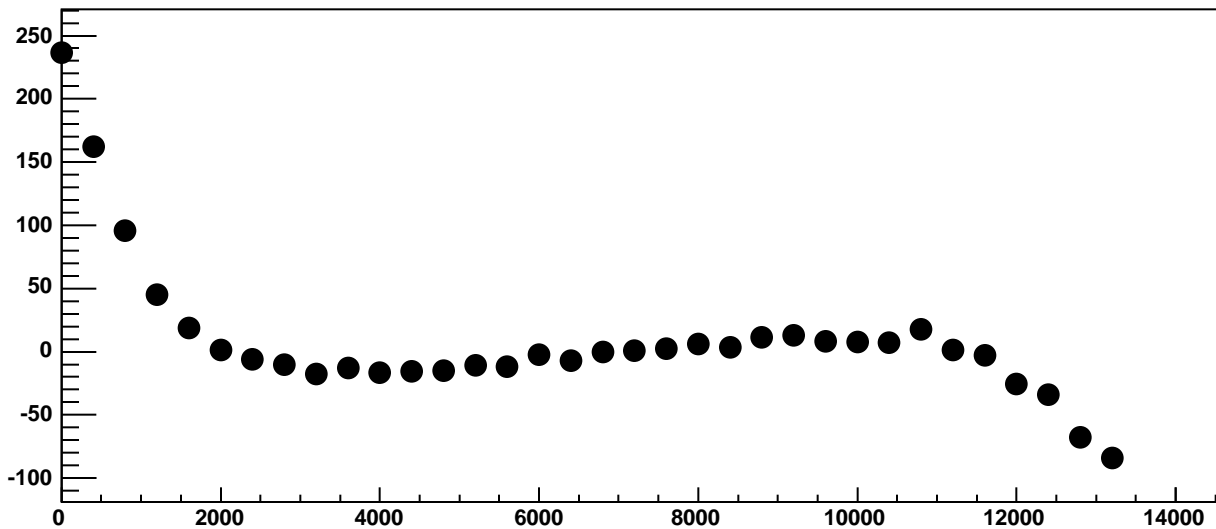
Chip 11, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC



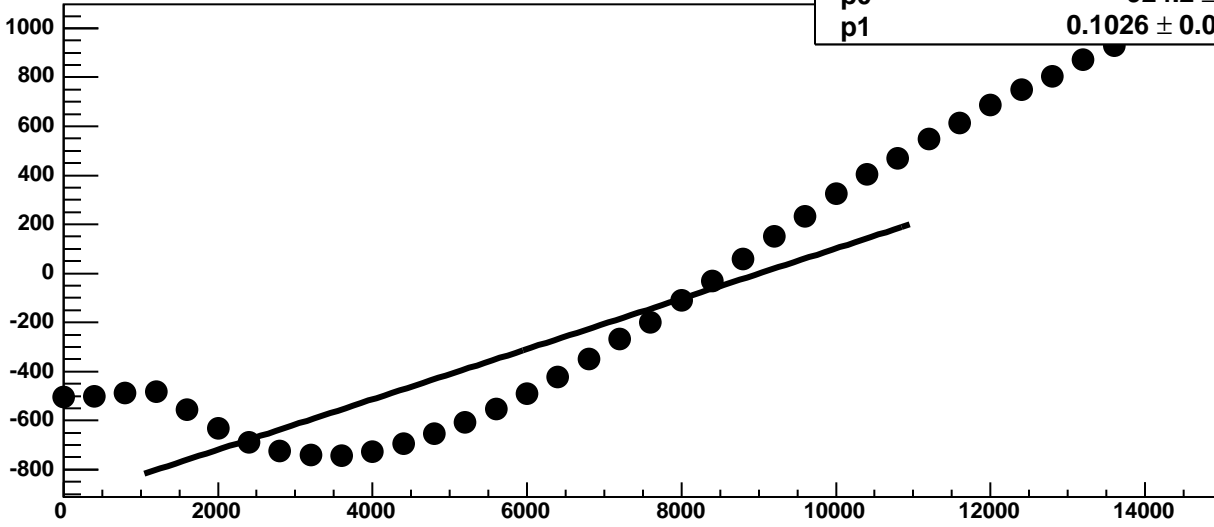
Chip 11, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



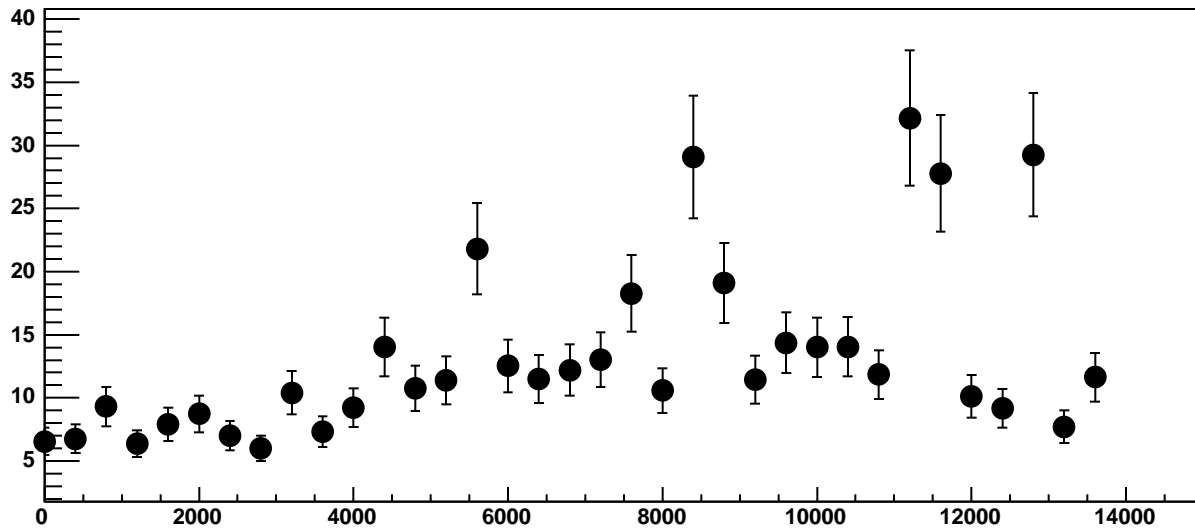
Chip 11, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC



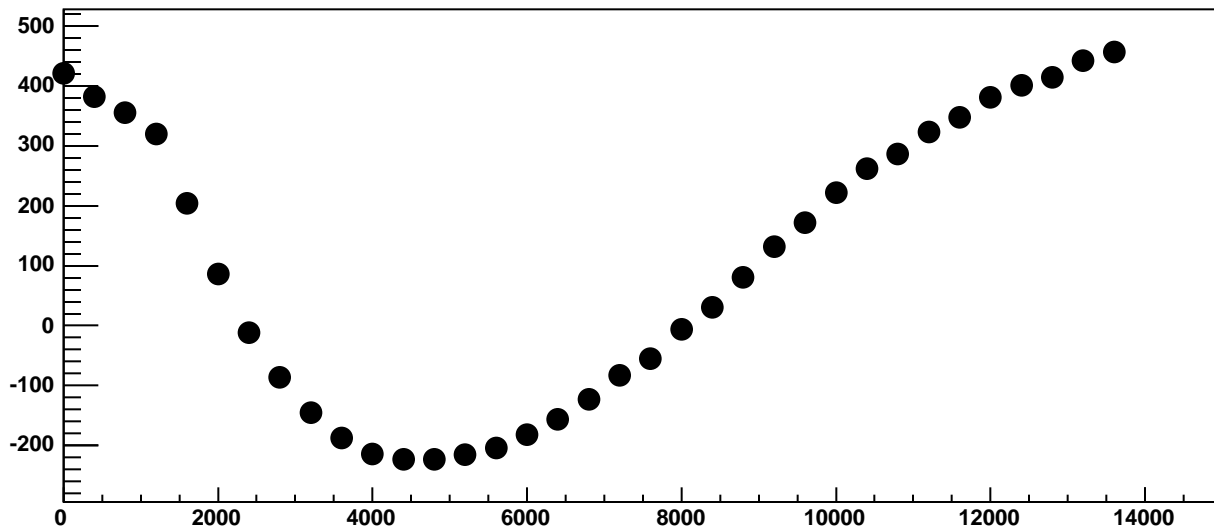
Chip 11, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



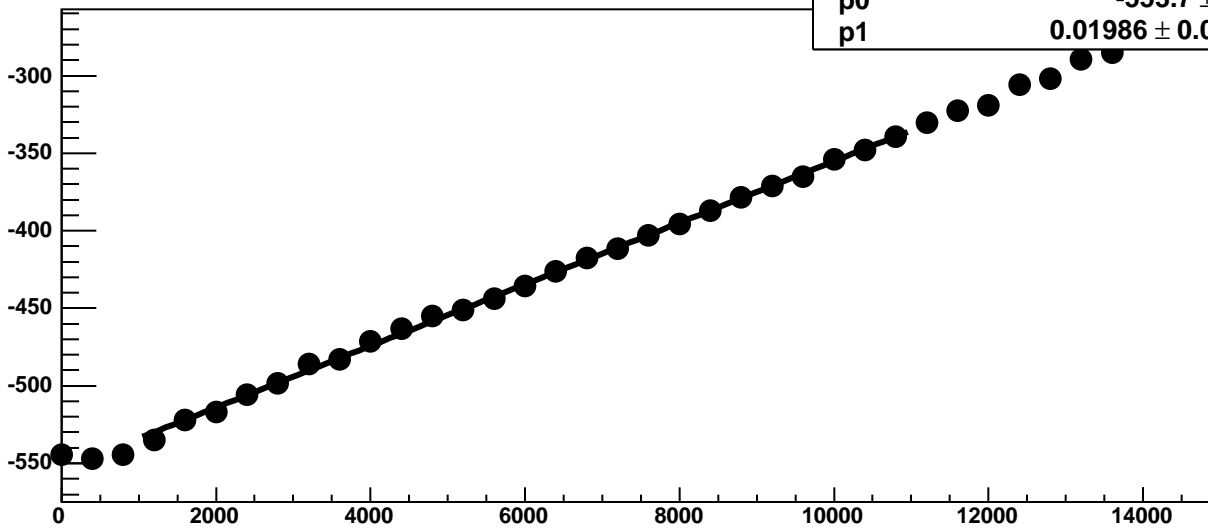
Chip 11, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

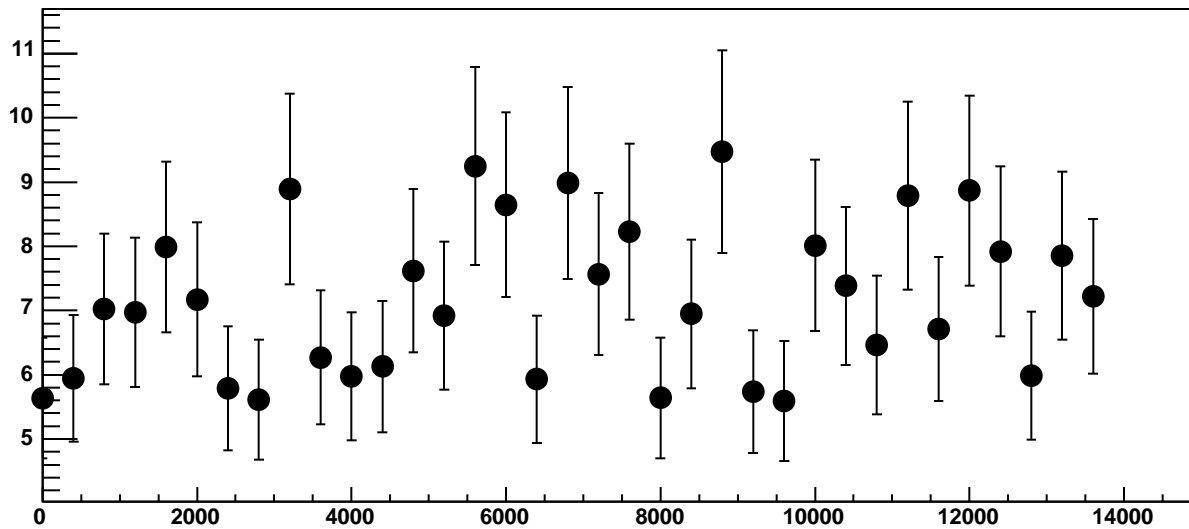


Chip 11, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

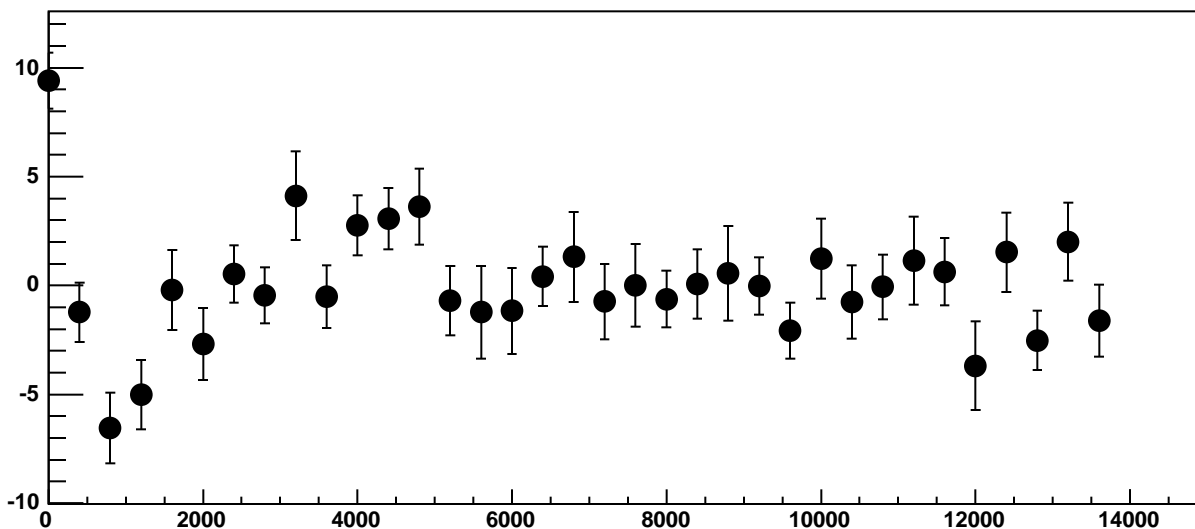


χ^2 / ndf 35.2 / 23
p0 -553.7 ± 0.7157
p1 0.01986 ± 0.0001075

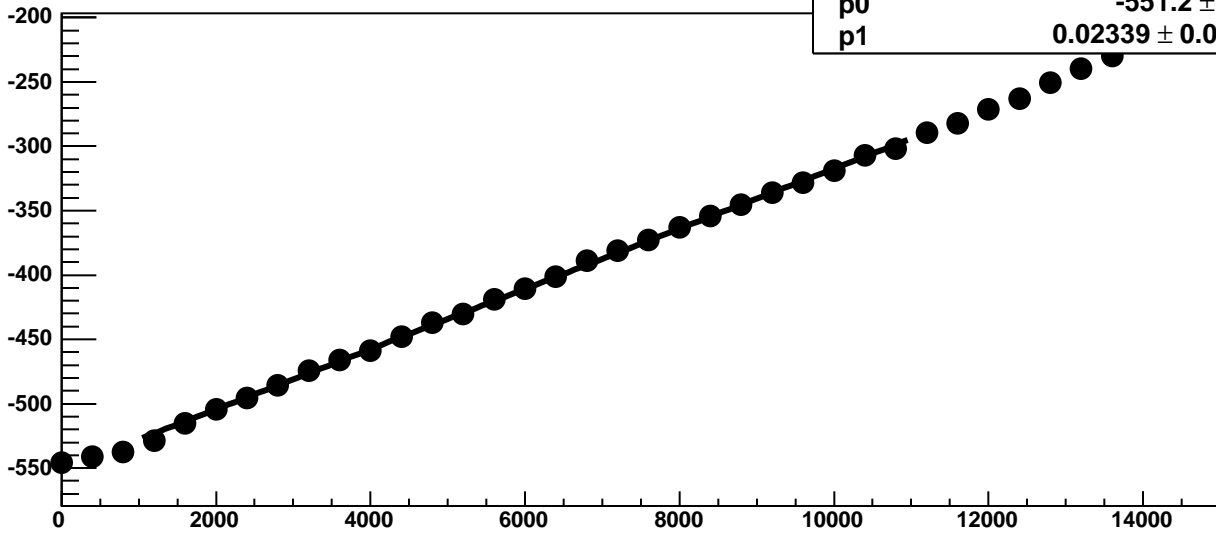
Chip 11, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

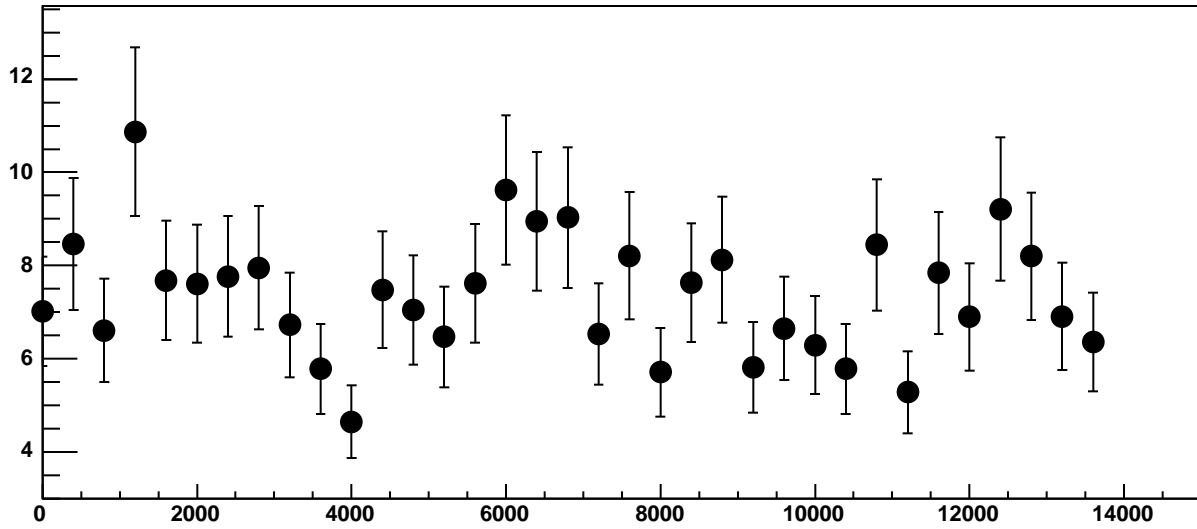


Chip 11, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC

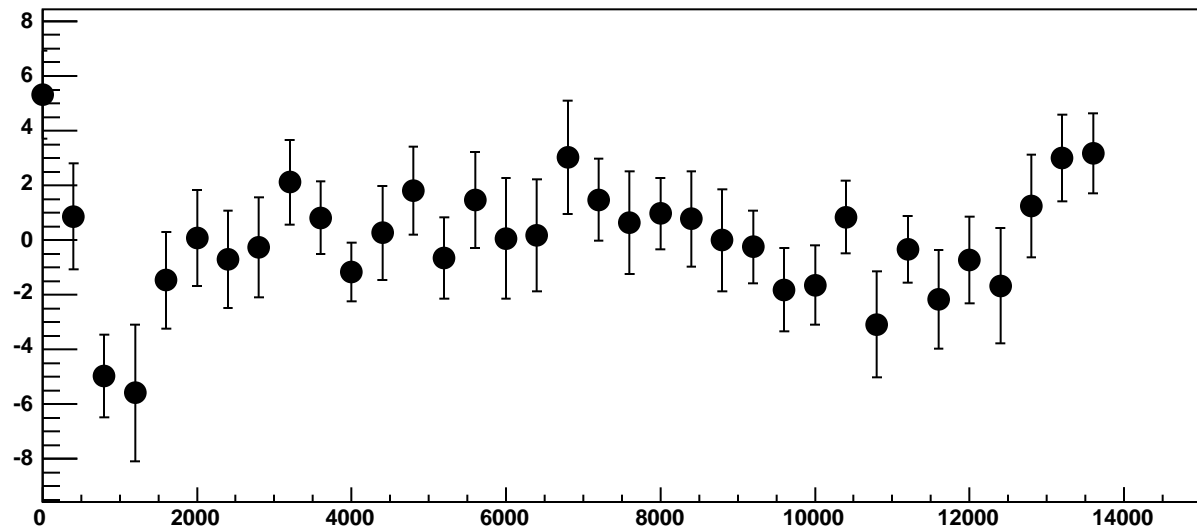


χ^2 / ndf 21.18 / 23
p0 -551.2 ± 0.7689
p1 0.02339 ± 0.0001132

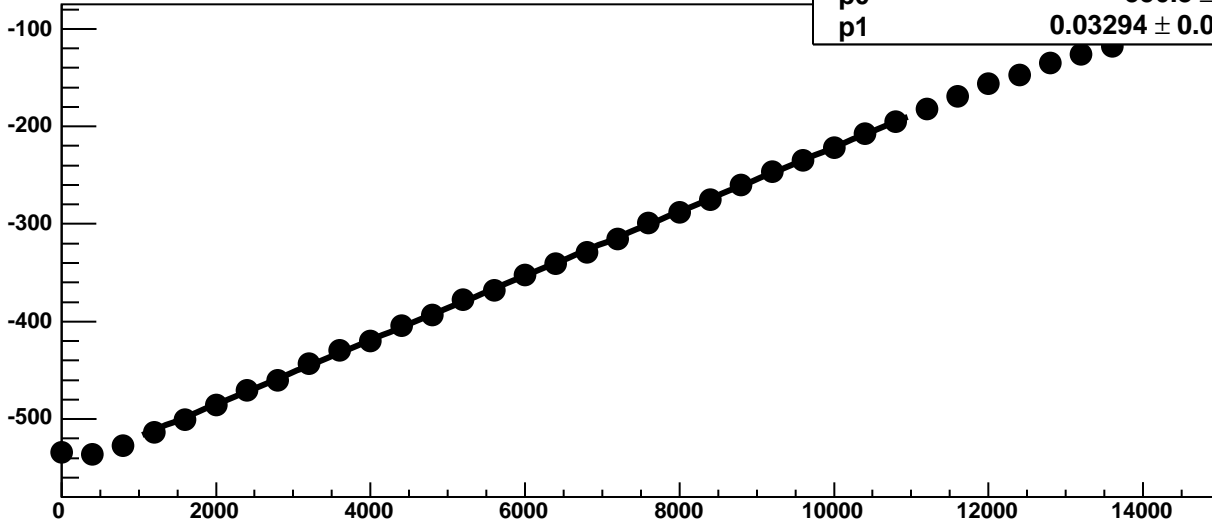
Chip 11, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC

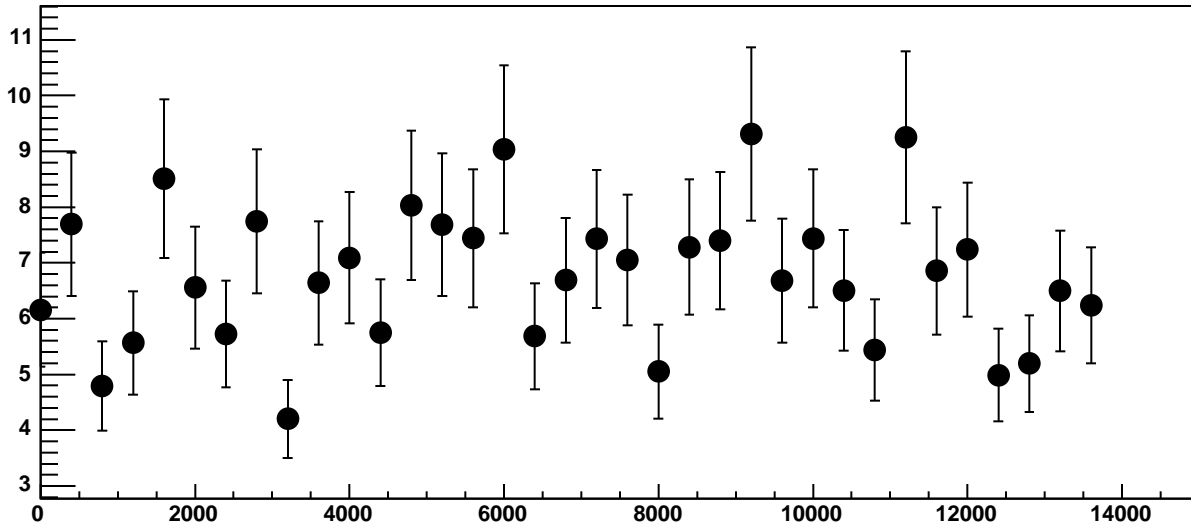


Chip 11, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

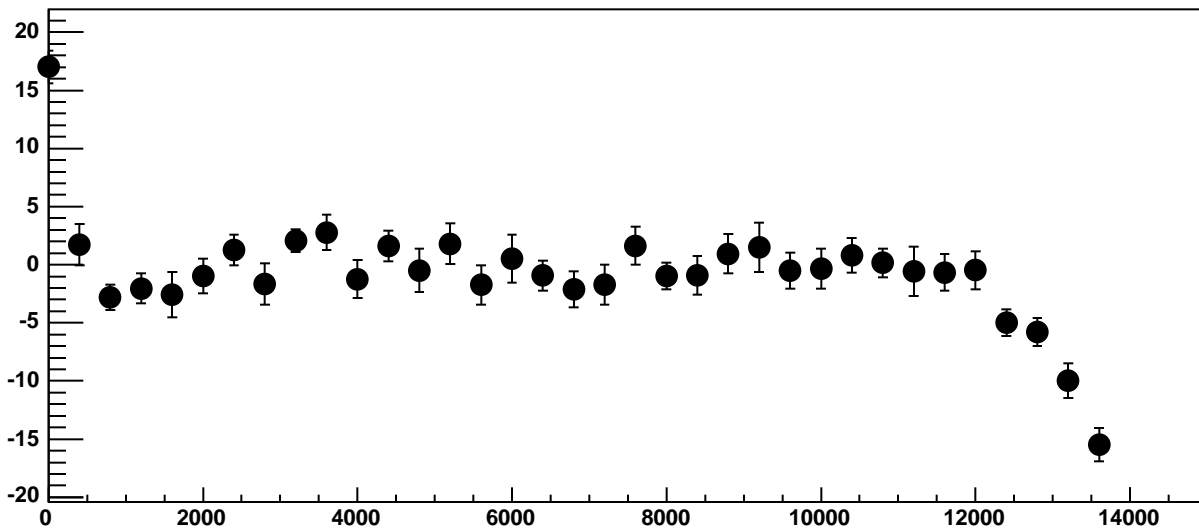


χ^2 / ndf 25.5 / 23
p0 -550.8 ± 0.6623
p1 0.03294 ± 0.0001012

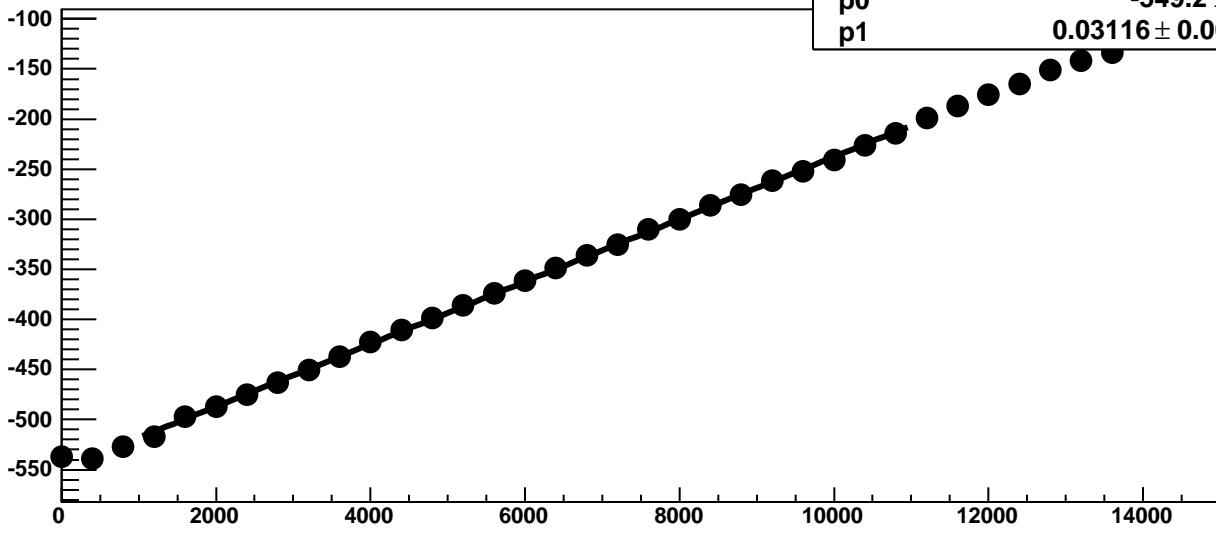
Chip 11, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

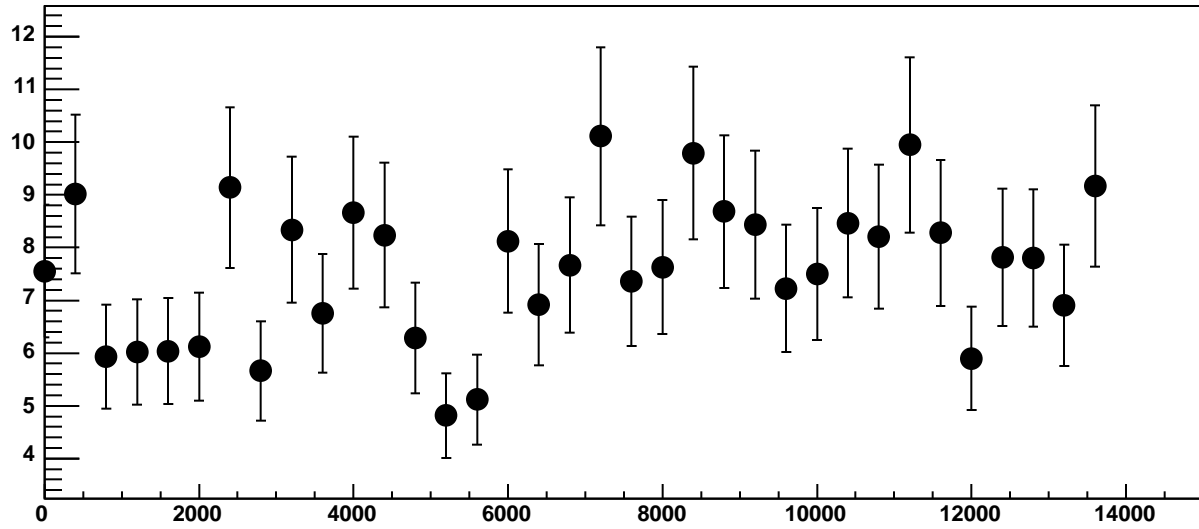


Chip 11, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

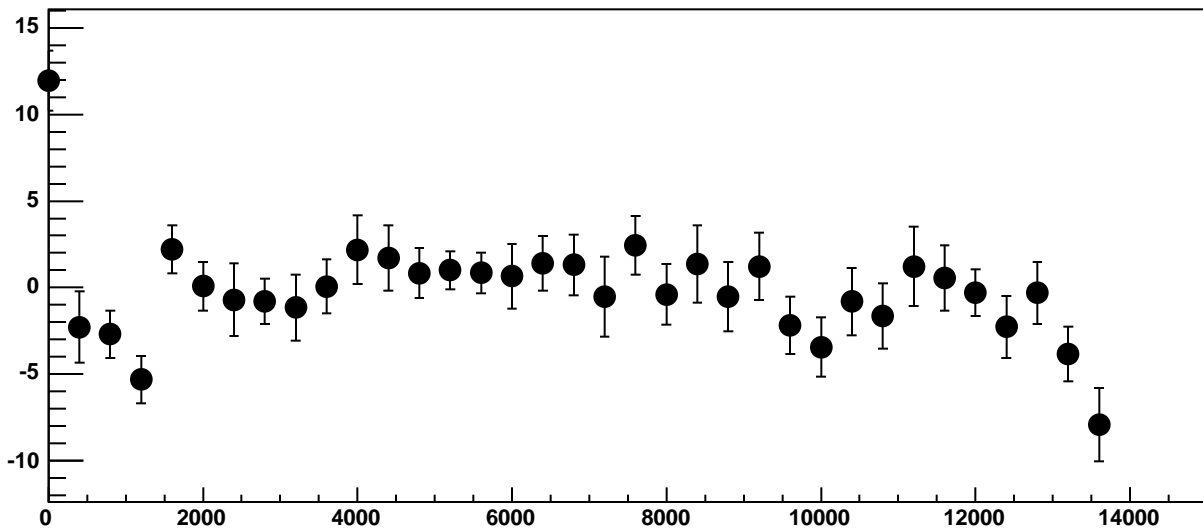


χ^2 / ndf 33.13 / 23
p0 -549.2 ± 0.714
p1 0.03116 ± 0.0001156

Chip 11, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 11, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

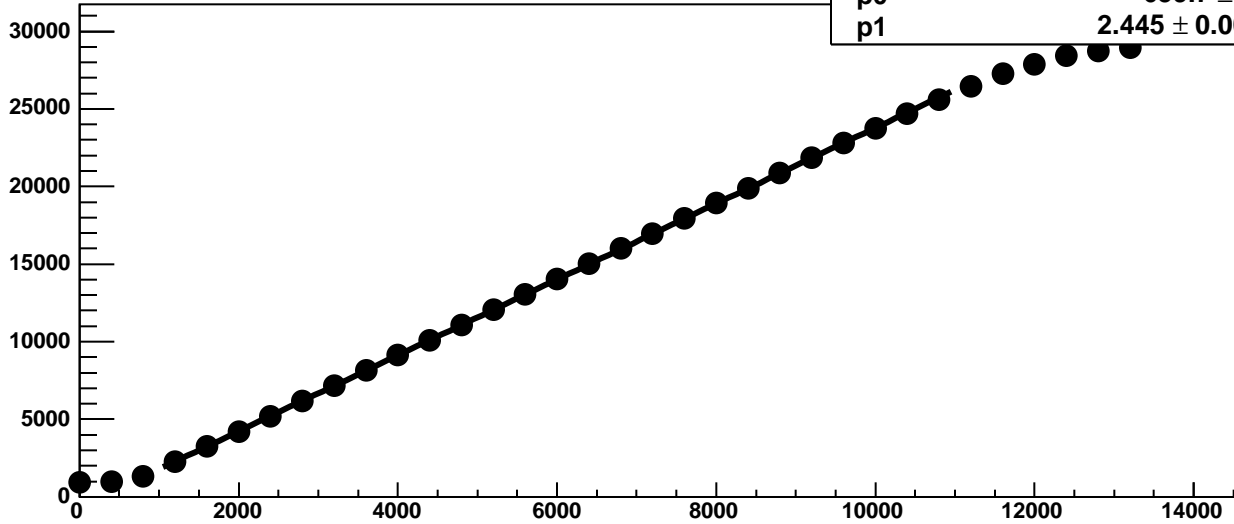
4990 / 23

p0

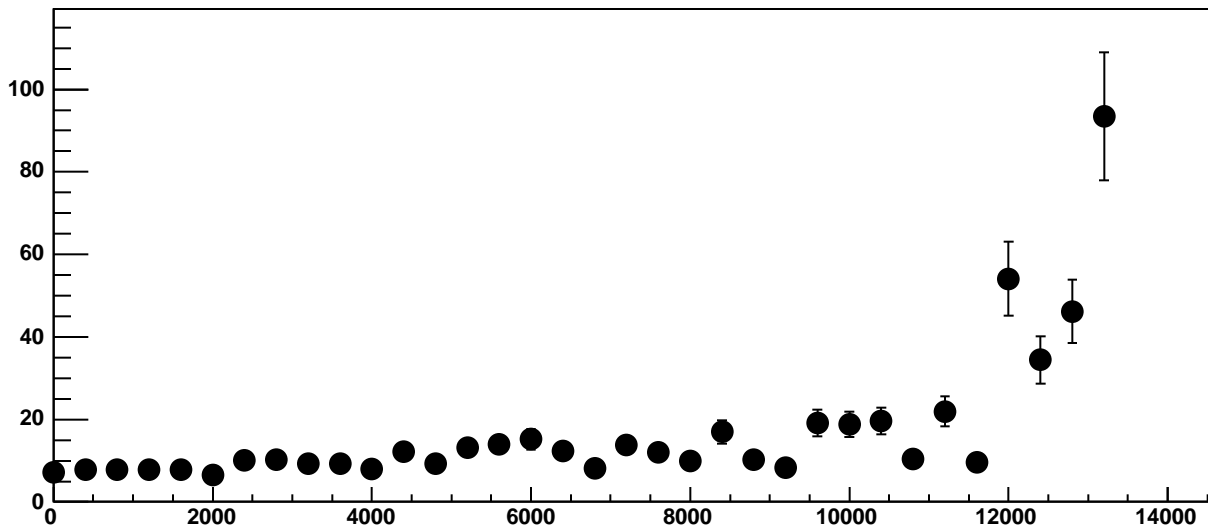
-659.7 ± 0.9469

p1

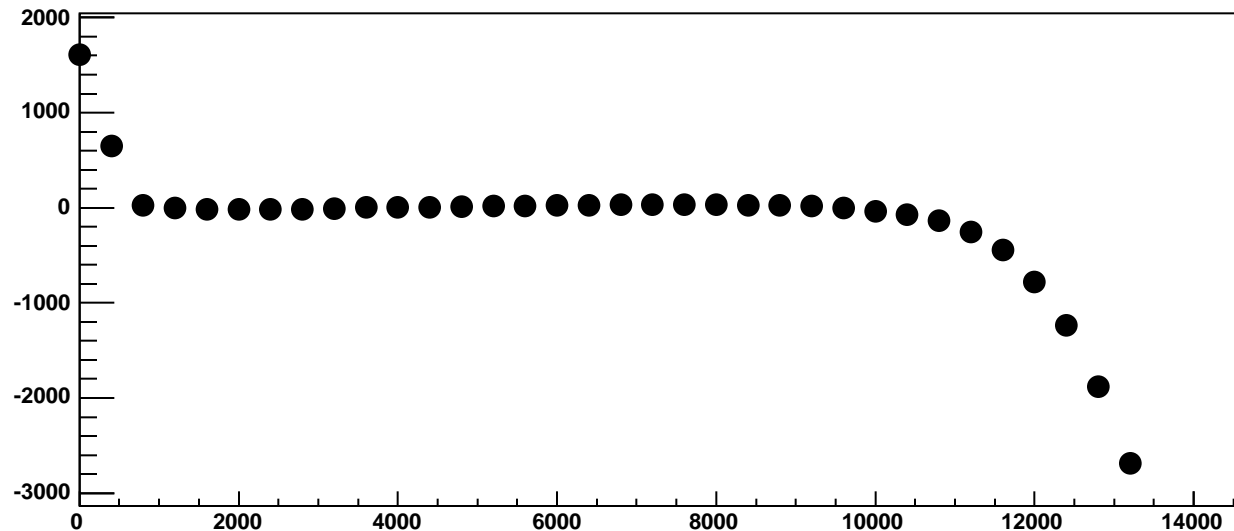
2.445 ± 0.0001624



Chip 11, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC

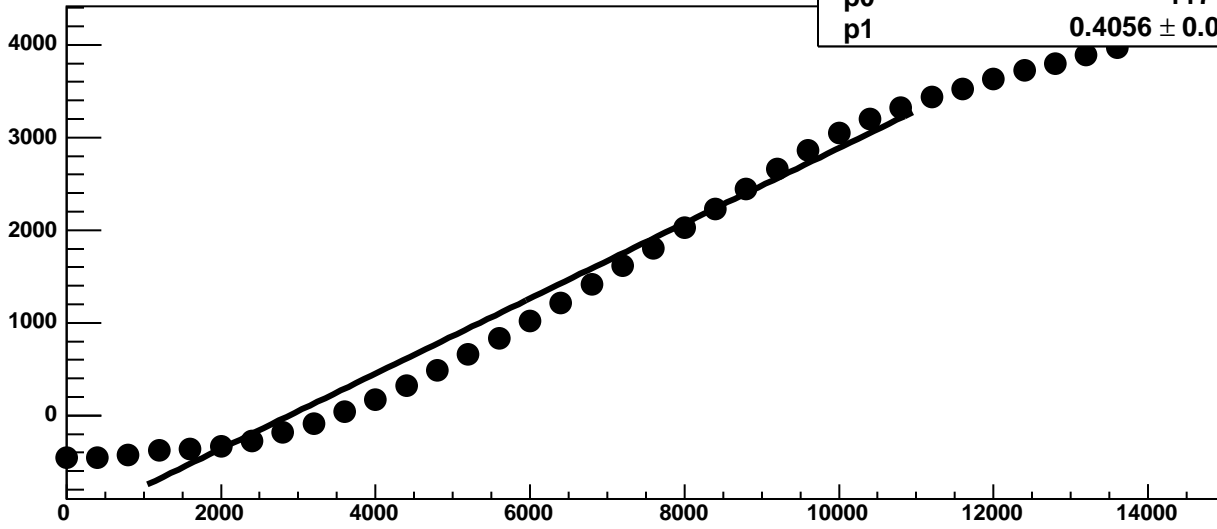


Chip 11, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC

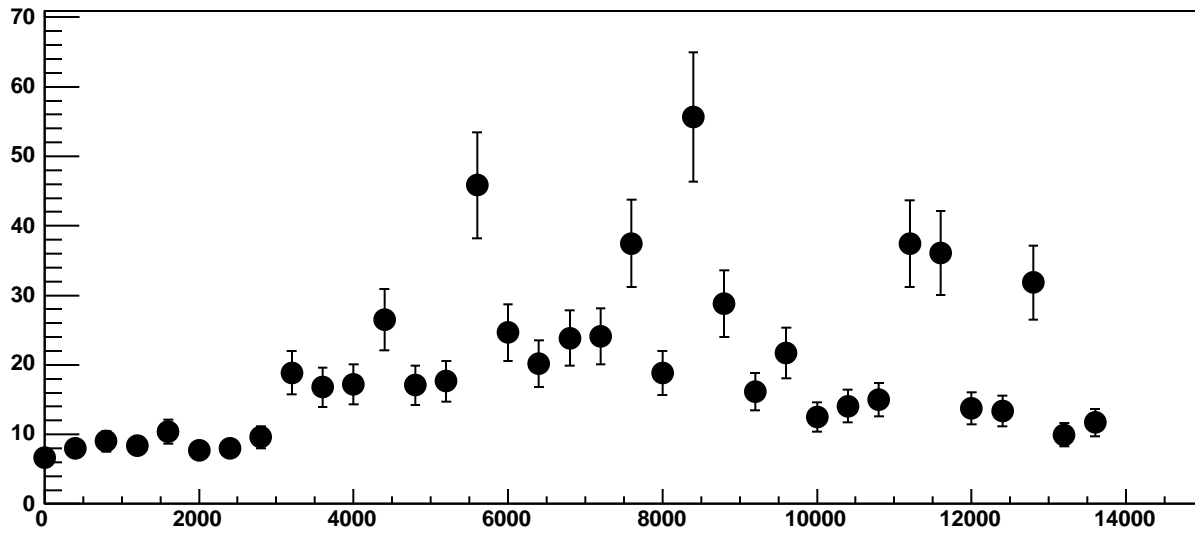


Chip 11, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

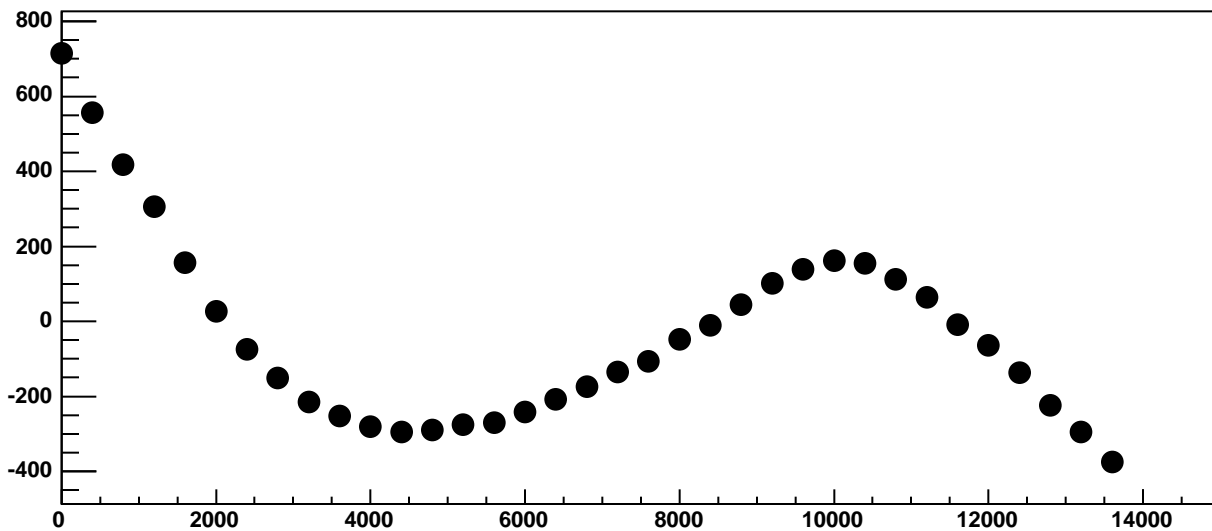
χ^2 / ndf 7.486e+04 / 23
p0 -1171 ± 1.12
p1 0.4056 ± 0.0002058



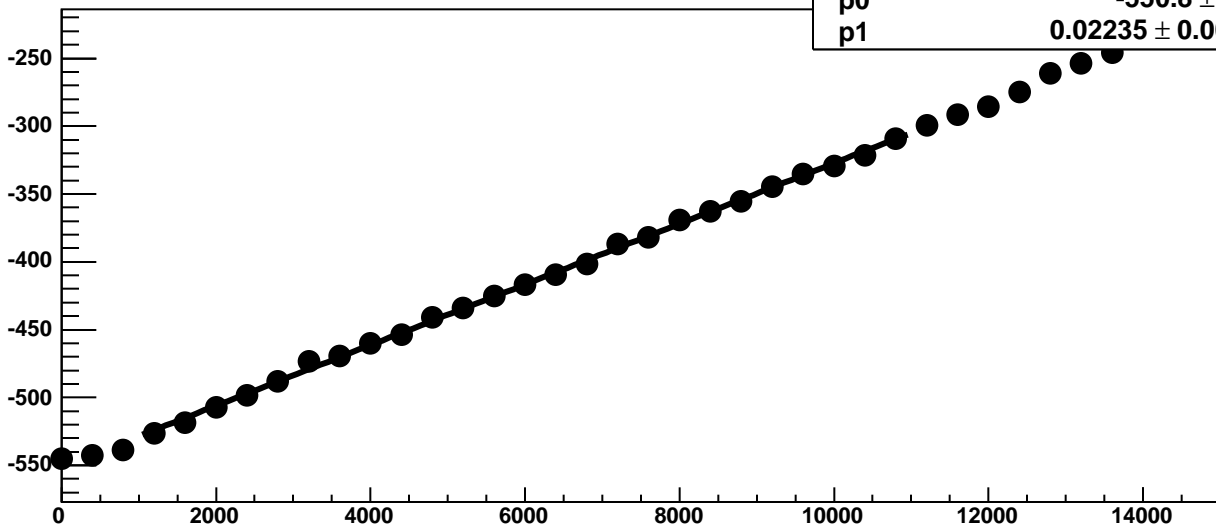
Chip 11, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

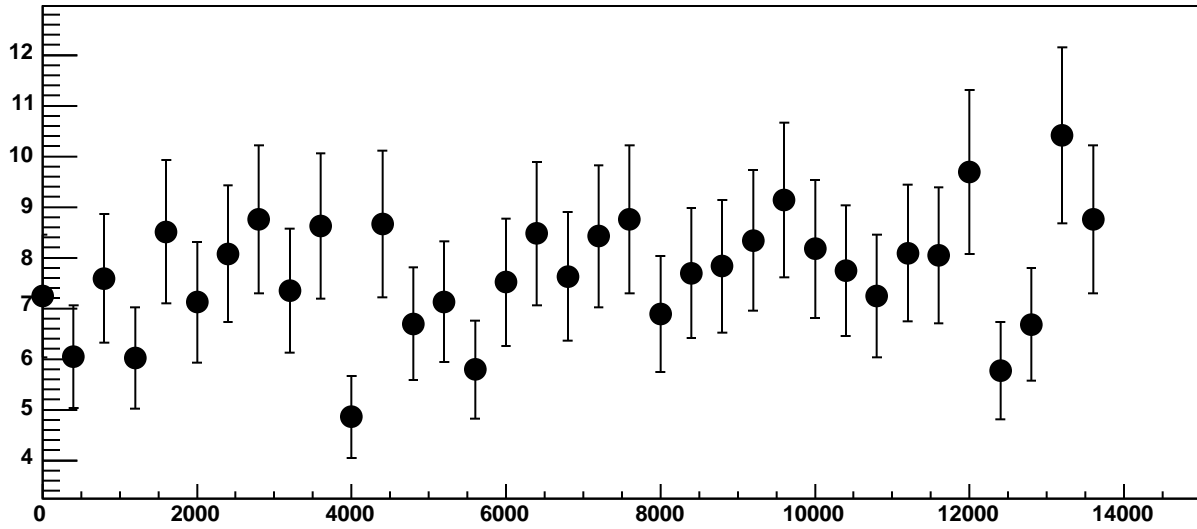


Chip 11, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

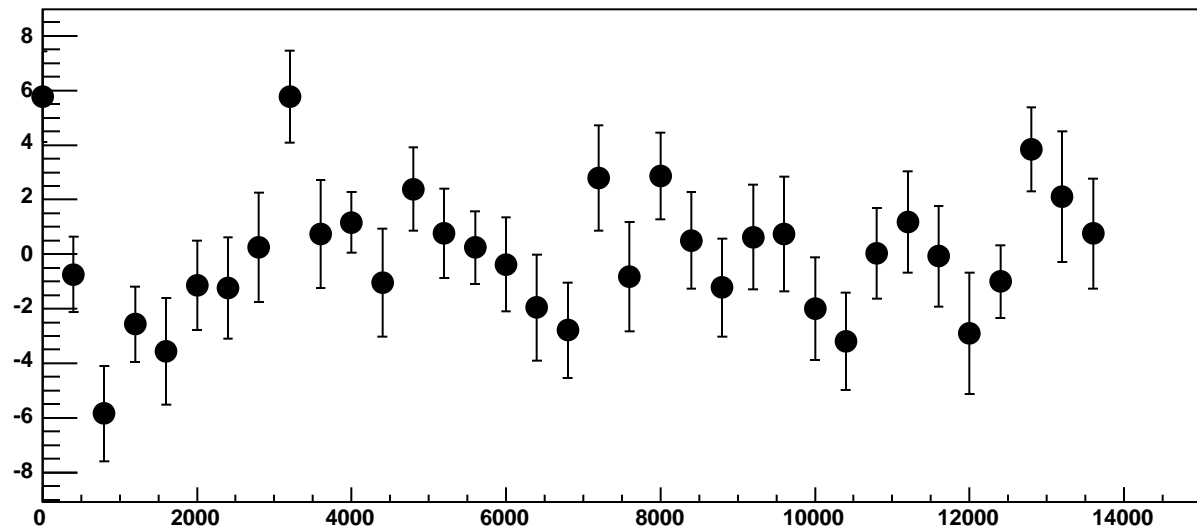


χ^2 / ndf 37.94 / 23
p0 -550.8 ± 0.7672
p1 0.02235 ± 0.0001196

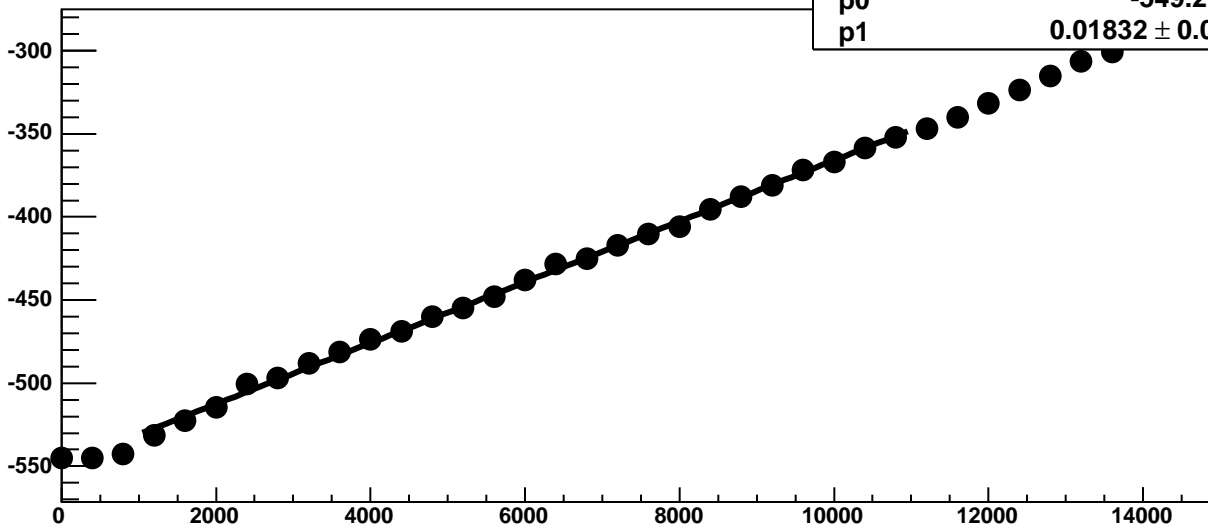
Chip 11, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

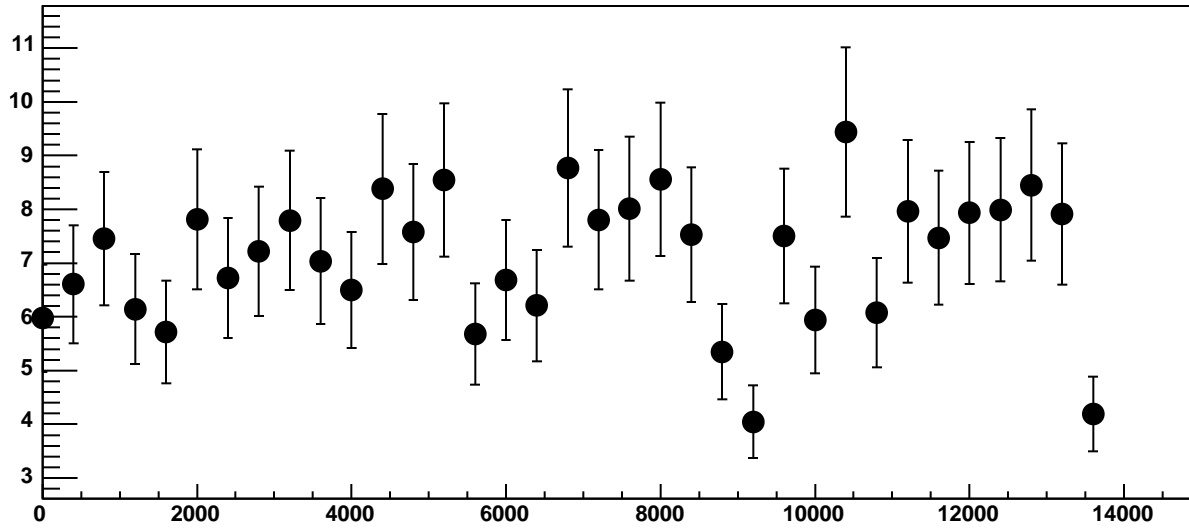


Chip 11, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

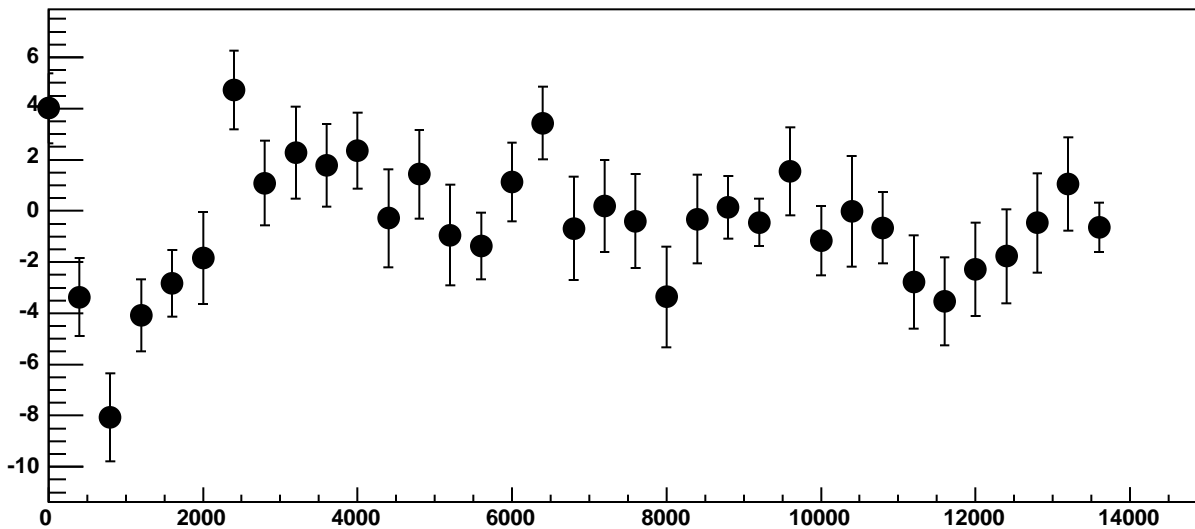


χ^2 / ndf 42.77 / 23
p0 -549.2 ± 0.706
p1 0.01832 ± 0.0001026

Chip 11, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC

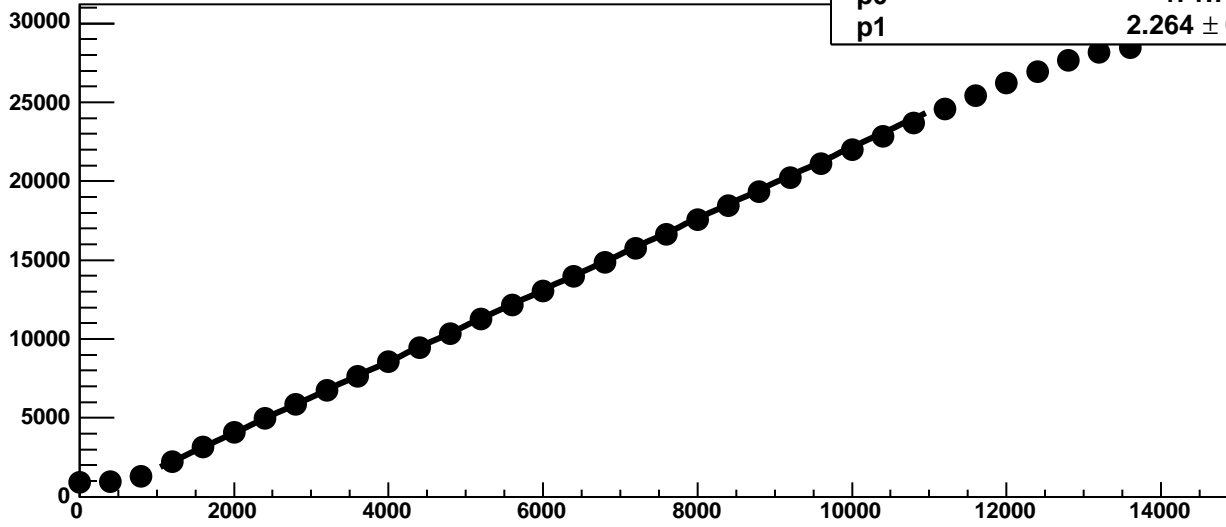


Chip 11, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

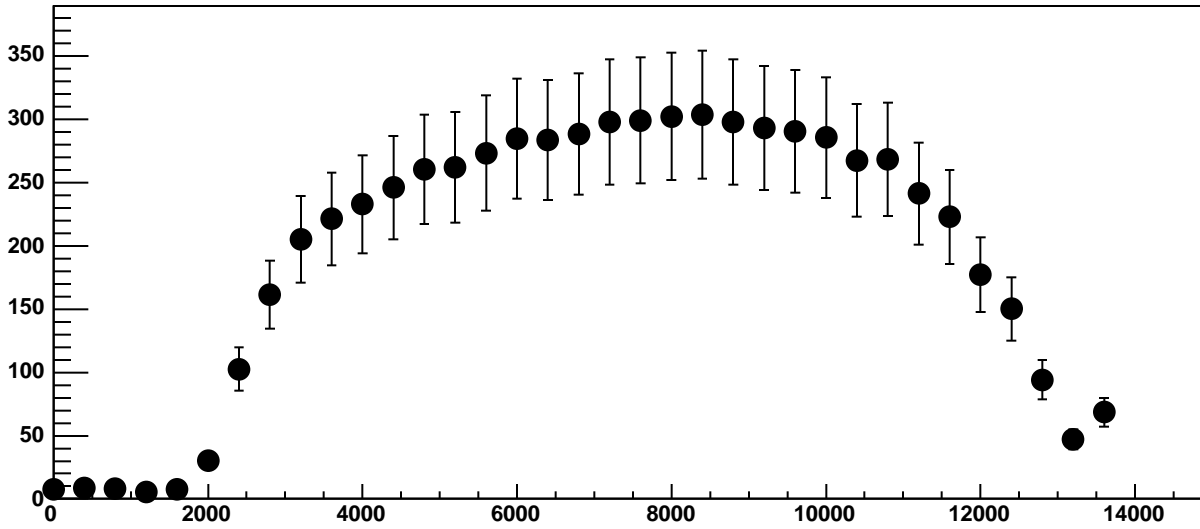


Chip 11, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC

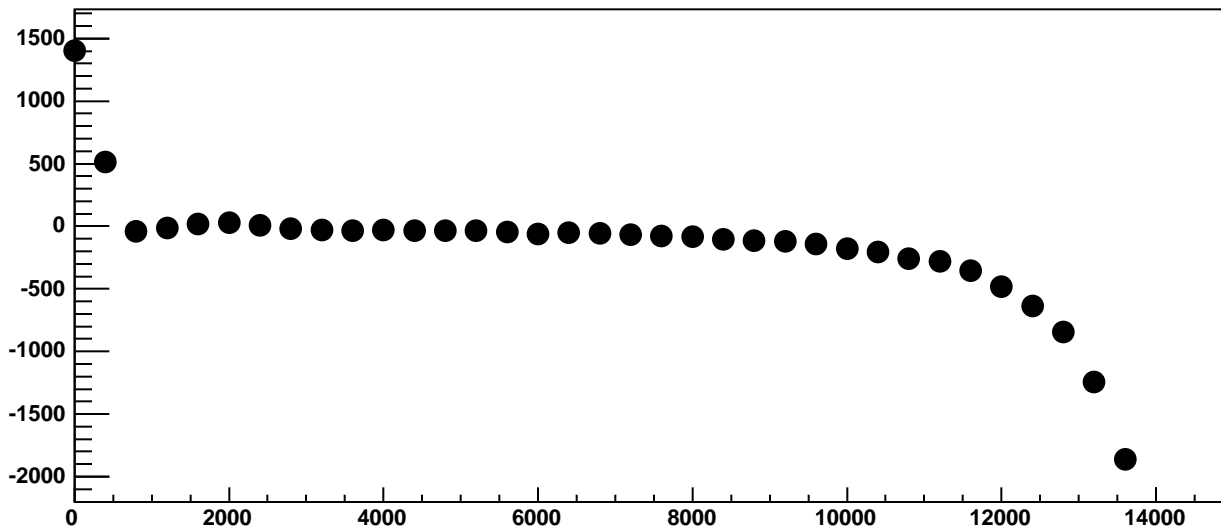
χ^2 / ndf 282.2 / 23
p0 -471.7 ± 3.113
p1 2.264 ± 0.00212



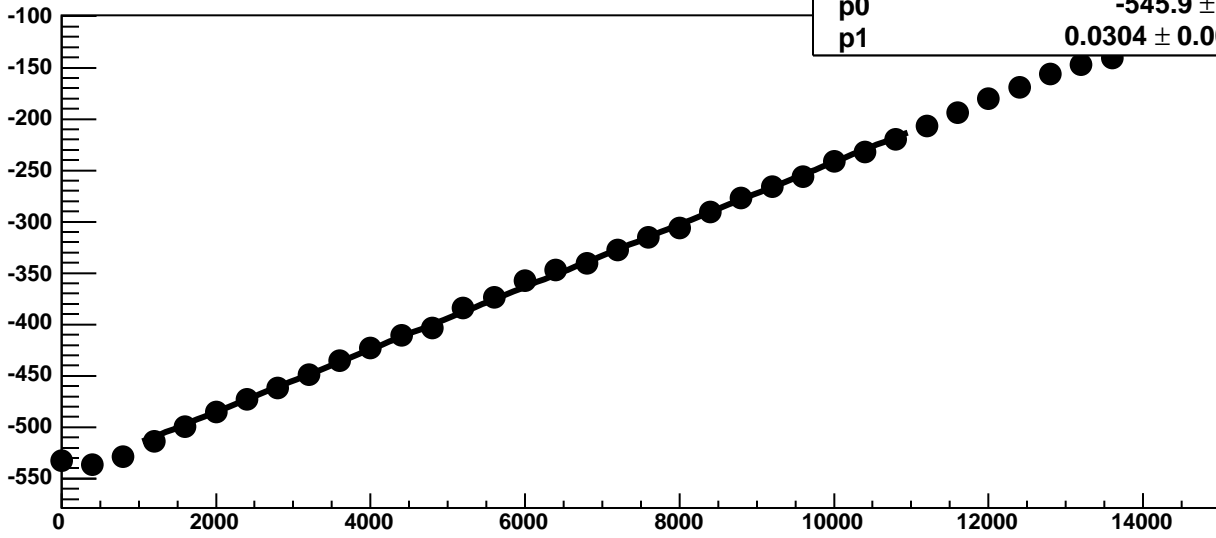
Chip 11, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC

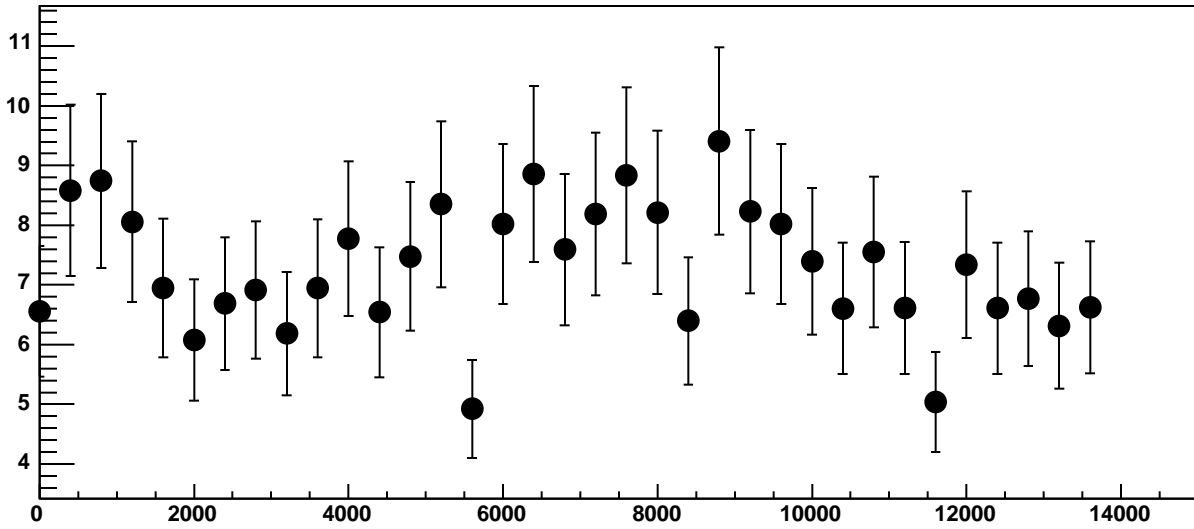


Chip 11, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

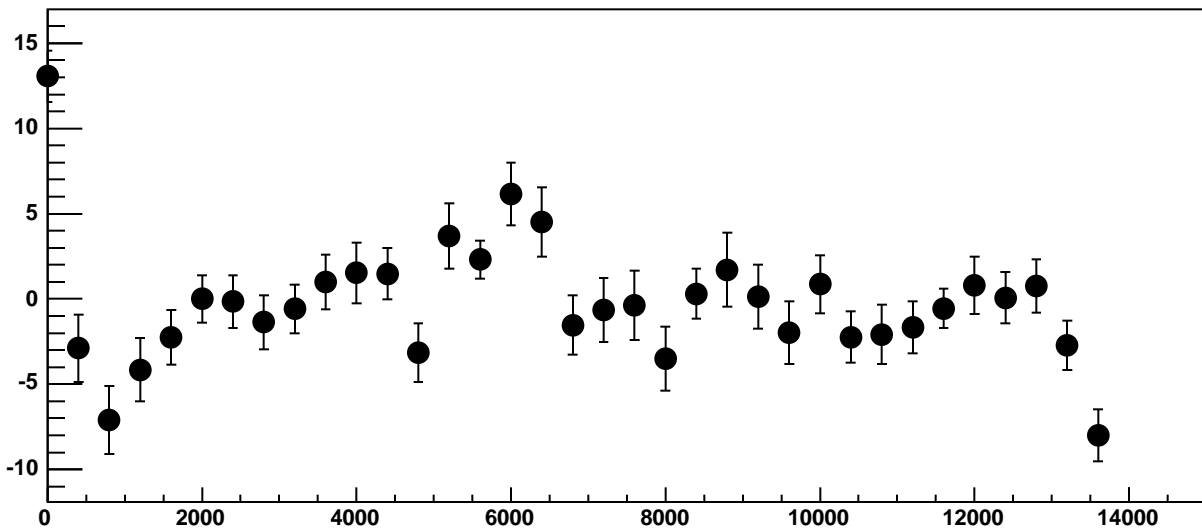


χ^2 / ndf 47.48 / 23
p0 -545.9 ± 0.7368
p1 0.0304 ± 0.0001147

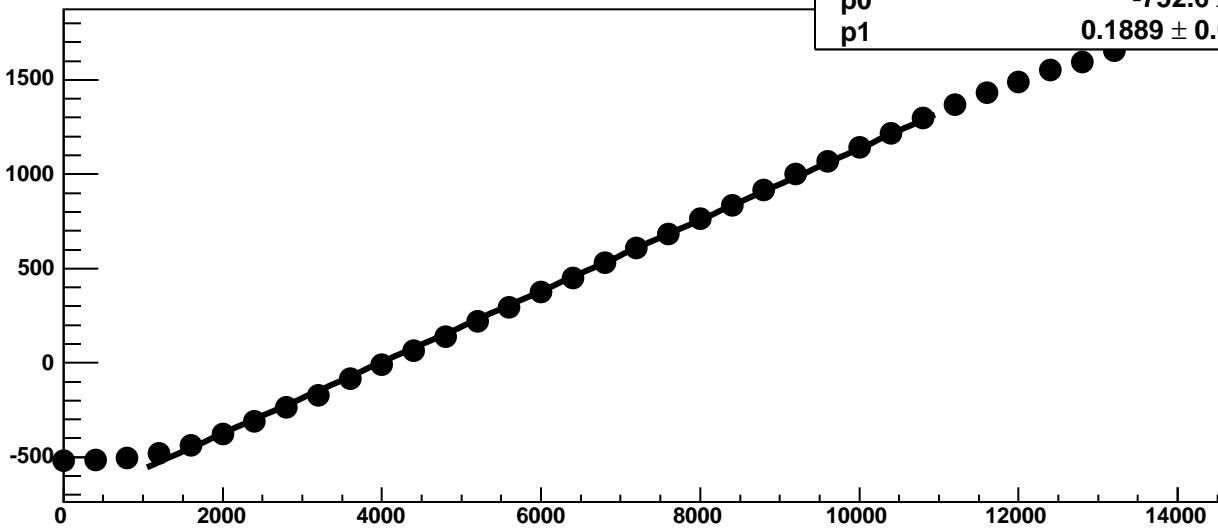
Chip 11, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



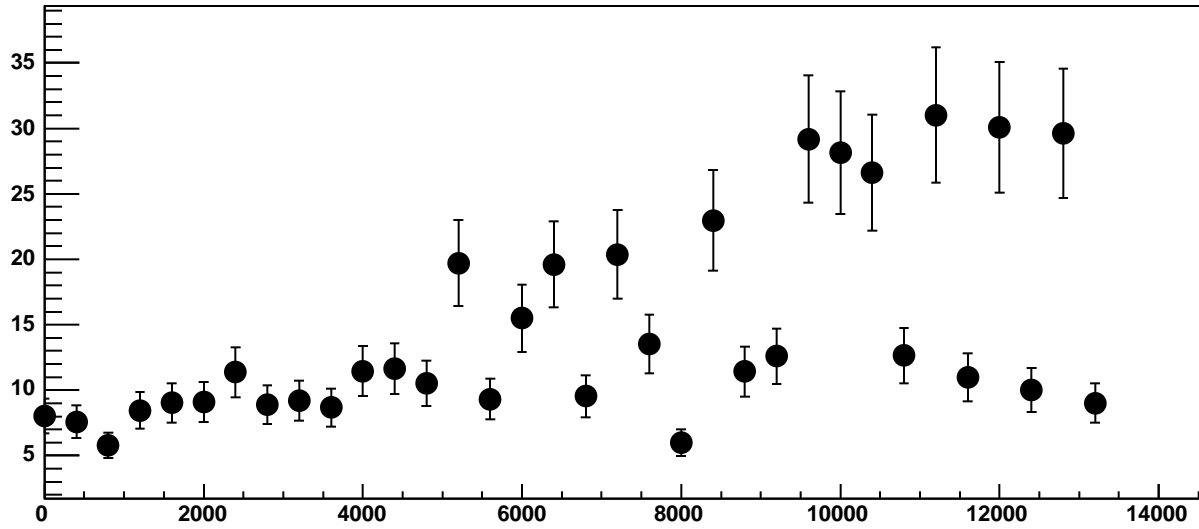
Chip 11, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



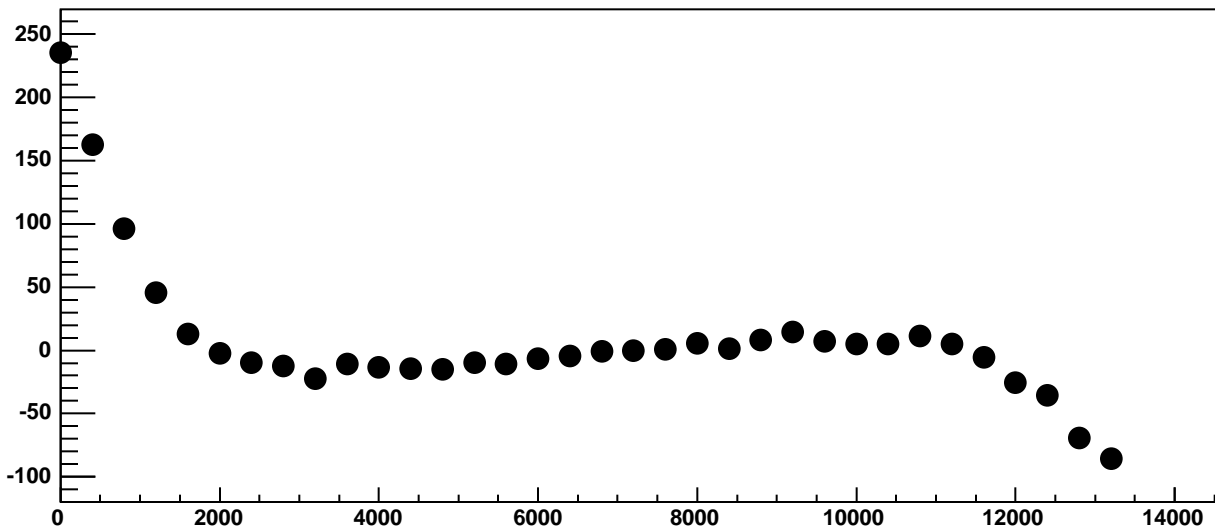
Chip 11, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



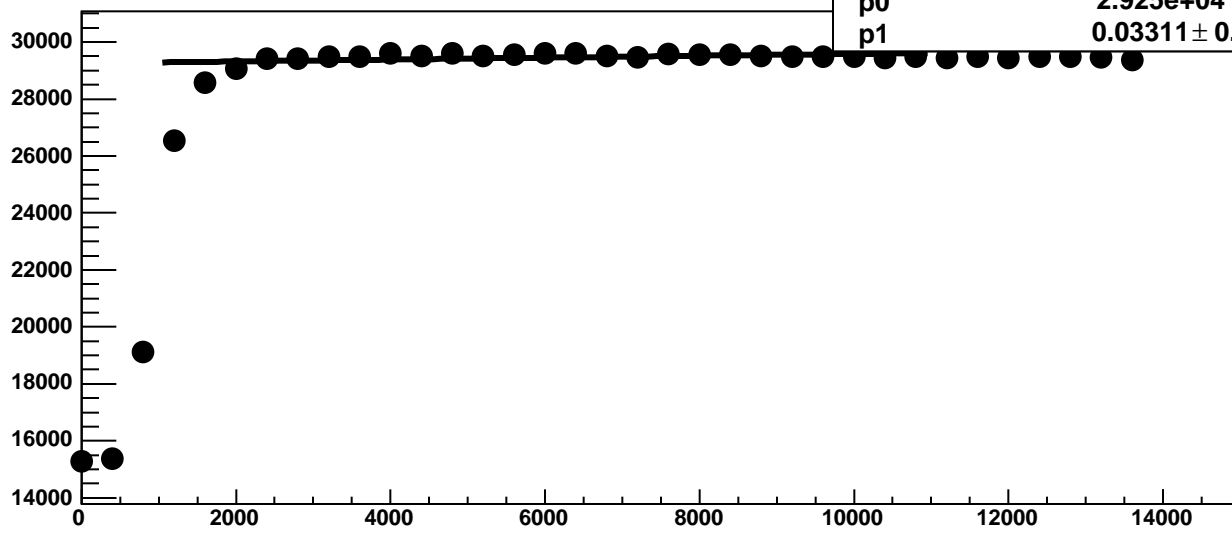
Chip 11, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



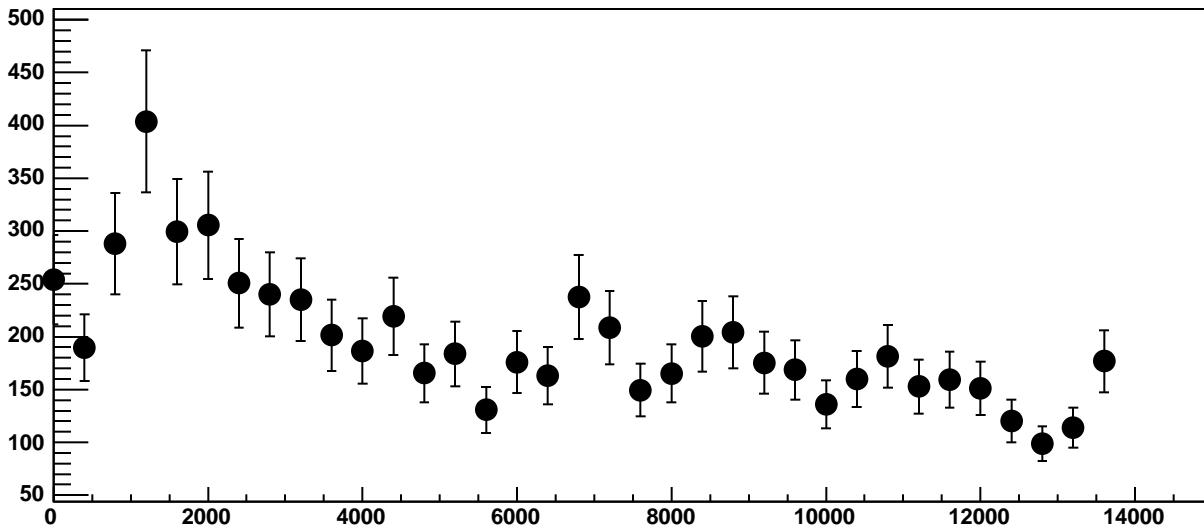
Chip 11, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



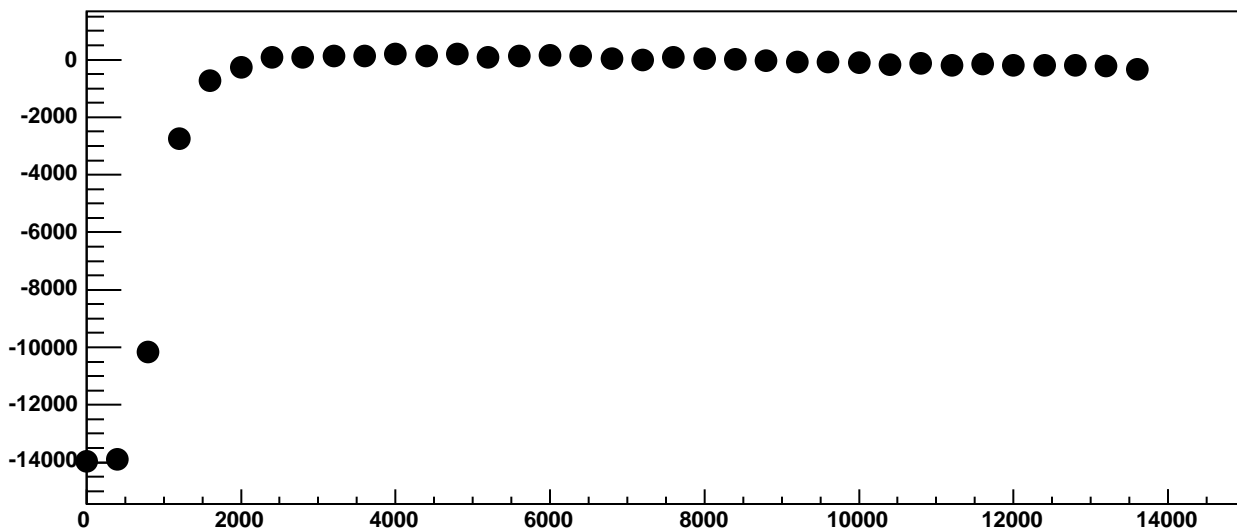
Chip 11, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC



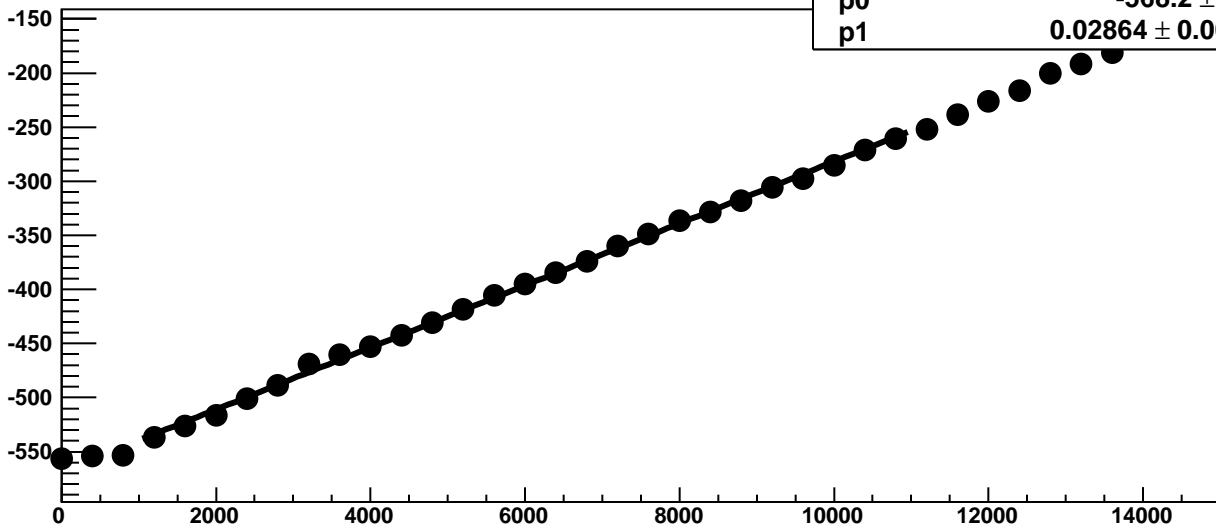
Chip 11, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



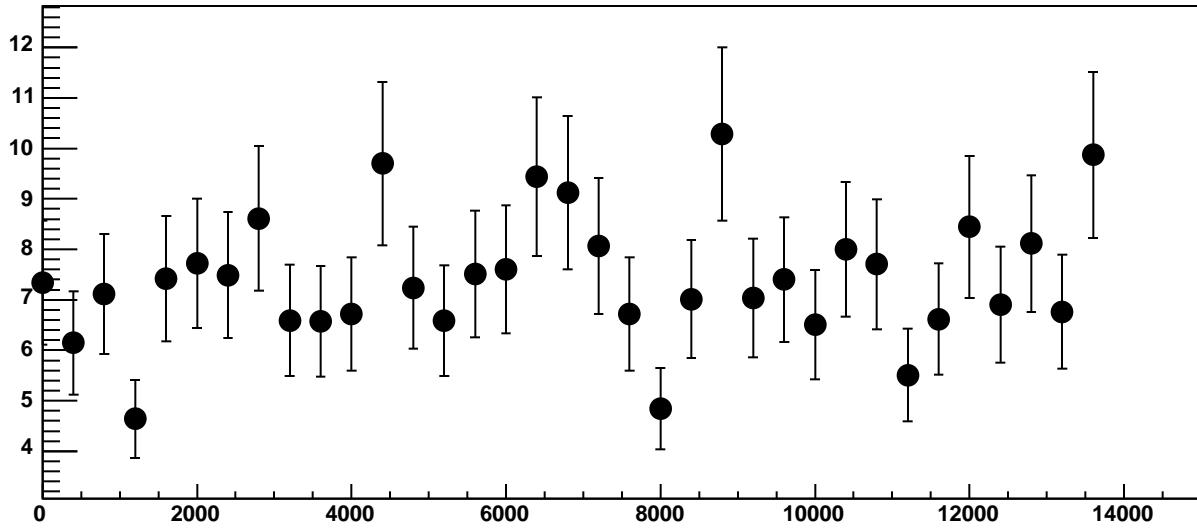
Chip 11, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC



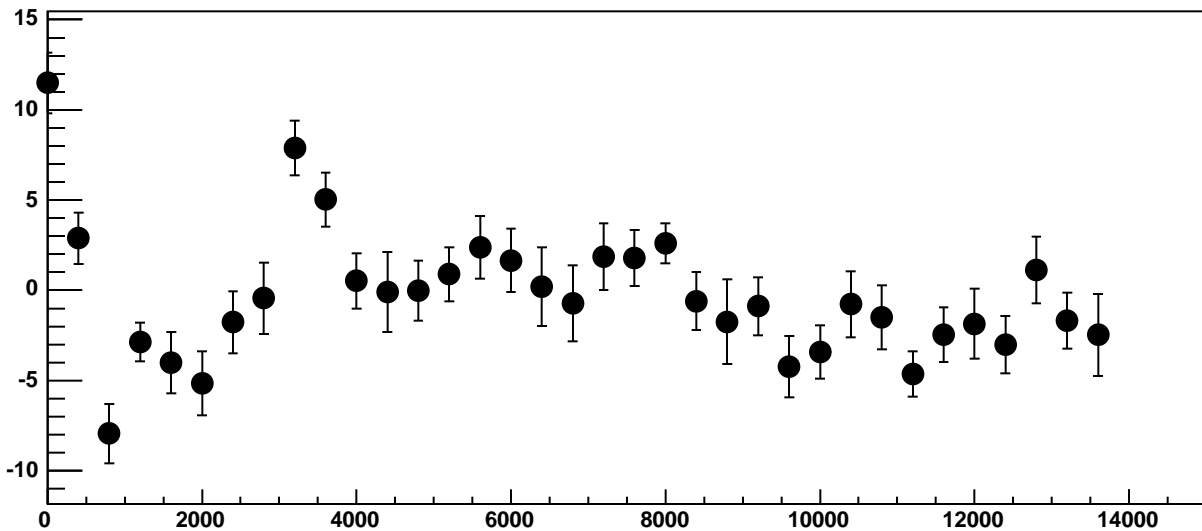
Chip 11, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



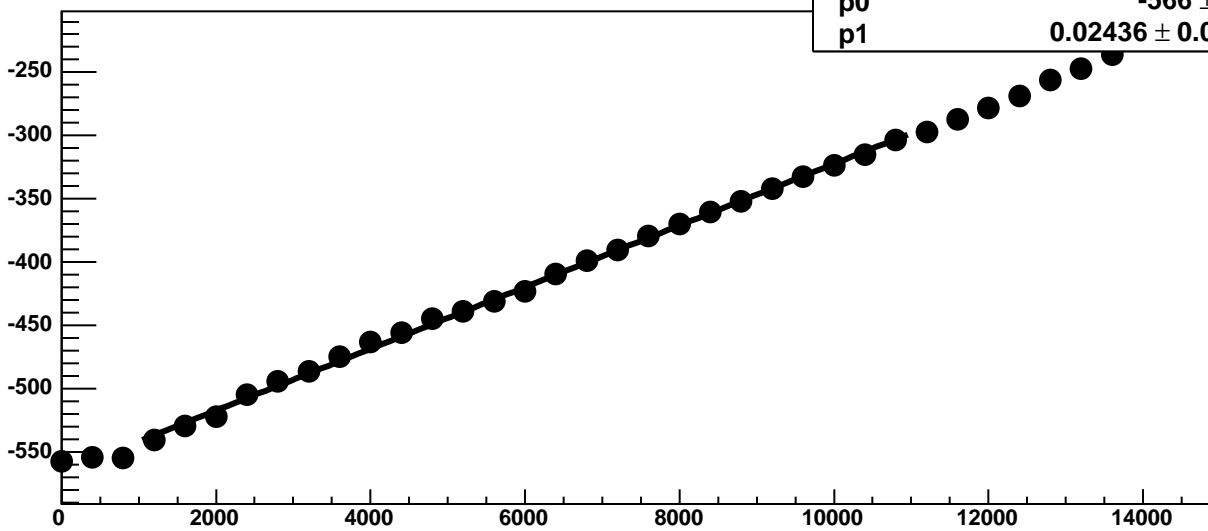
Chip 11, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

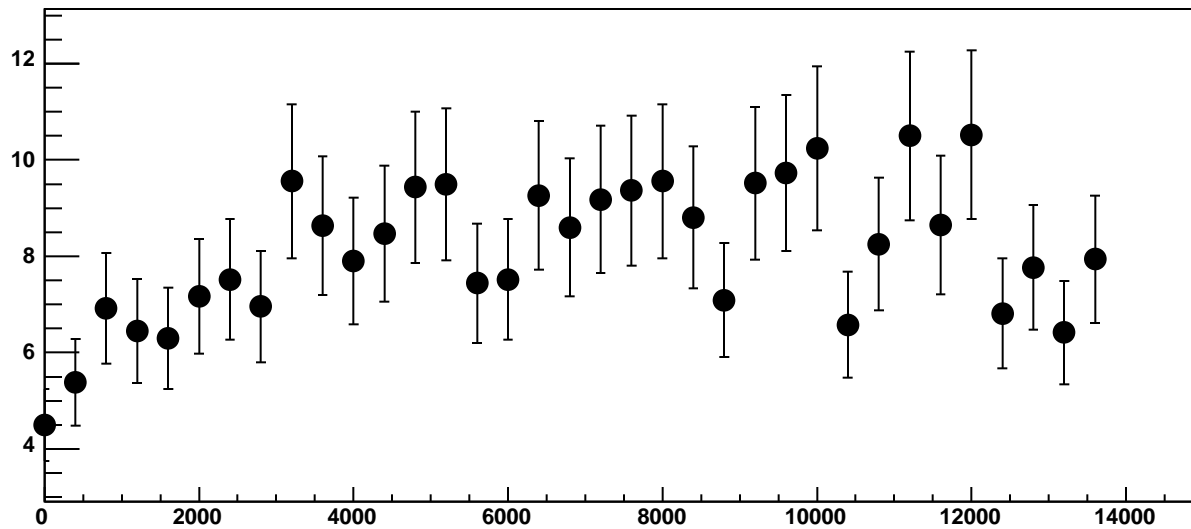


Chip 11, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

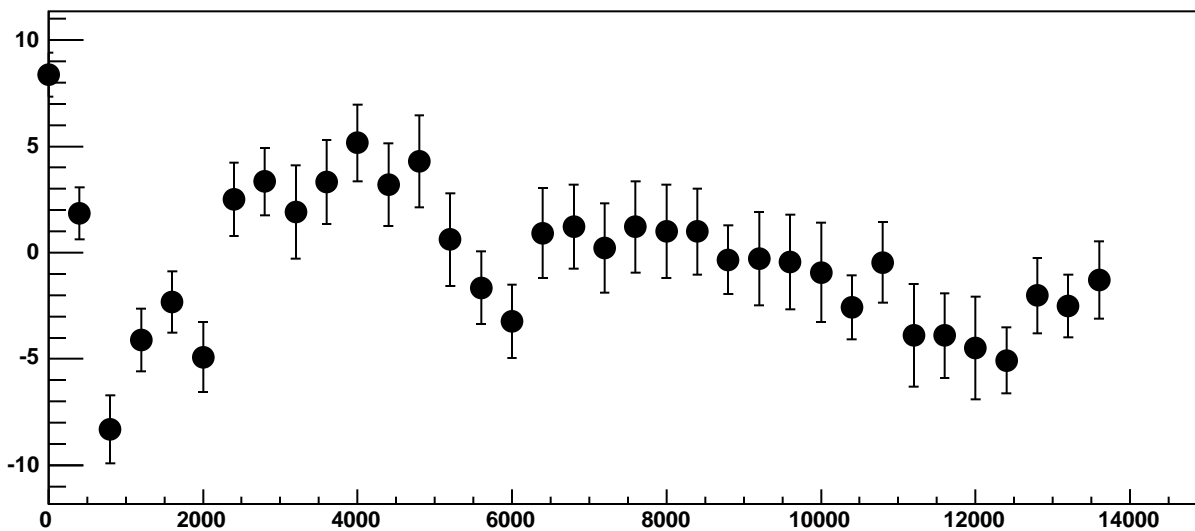


χ^2 / ndf 53.2 / 23
p0 -566 ± 0.7799
p1 0.02436 ± 0.0001218

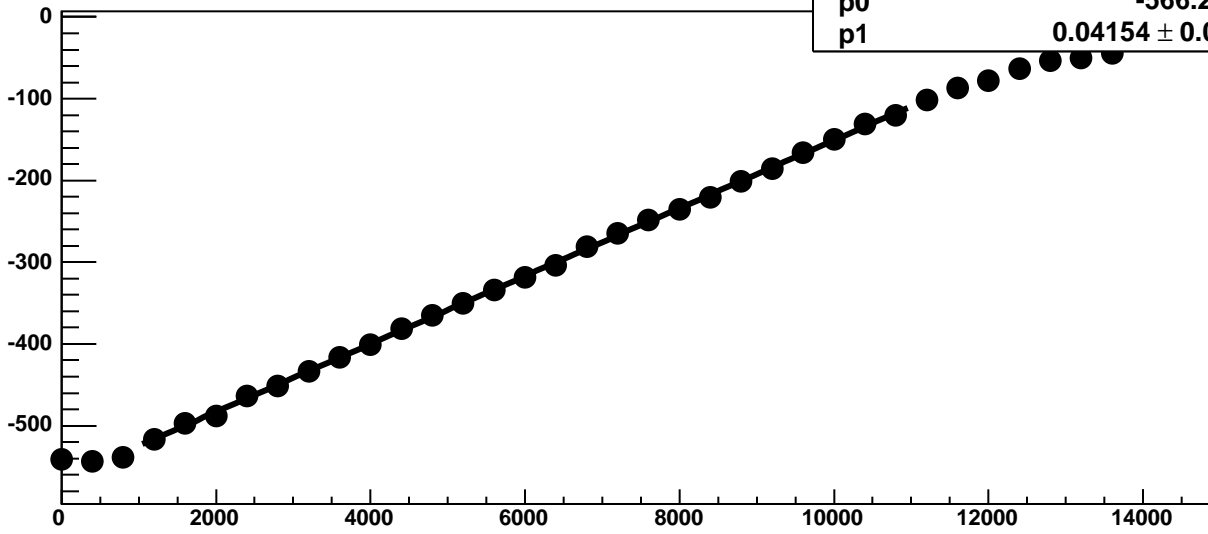
Chip 11, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

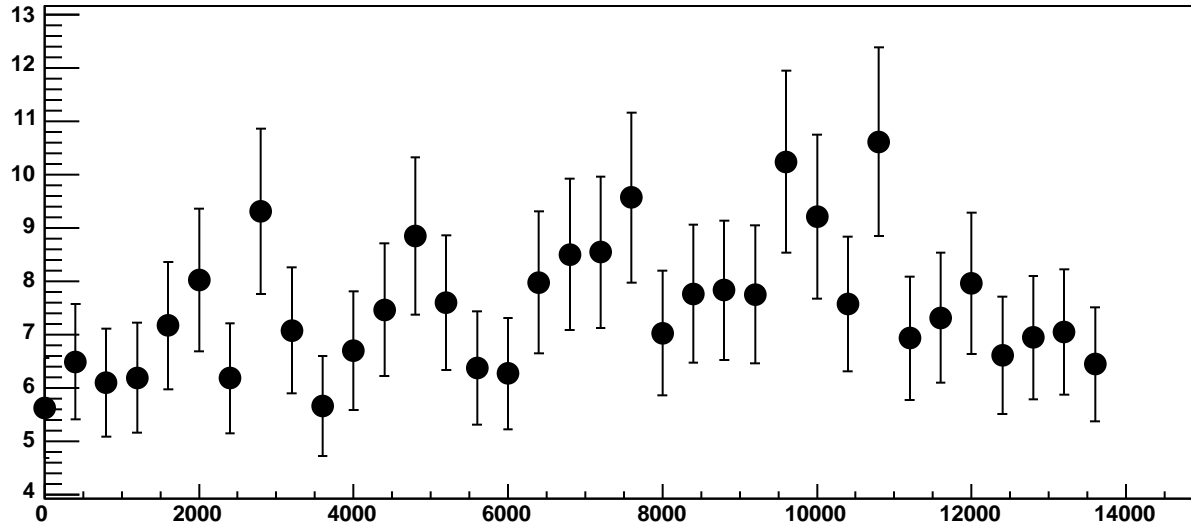


Chip 11, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC

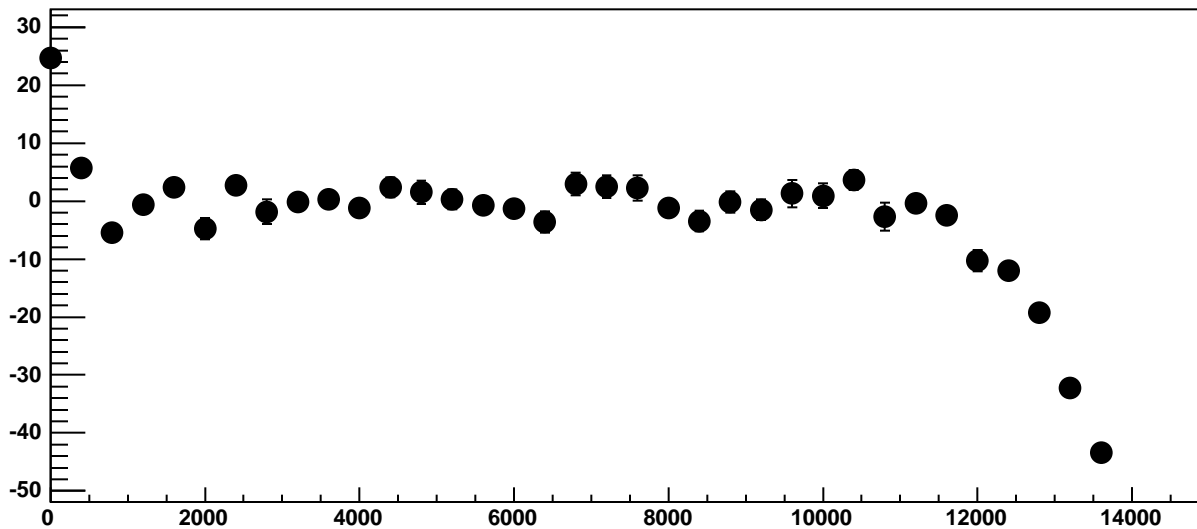


χ^2 / ndf 38.03 / 23
p0 -566.2 ± 0.761
p1 0.04154 ± 0.0001231

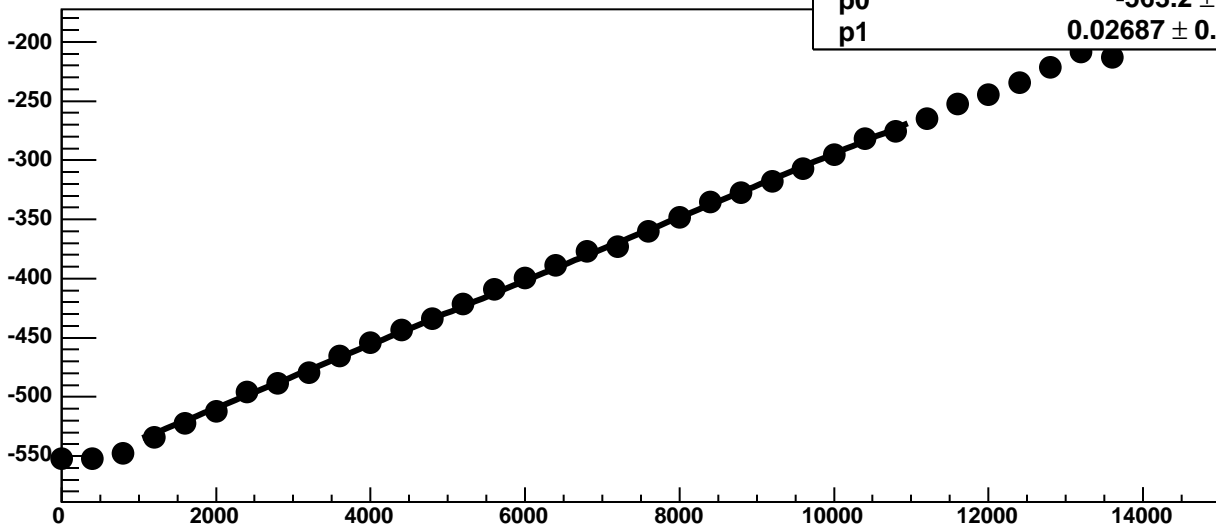
Chip 11, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

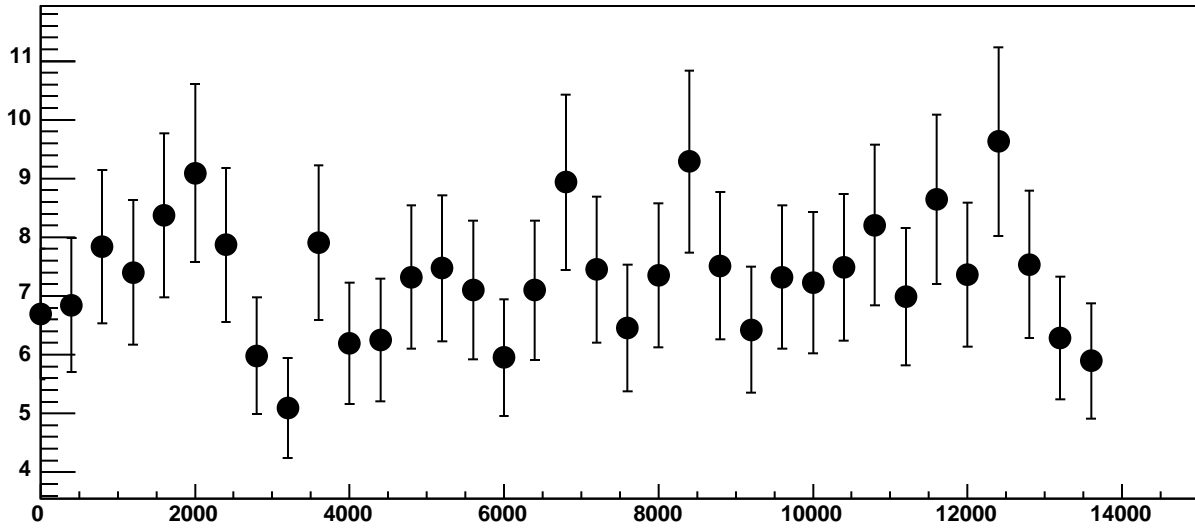


Chip 11, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

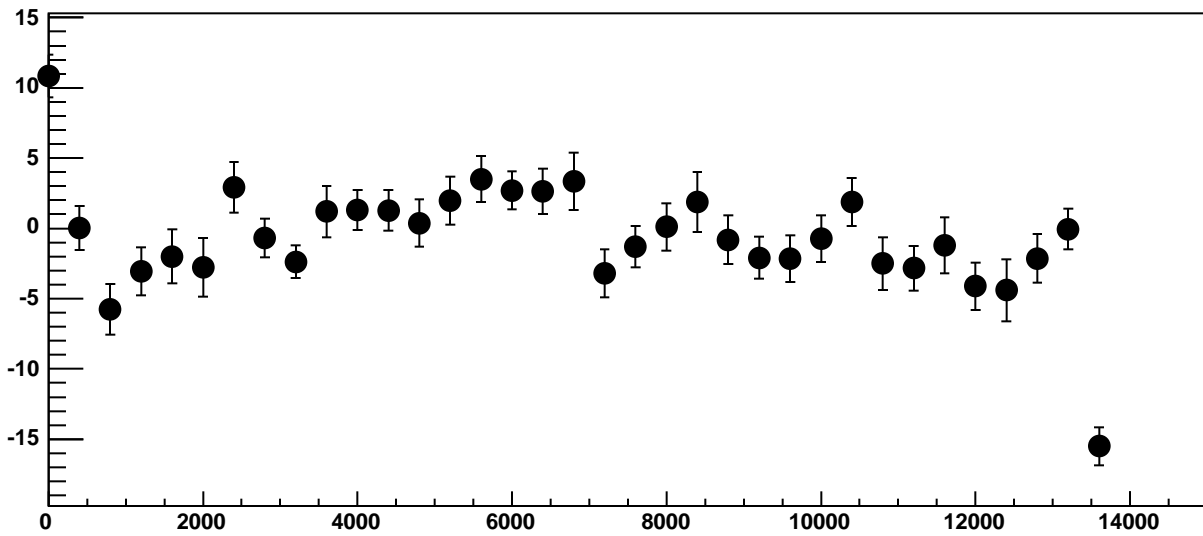


χ^2 / ndf 42.33 / 23
p0 -563.2 ± 0.7596
p1 0.02687 ± 0.000117

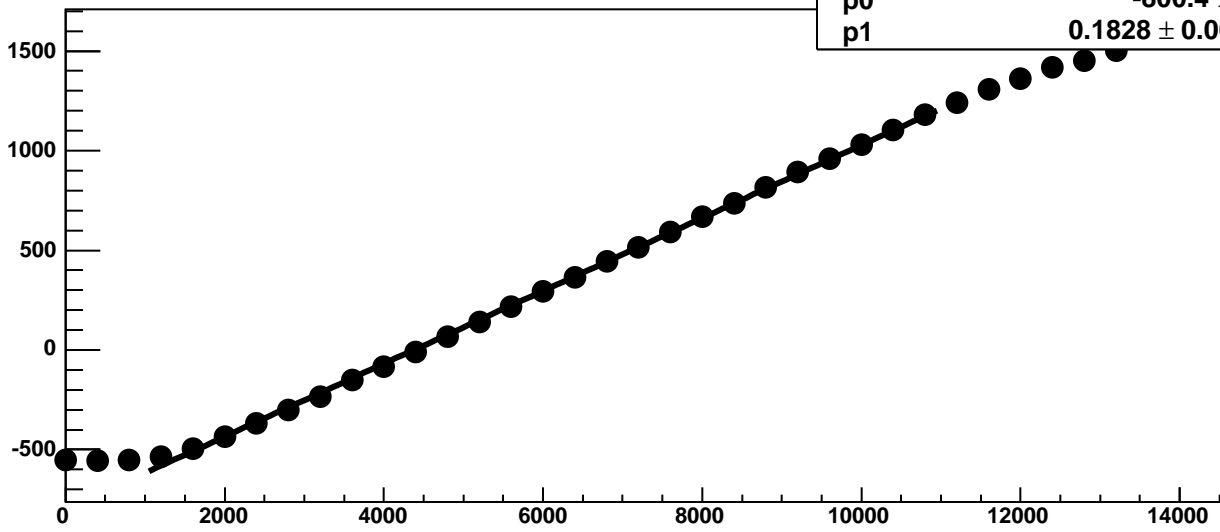
Chip 11, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



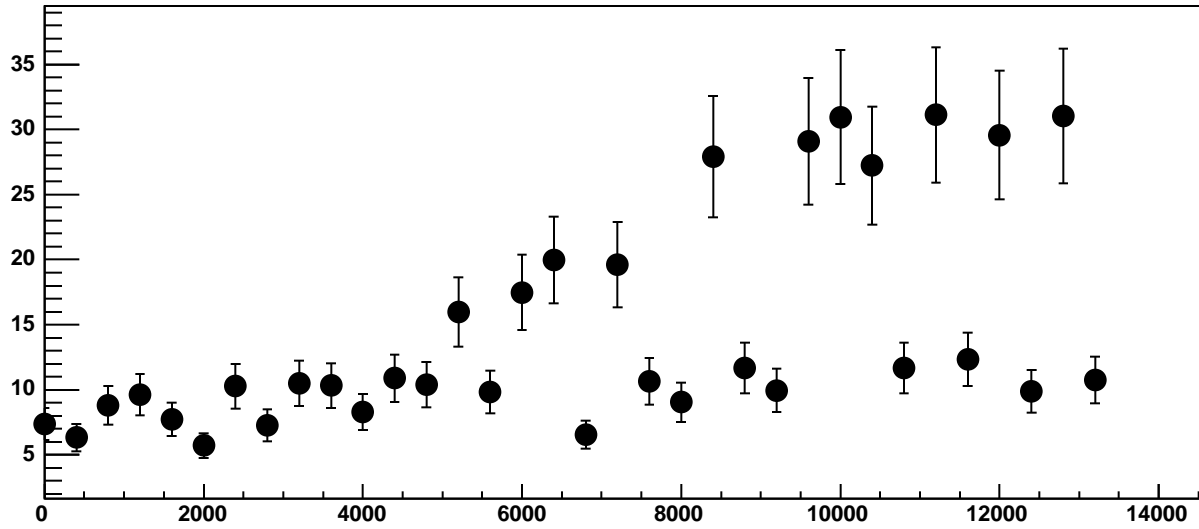
Chip 11, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



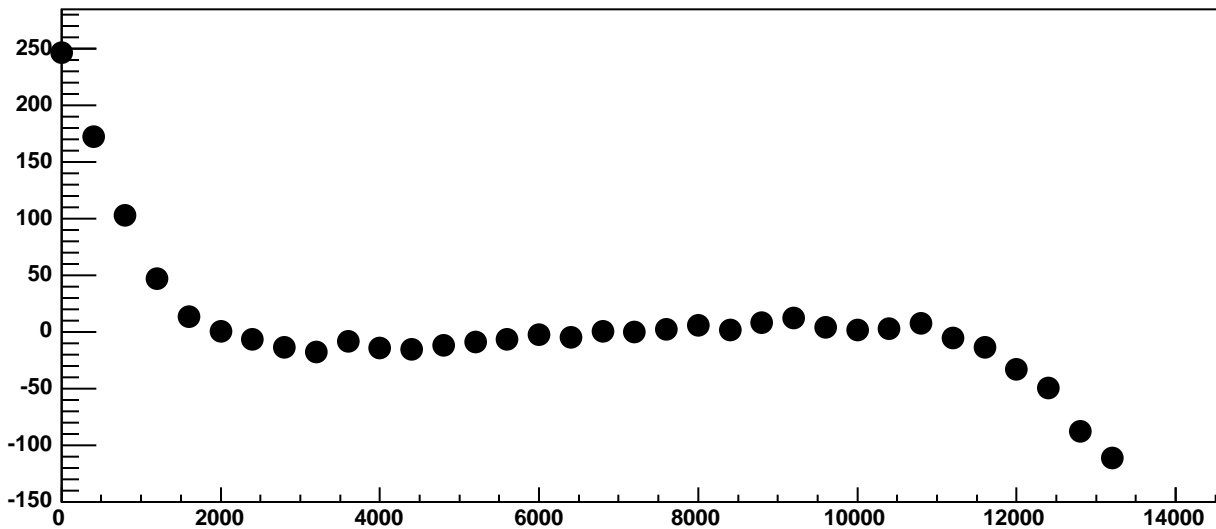
Chip 11, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



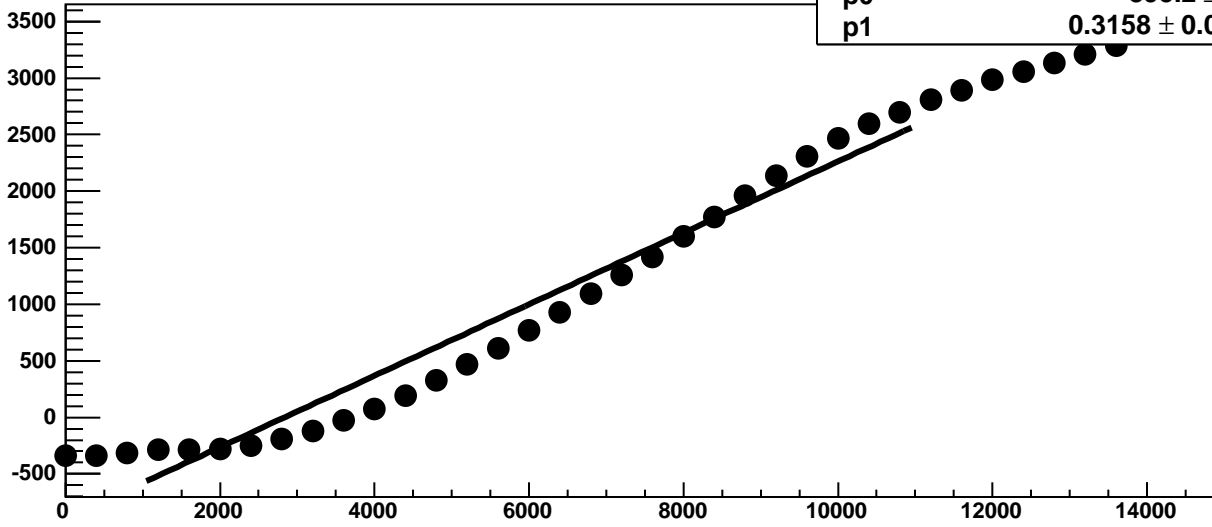
Chip 11, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



Chip 11, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

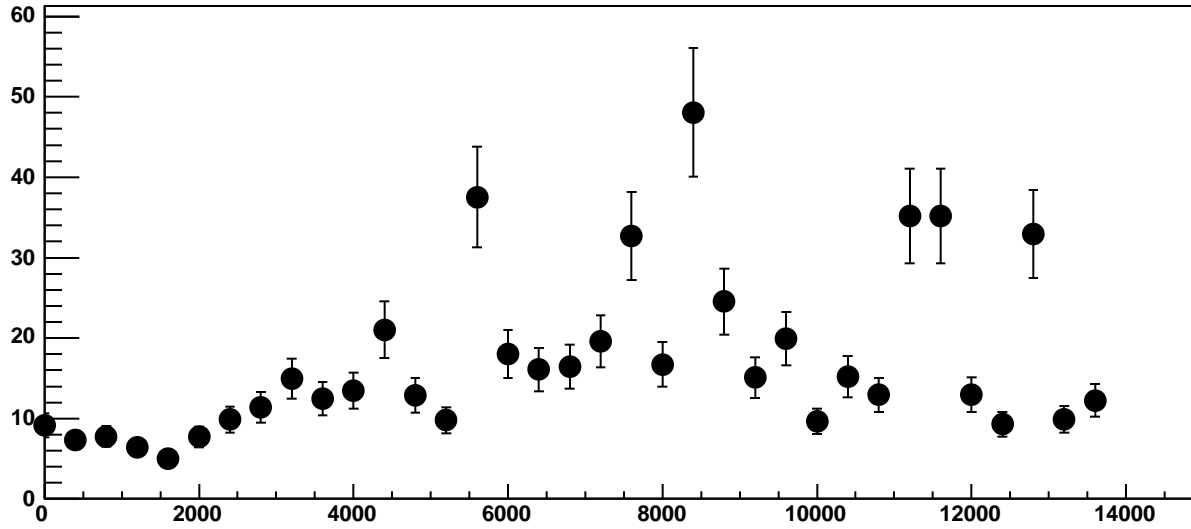


Chip 11, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC

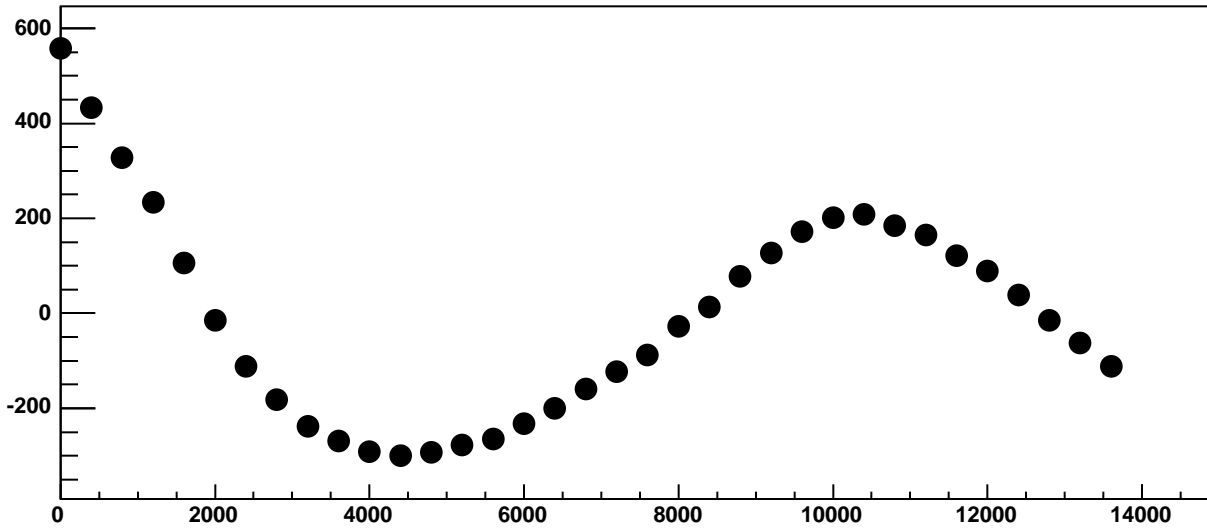


χ^2 / ndf 1.211e+05 / 23
p0 -895.2 ± 0.8849
p1 0.3158 ± 0.0001707

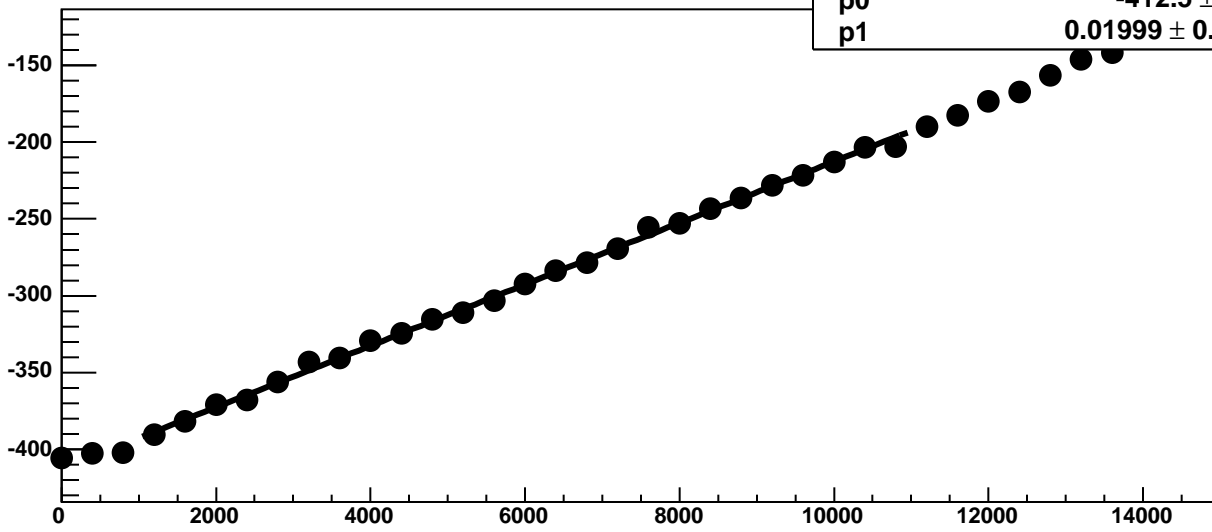
Chip 11, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC

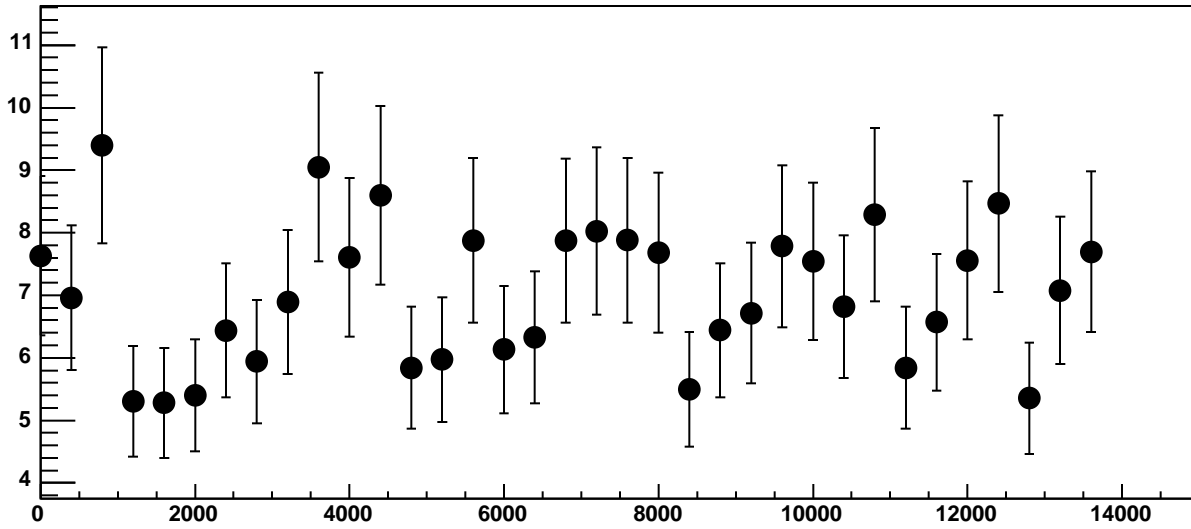


Chip 11, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

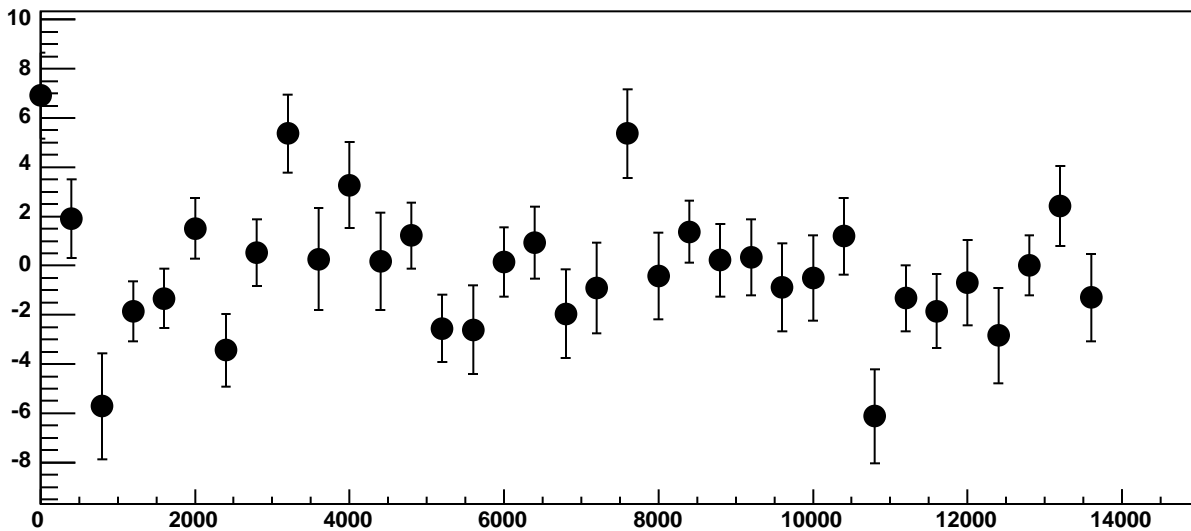


χ^2 / ndf 55.28 / 23
p0 -412.5 ± 0.6506
p1 0.01999 ± 0.000103

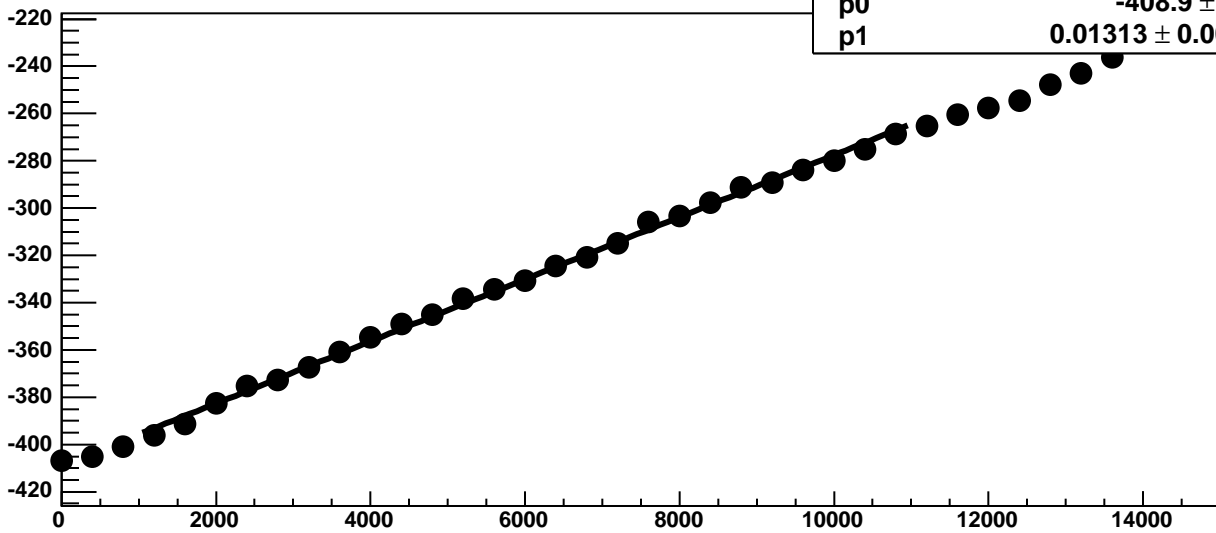
Chip 11, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

23.15 / 23

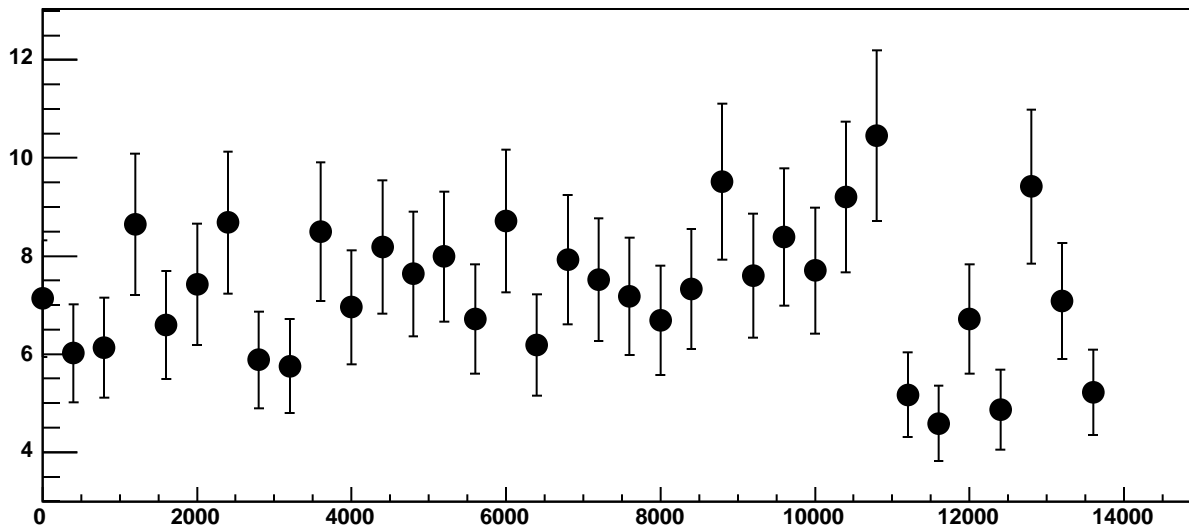
p0

-408.9 ± 0.7883

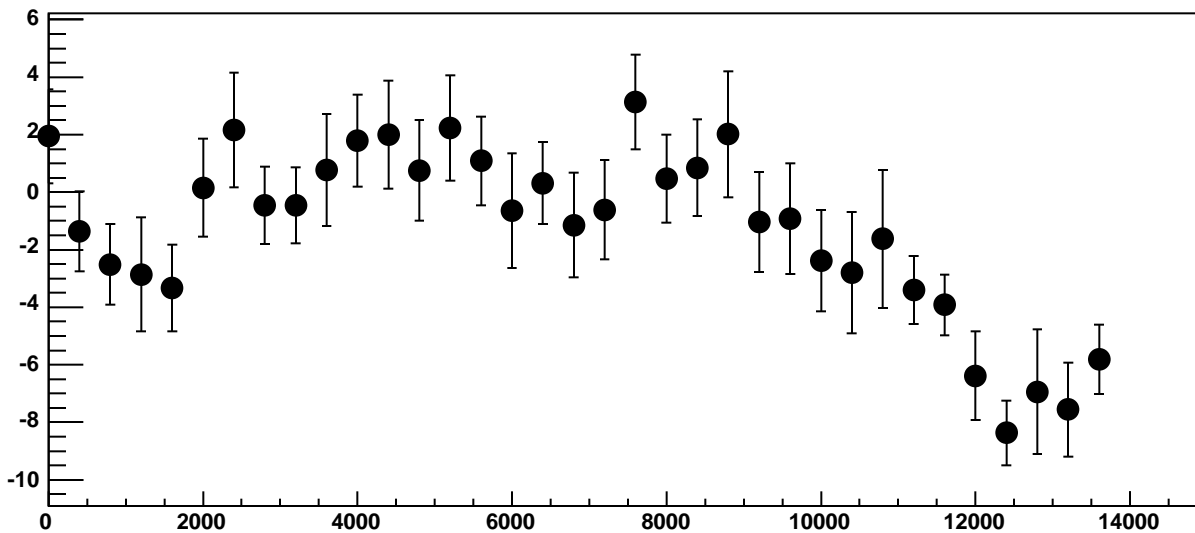
p1

0.01313 ± 0.0001246

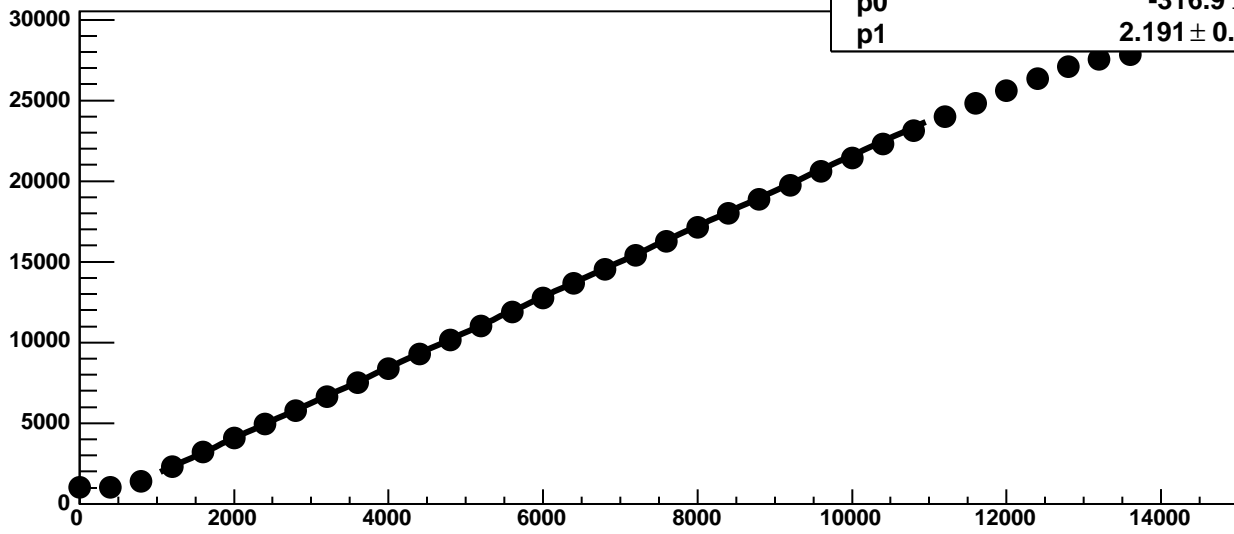
Chip 11, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

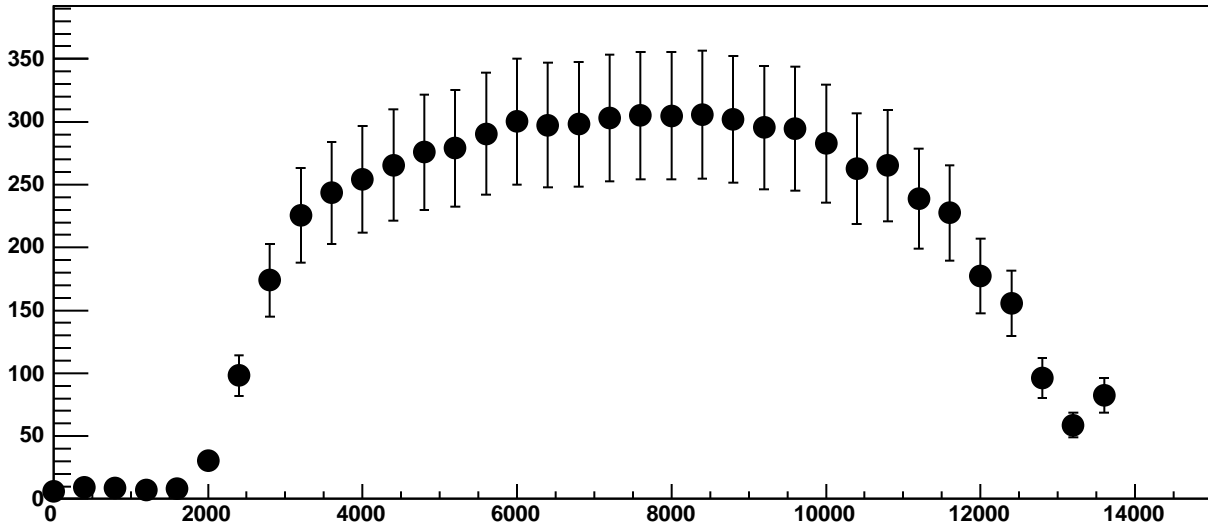


Chip 11, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC

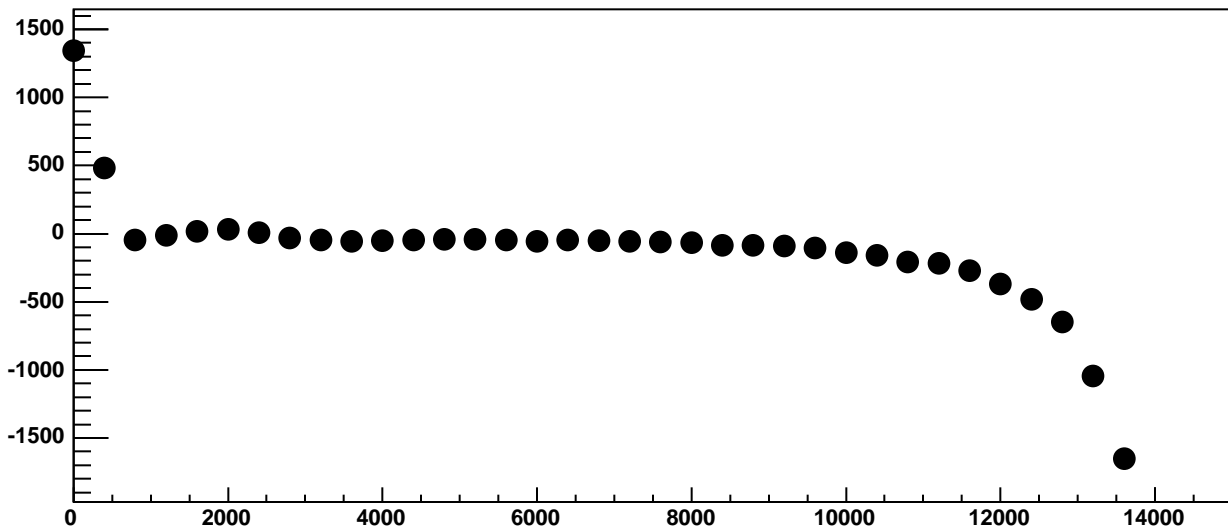


χ^2 / ndf 234.8 / 23
p0 -316.9 ± 3.375
p1 2.191 ± 0.002201

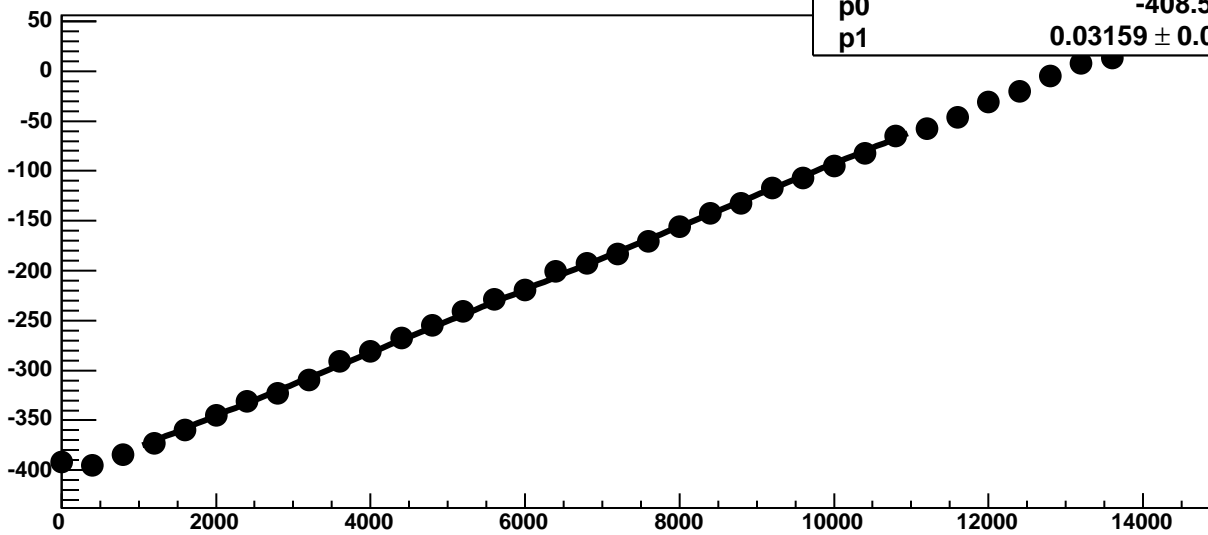
Chip 11, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

54.82 / 23

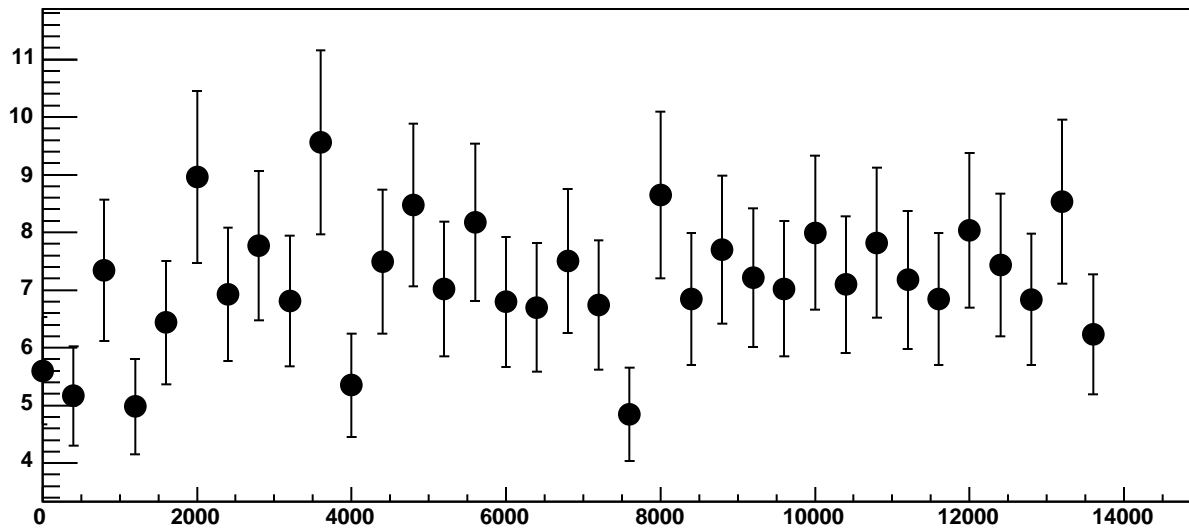
p0

-408.5 ± 0.711

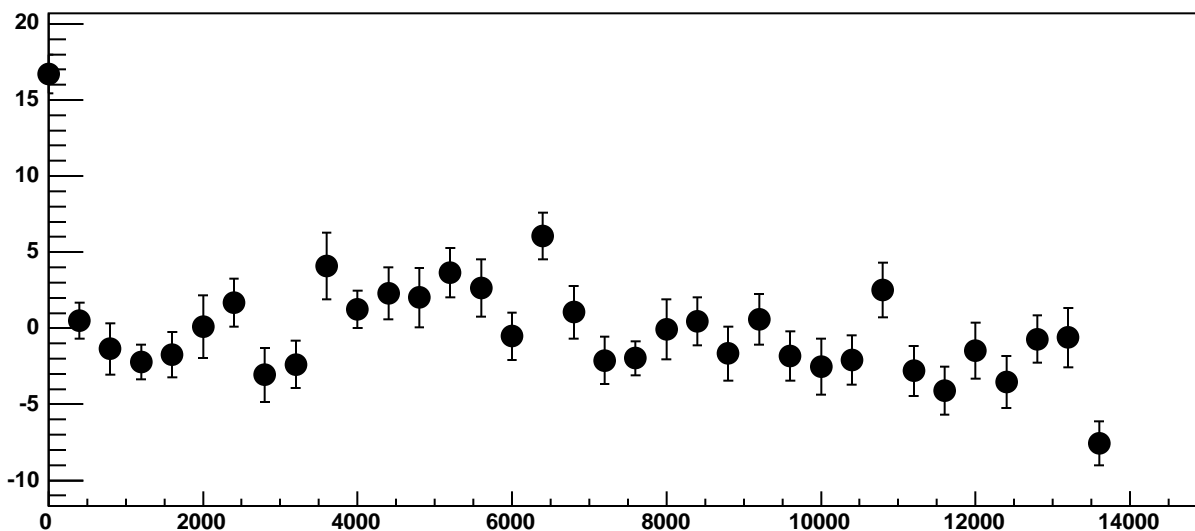
p1

0.03159 ± 0.0001093

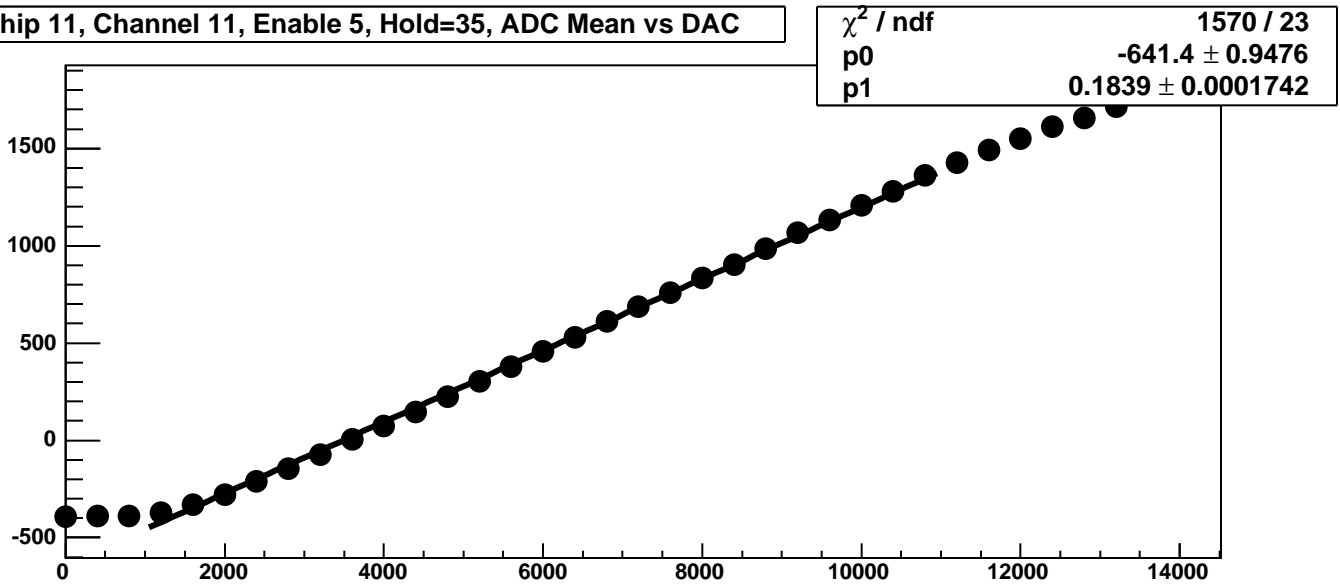
Chip 11, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



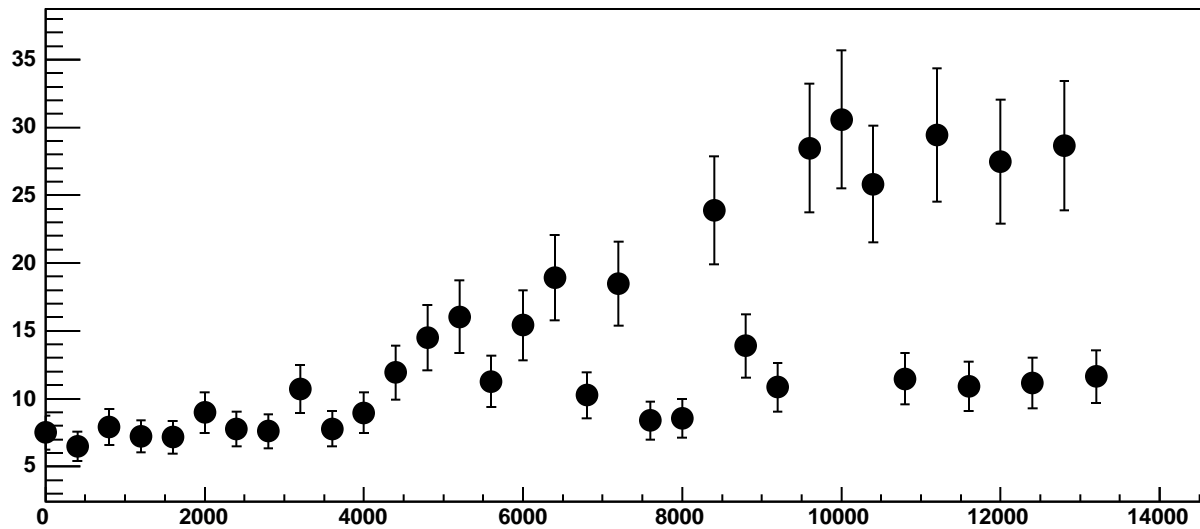
Chip 11, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



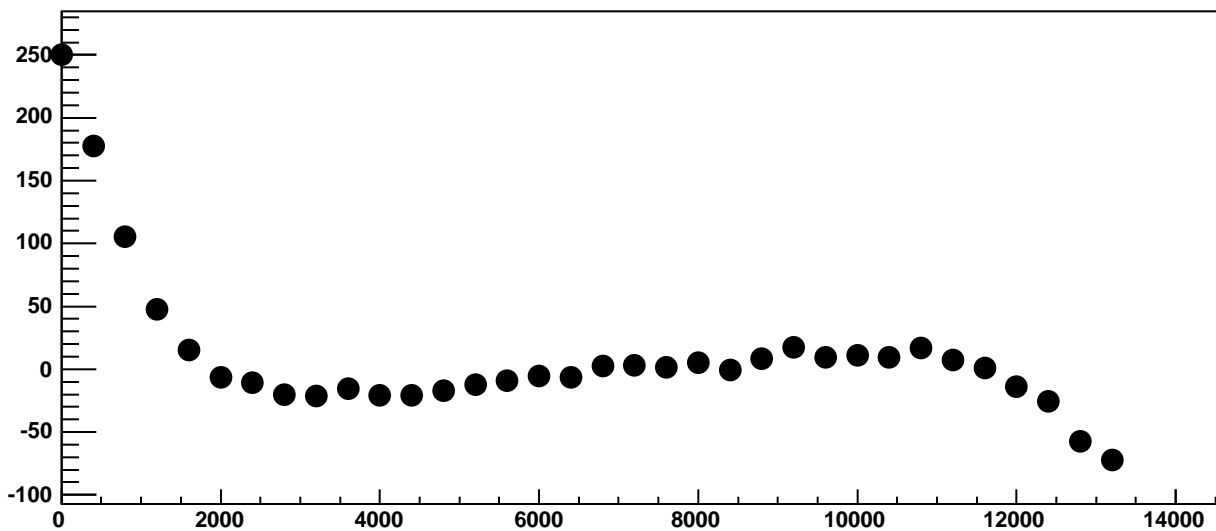
Chip 11, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



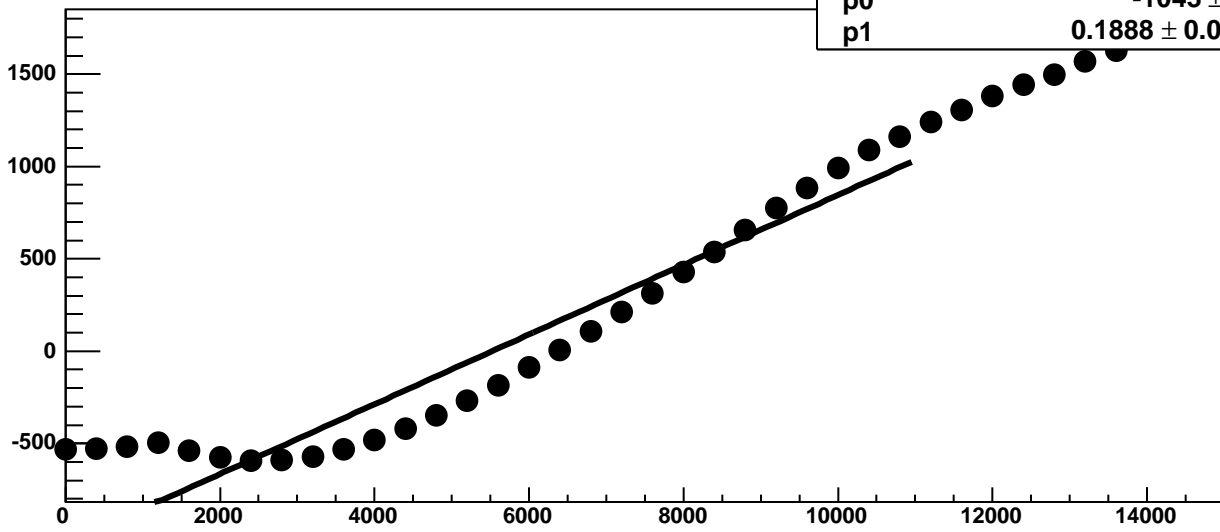
Chip 11, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



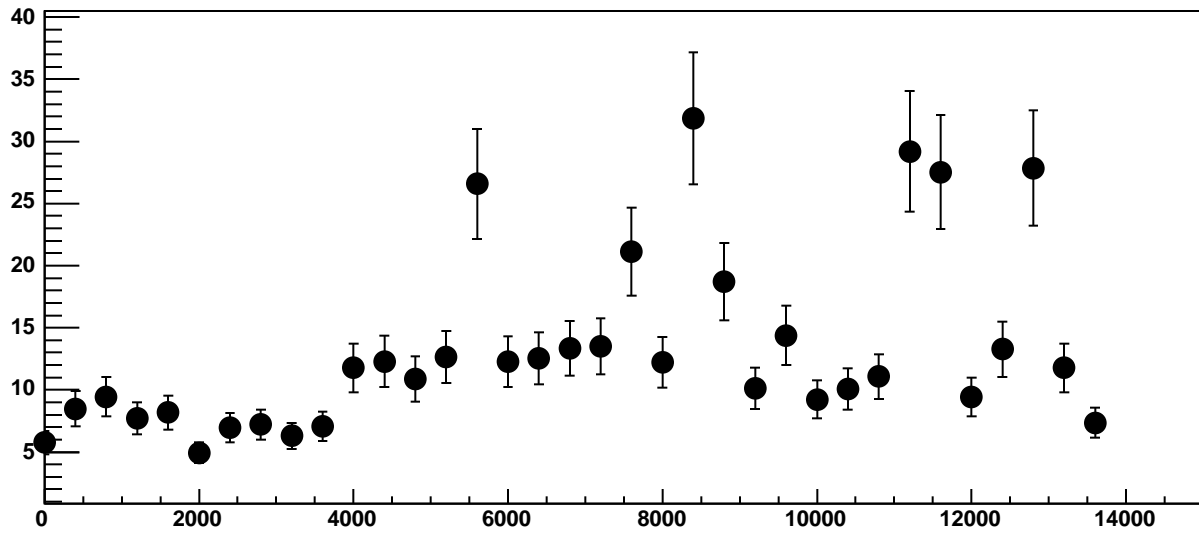
Chip 11, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



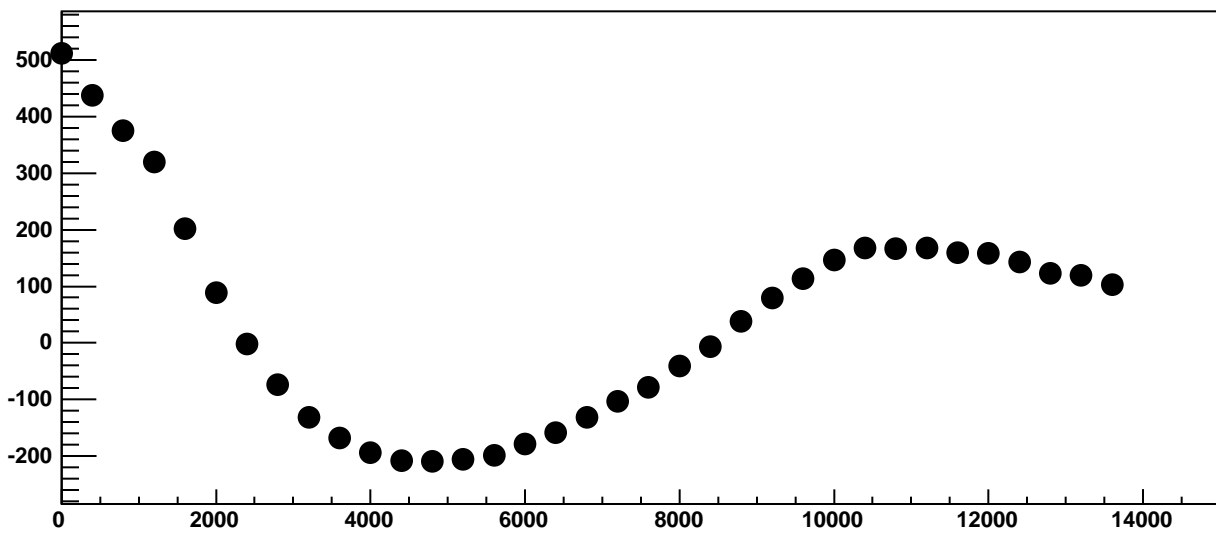
Chip 11, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



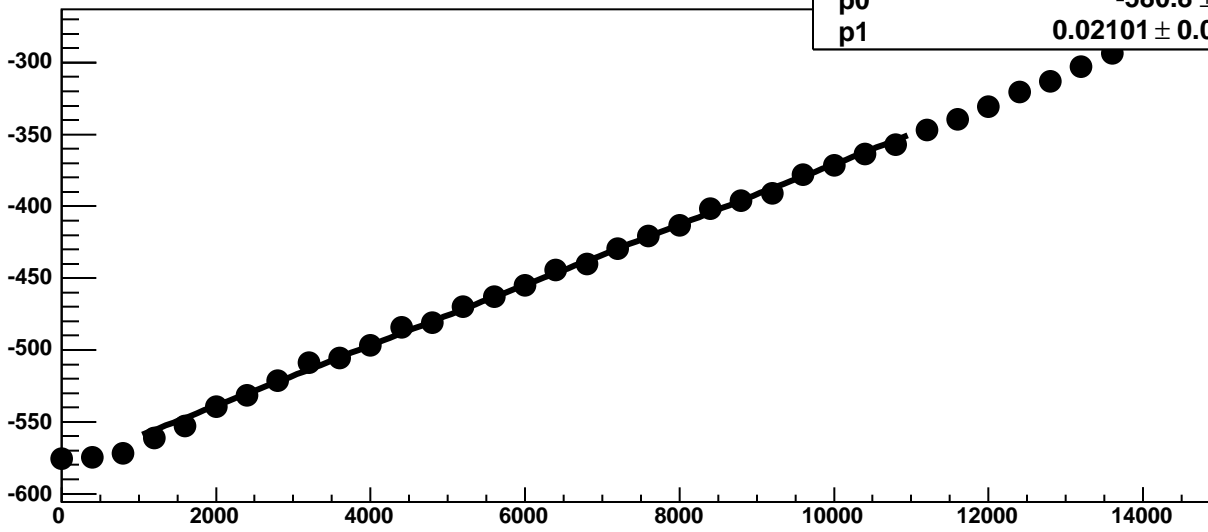
Chip 11, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

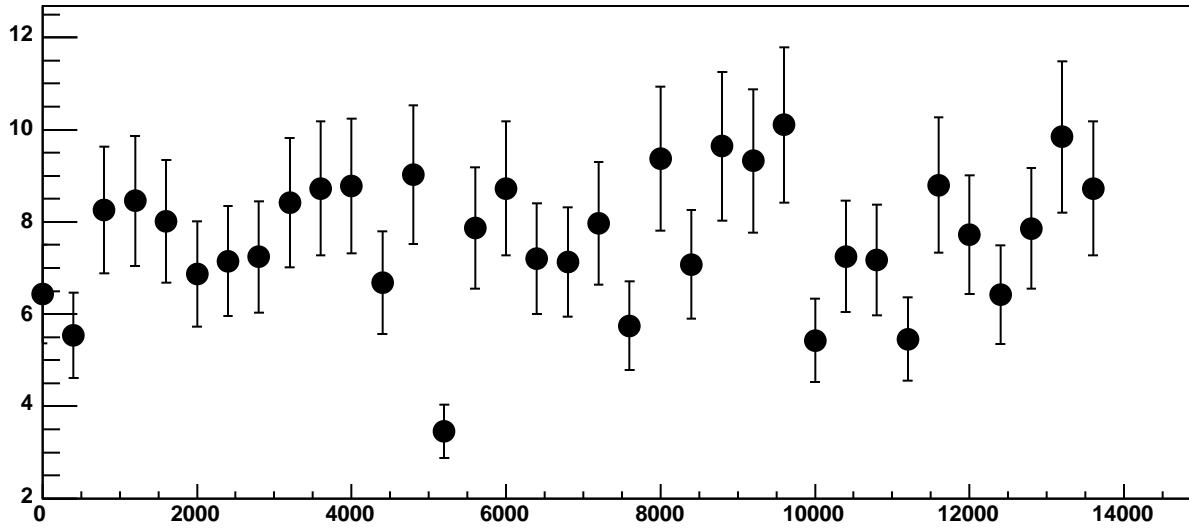


Chip 11, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

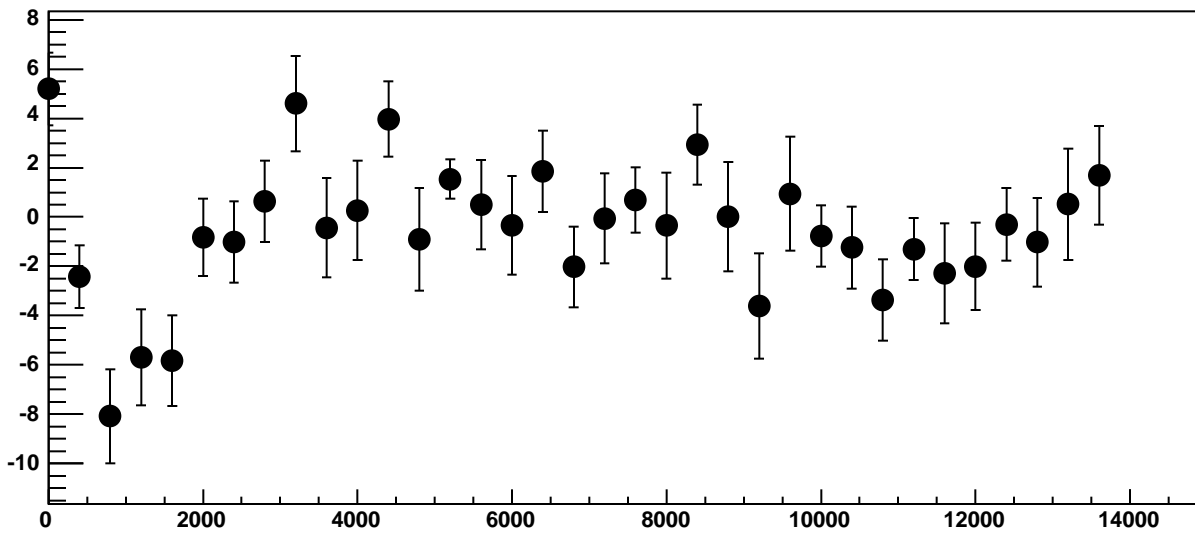


χ^2 / ndf 50.53 / 23
p0 -580.8 ± 0.7828
p1 0.02101 ± 0.0001185

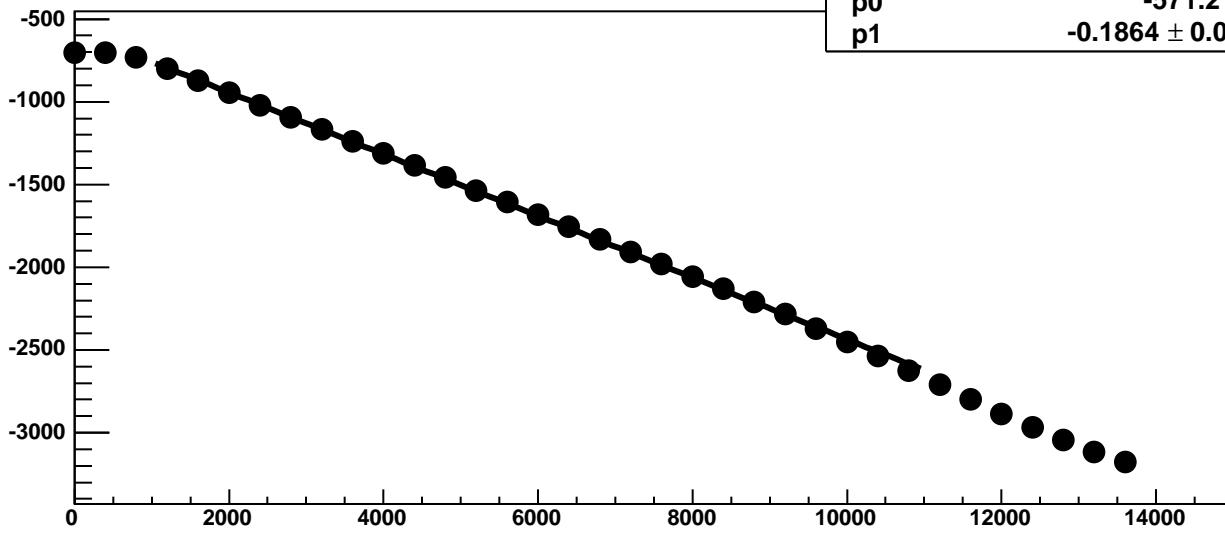
Chip 11, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC

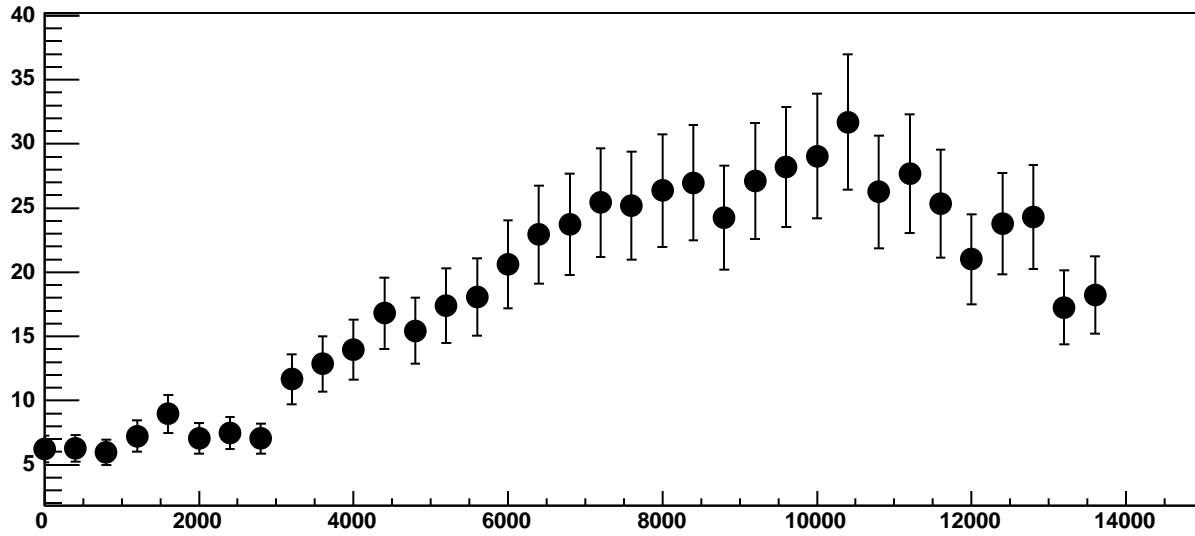


Chip 11, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

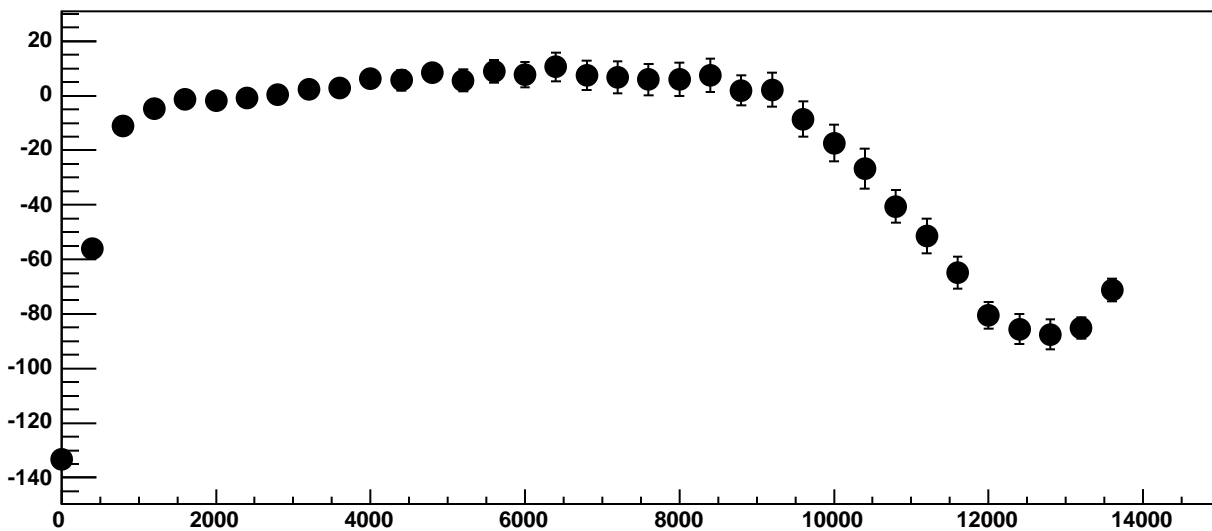


χ^2 / ndf 110.8 / 23
p0 -571.2 ± 1.092
p1 -0.1864 ± 0.0002687

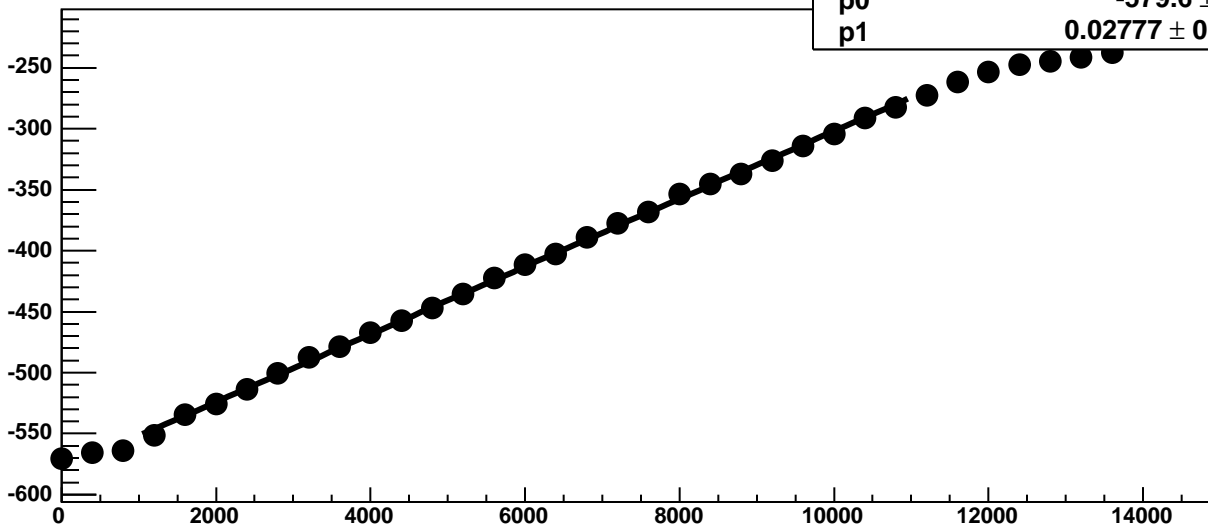
Chip 11, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

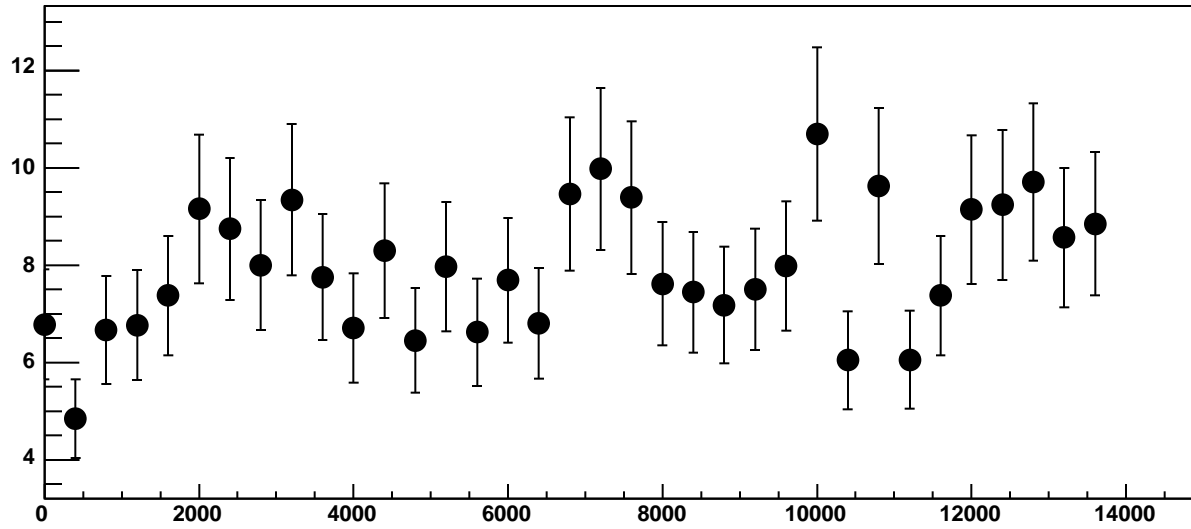


Chip 11, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

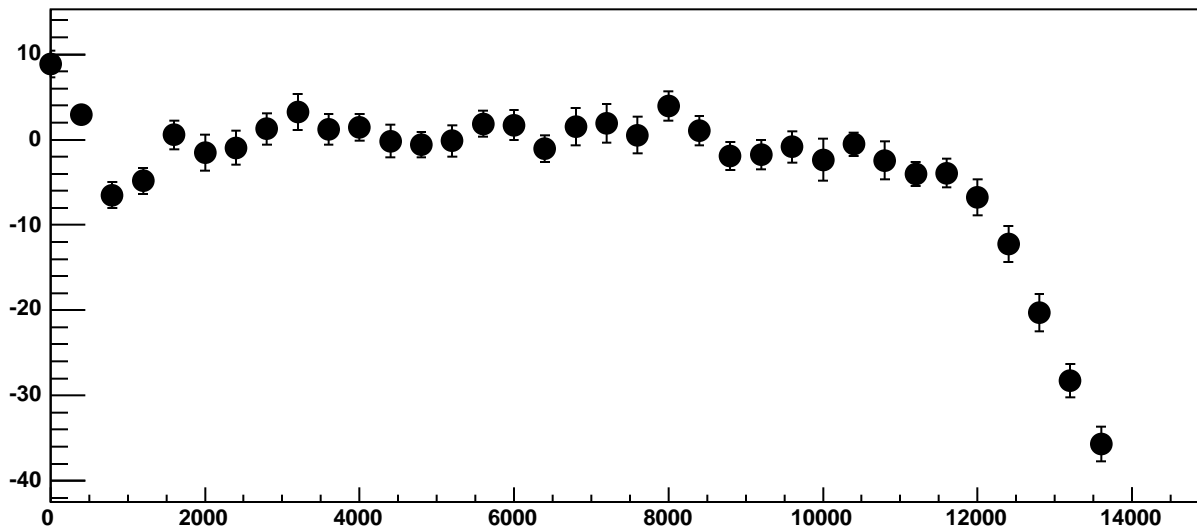


χ^2 / ndf 29.46 / 23
p0 -579.6 ± 0.8197
p1 0.02777 ± 0.000124

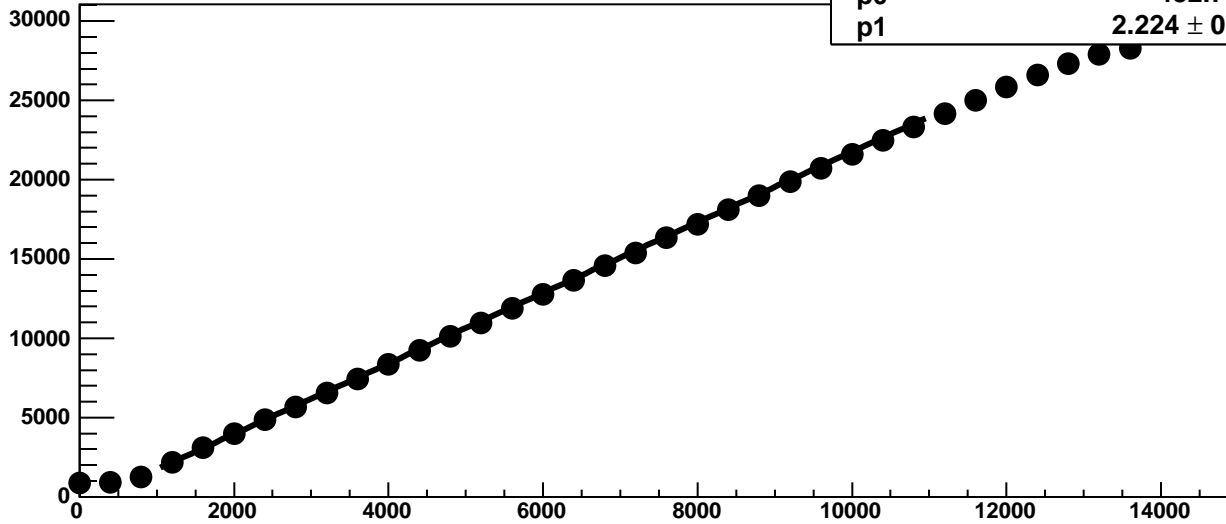
Chip 11, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



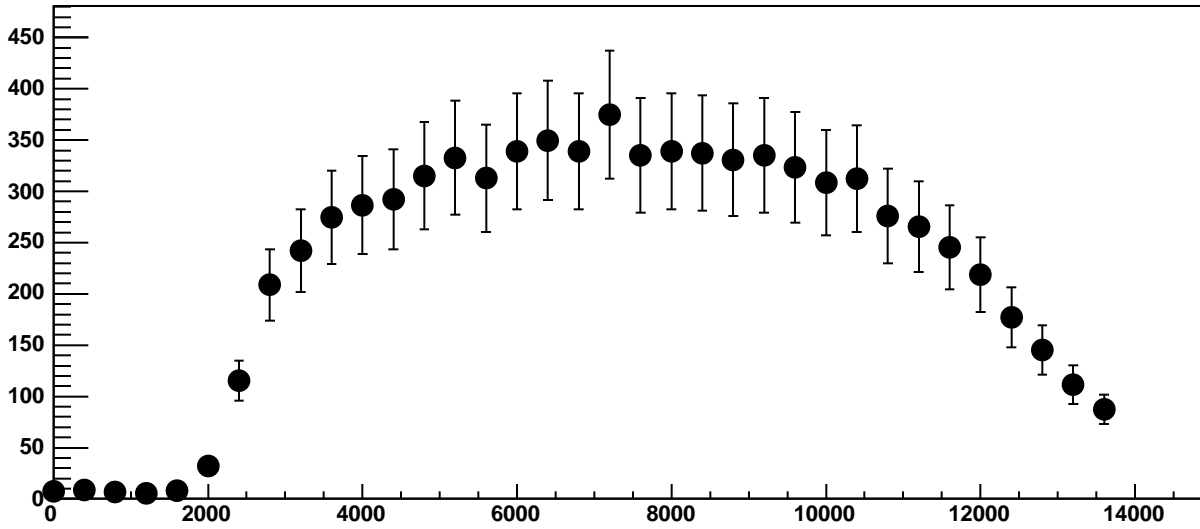
Chip 11, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



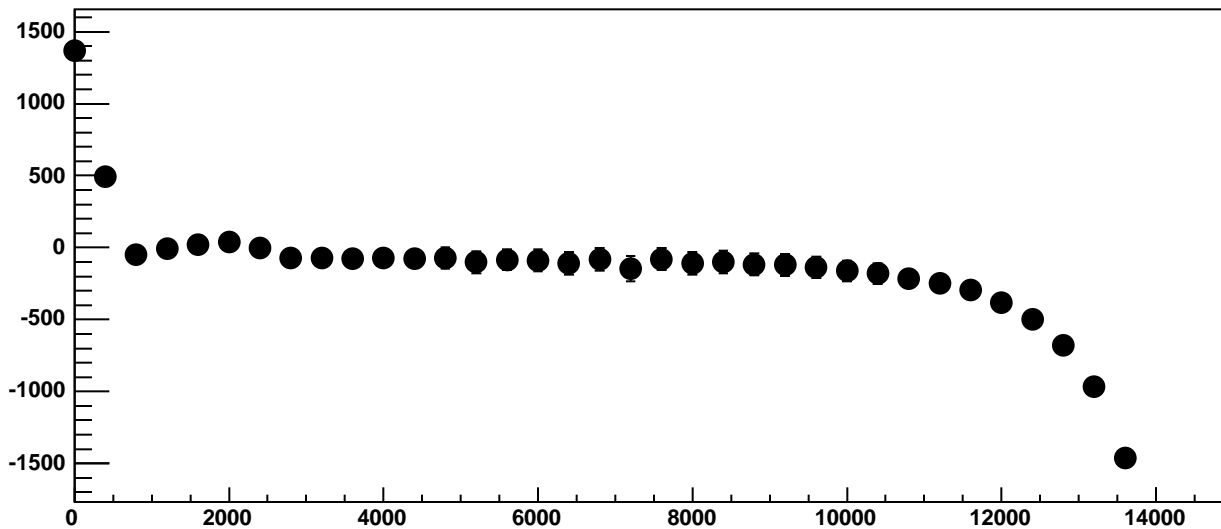
Chip 11, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC



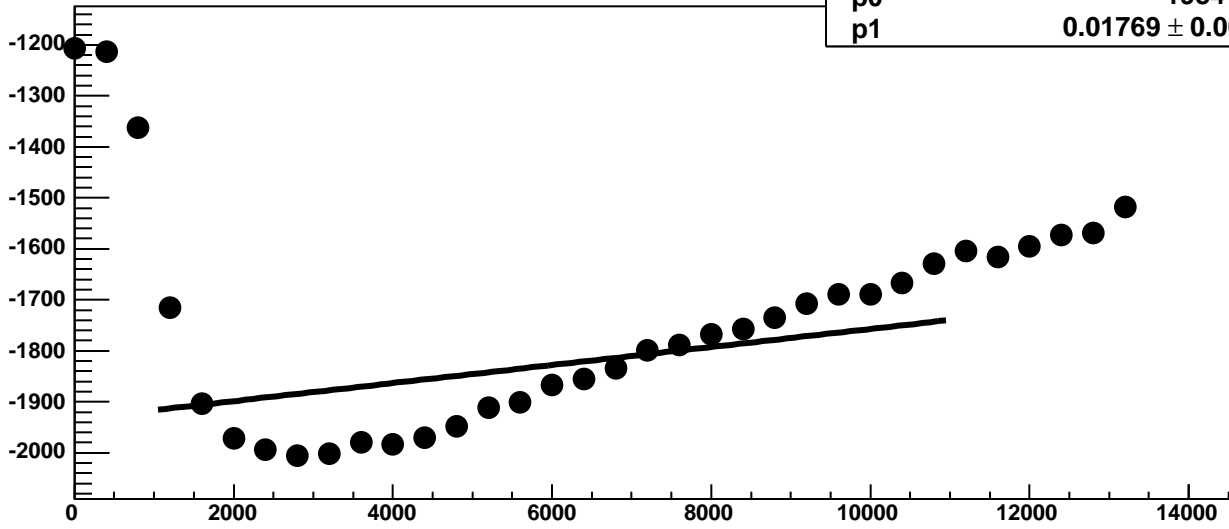
Chip 11, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

8093 / 23

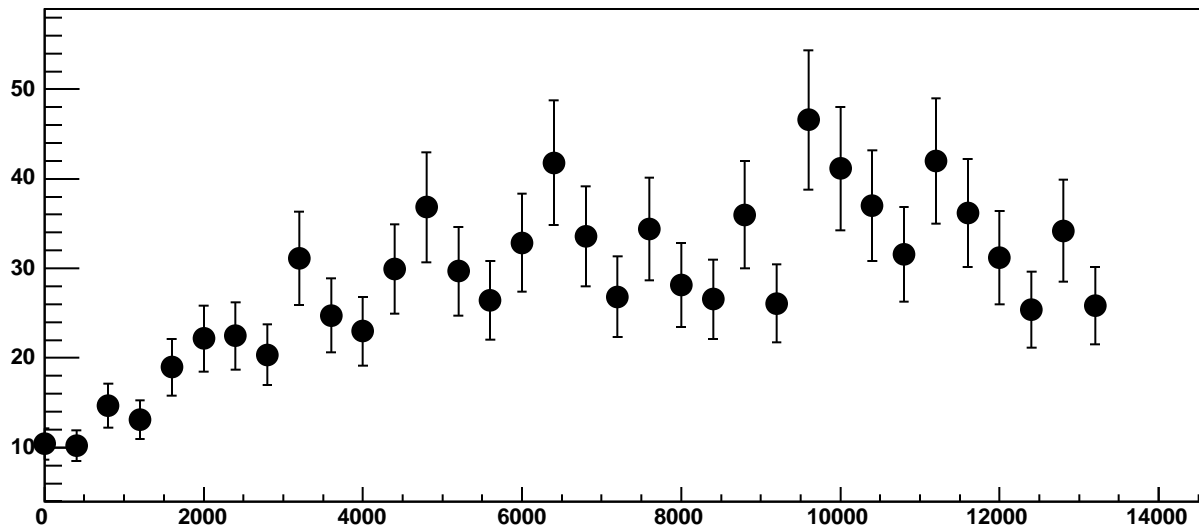
p0

-1934 ± 2.221

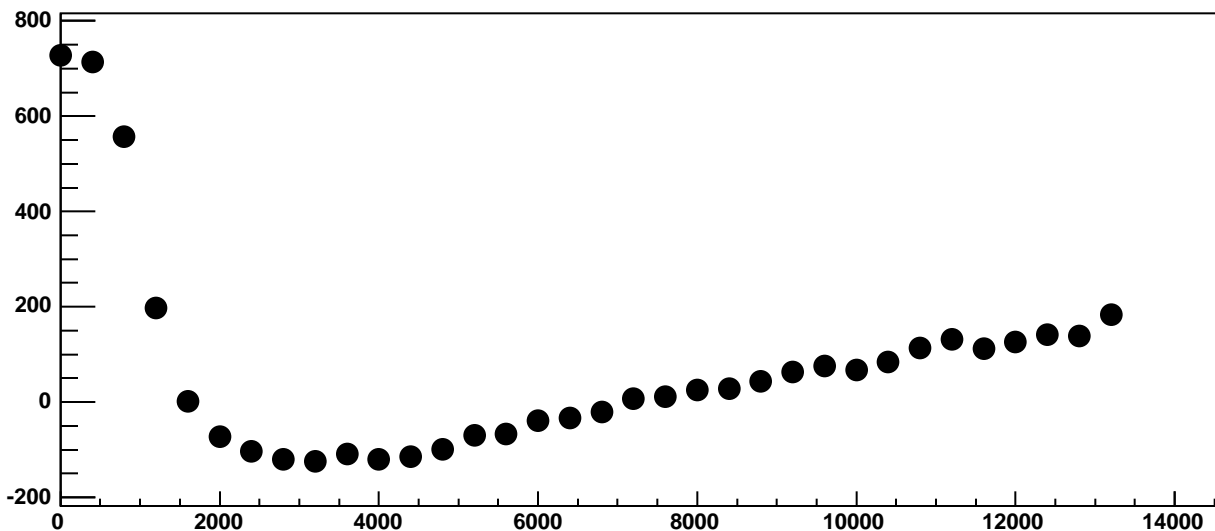
p1

0.01769 ± 0.0004017

Chip 11, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

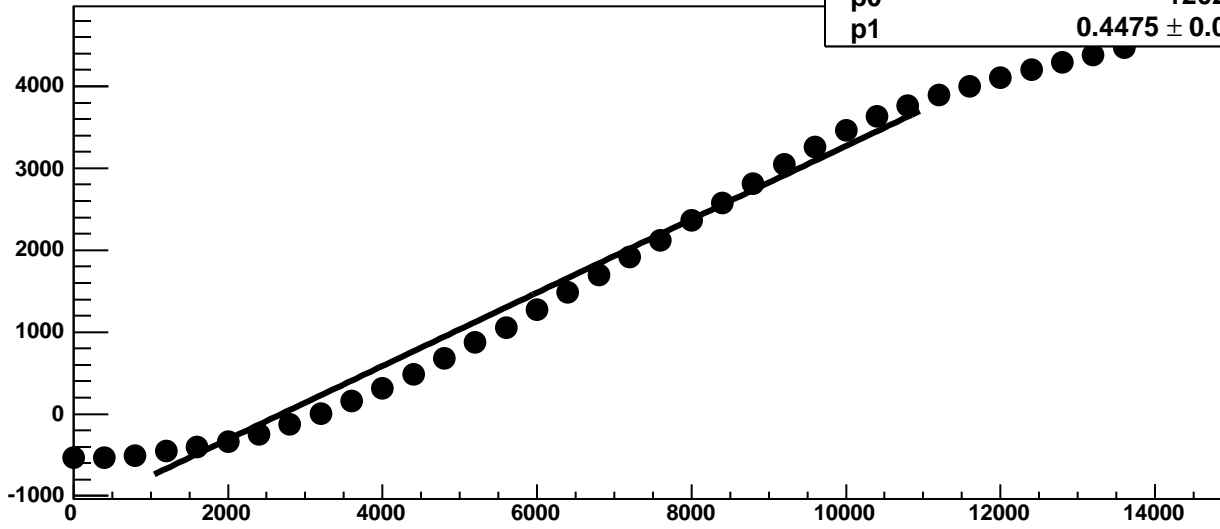


Chip 11, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

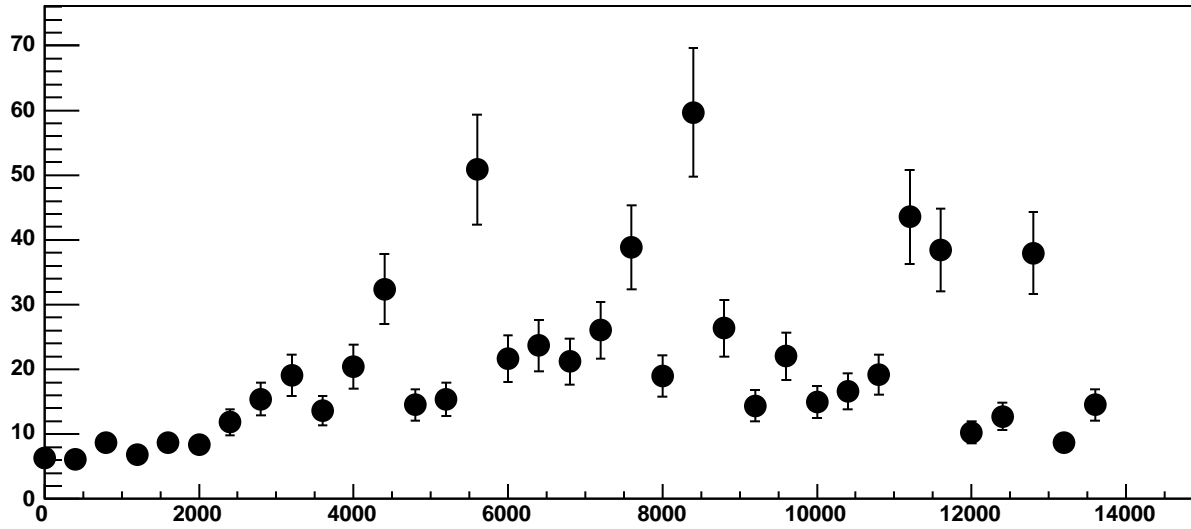


Chip 11, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

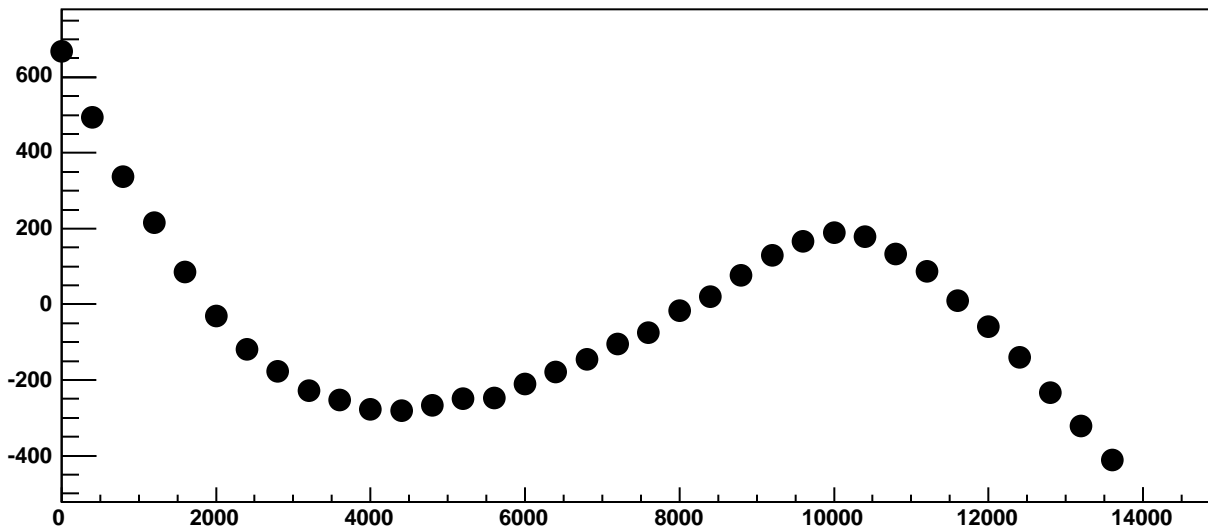
χ^2 / ndf 6.465e+04 / 23
p0 -1202 ± 1.13
p1 0.4475 ± 0.0002172



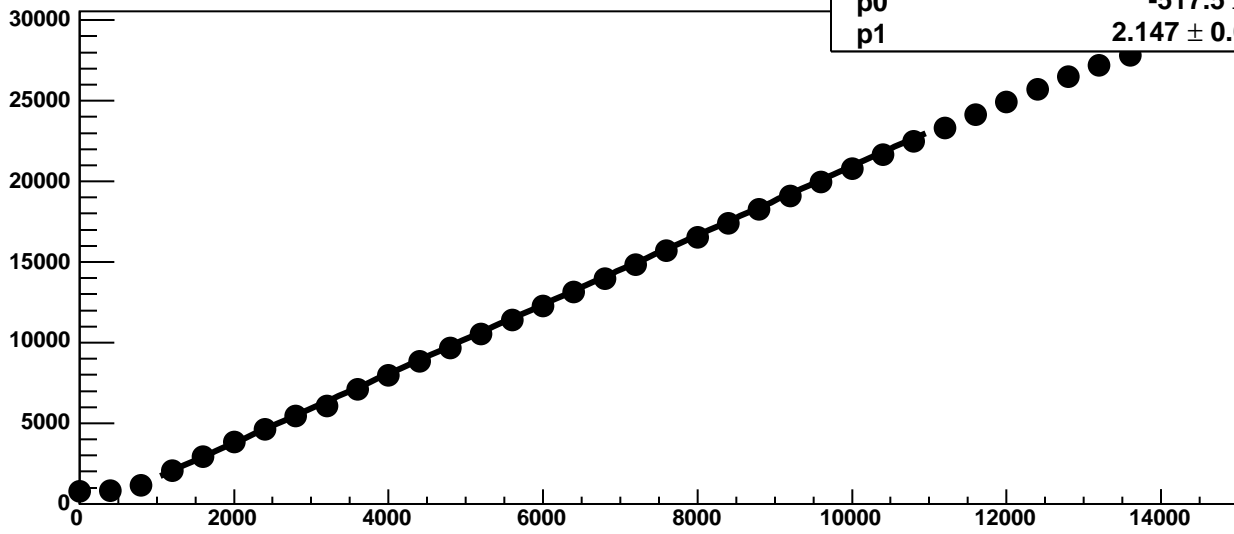
Chip 11, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



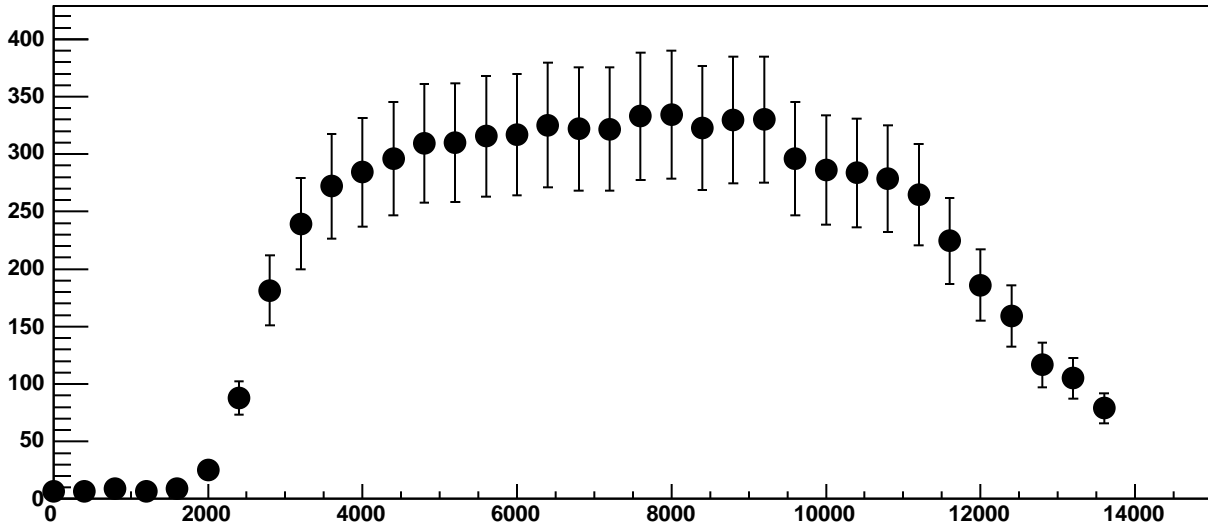
Chip 11, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



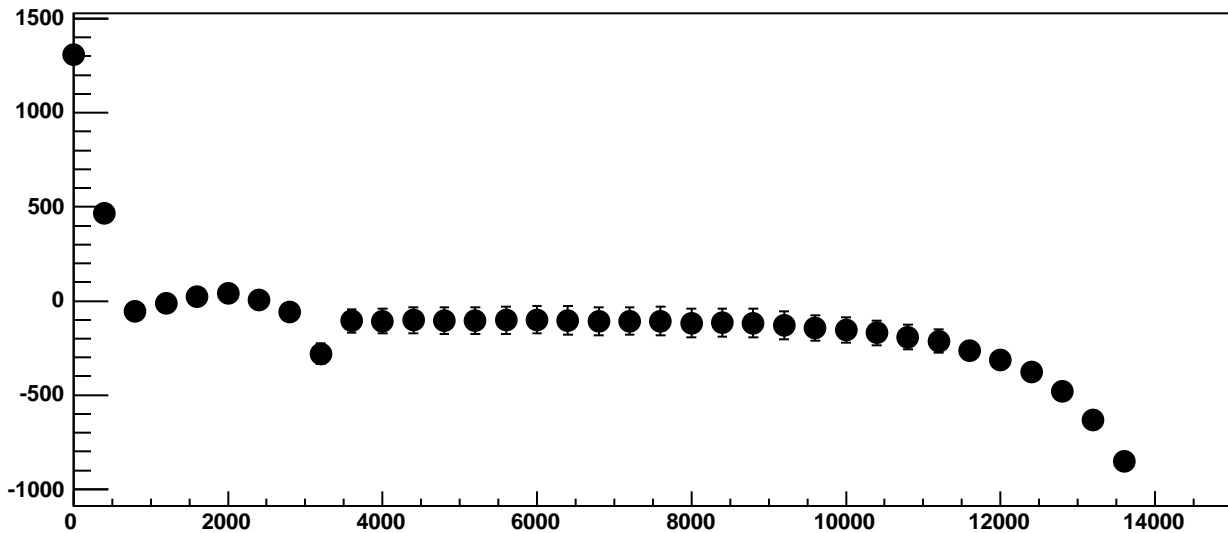
Chip 11, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC



Chip 11, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC

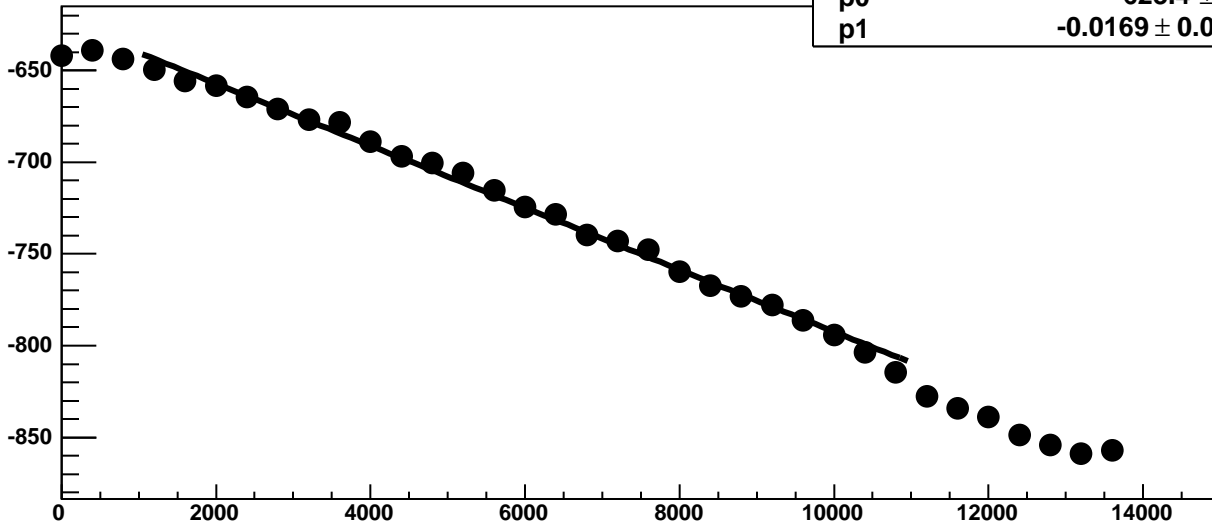


Chip 11, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC

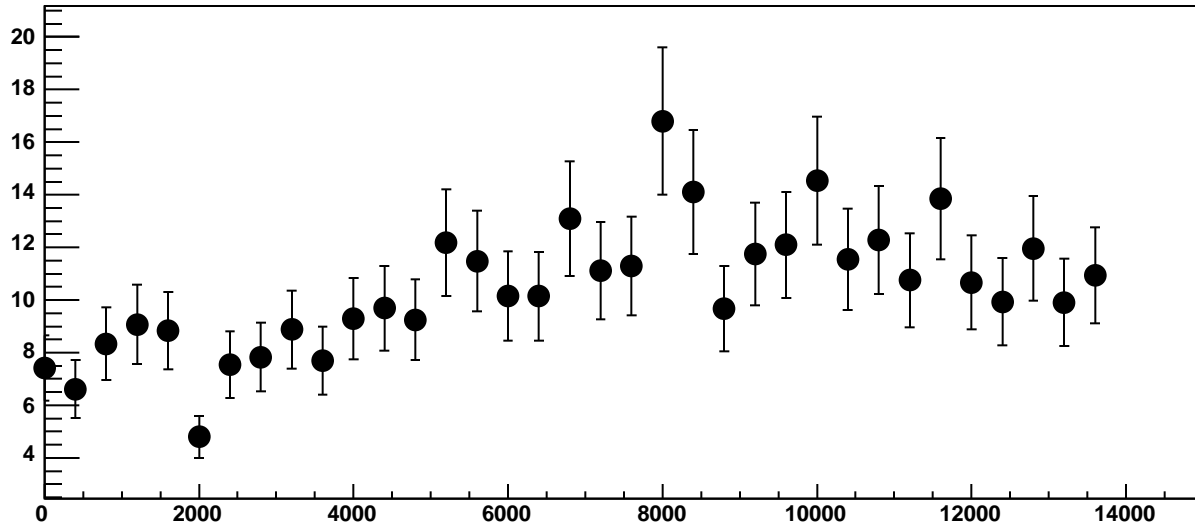


Chip 11, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

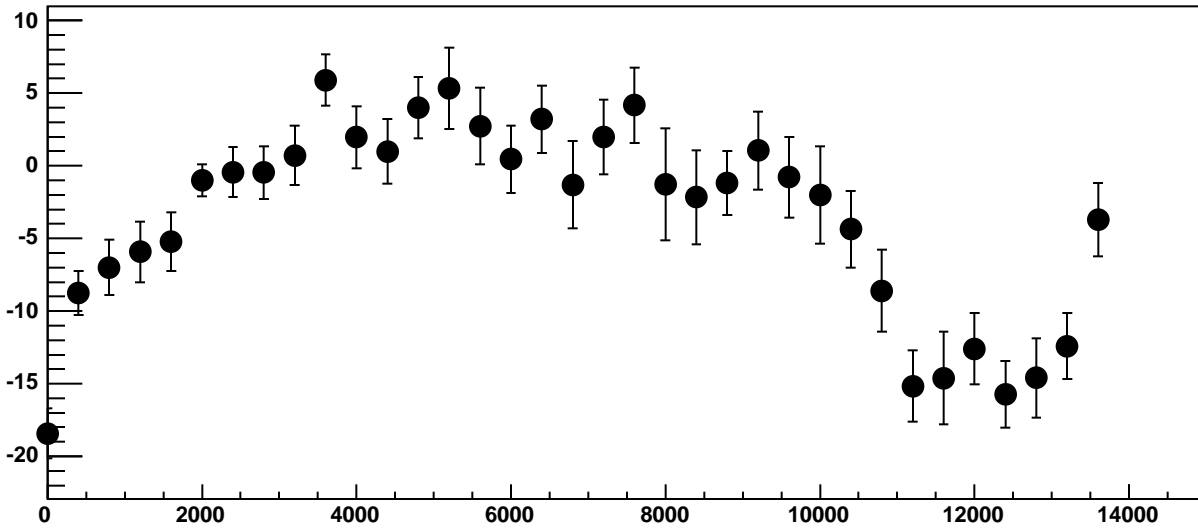
χ^2 / ndf 55.08 / 23
p0 -623.4 ± 0.8595
p1 -0.0169 ± 0.0001543



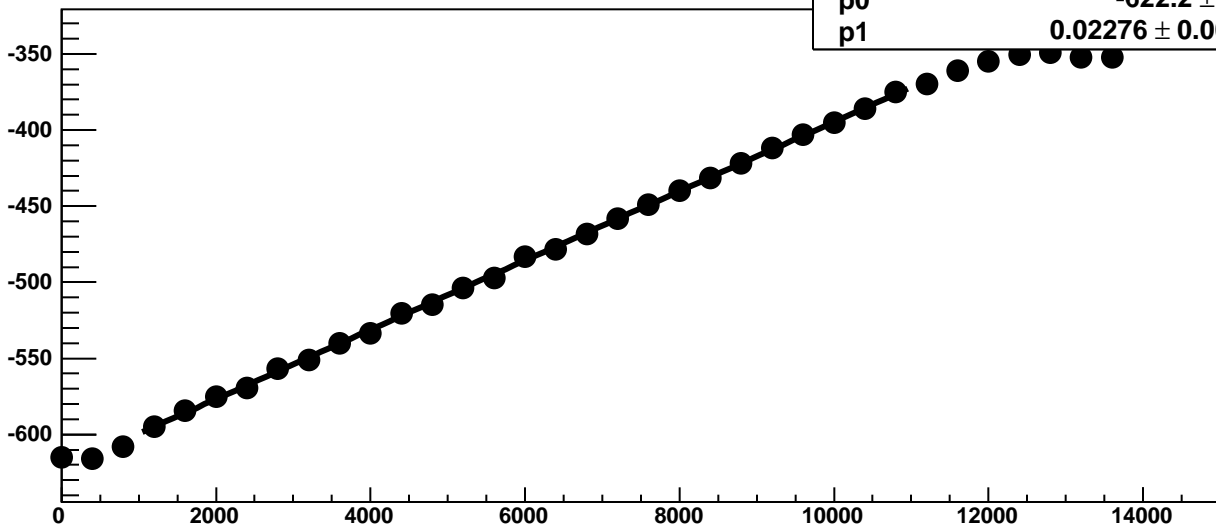
Chip 11, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



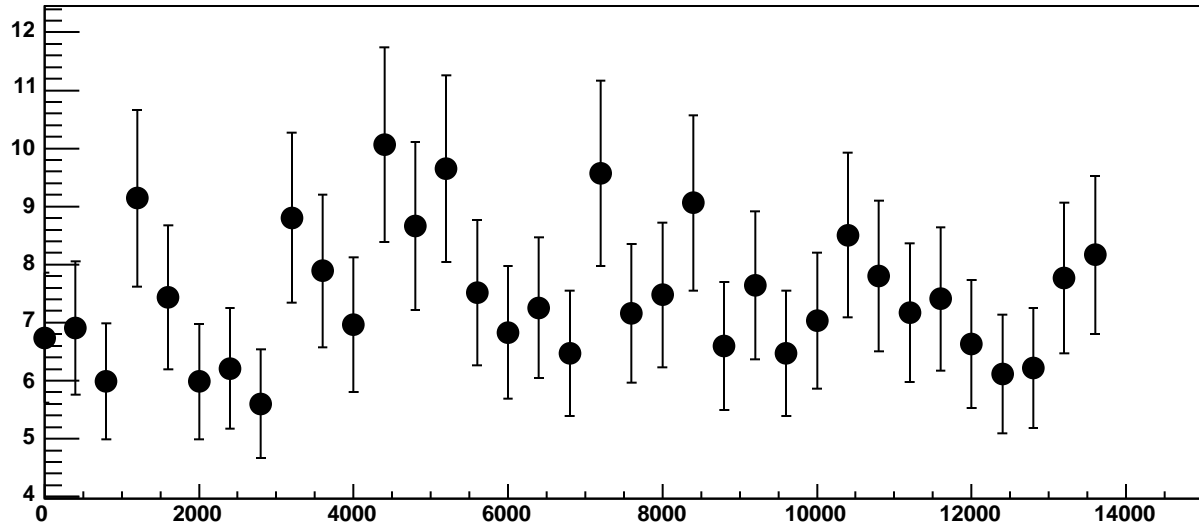
Chip 11, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



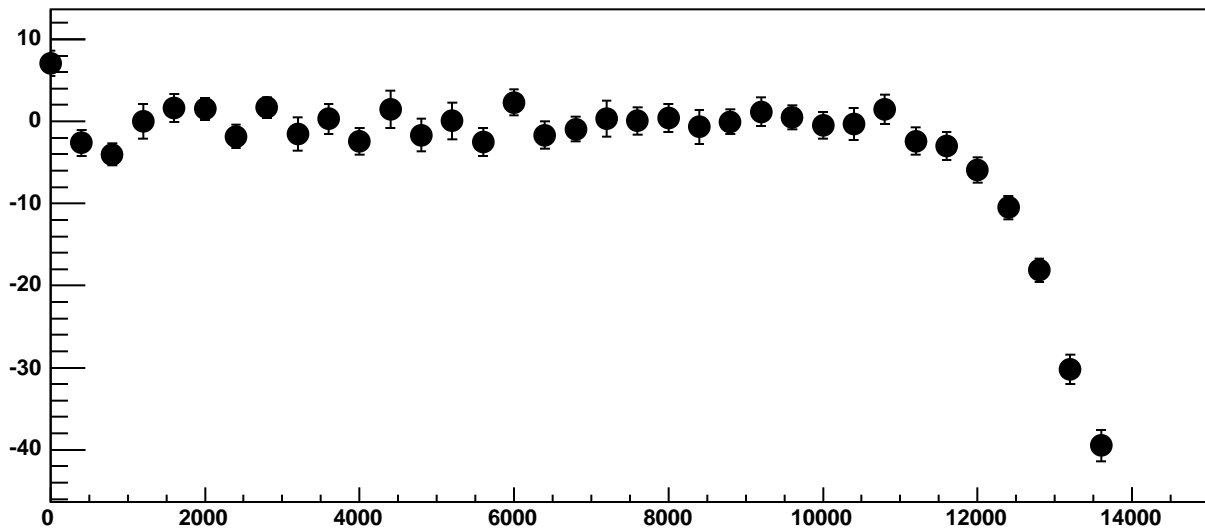
Chip 11, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC



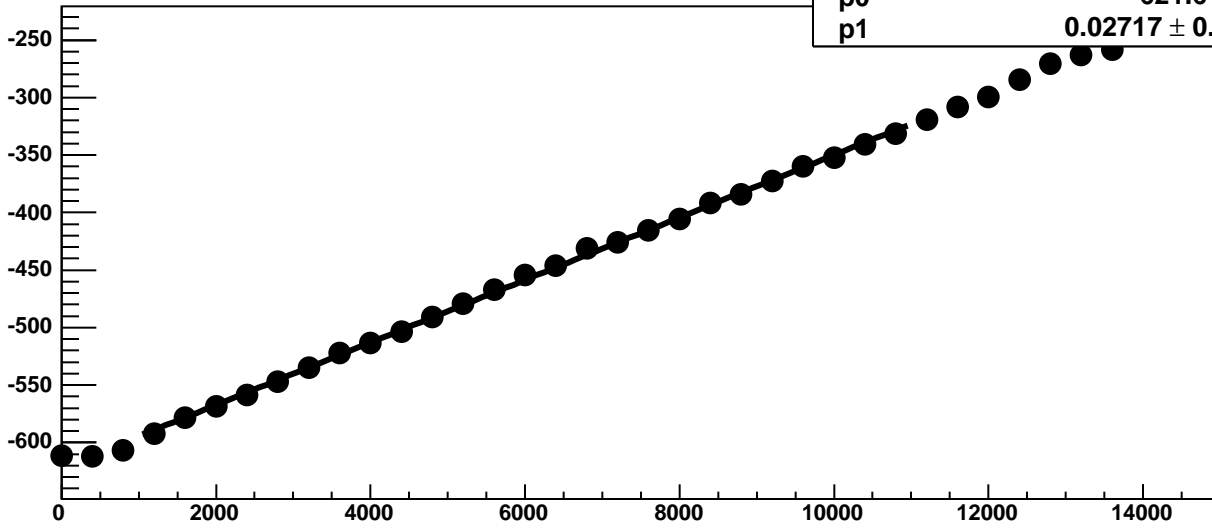
Chip 11, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

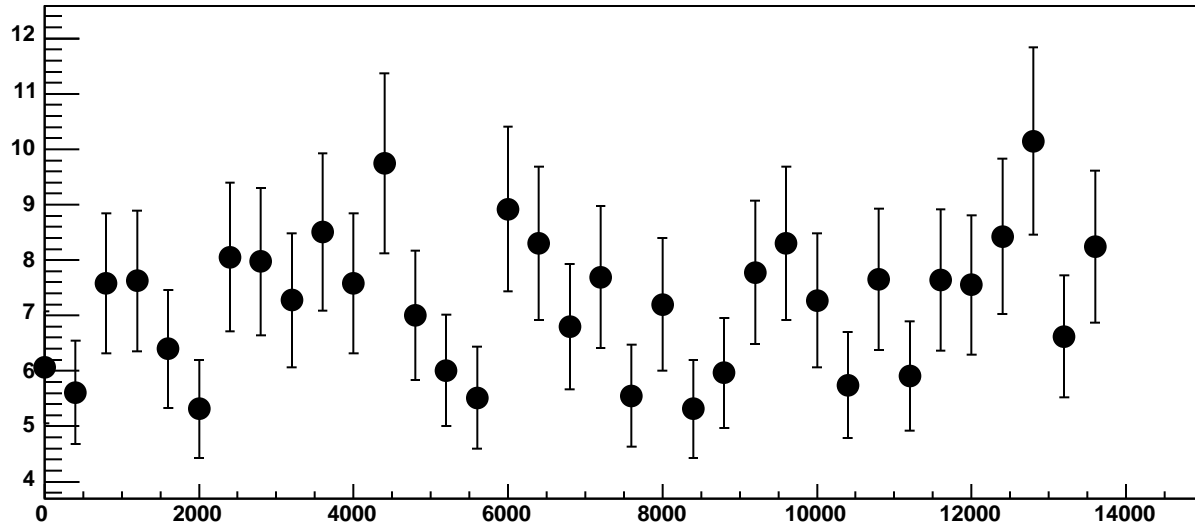


Chip 11, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC

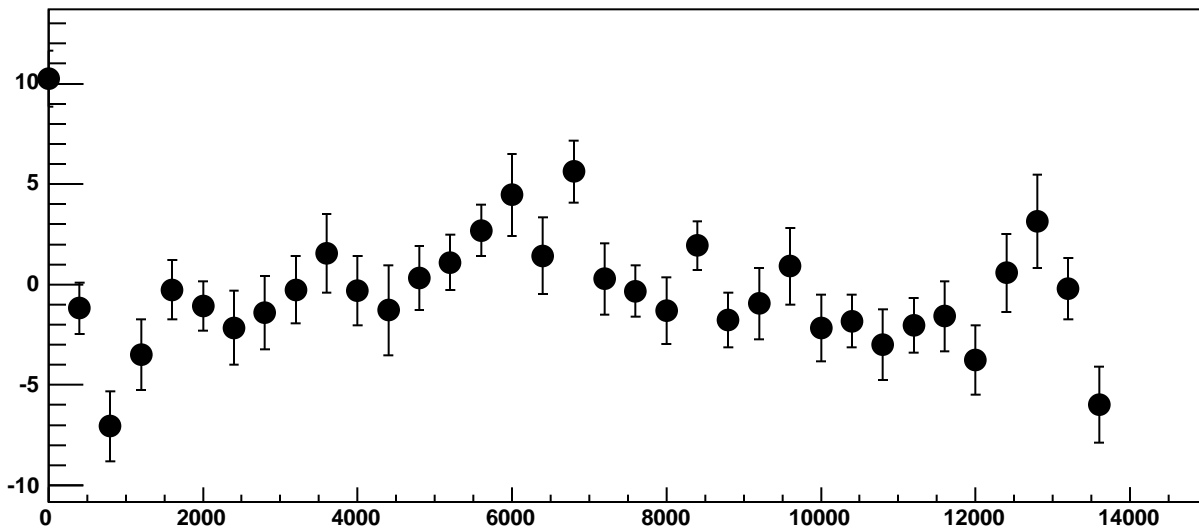


χ^2 / ndf 43.21 / 23
p0 -621.6 ± 0.737
p1 0.02717 ± 0.000109

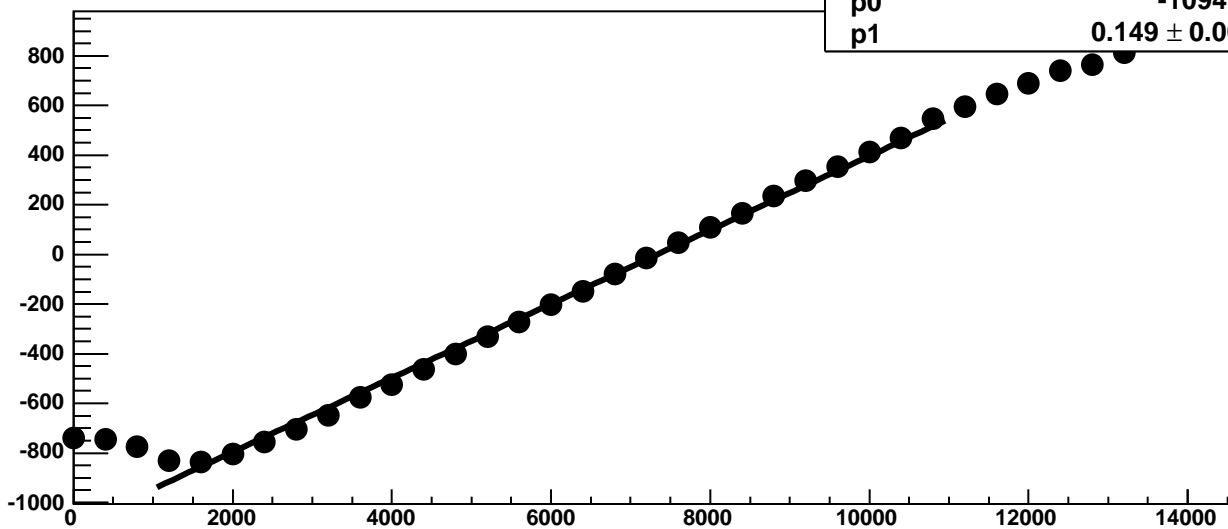
Chip 11, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



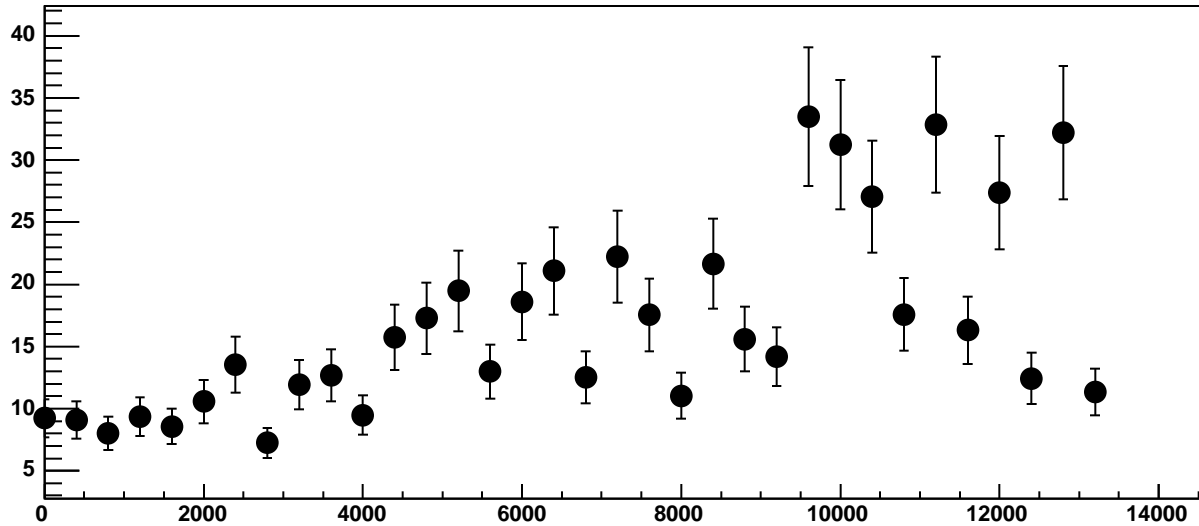
Chip 11, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC



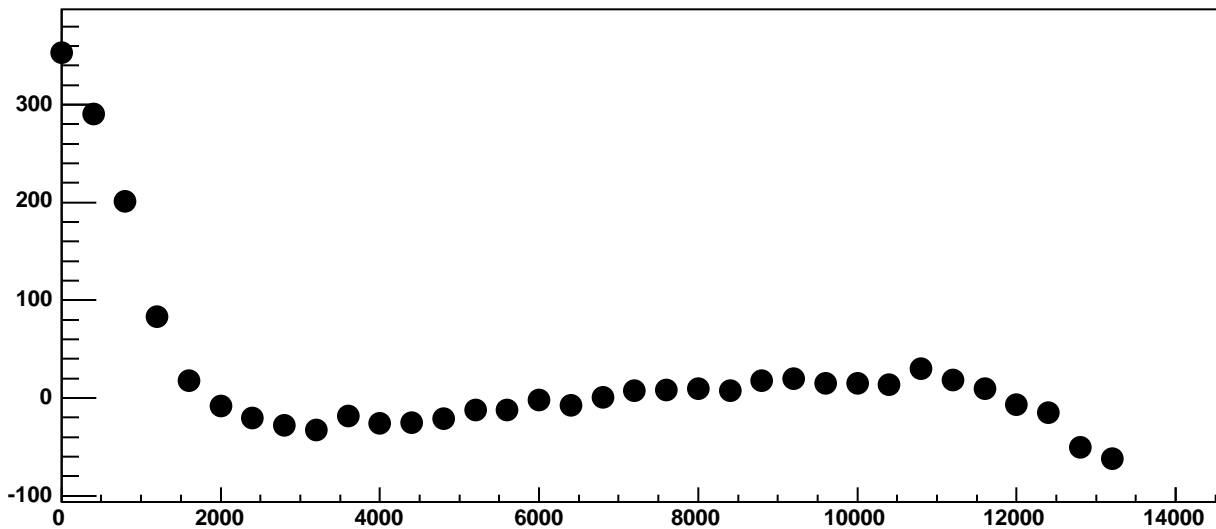
Chip 11, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



Chip 11, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

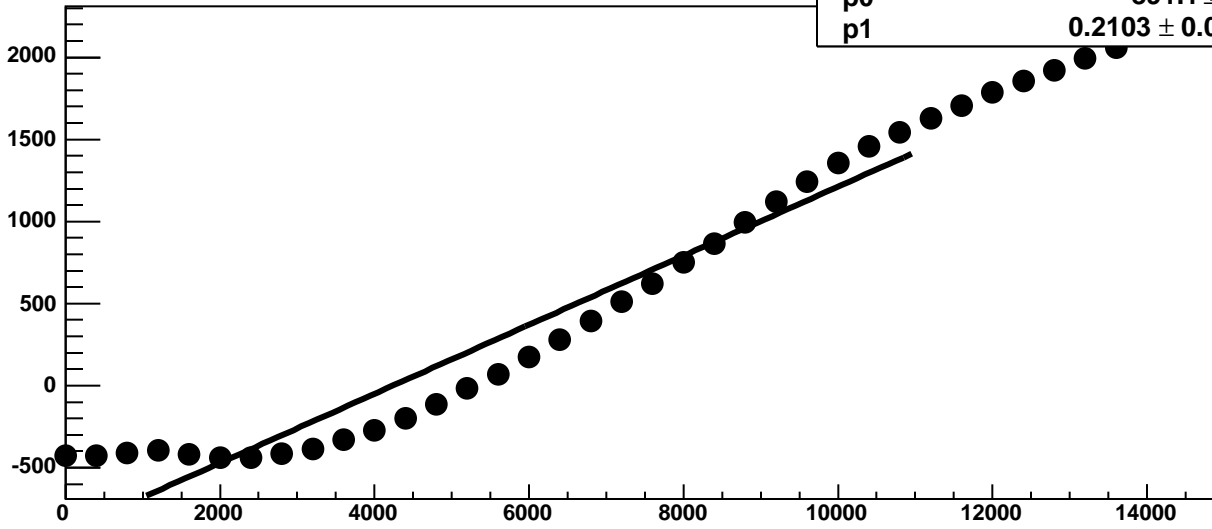


Chip 11, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

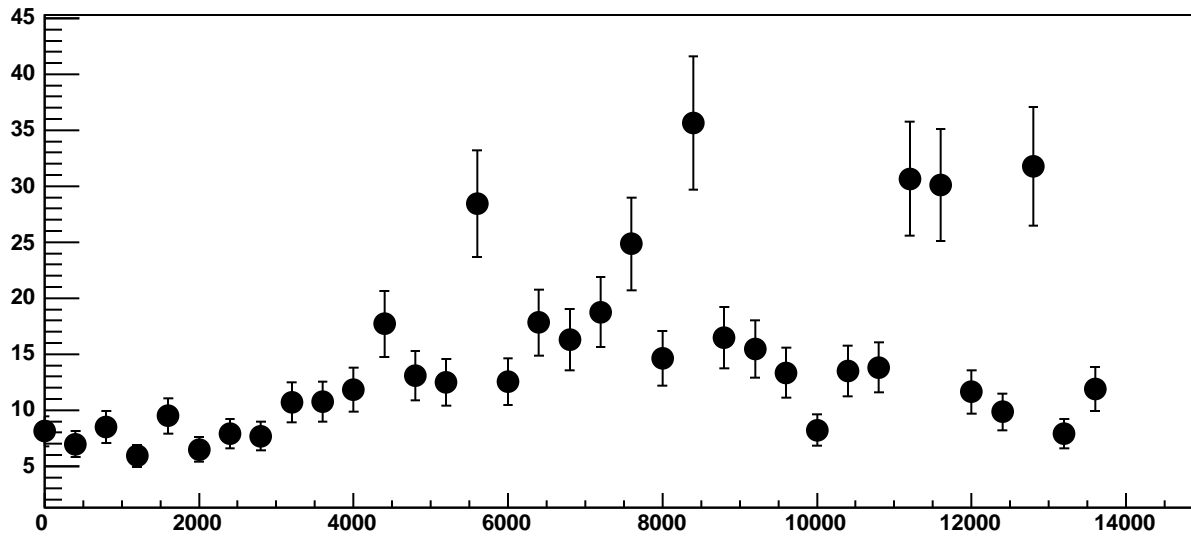


Chip 11, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

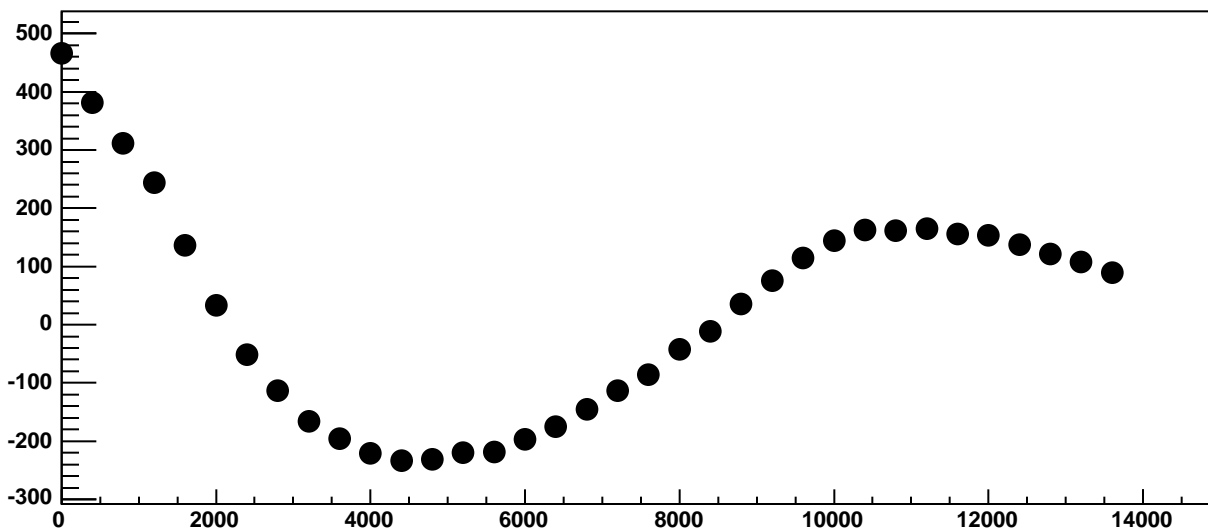
χ^2 / ndf 9.801e+04 / 23
p0 -891.1 ± 0.8934
p1 0.2103 ± 0.0001616



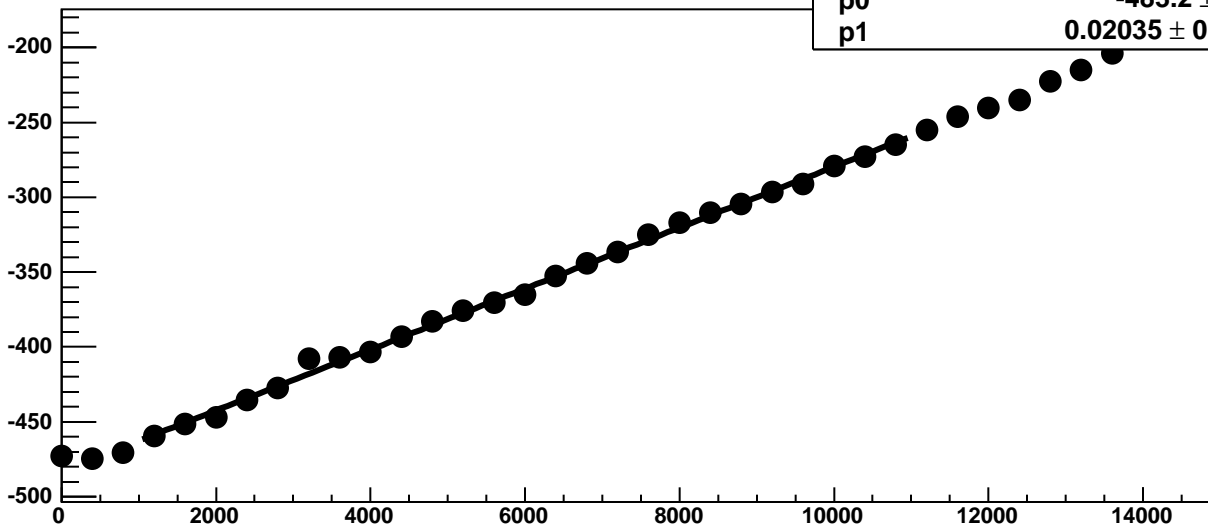
Chip 11, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

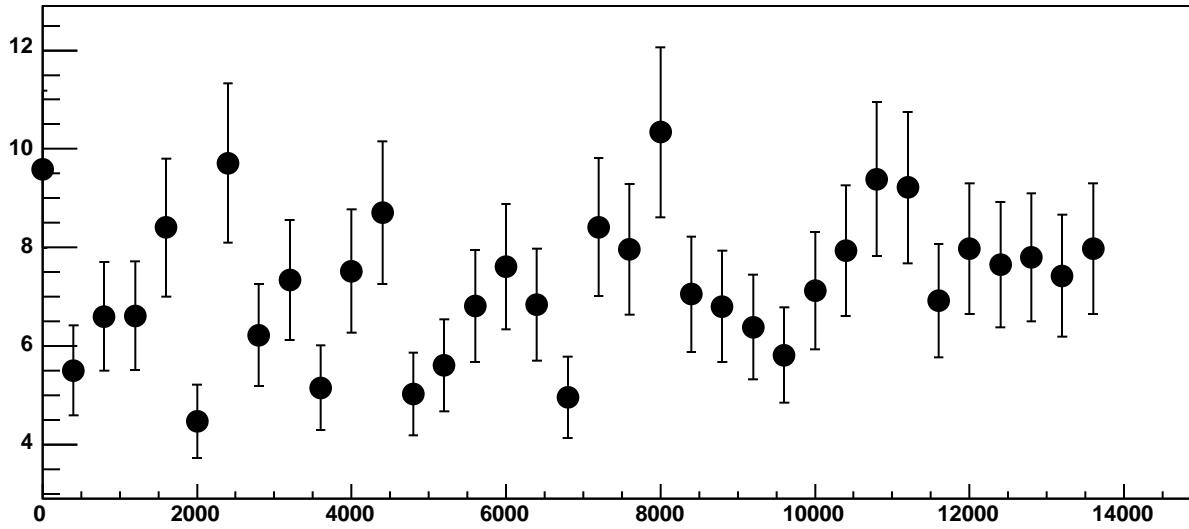


Chip 11, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC

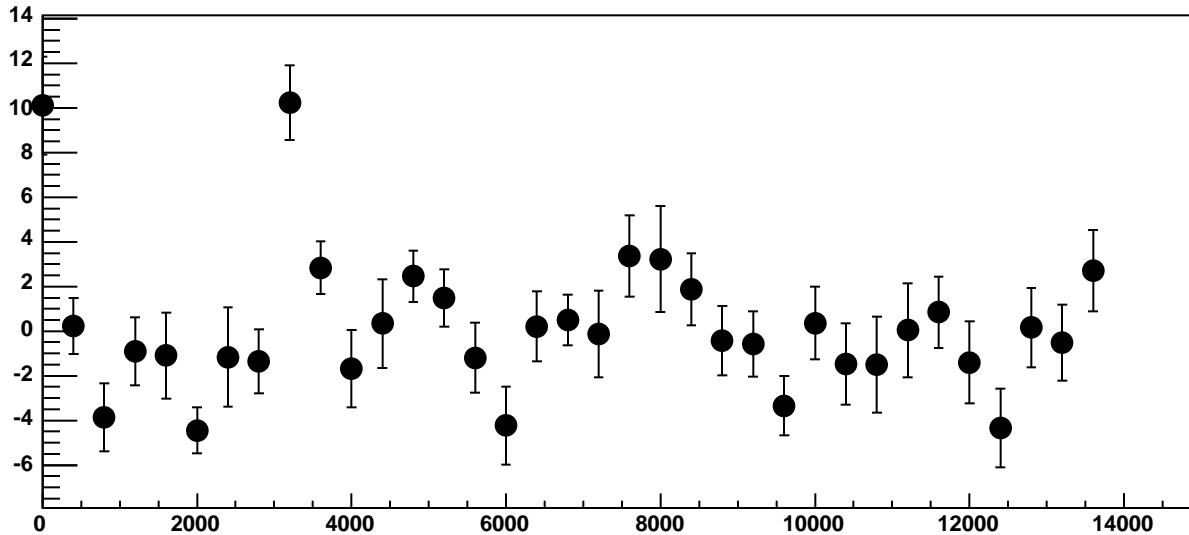


χ^2 / ndf 91.15 / 23
p0 -483.2 ± 0.6913
p1 0.02035 ± 0.000109

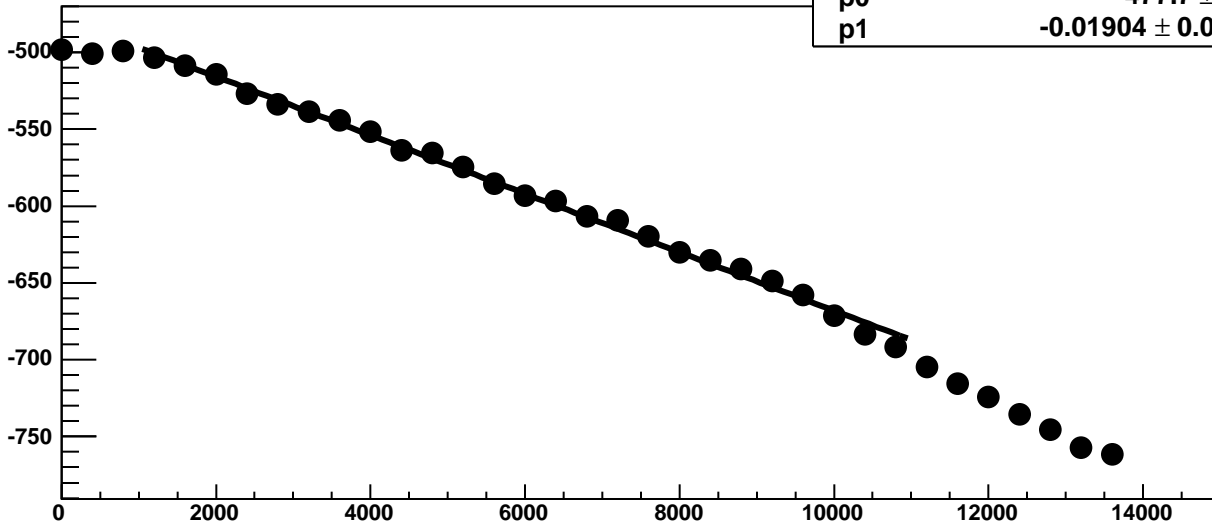
Chip 11, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC

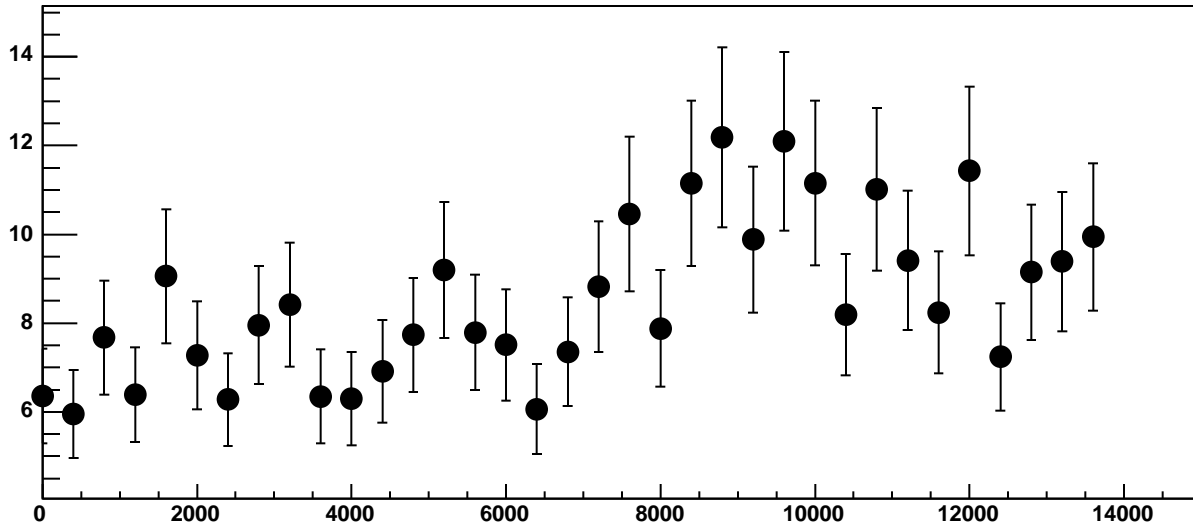


Chip 11, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

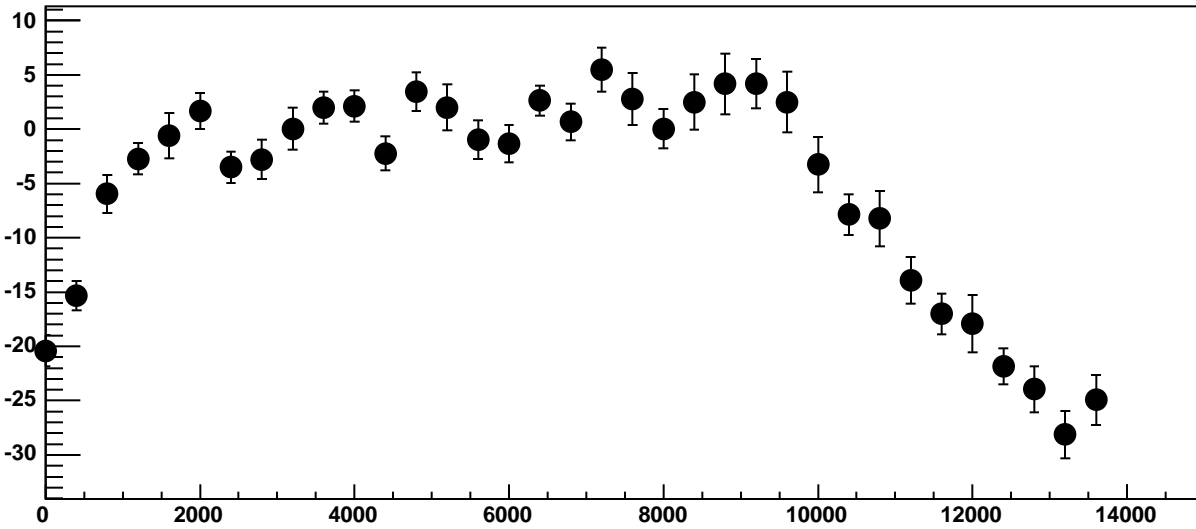


χ^2 / ndf 73.88 / 23
p0 -477.7 ± 0.8043
p1 -0.01904 ± 0.0001359

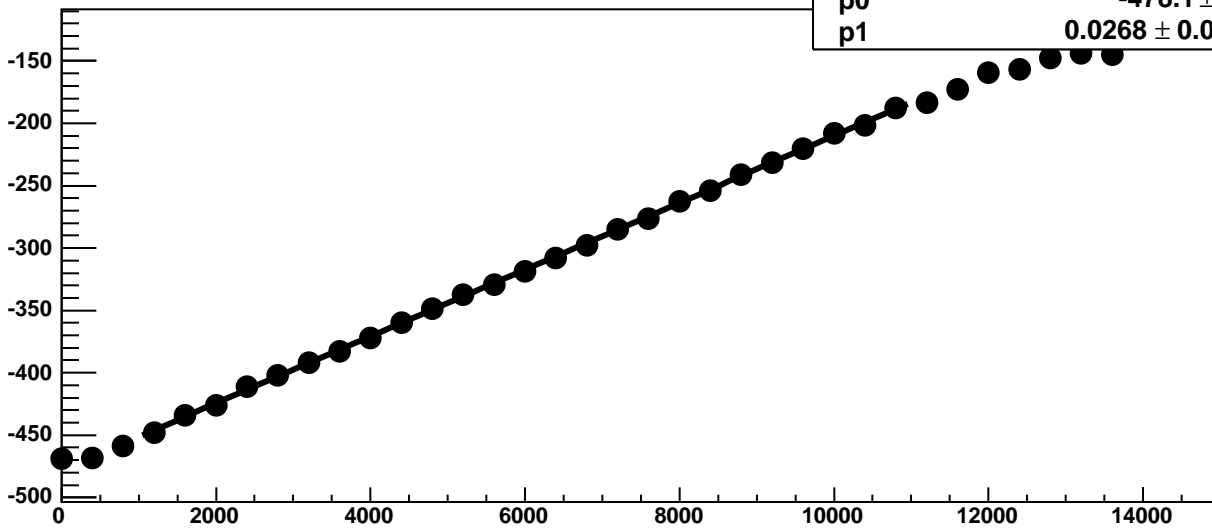
Chip 11, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



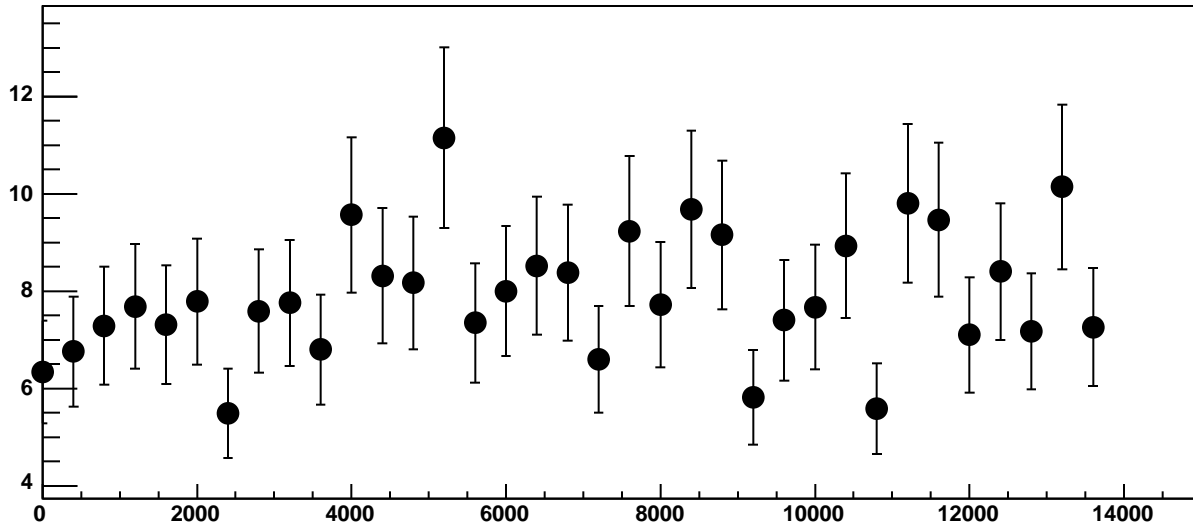
Chip 11, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



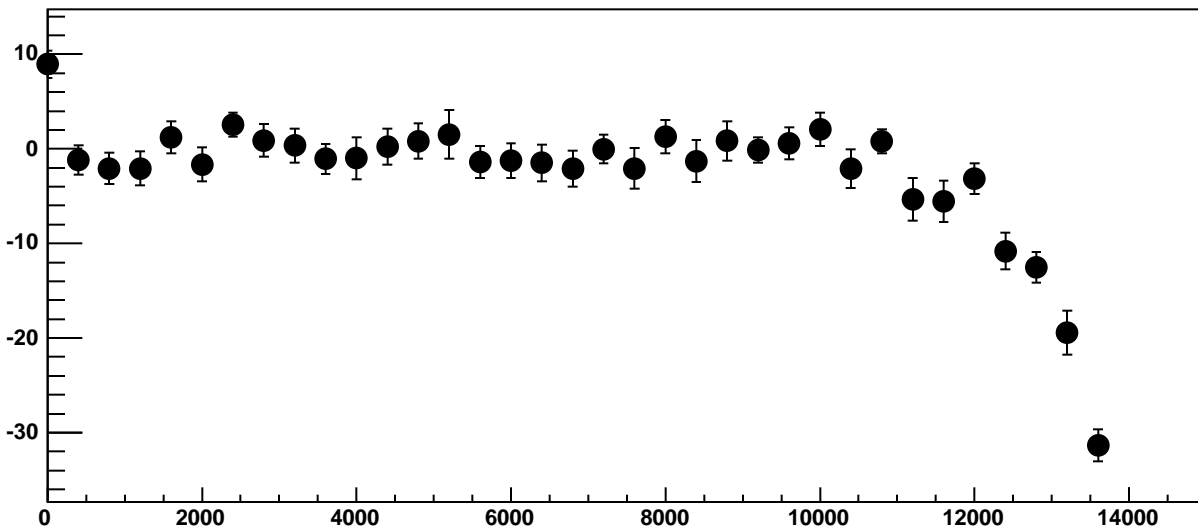
Chip 11, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



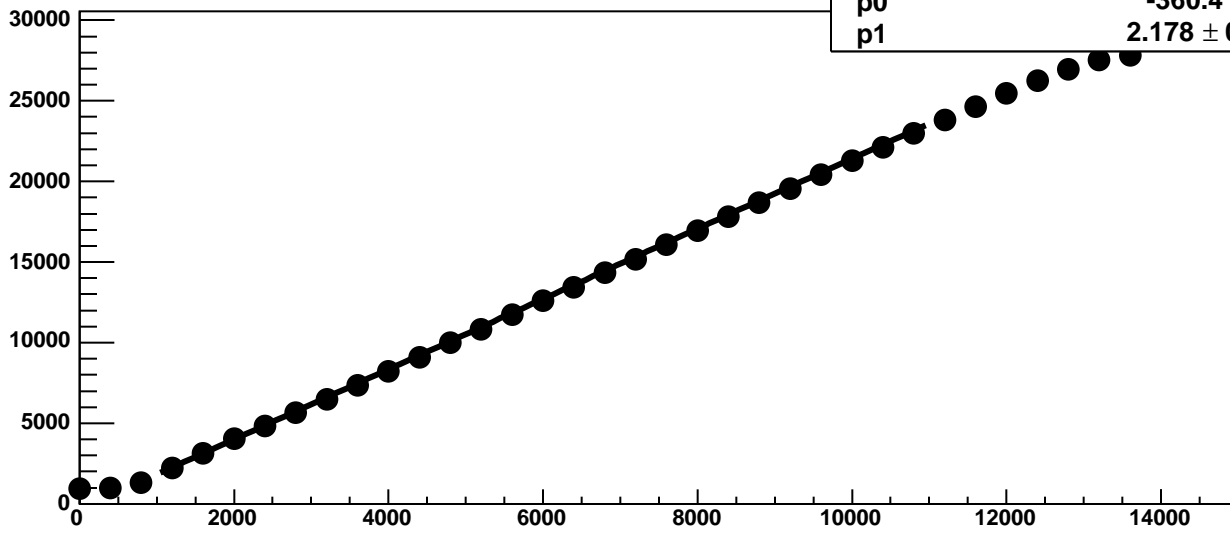
Chip 11, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

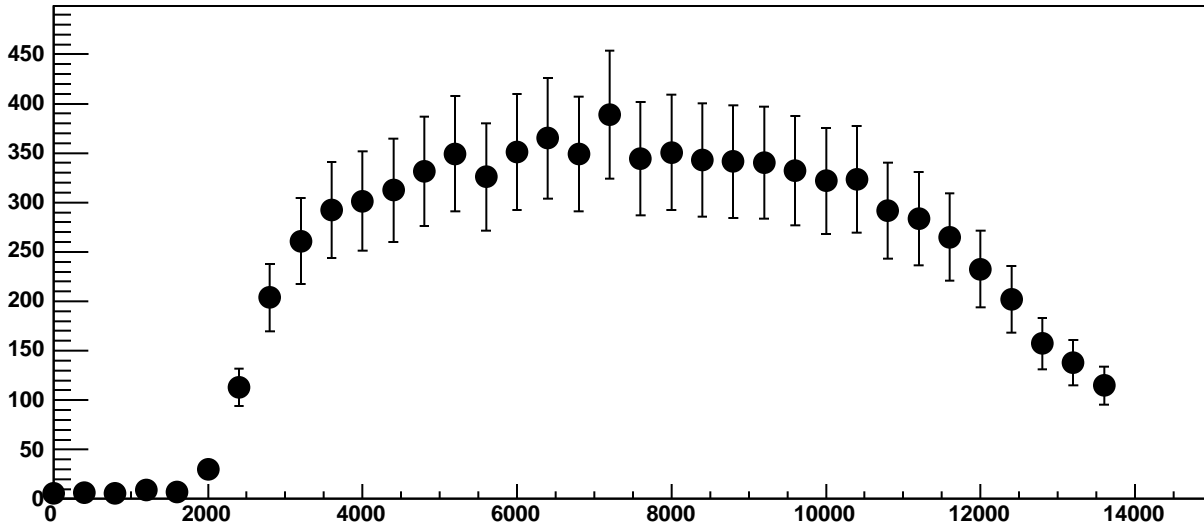


Chip 11, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC

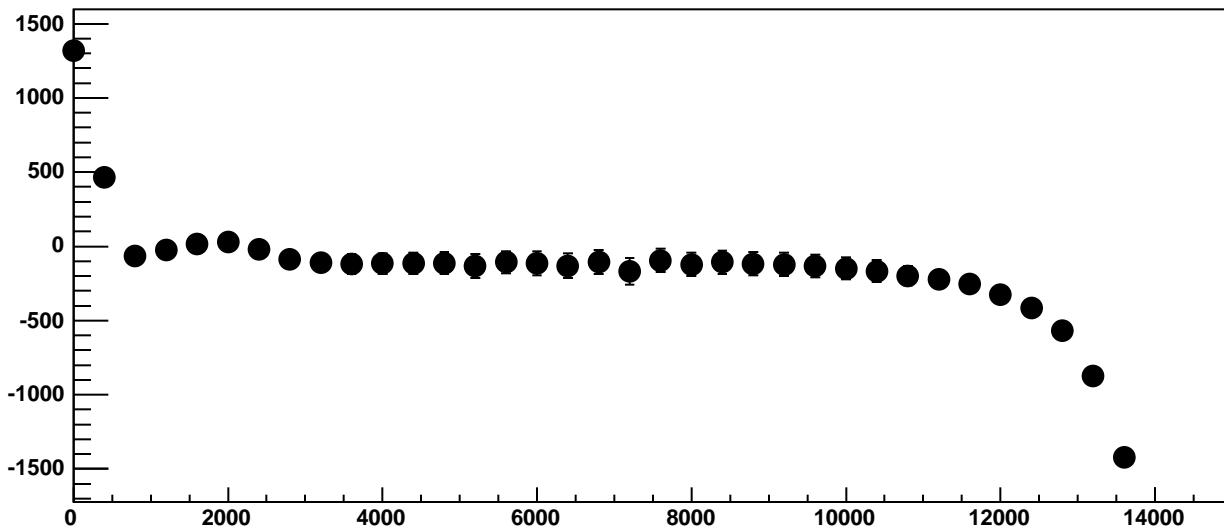


χ^2 / ndf 310.4 / 23
p0 -360.4 ± 3.986
p1 2.178 ± 0.00253

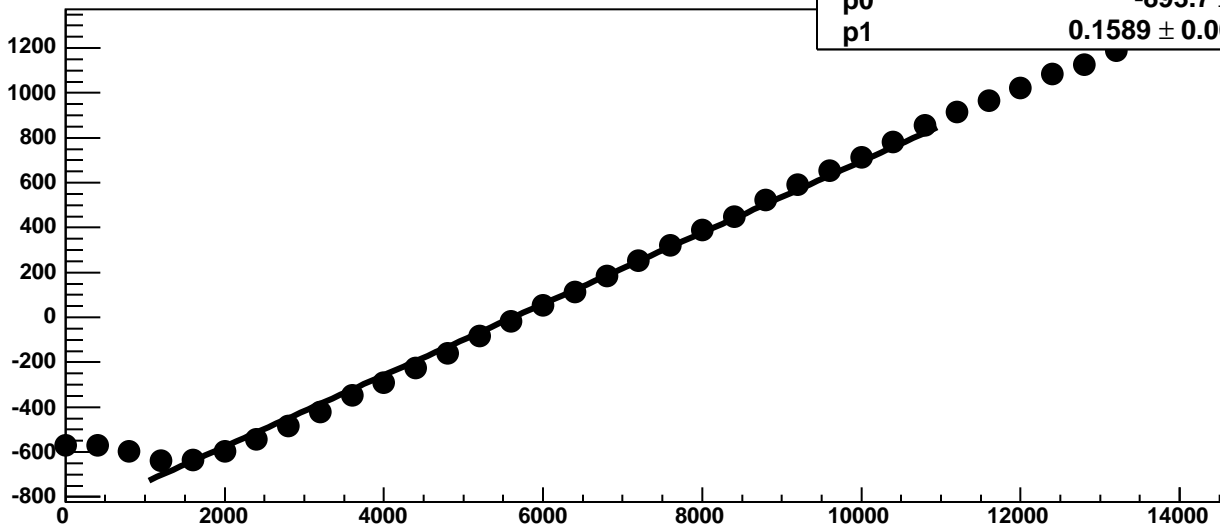
Chip 11, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



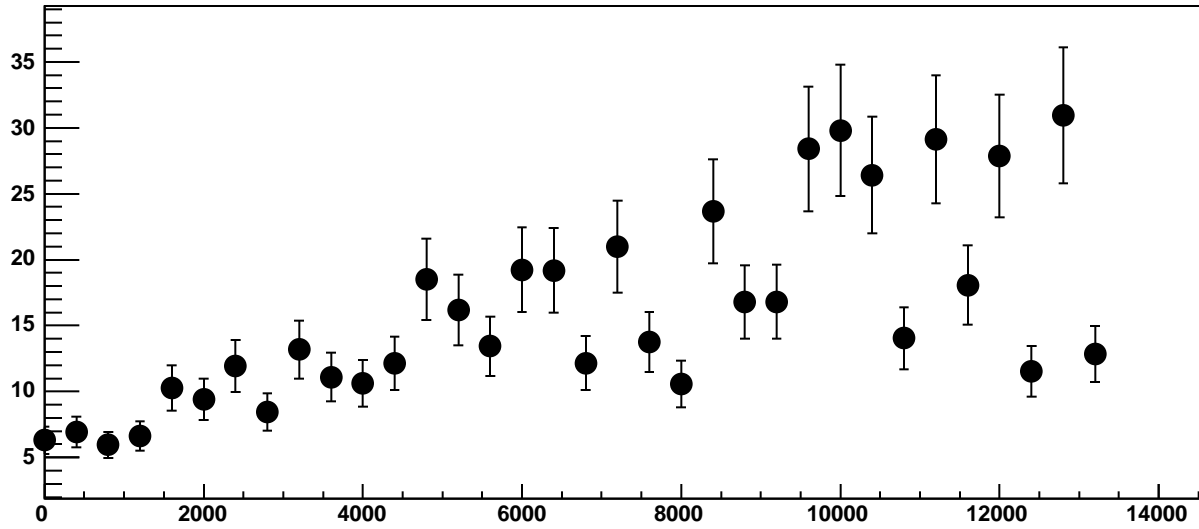
Chip 11, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC



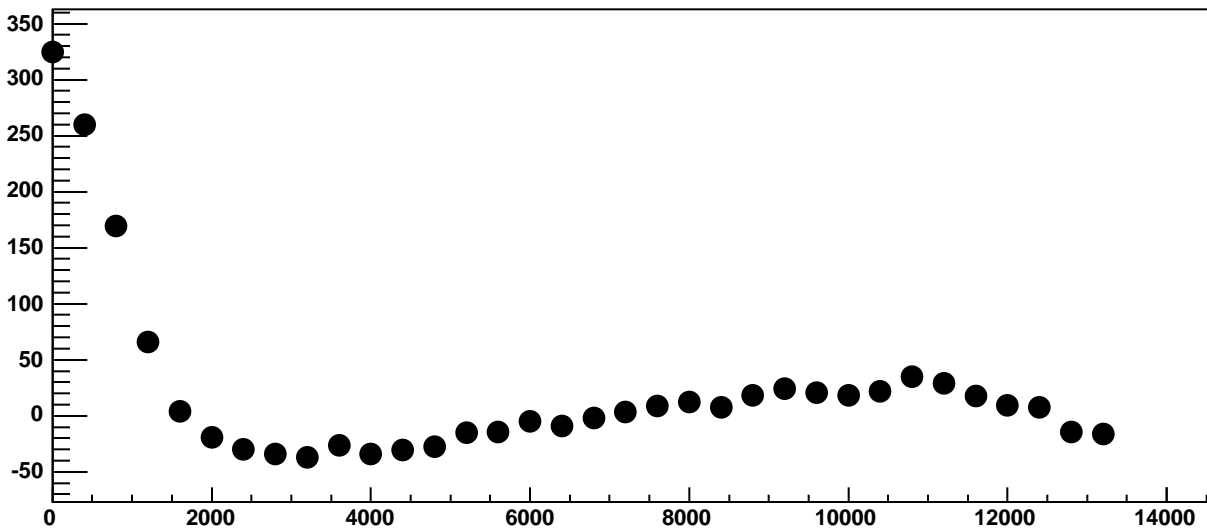
Chip 11, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



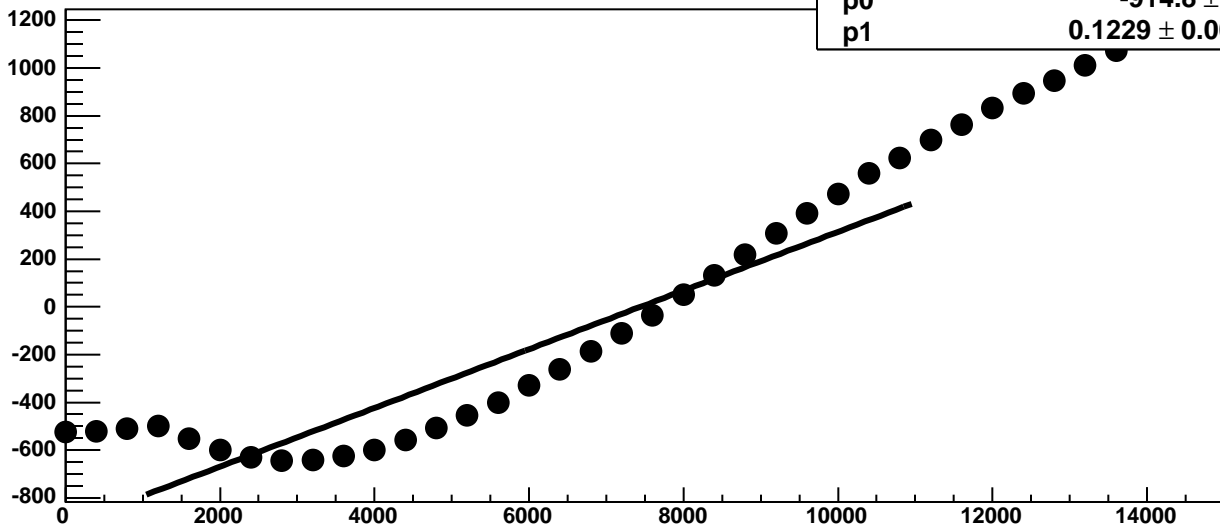
Chip 11, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



Chip 11, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1.08e+05 / 23

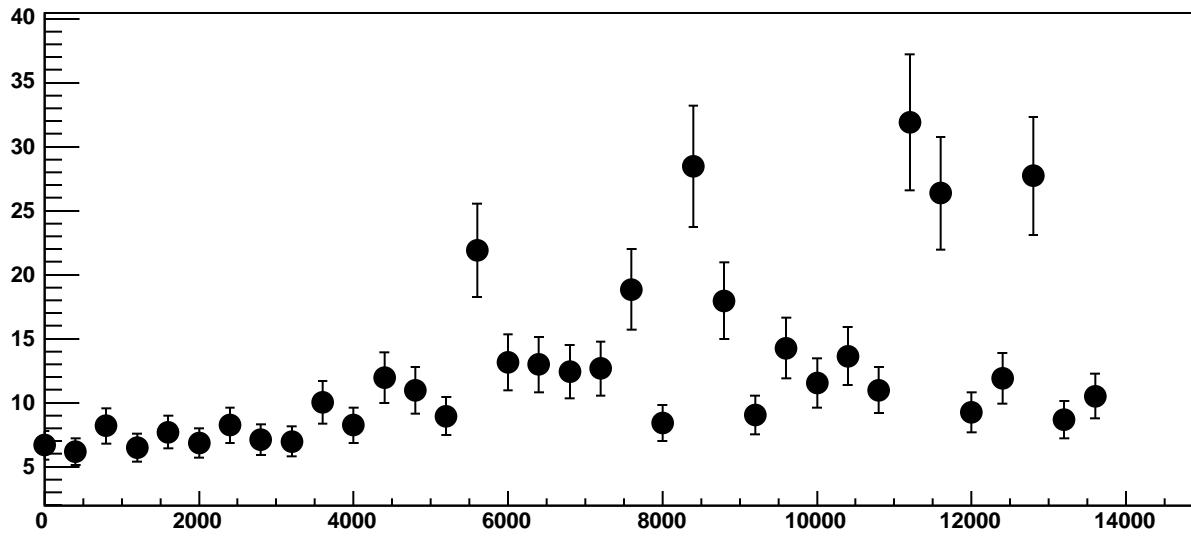
p0

-914.8 ± 0.8565

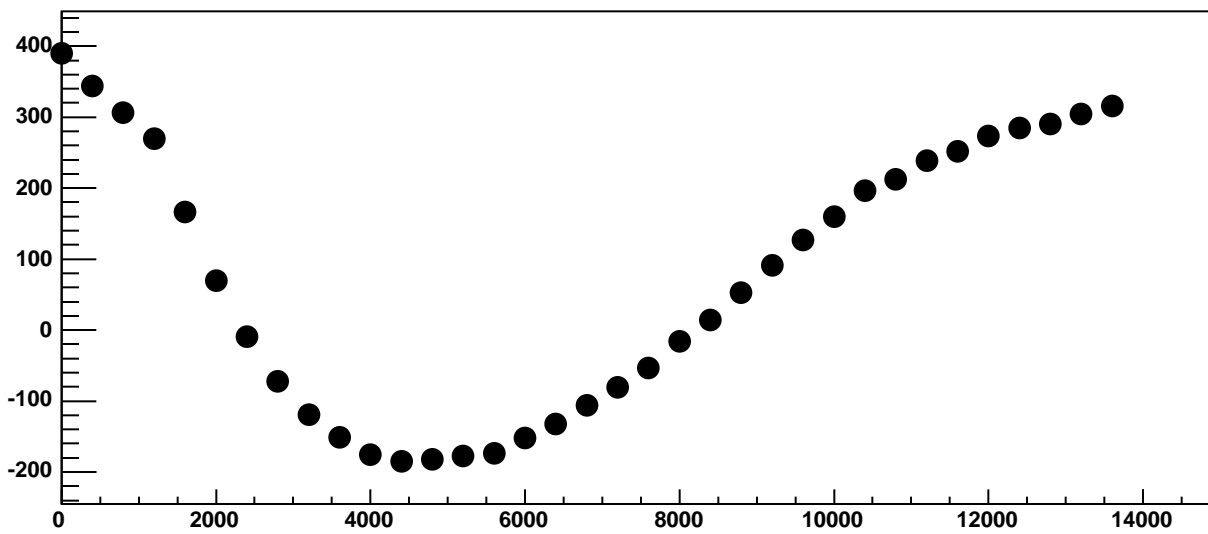
p1

0.1229 ± 0.0001532

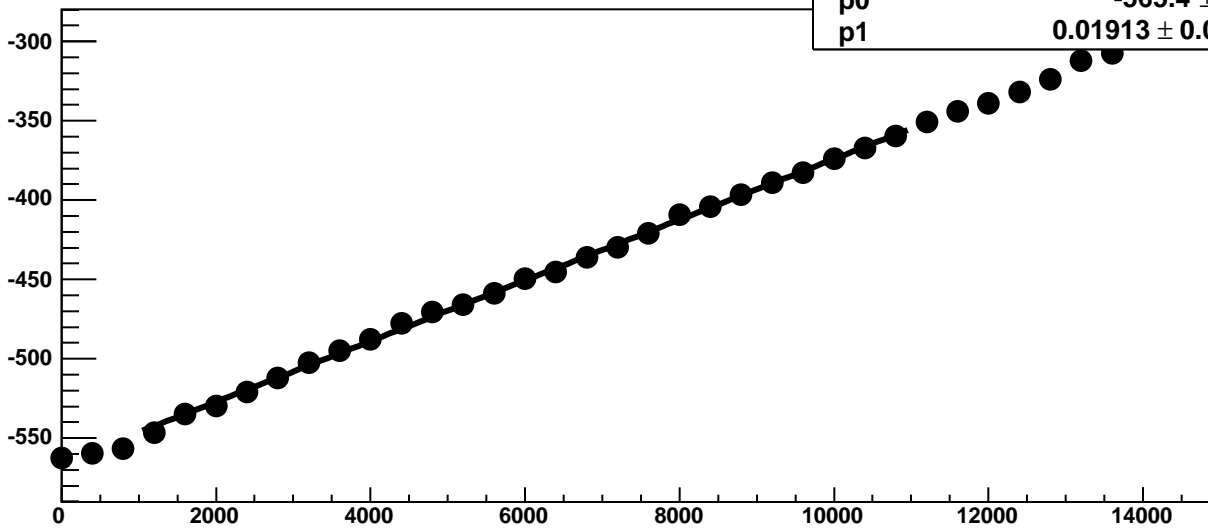
Chip 11, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

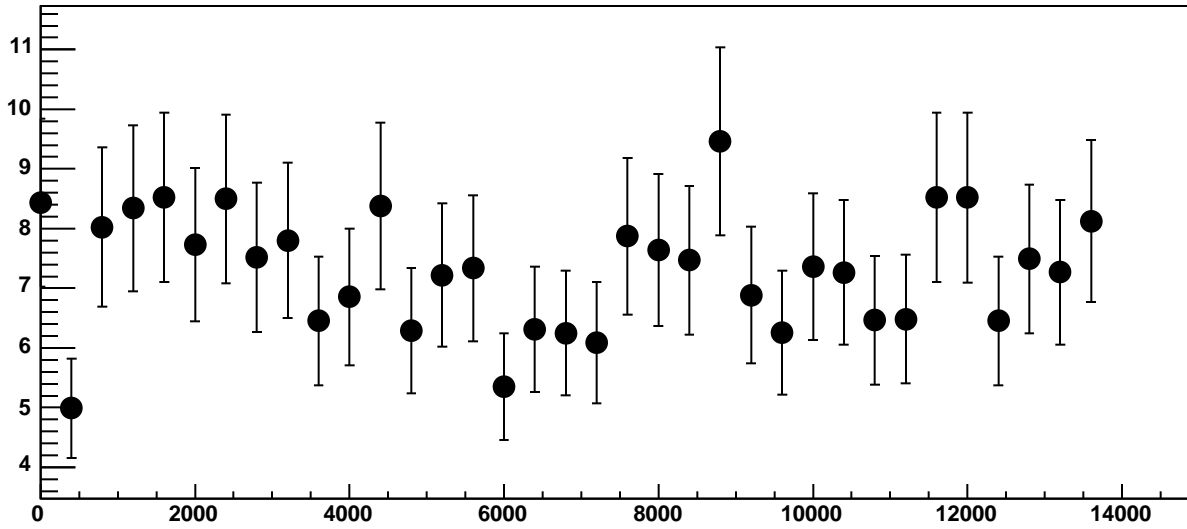


Chip 11, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

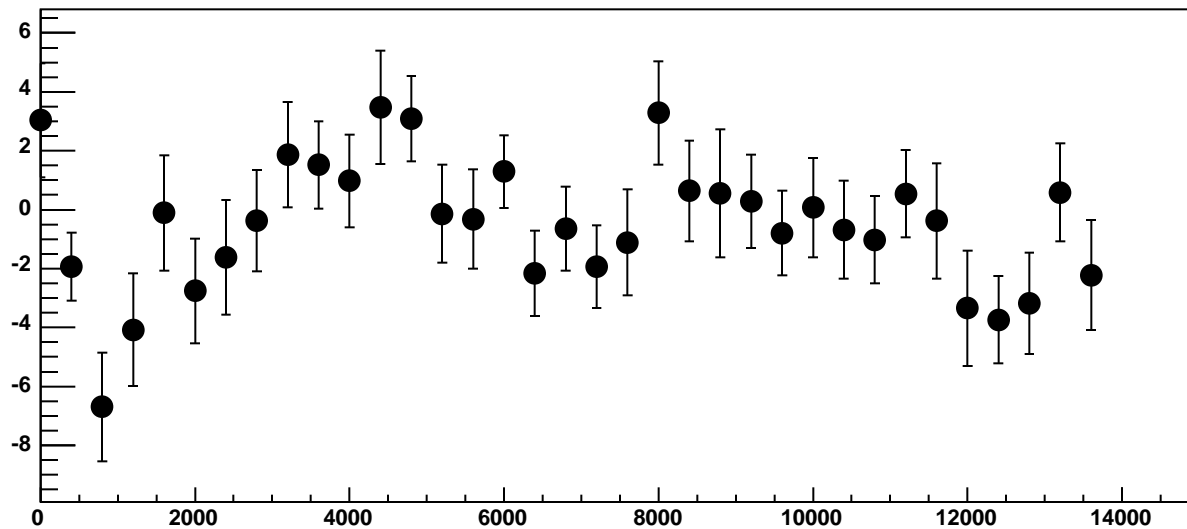


χ^2 / ndf 28.66 / 23
p0 -565.4 ± 0.8028
p1 0.01913 ± 0.0001182

Chip 11, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

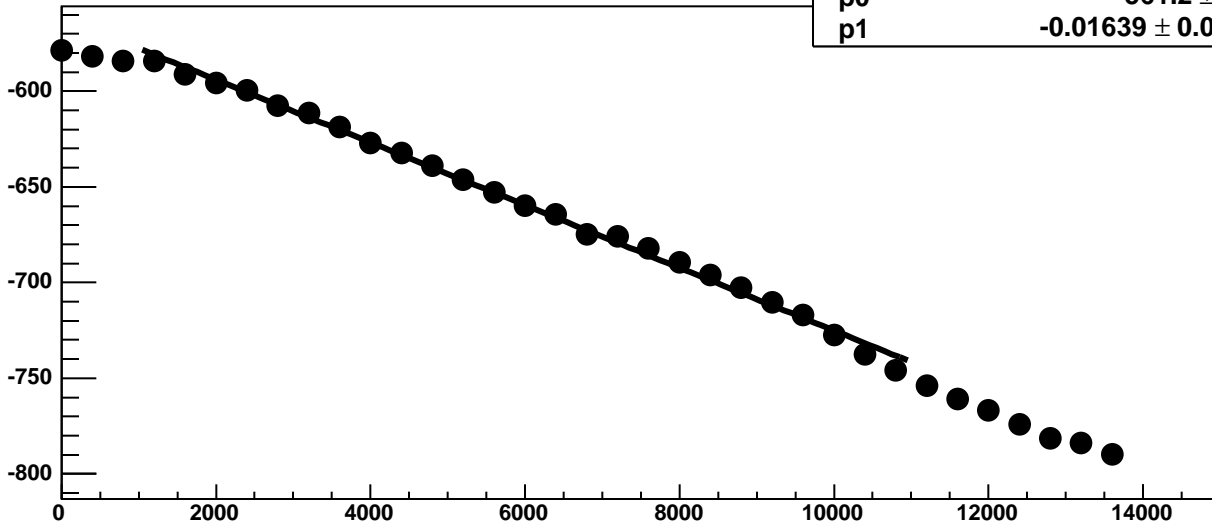


Chip 11, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

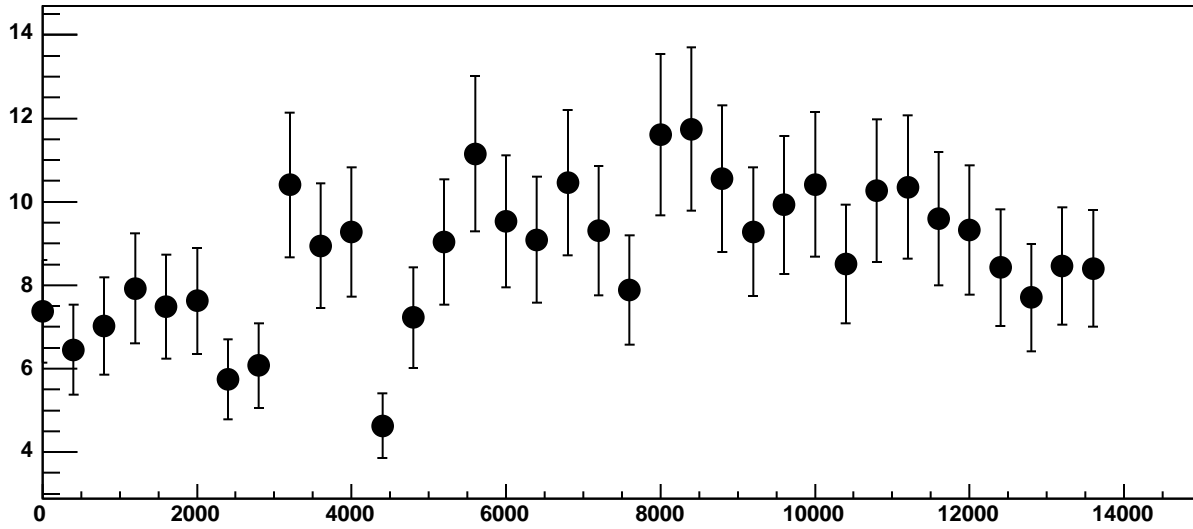


Chip 11, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

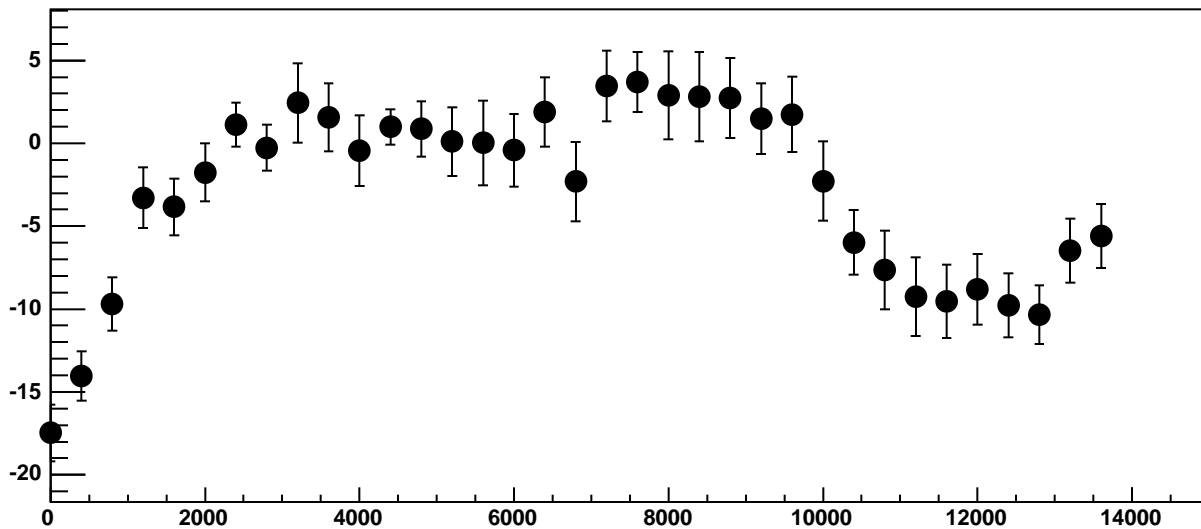
χ^2 / ndf 46.92 / 23
p0 -561.2 ± 0.8022
p1 -0.01639 ± 0.0001353



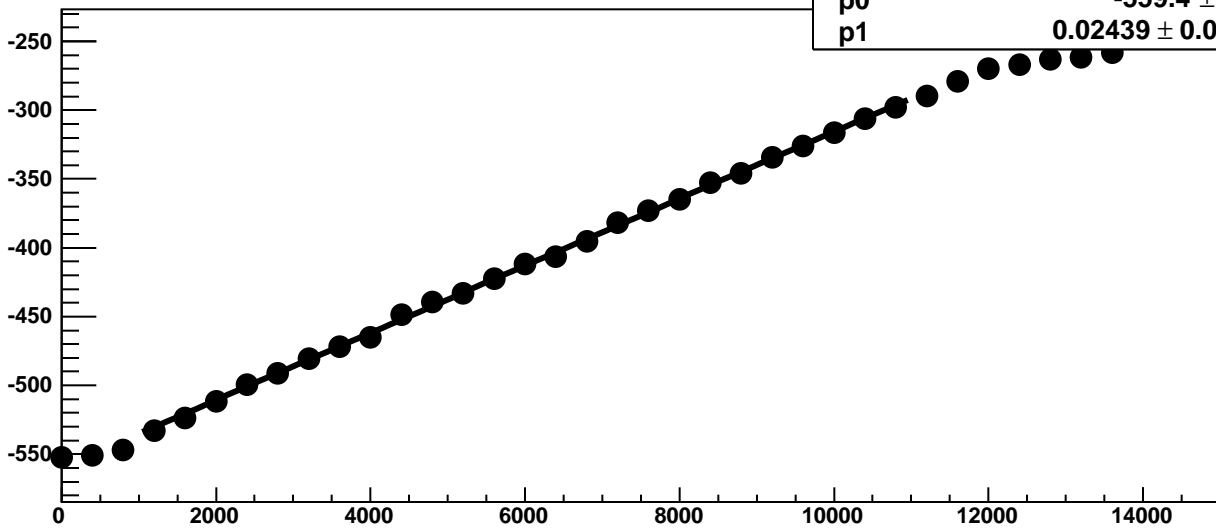
Chip 11, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC

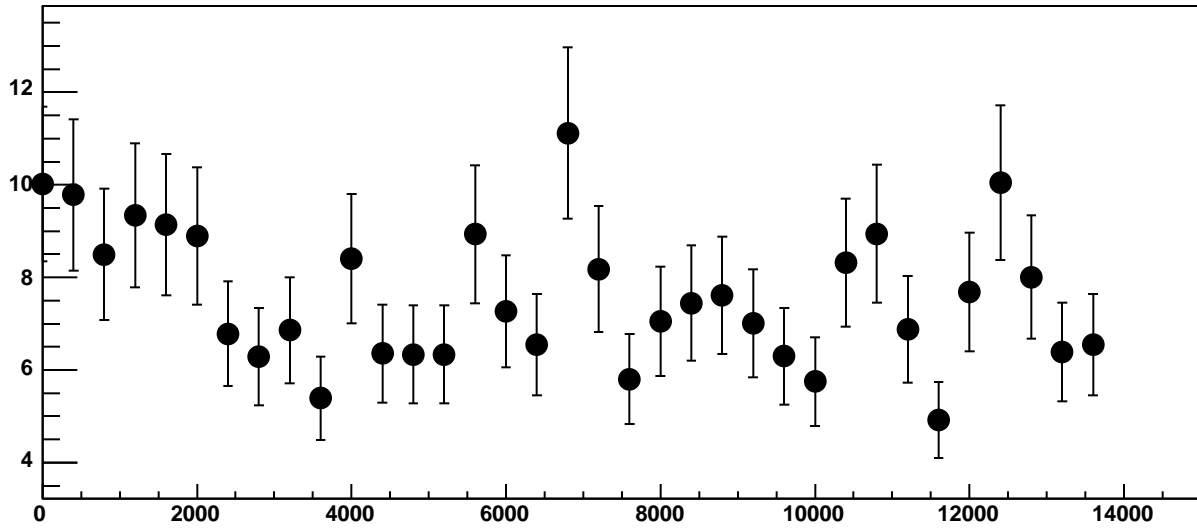


Chip 11, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

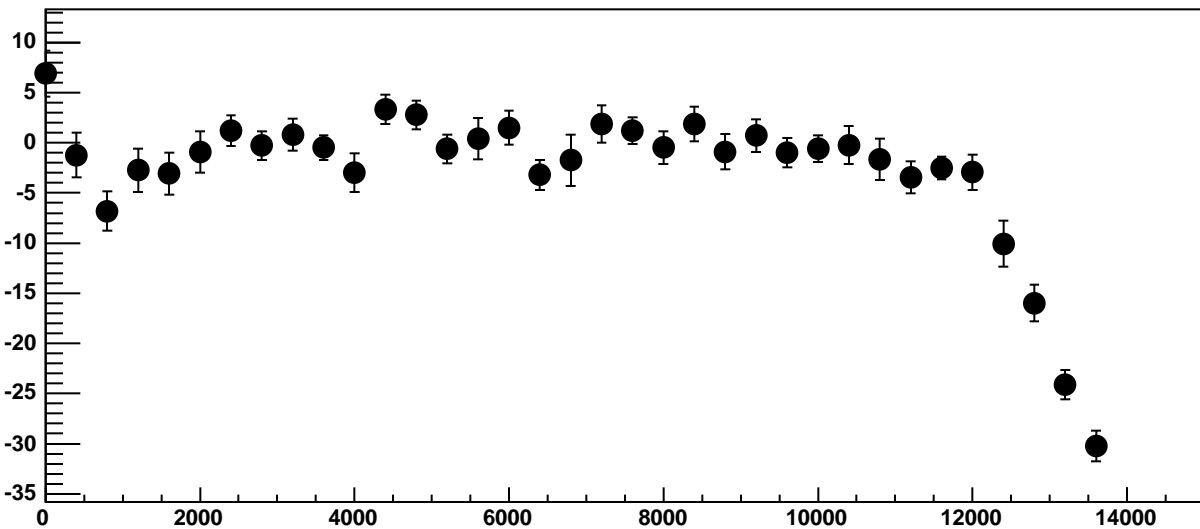


χ^2 / ndf 27.18 / 23
p0 -559.4 ± 0.7859
p1 0.02439 ± 0.0001179

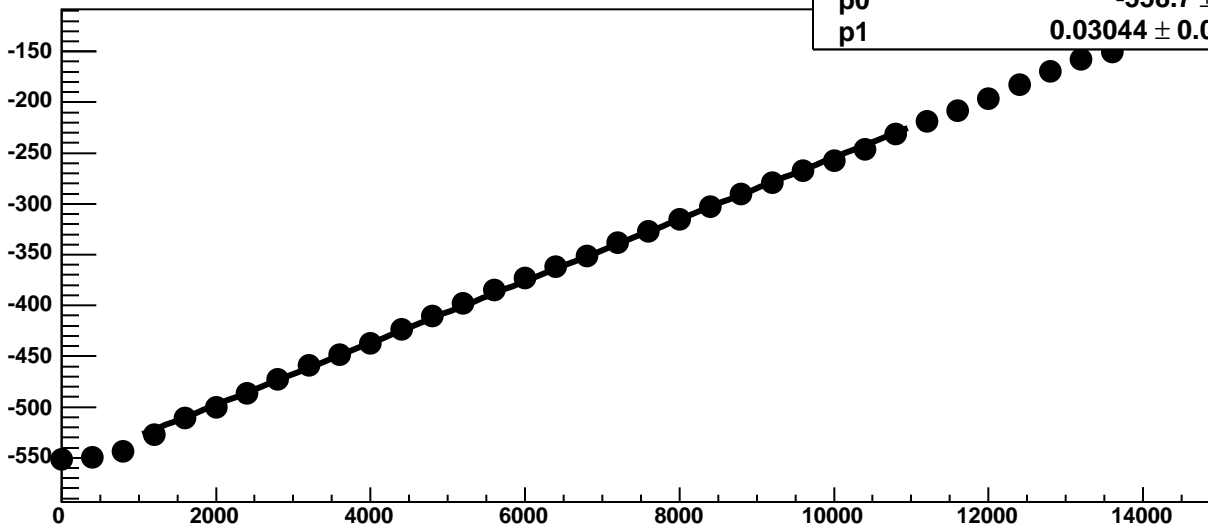
Chip 11, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

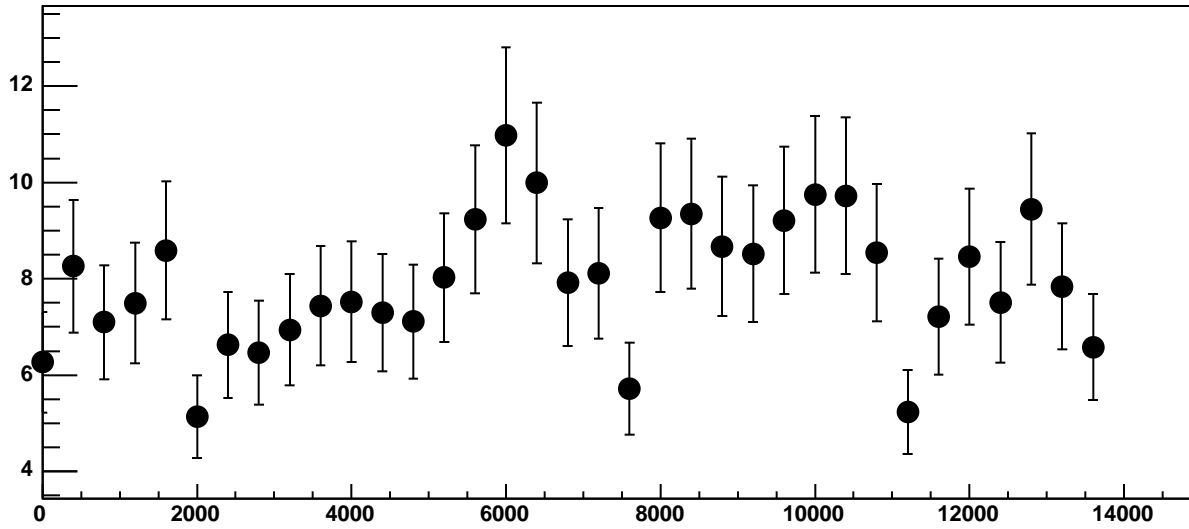


Chip 11, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

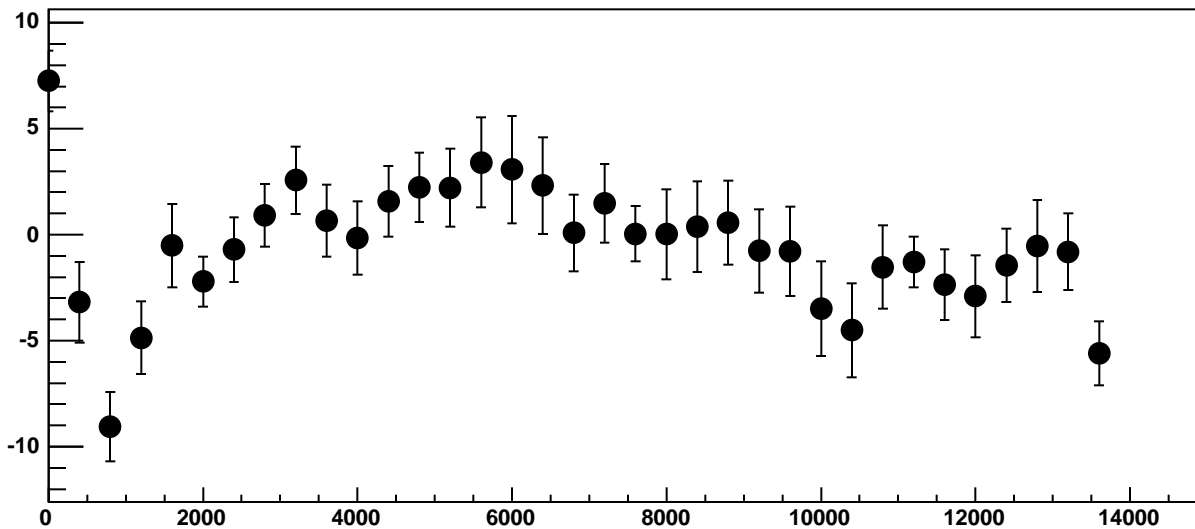


χ^2 / ndf 32.39 / 23
p0 -558.7 ± 0.7596
p1 0.03044 ± 0.0001243

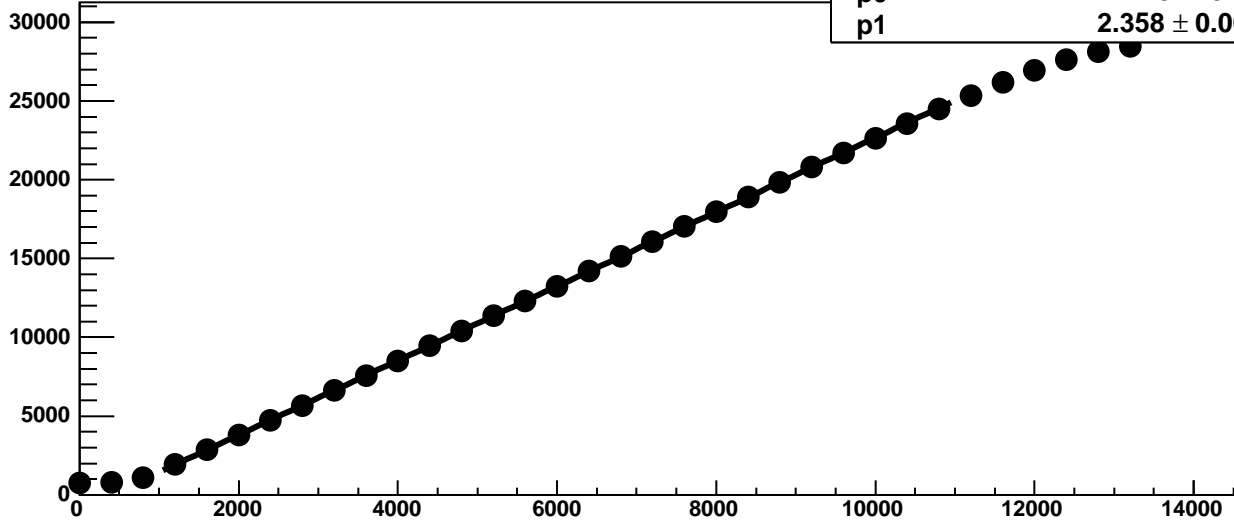
Chip 11, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



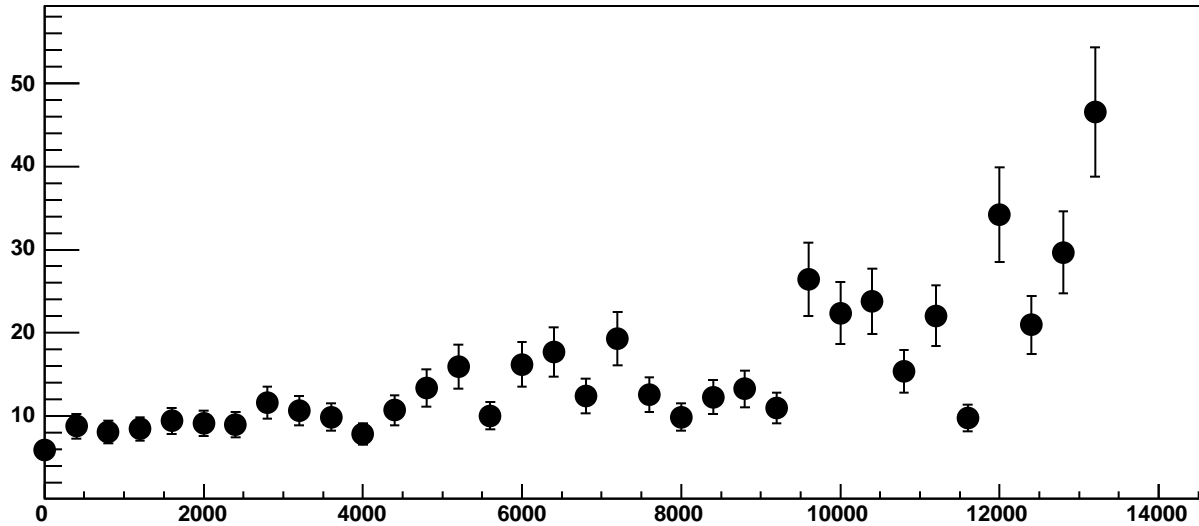
Chip 11, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



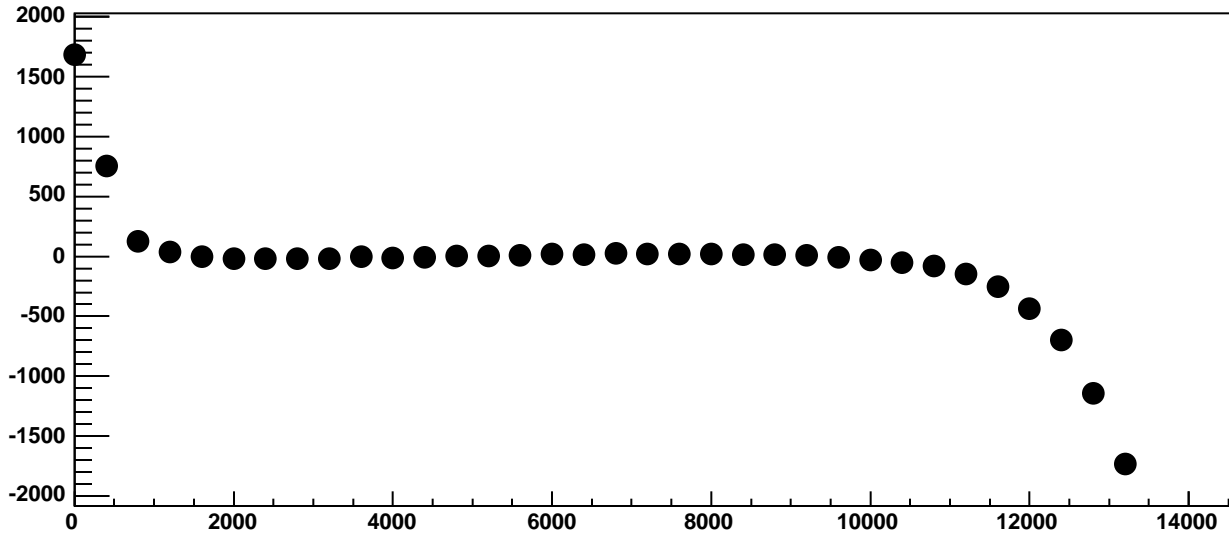
Chip 11, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC



Chip 11, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC

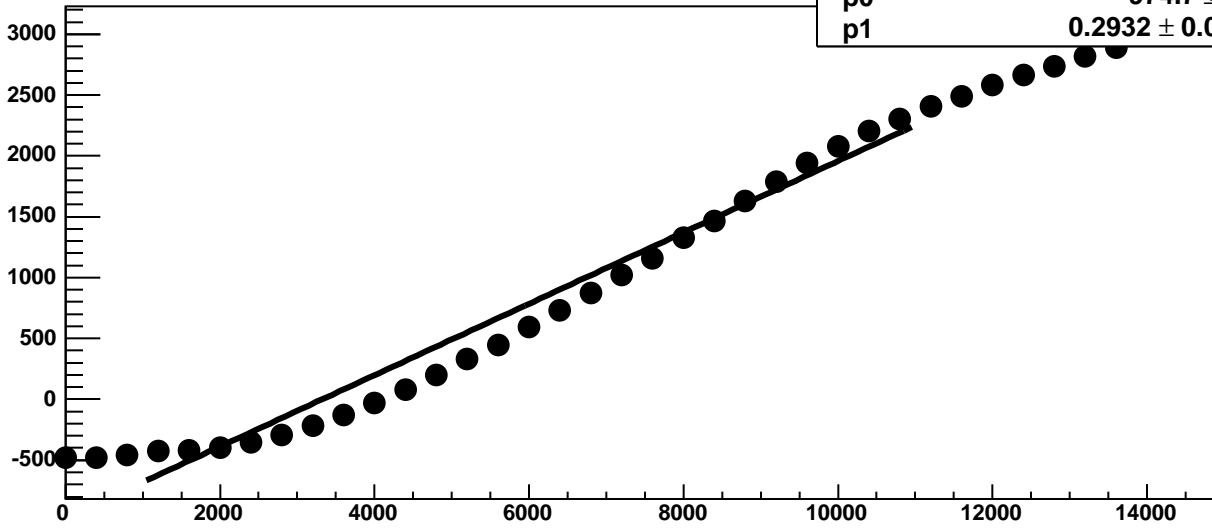


Chip 11, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC

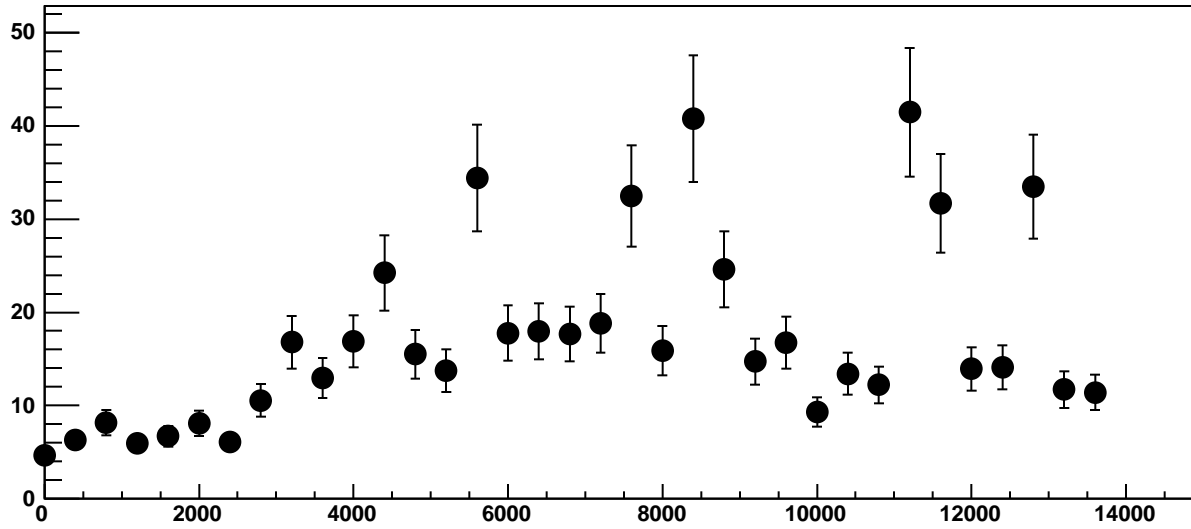


Chip 11, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

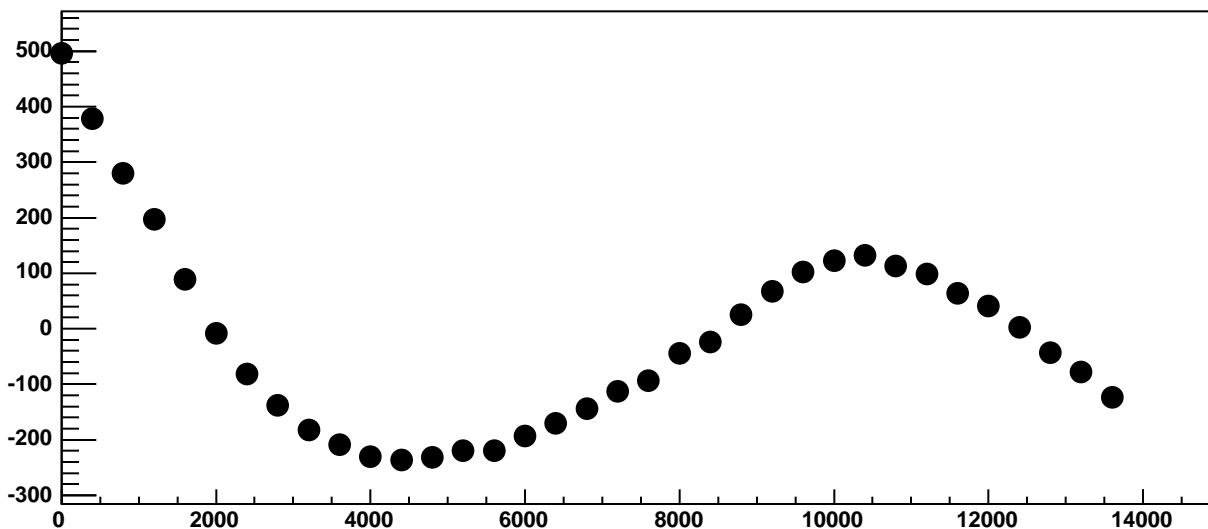
χ^2 / ndf 6.708e+04 / 23
p0 -974.7 ± 0.8946
p1 0.2932 ± 0.0001662



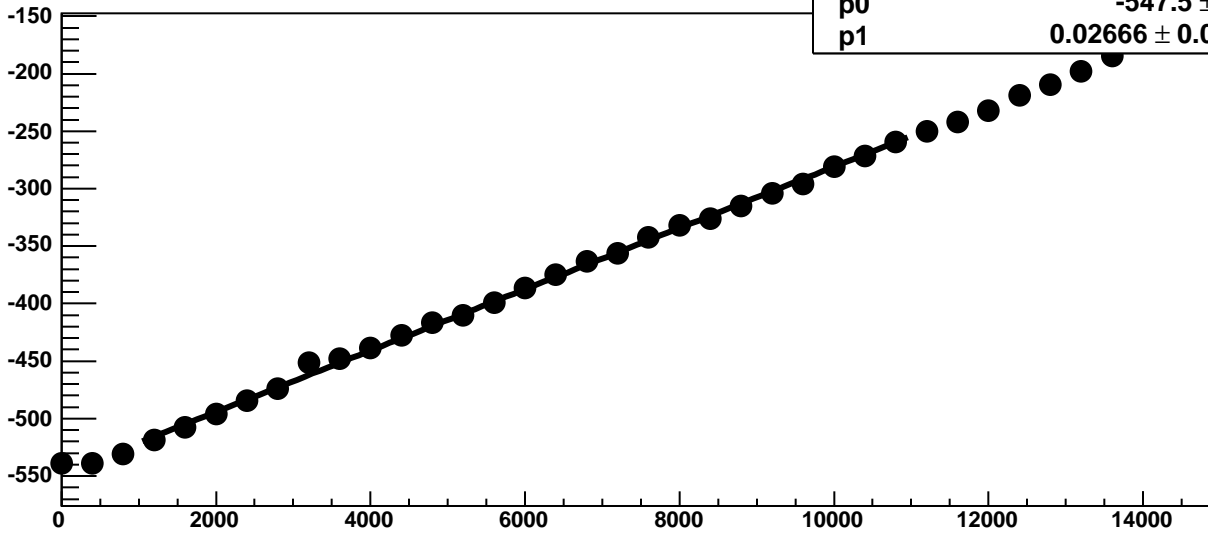
Chip 11, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

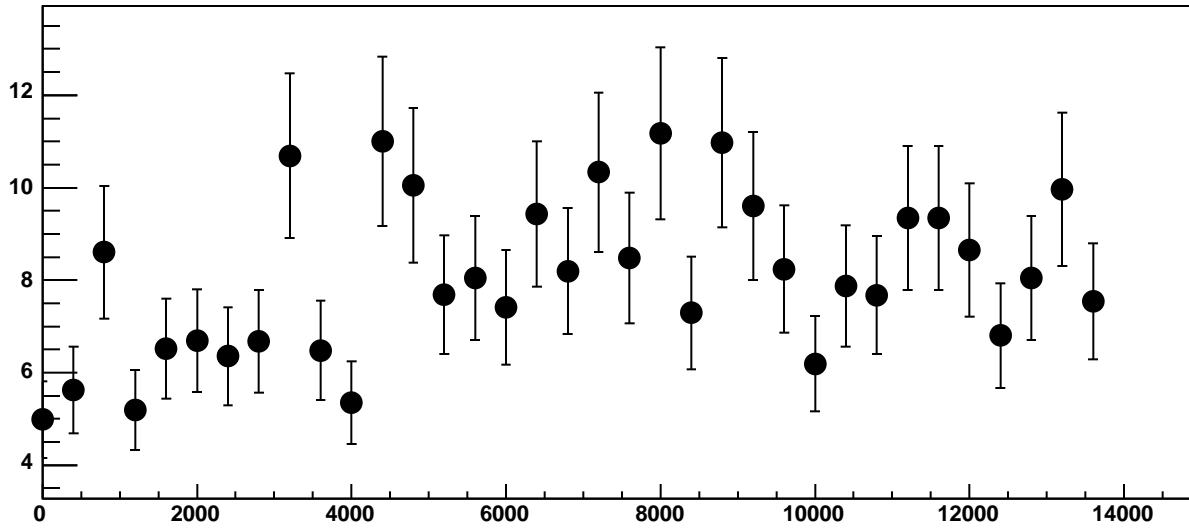


Chip 11, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

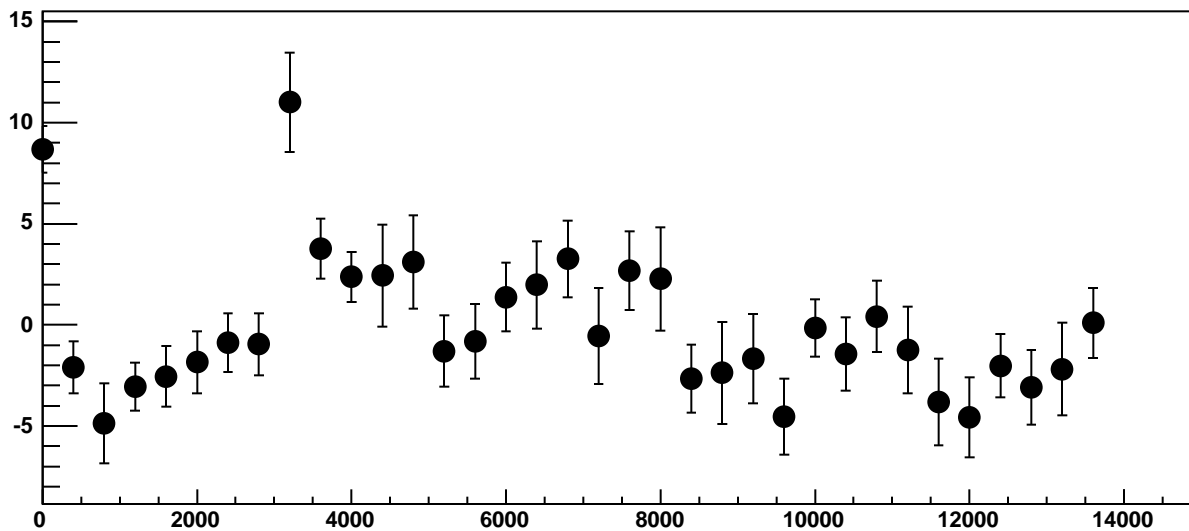


χ^2 / ndf 63.11 / 23
p0 -547.5 ± 0.7024
p1 0.02666 ± 0.0001125

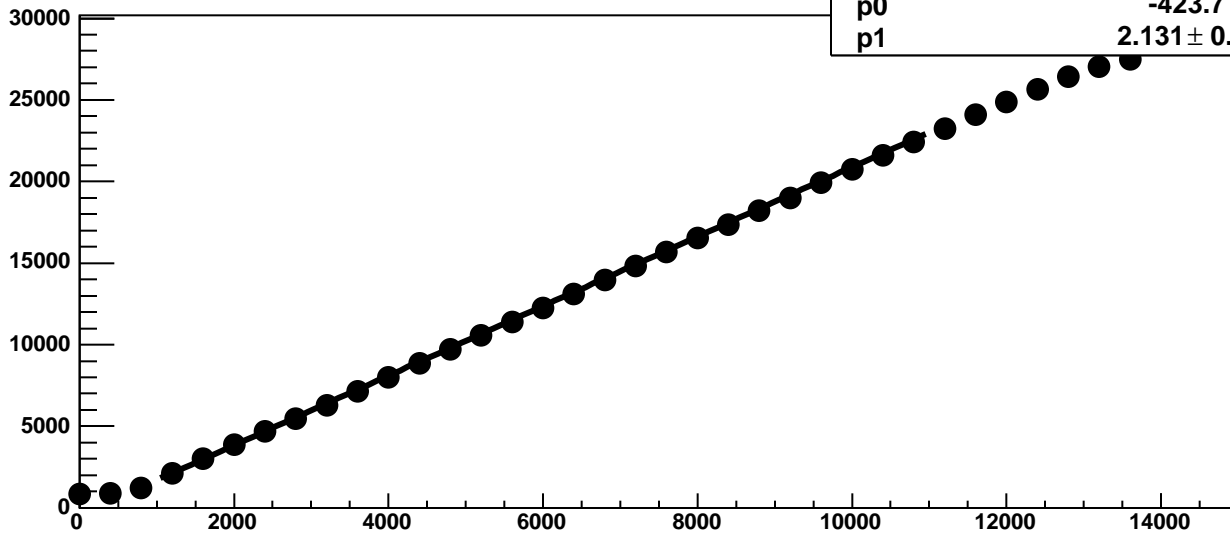
Chip 11, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

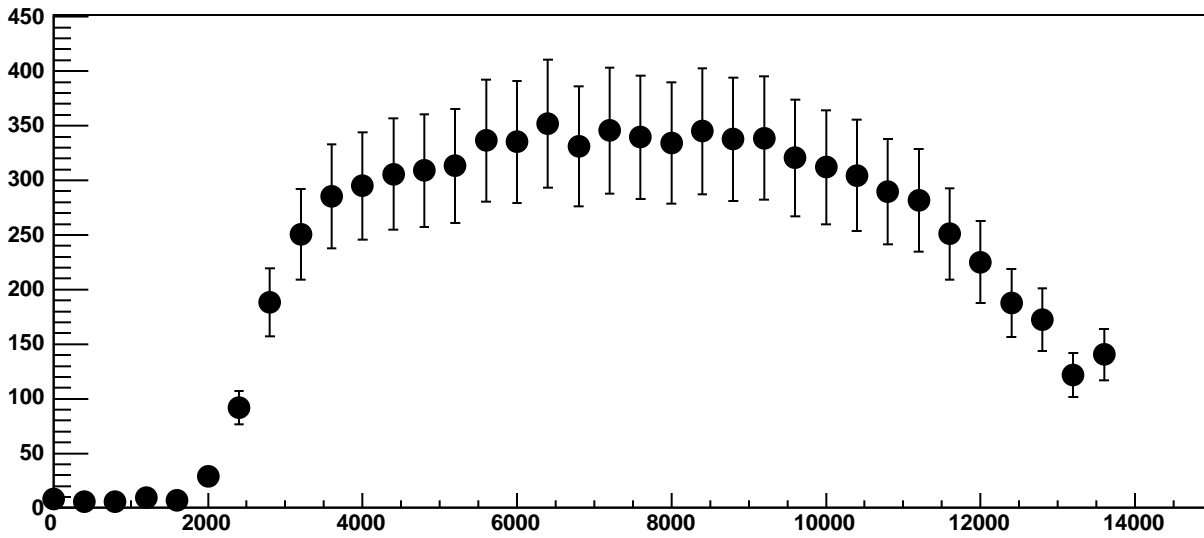


Chip 11, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC

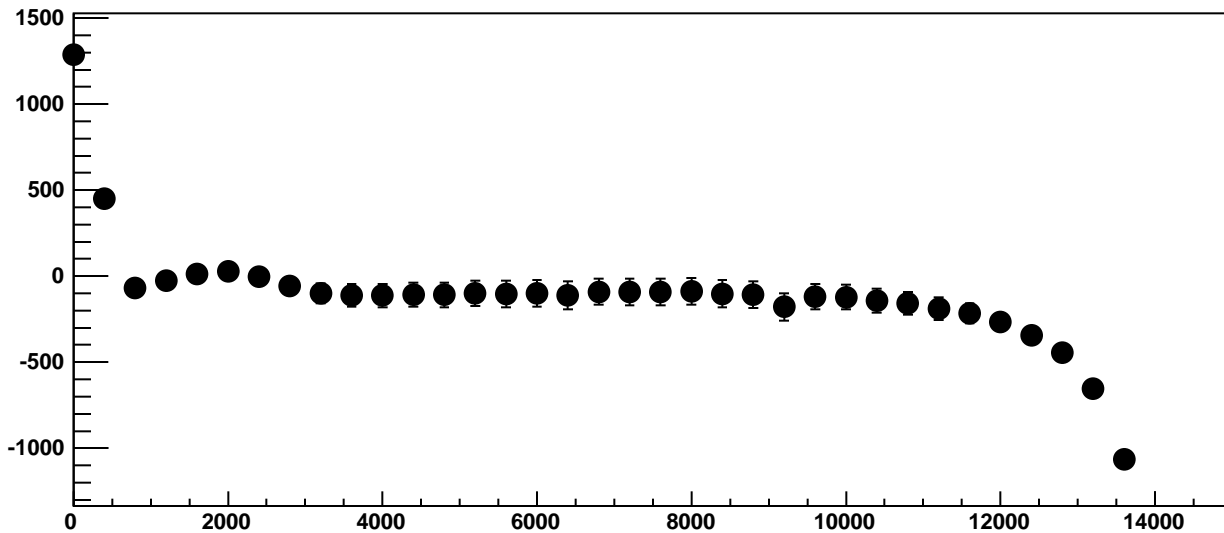


χ^2 / ndf 274.3 / 23
p0 -423.7 ± 3.957
p1 2.131 ± 0.002477

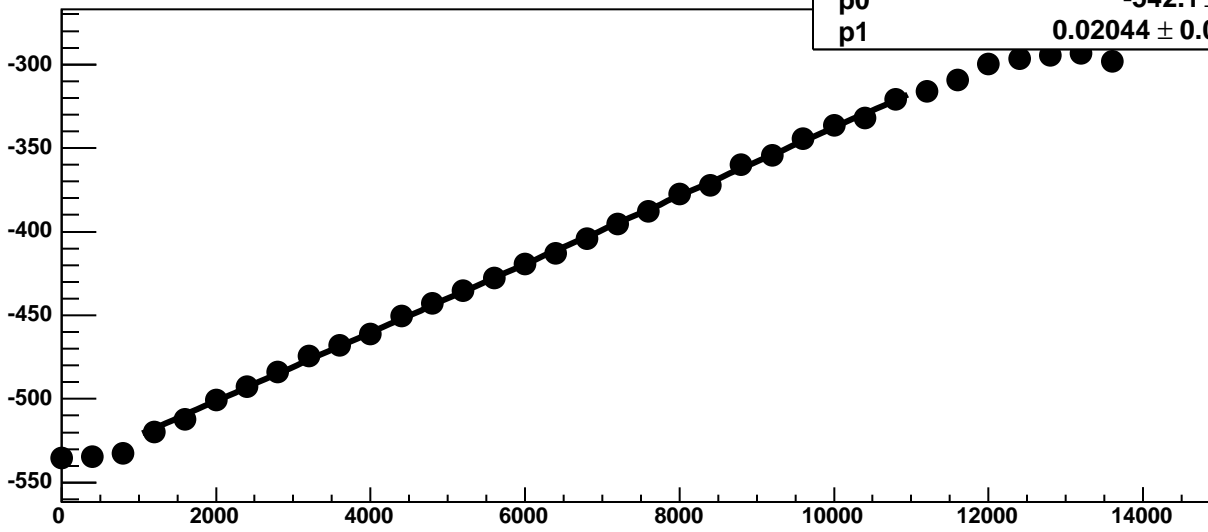
Chip 11, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC

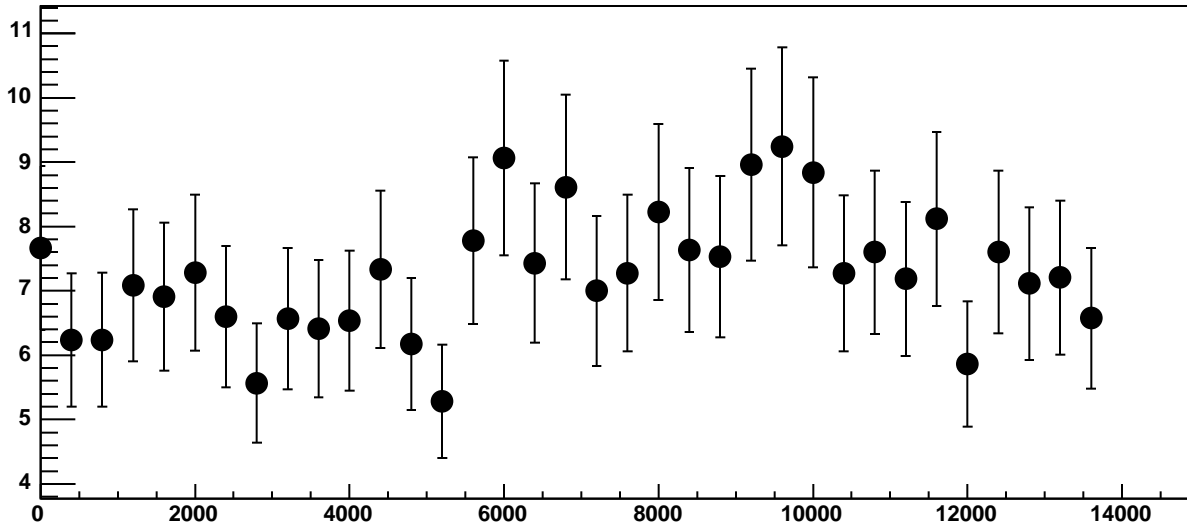


Chip 11, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

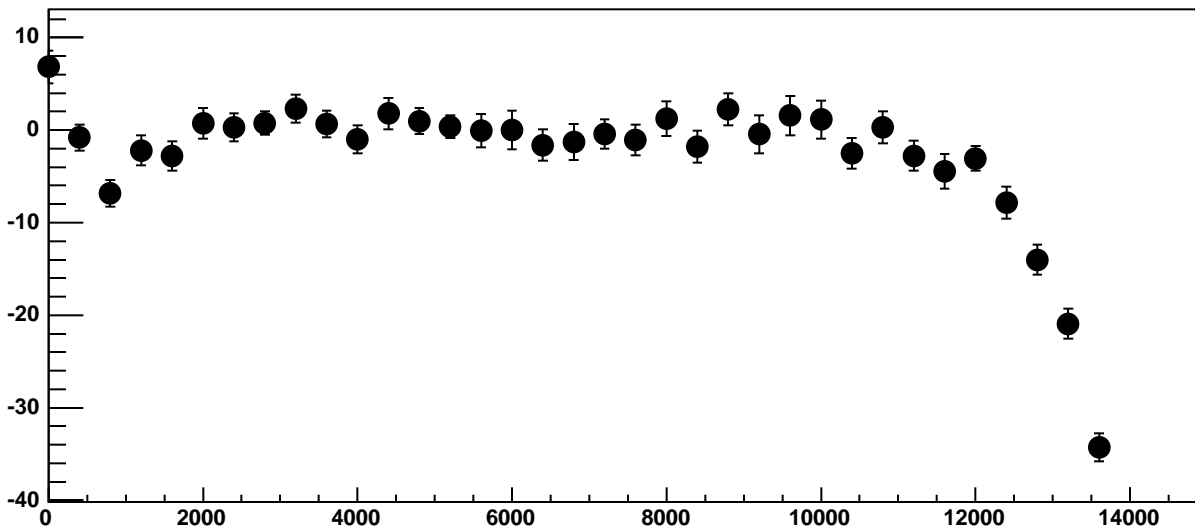


χ^2 / ndf 18.37 / 23
p0 -542.1 ± 0.7261
p1 0.02044 ± 0.0001169

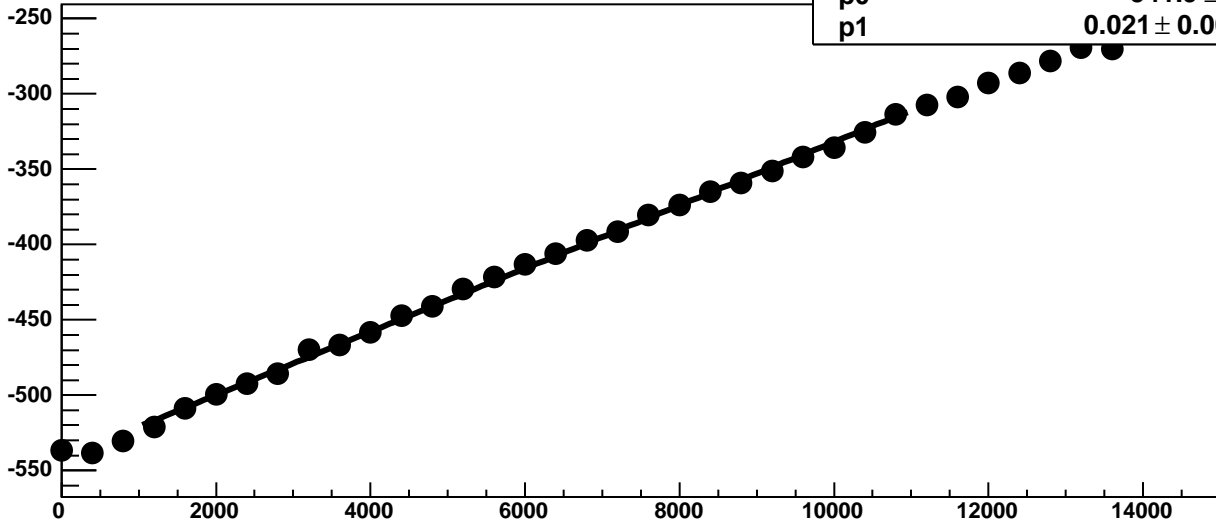
Chip 11, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

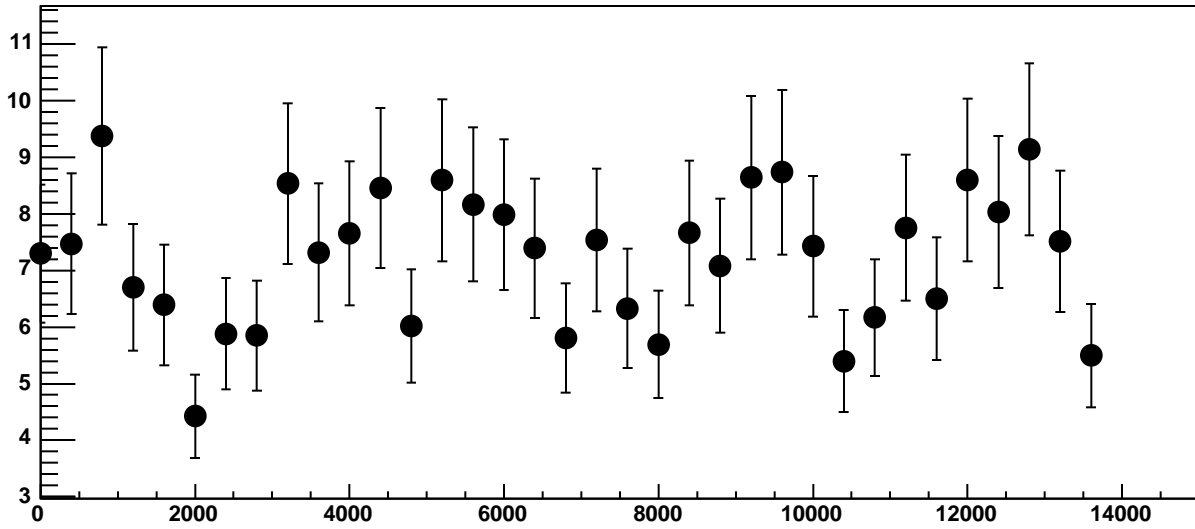


Chip 11, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

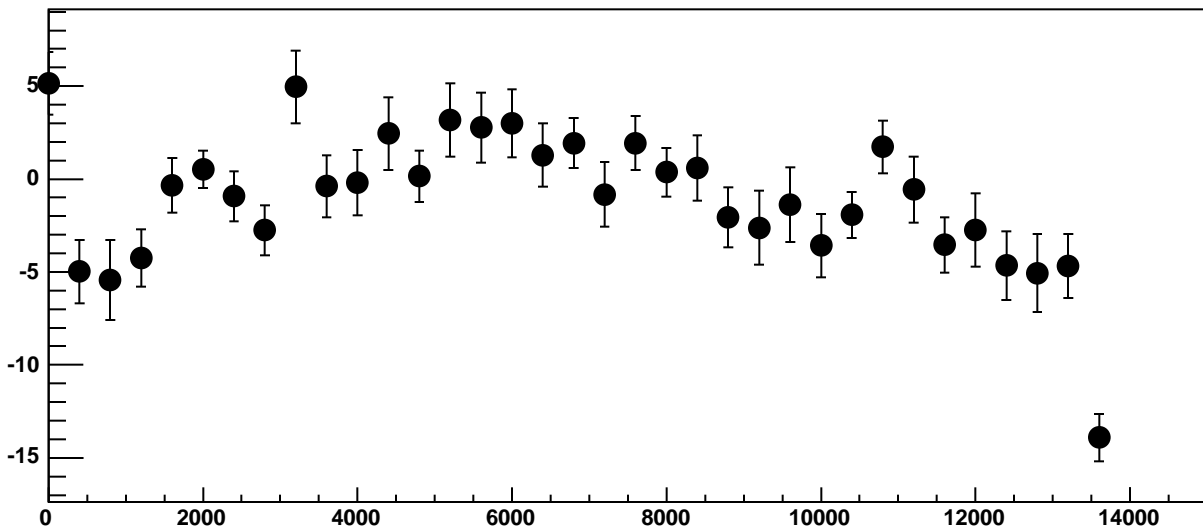


χ^2 / ndf 45.2 / 23
p0 -541.9 ± 0.6601
p1 0.021 ± 0.0001008

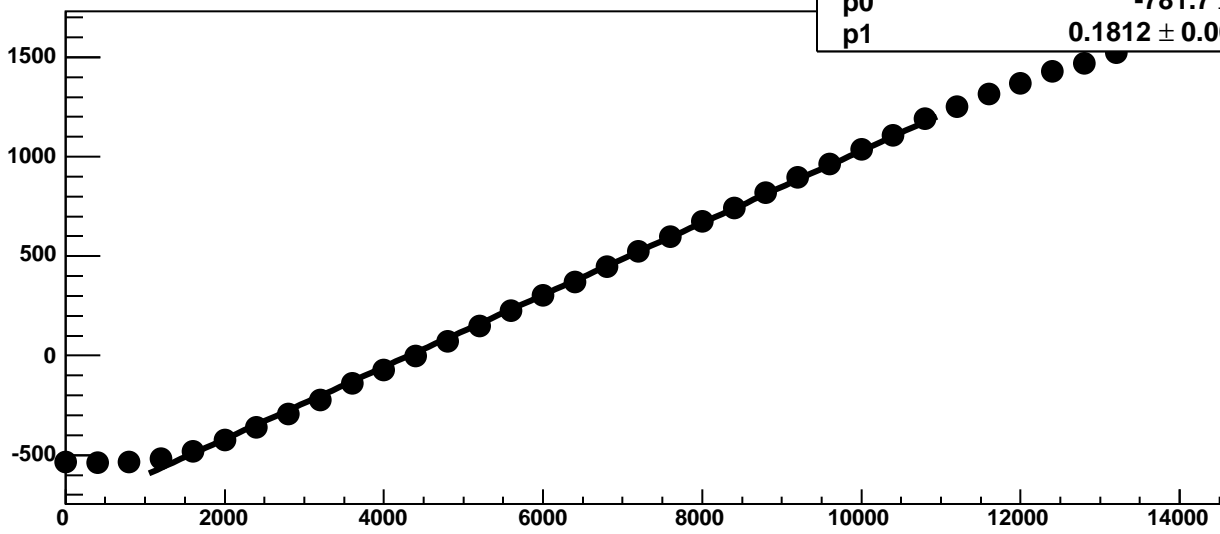
Chip 11, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



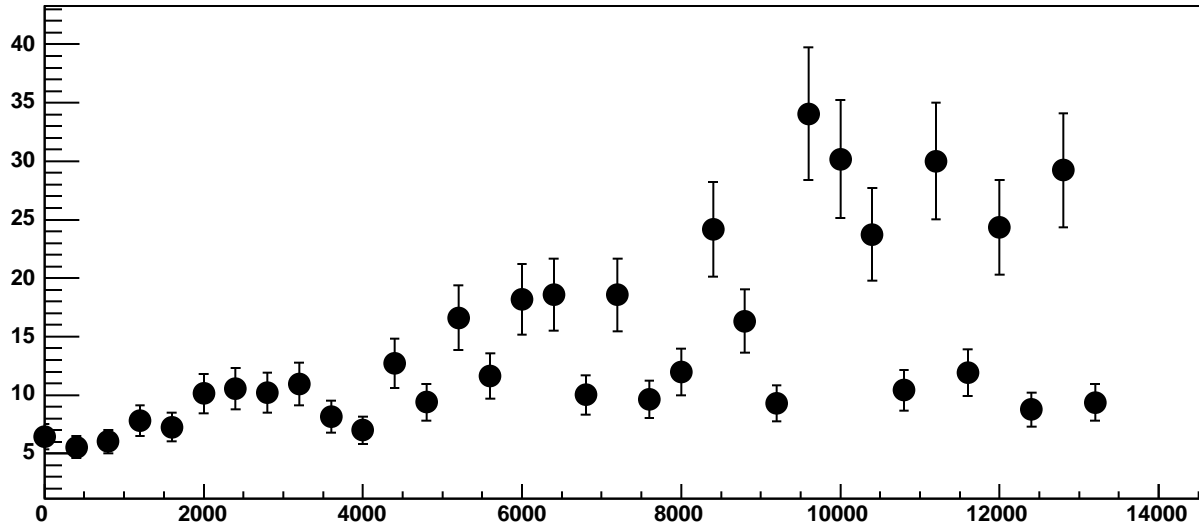
Chip 11, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



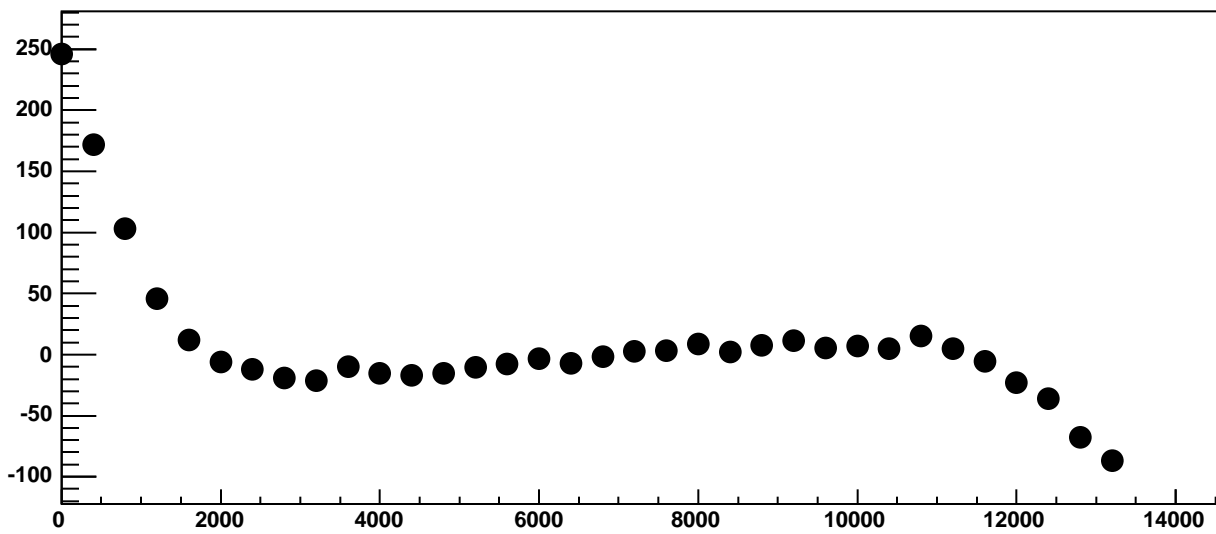
Chip 11, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC



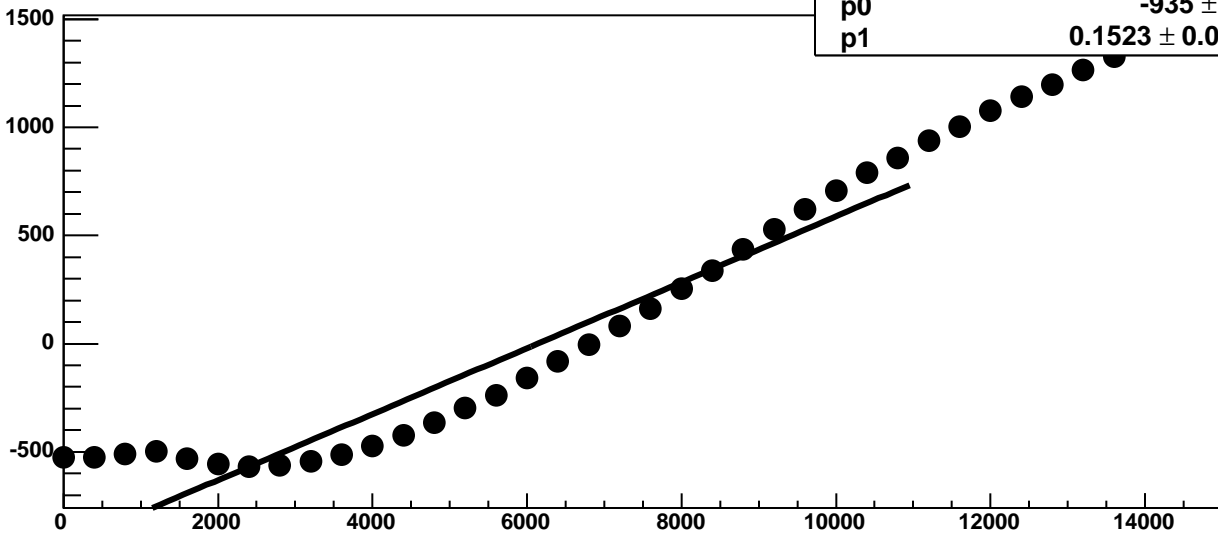
Chip 11, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



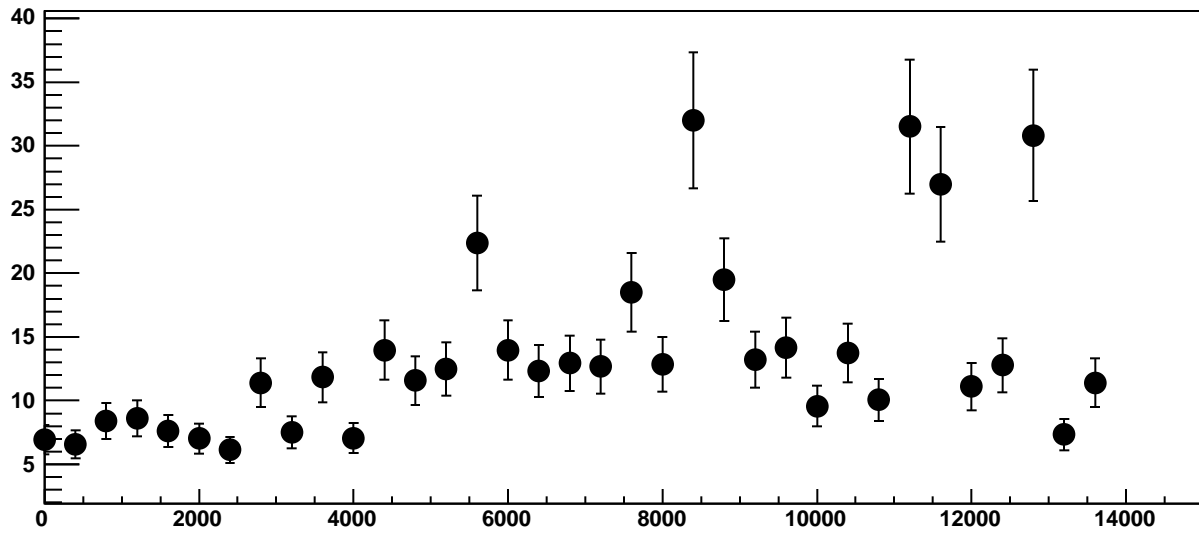
Chip 11, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC



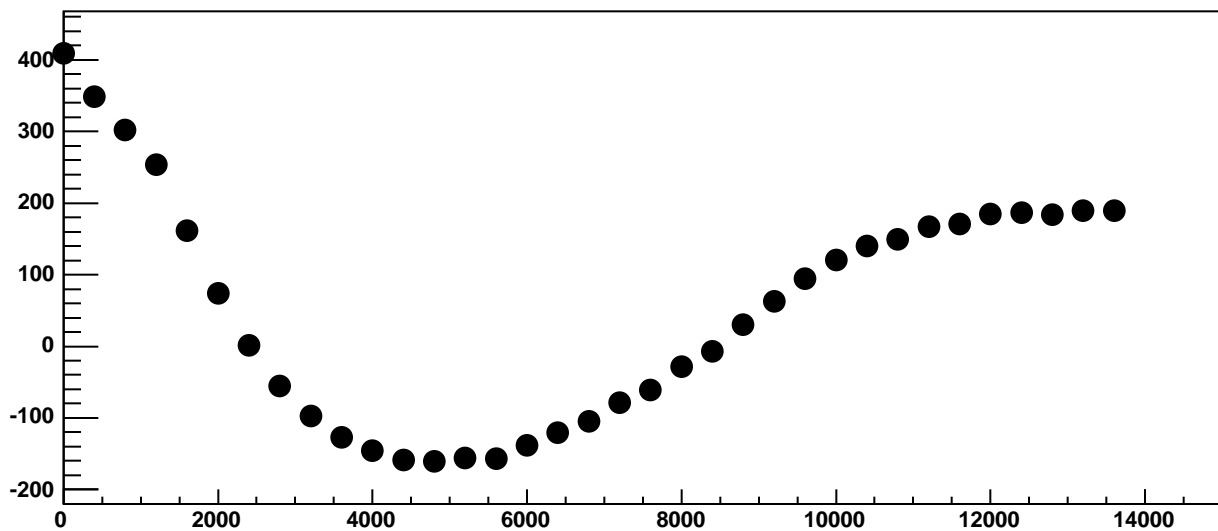
Chip 11, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



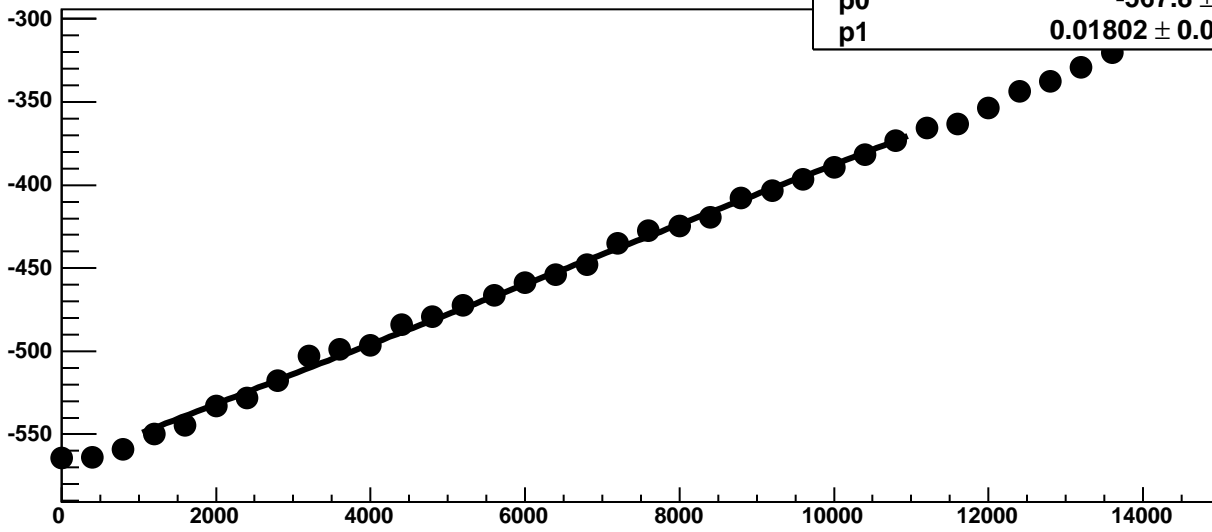
Chip 11, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

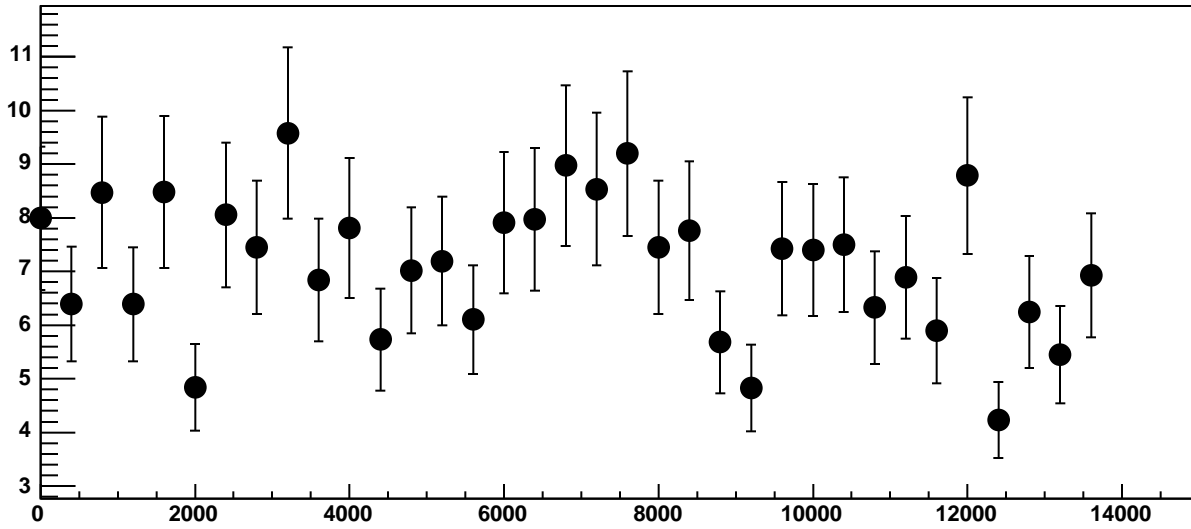


Chip 11, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

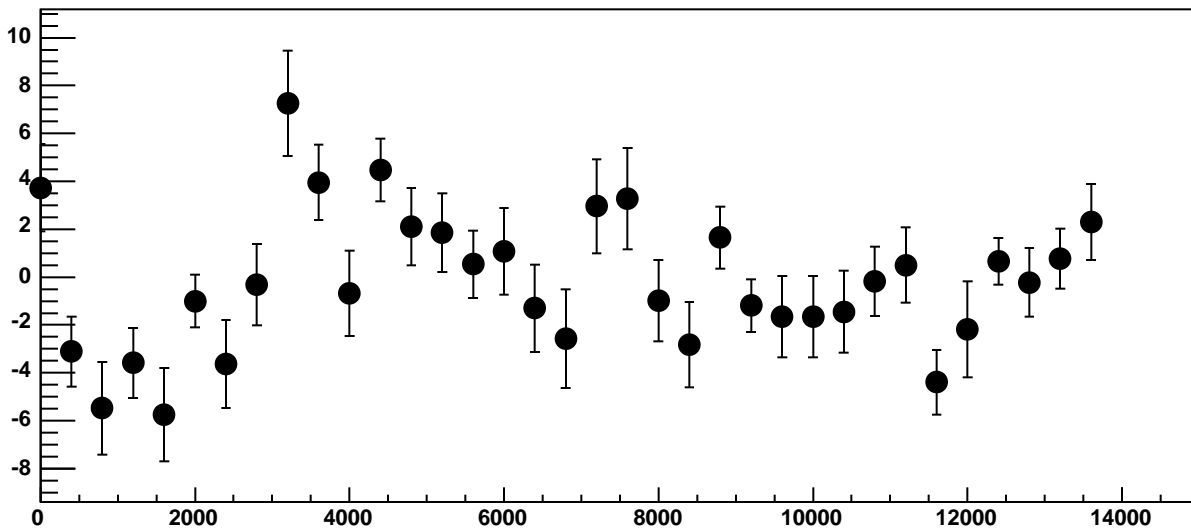


χ^2 / ndf 66.88 / 23
p0 -567.8 ± 0.7152
p1 0.01802 ± 0.0001063

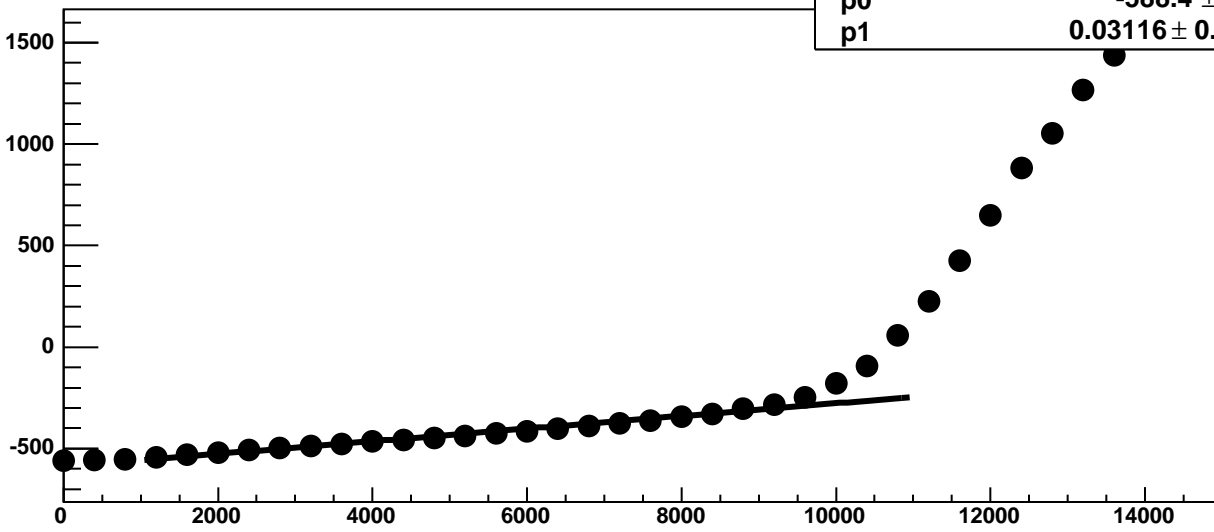
Chip 11, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

2080 / 23

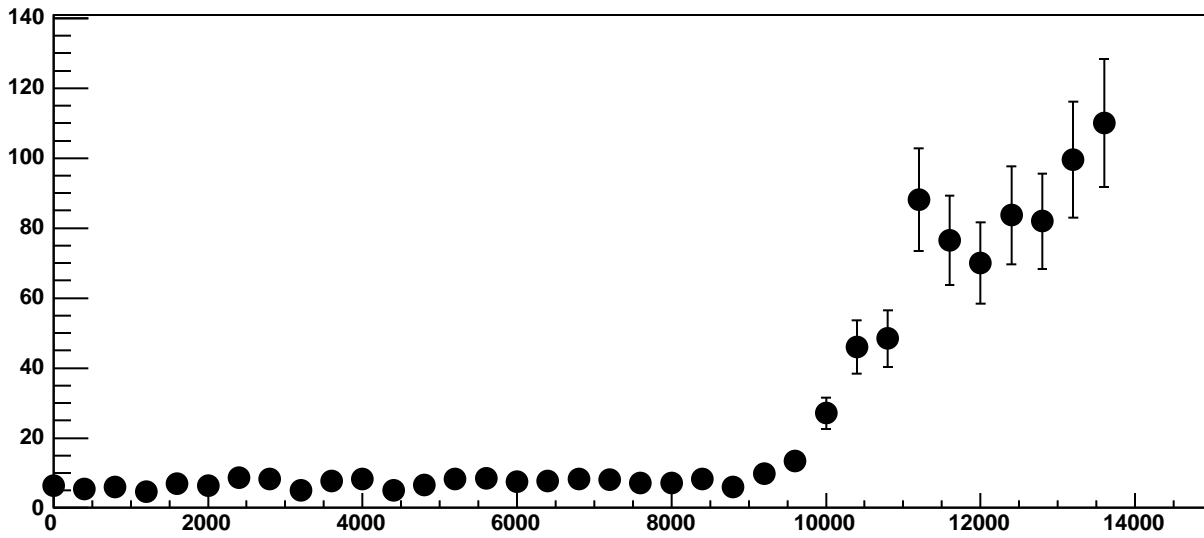
p0

-588.4 ± 0.7393

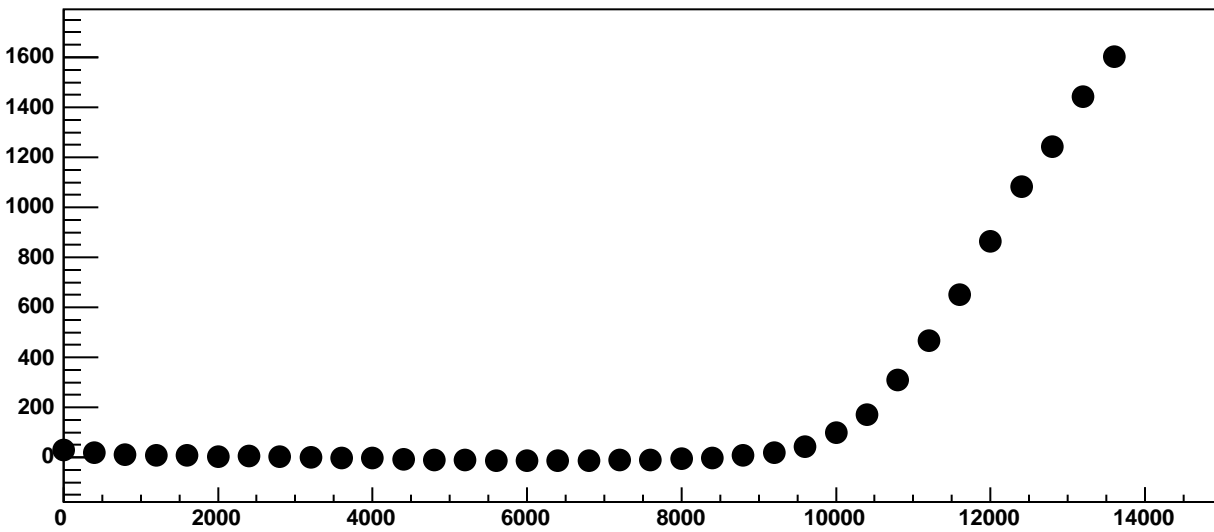
p1

0.03116 ± 0.000135

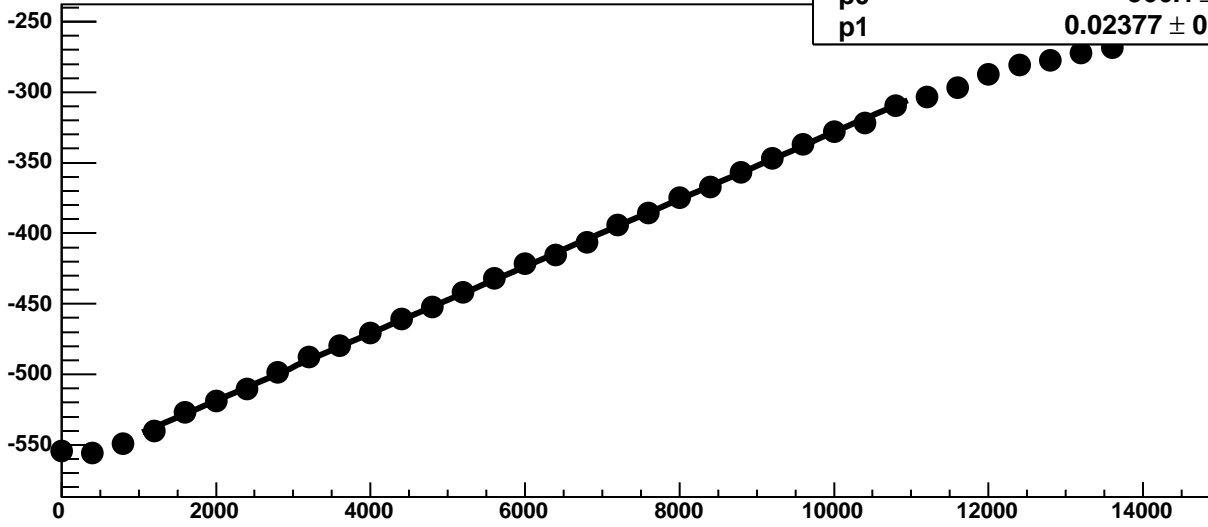
Chip 11, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



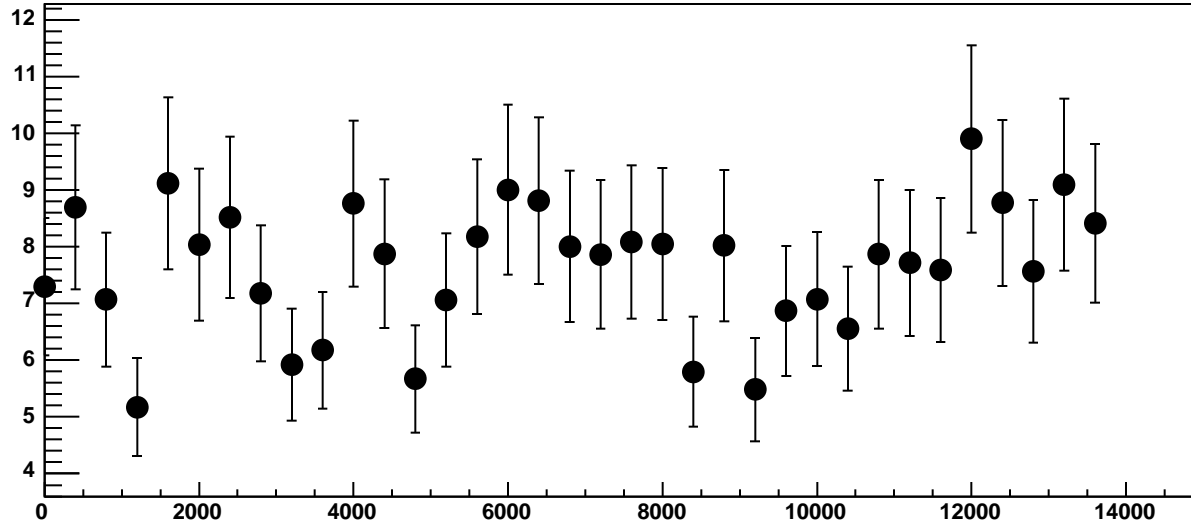
Chip 11, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC



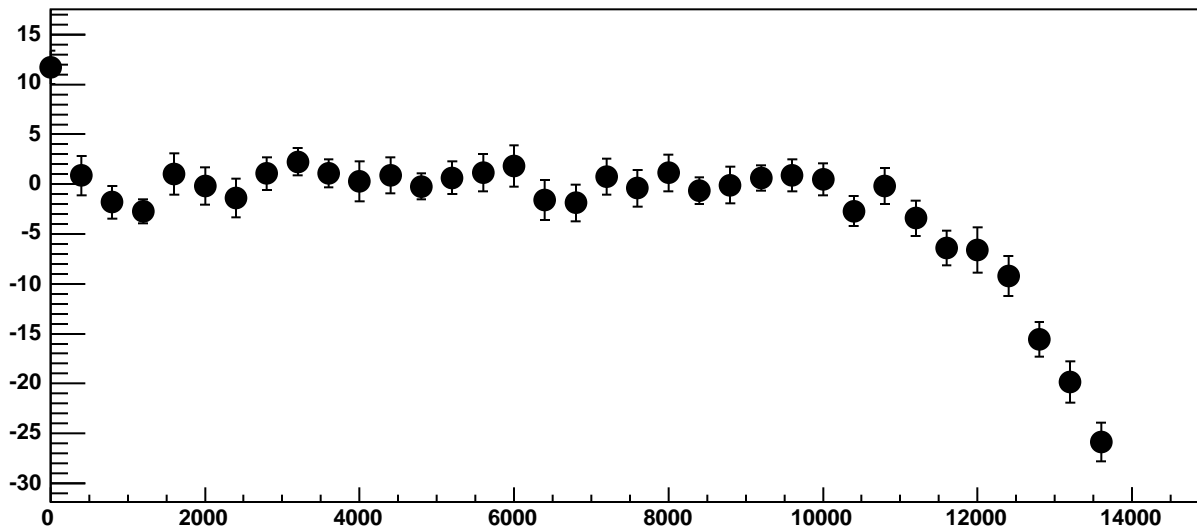
Chip 11, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



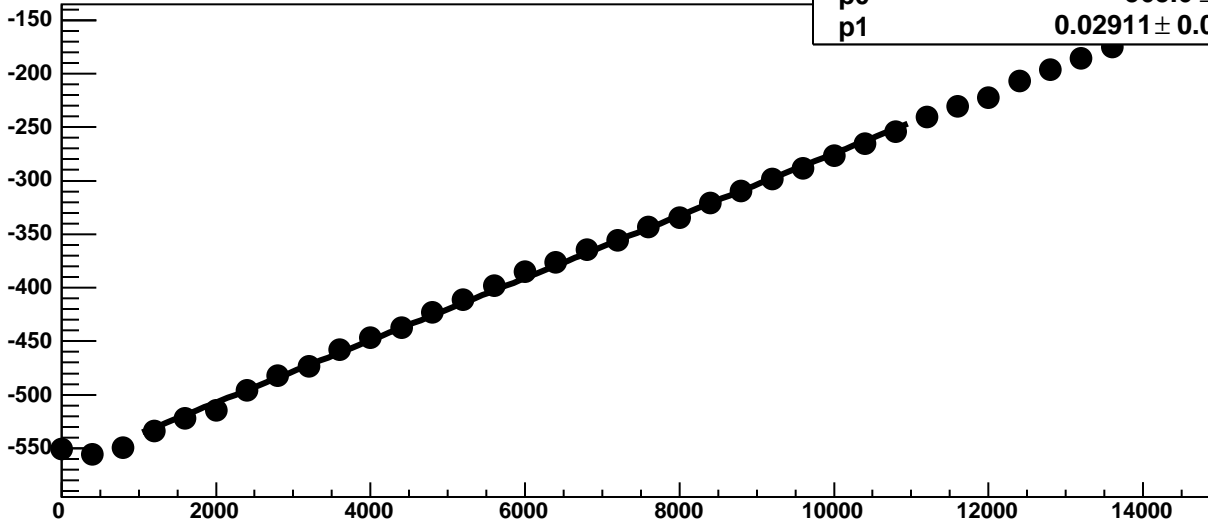
Chip 11, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



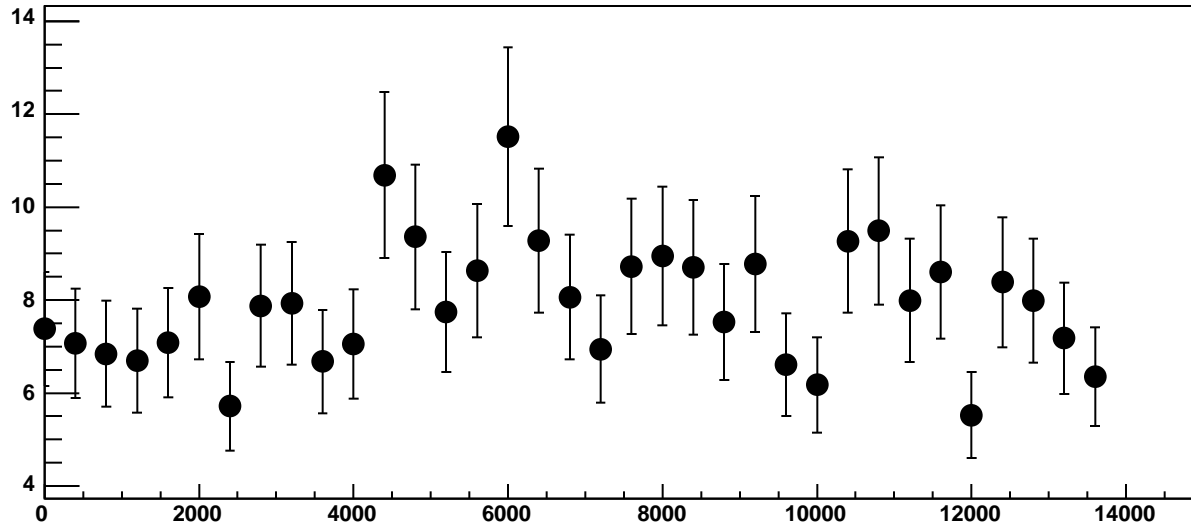
Chip 11, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



Chip 11, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



Chip 11, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

