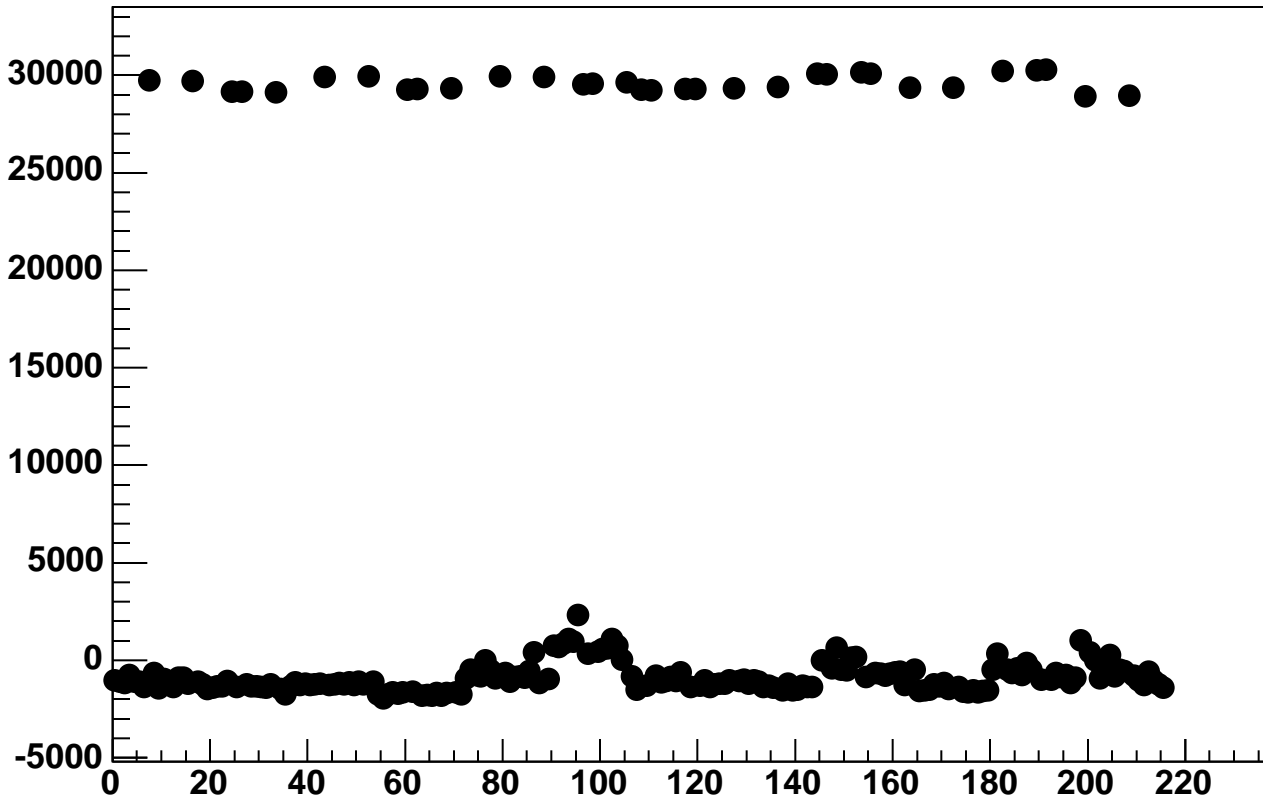
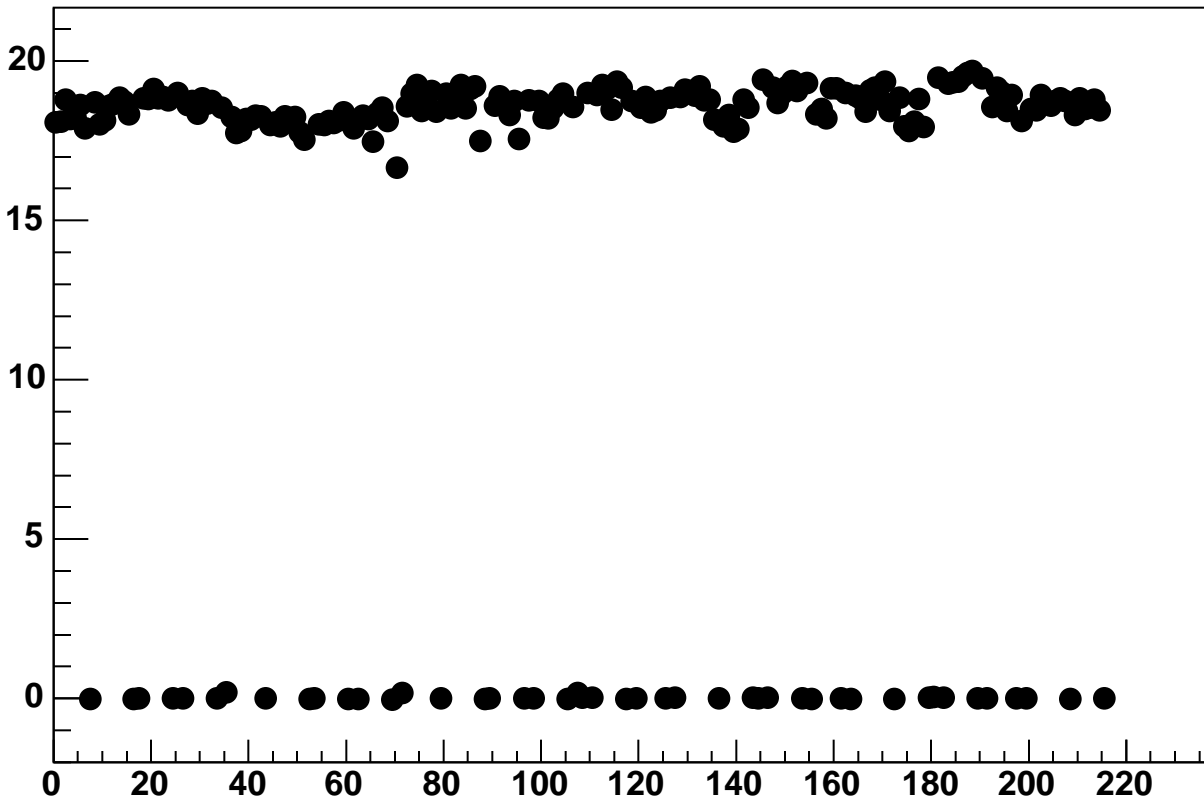


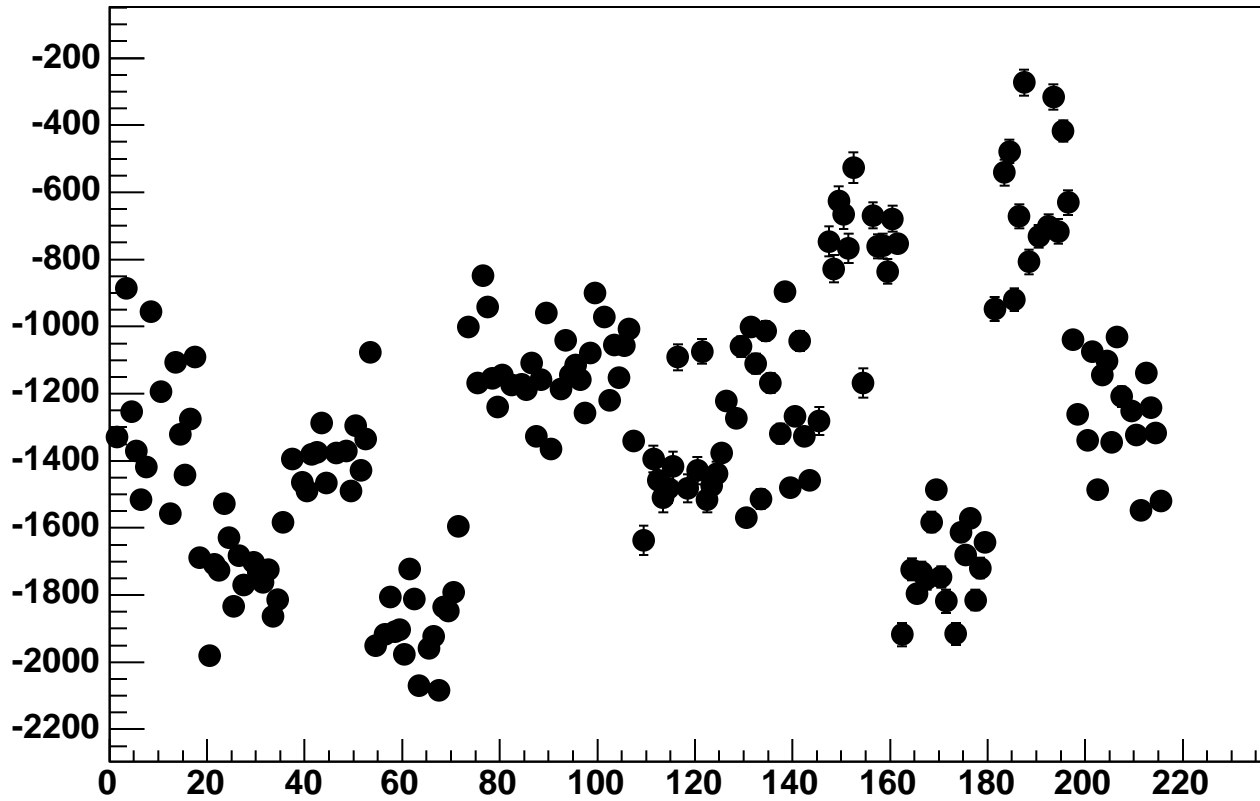
Channel Enabled, Hold=30, Fit Intercept vs 18\*Chip+Chan



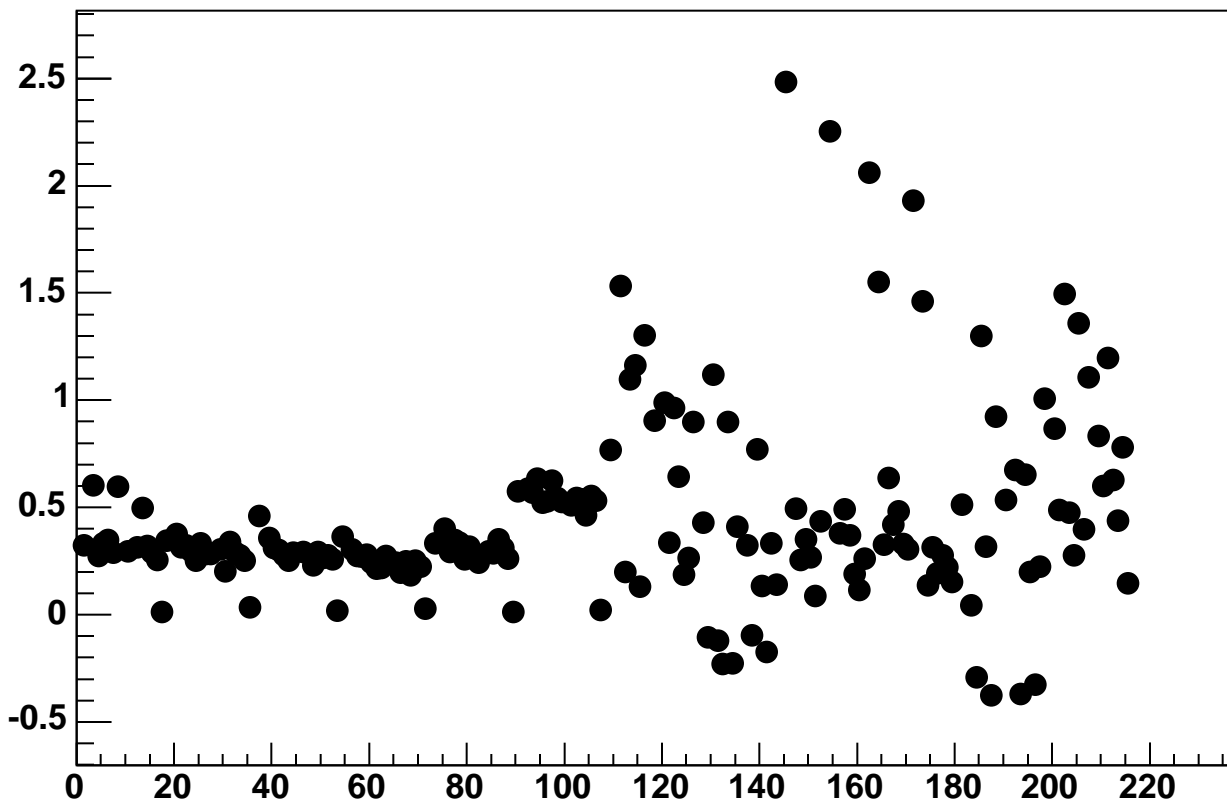
Channel Enabled, Hold=30, Fit Slope vs 18\*Chip+Chan



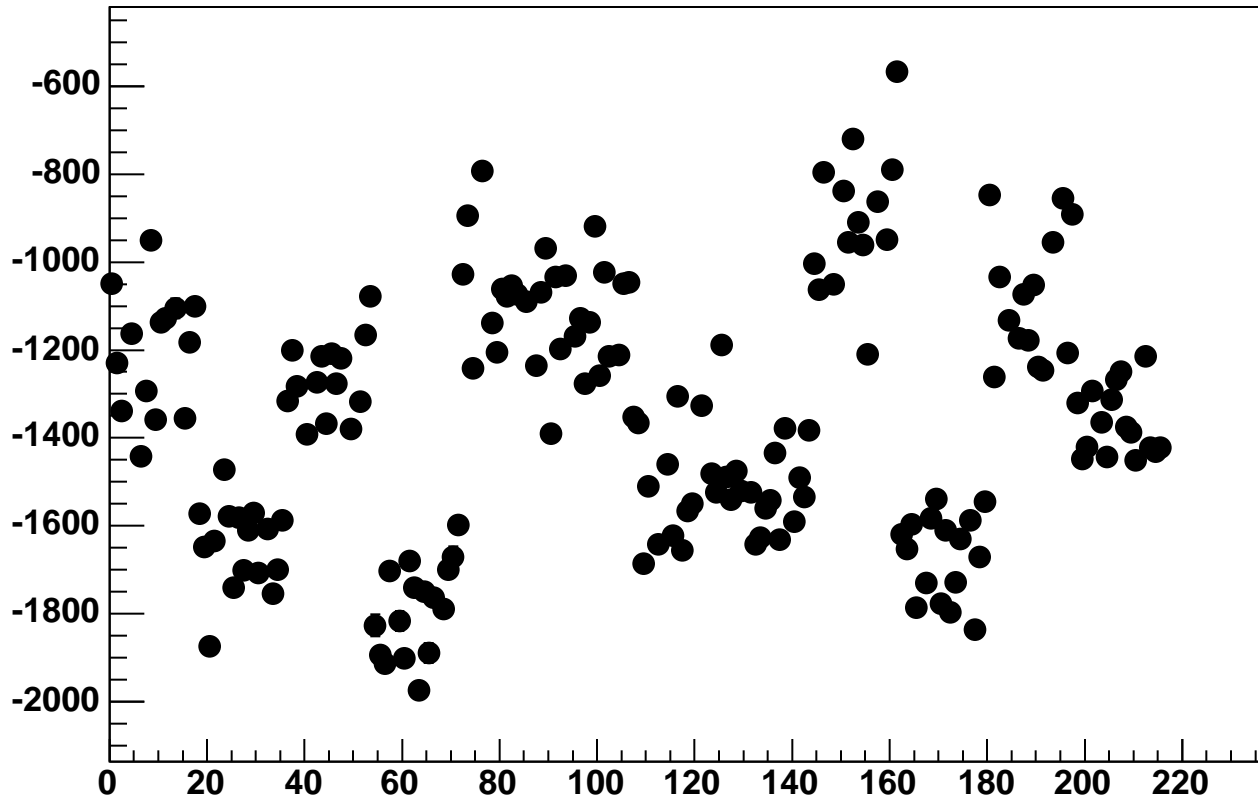
Disabled 0, Hold=30, Fit Intercept vs  $18 \cdot \text{Chip} + \text{Chan}$



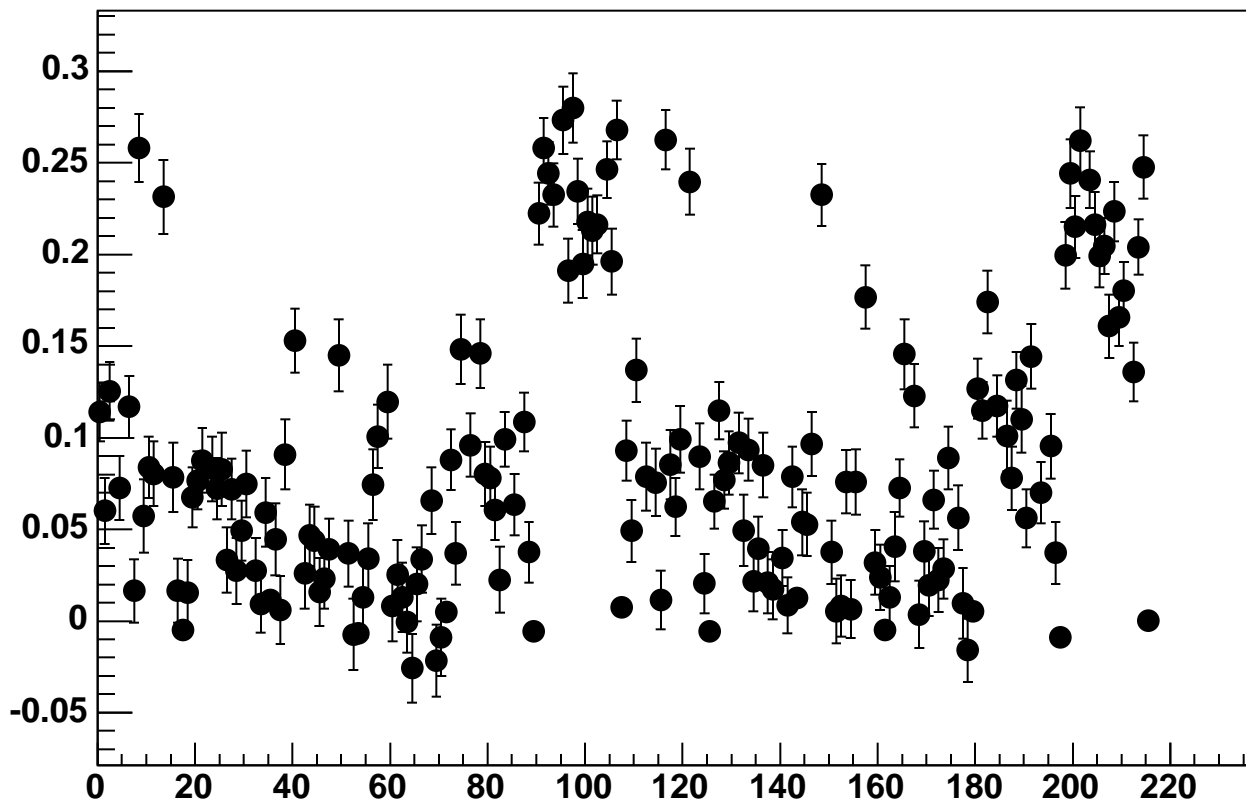
Disabled 0, Hold=30, Fit Slope vs  $18 \cdot \text{Chip} + \text{Chan}$



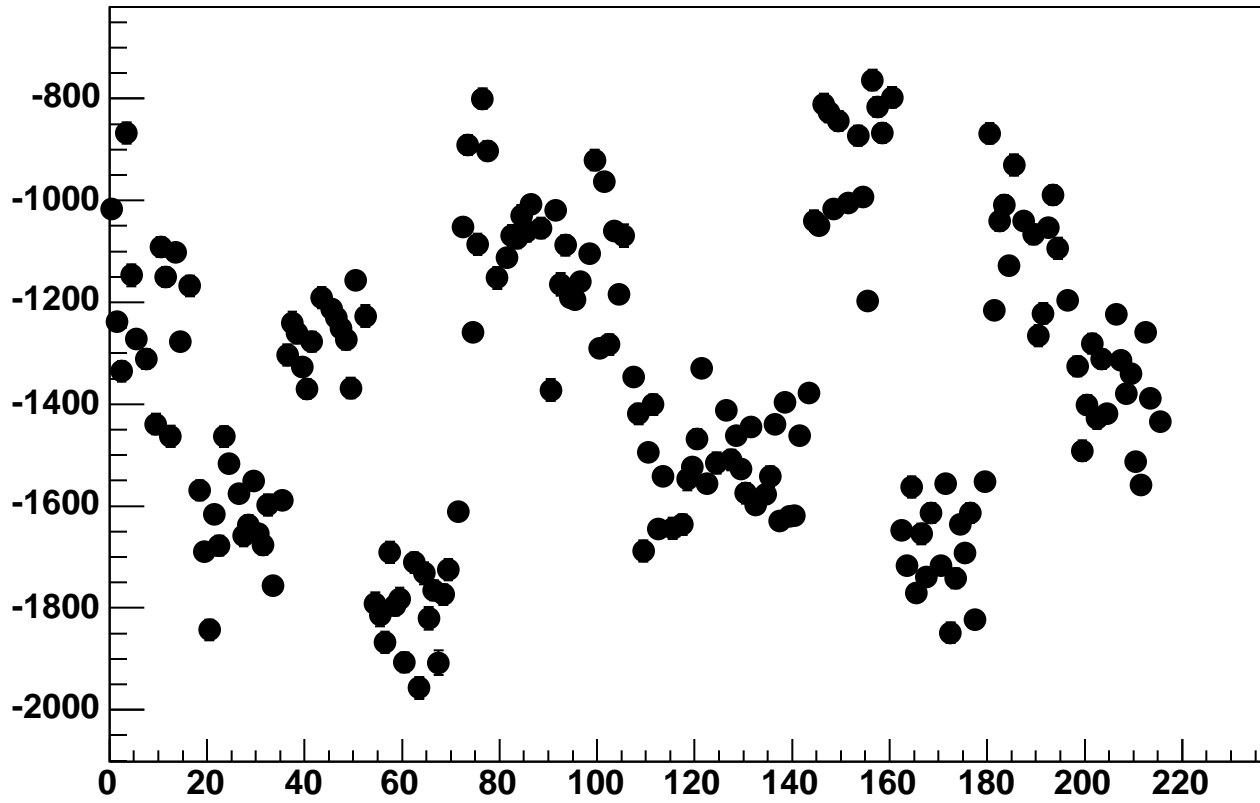
Disabled 1, Hold=30, Fit Intercept vs 18\*Chip+Chan



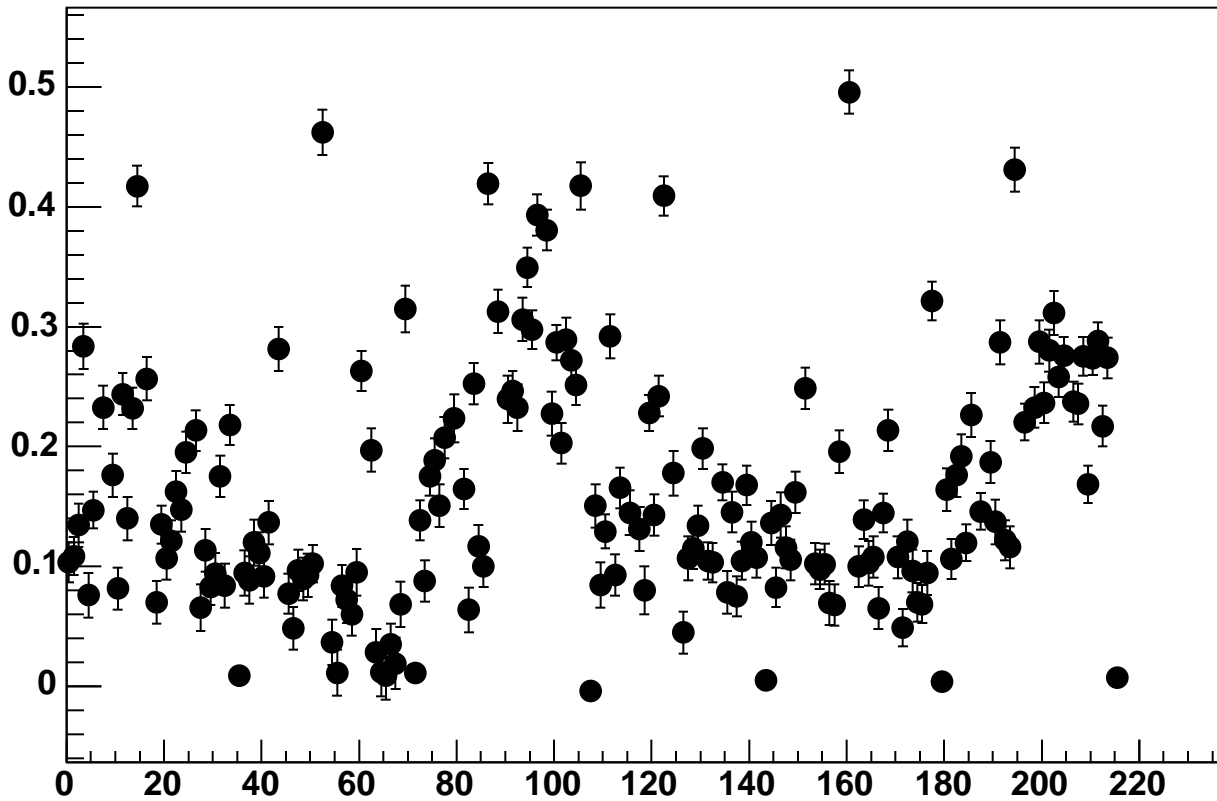
Disabled 1, Hold=30, Fit Slope vs 18\*Chip+Chan



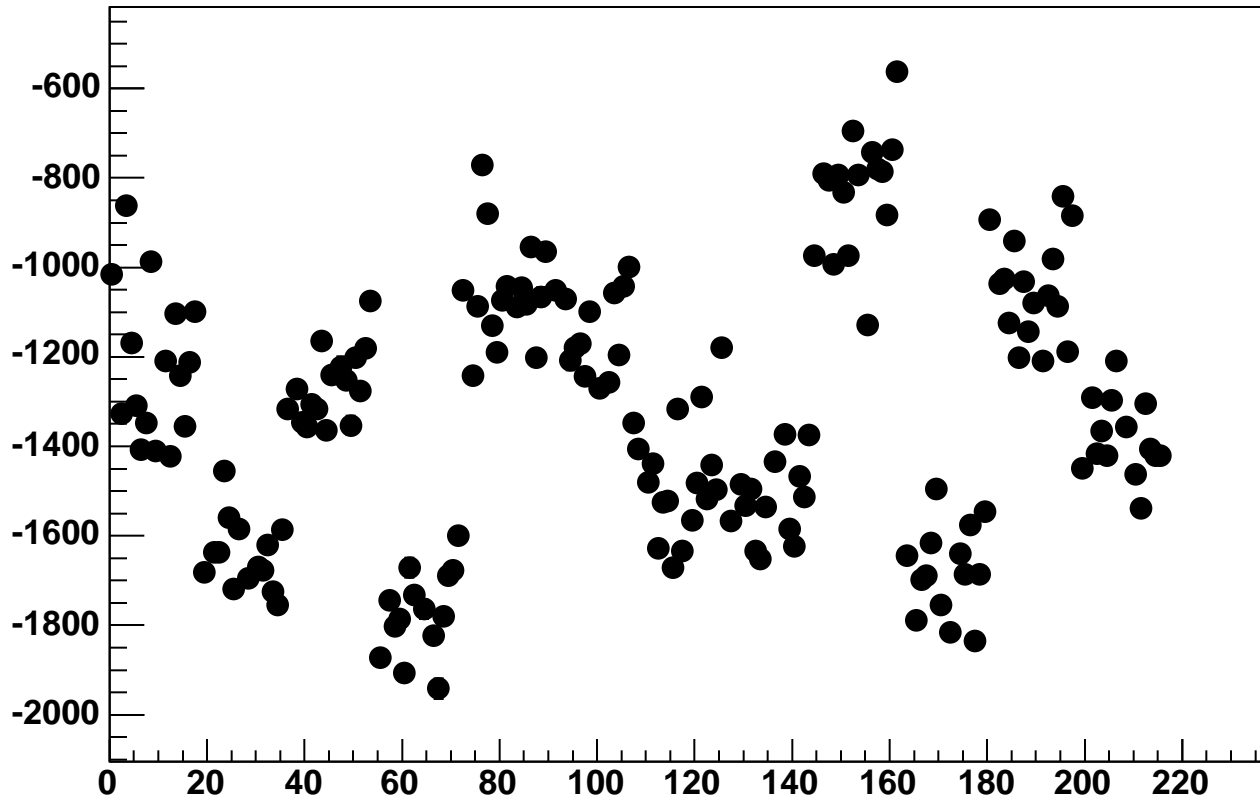
Disabled 2, Hold=30, Fit Intercept vs  $18 \cdot \text{Chip} + \text{Chan}$



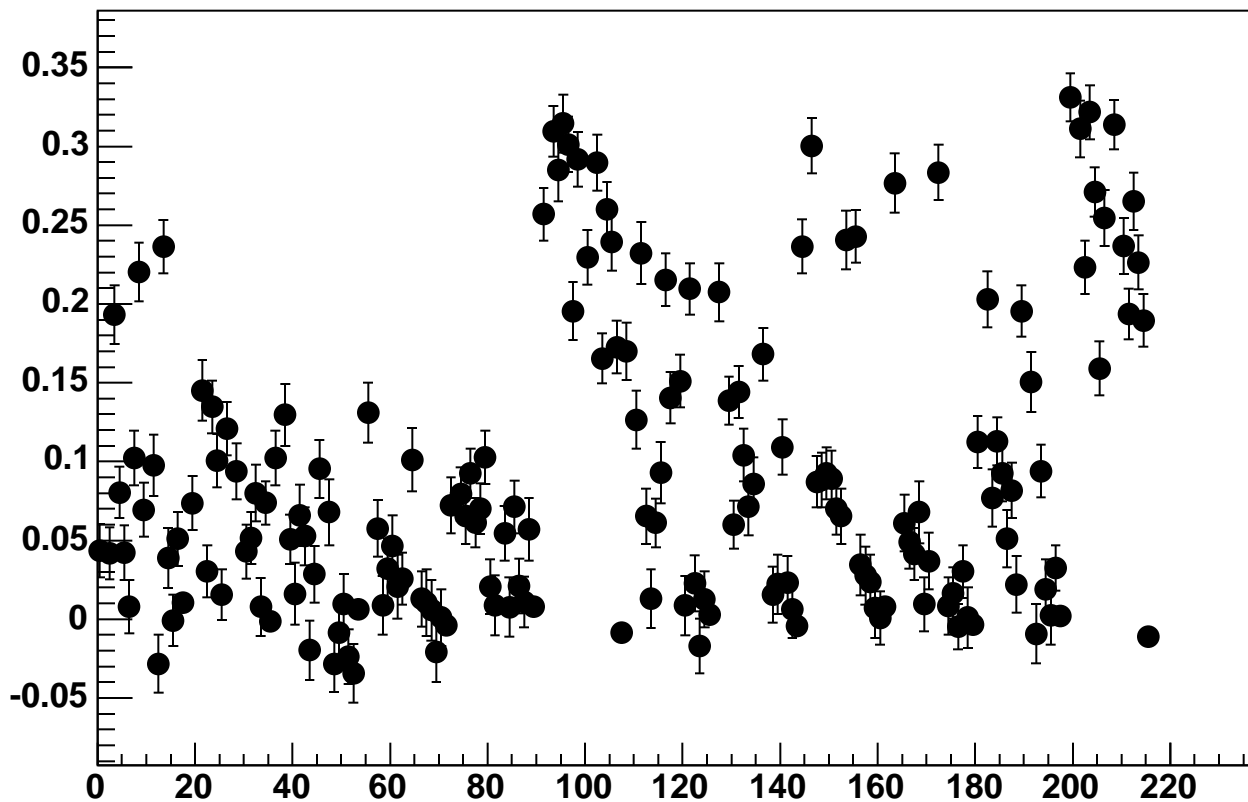
Disabled 2, Hold=30, Fit Slope vs  $18 \cdot \text{Chip} + \text{Chan}$



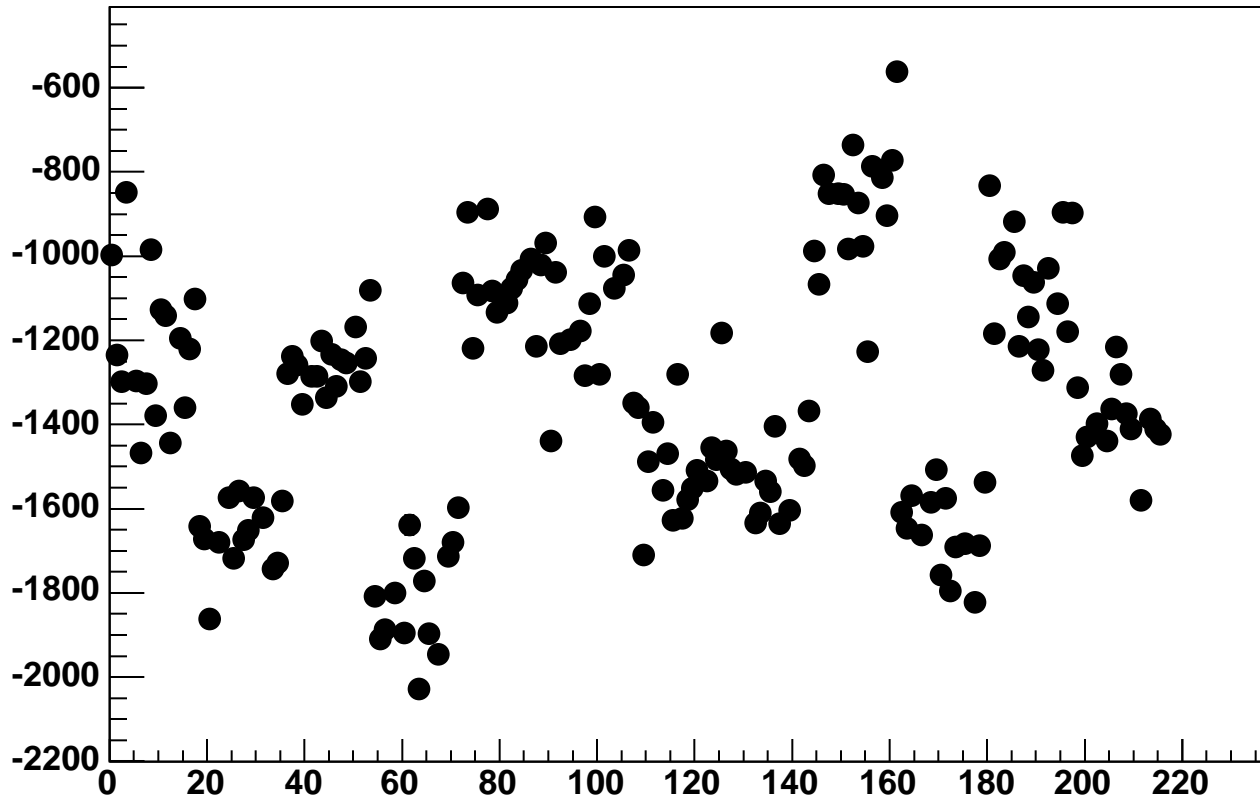
Disabled 3, Hold=30, Fit Intercept vs  $18 \cdot \text{Chip} + \text{Chan}$



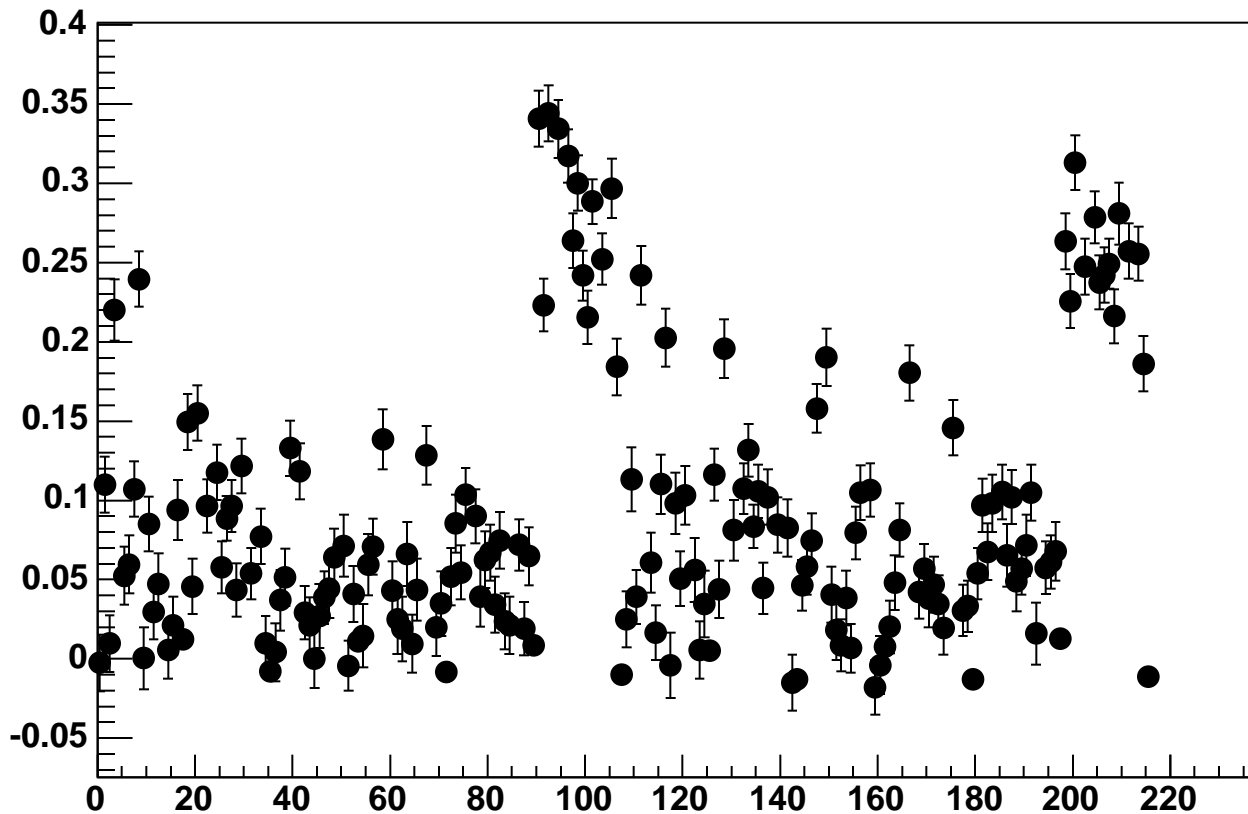
Disabled 3, Hold=30, Fit Slope vs  $18 \cdot \text{Chip} + \text{Chan}$



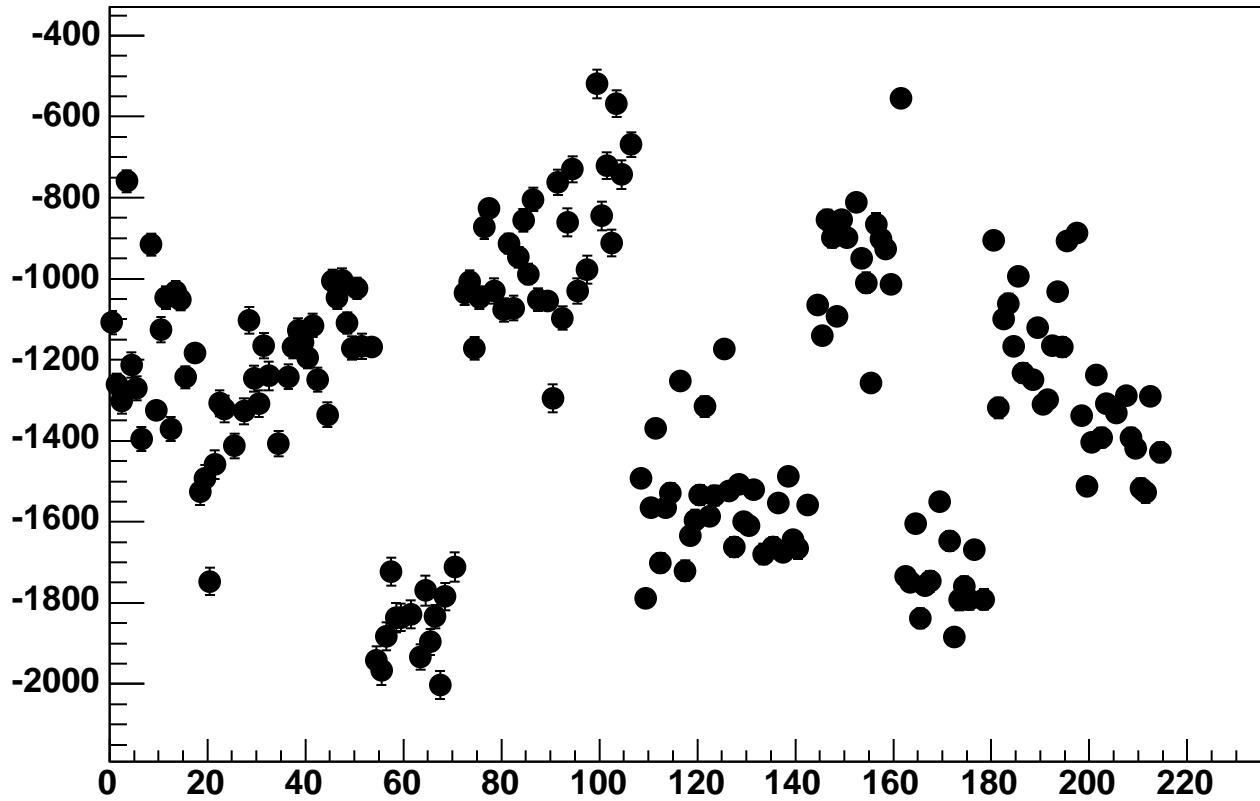
Disabled 4, Hold=30, Fit Intercept vs  $18 \cdot \text{Chip} + \text{Chan}$



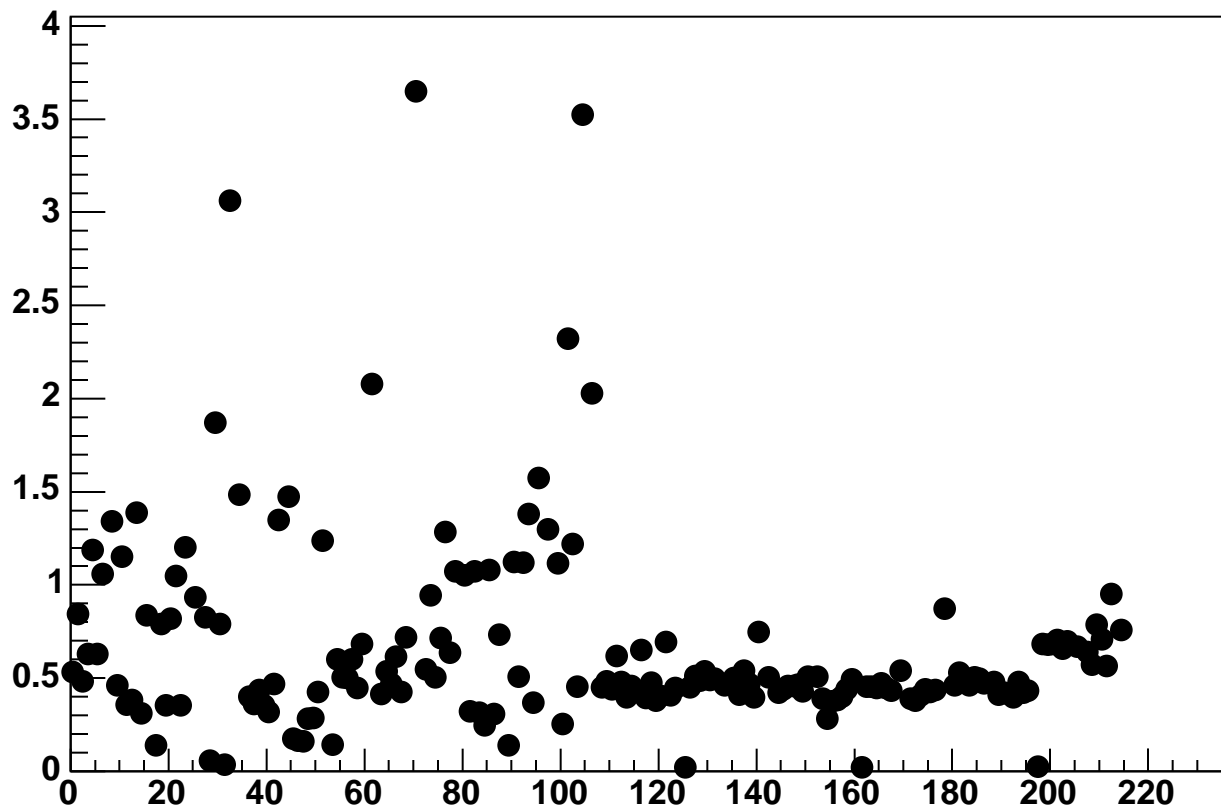
Disabled 4, Hold=30, Fit Slope vs  $18 \cdot \text{Chip} + \text{Chan}$



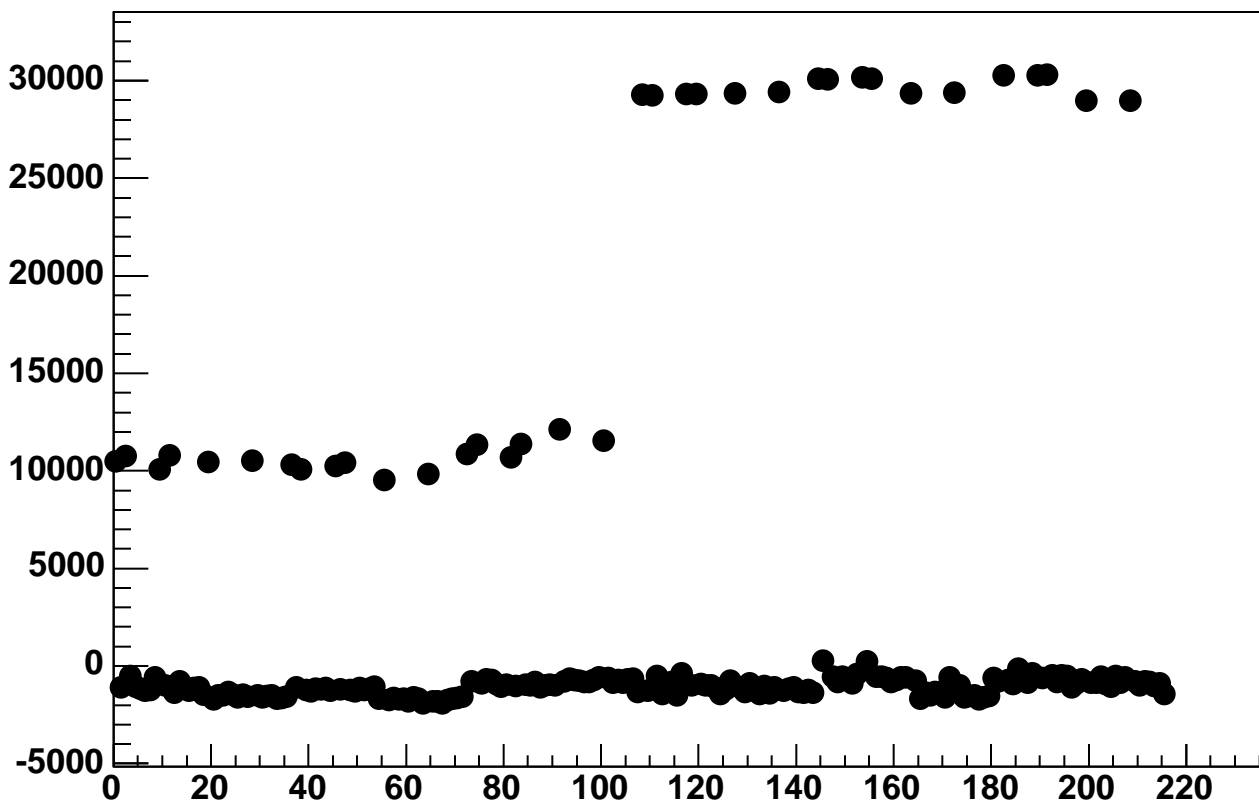
Disabled 5, Hold=30, Fit Intercept vs  $18 \cdot \text{Chip} + \text{Chan}$



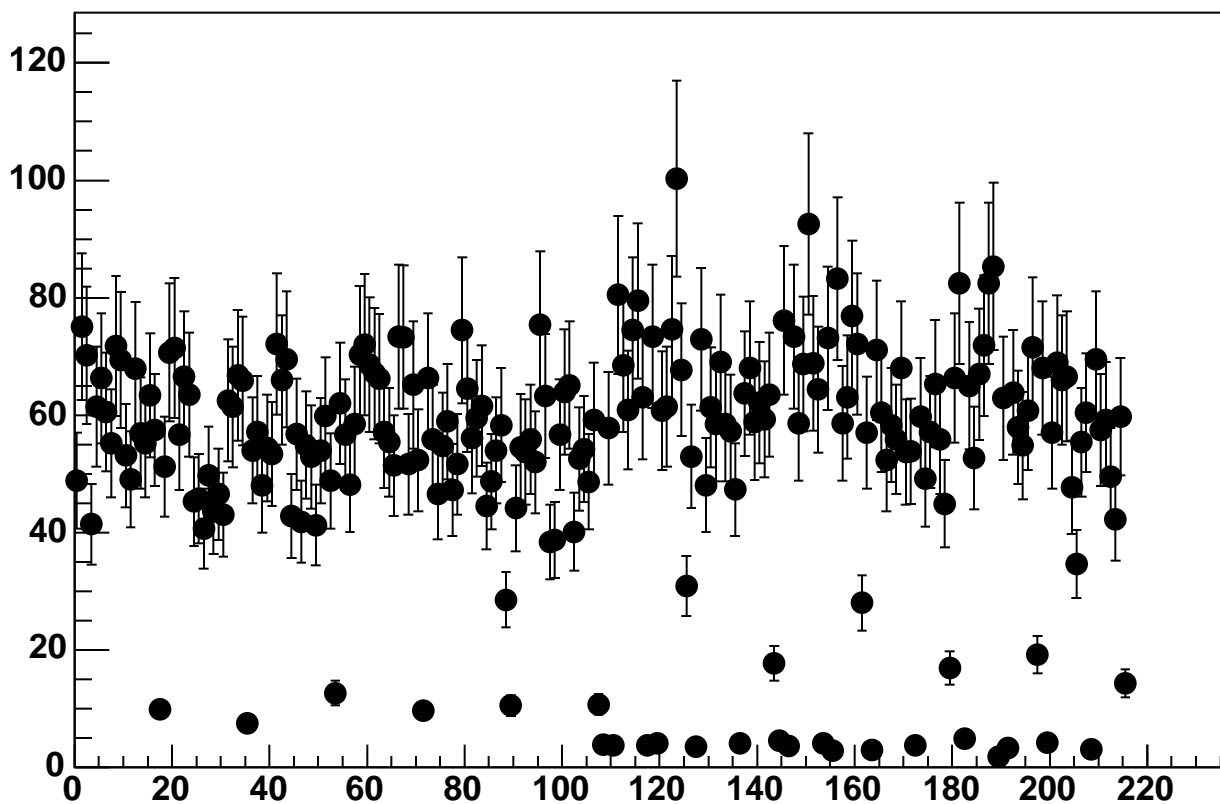
Disabled 5, Hold=30, Fit Slope vs  $18 \cdot \text{Chip} + \text{Chan}$



Enable 0, Hold=30, DAC=0, ADC Mean vs 18\*Chip+Chan

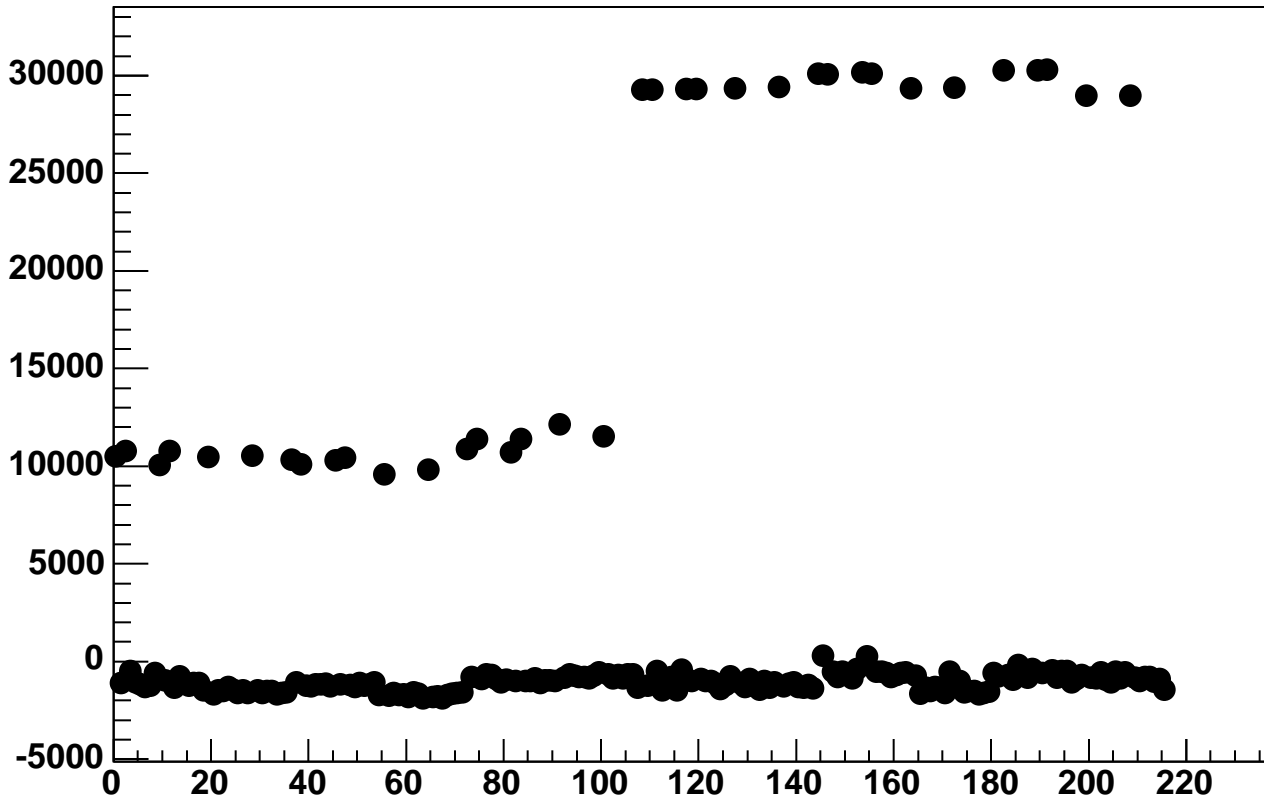


Enable 0, Hold=30, DAC=0, ADC Noise vs 18\*Chip+Chan

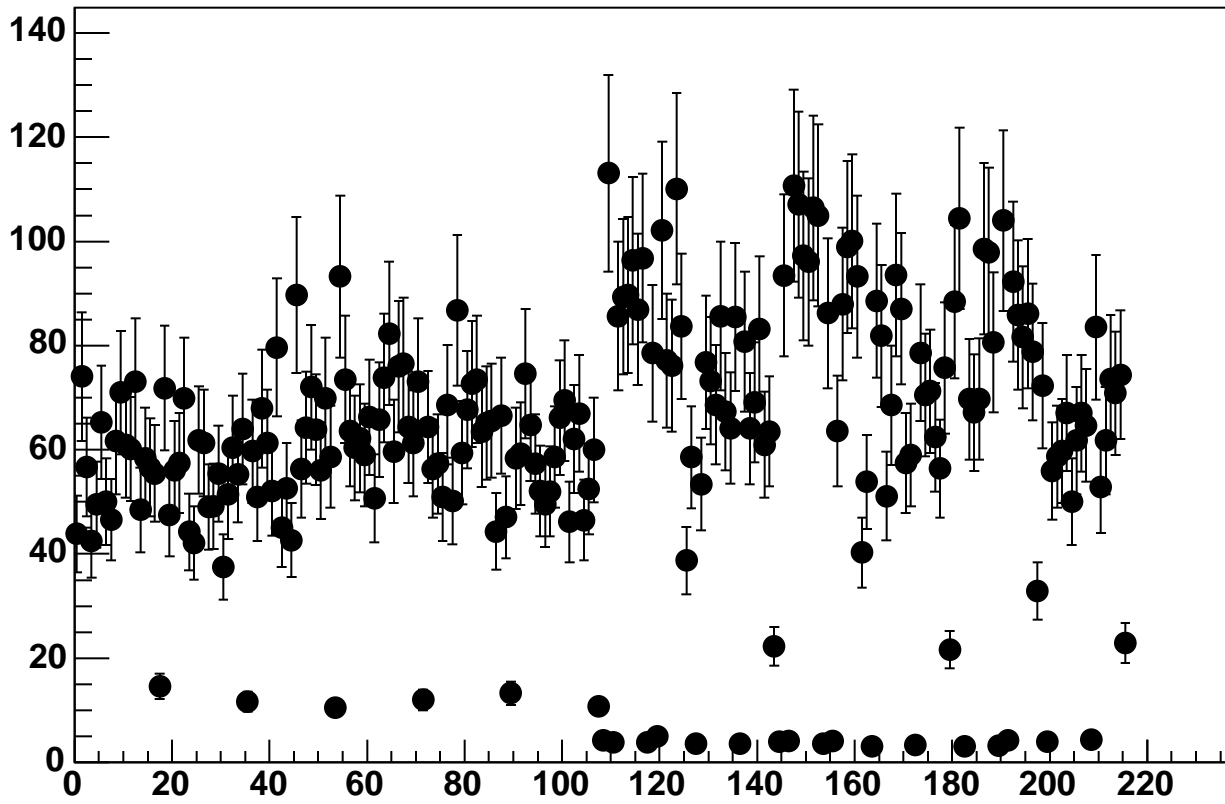




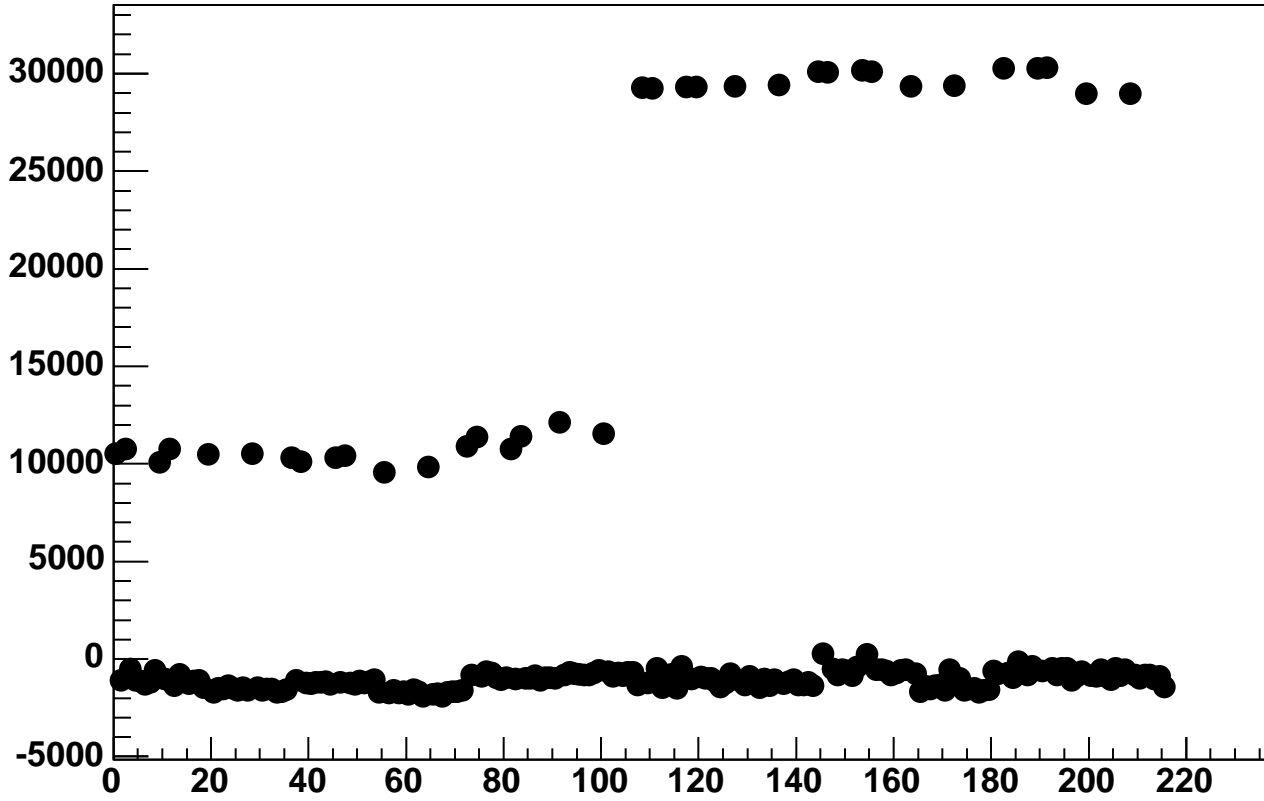
Enable 0, Hold=30, DAC=50, ADC Mean vs 18\*Chip+Chan



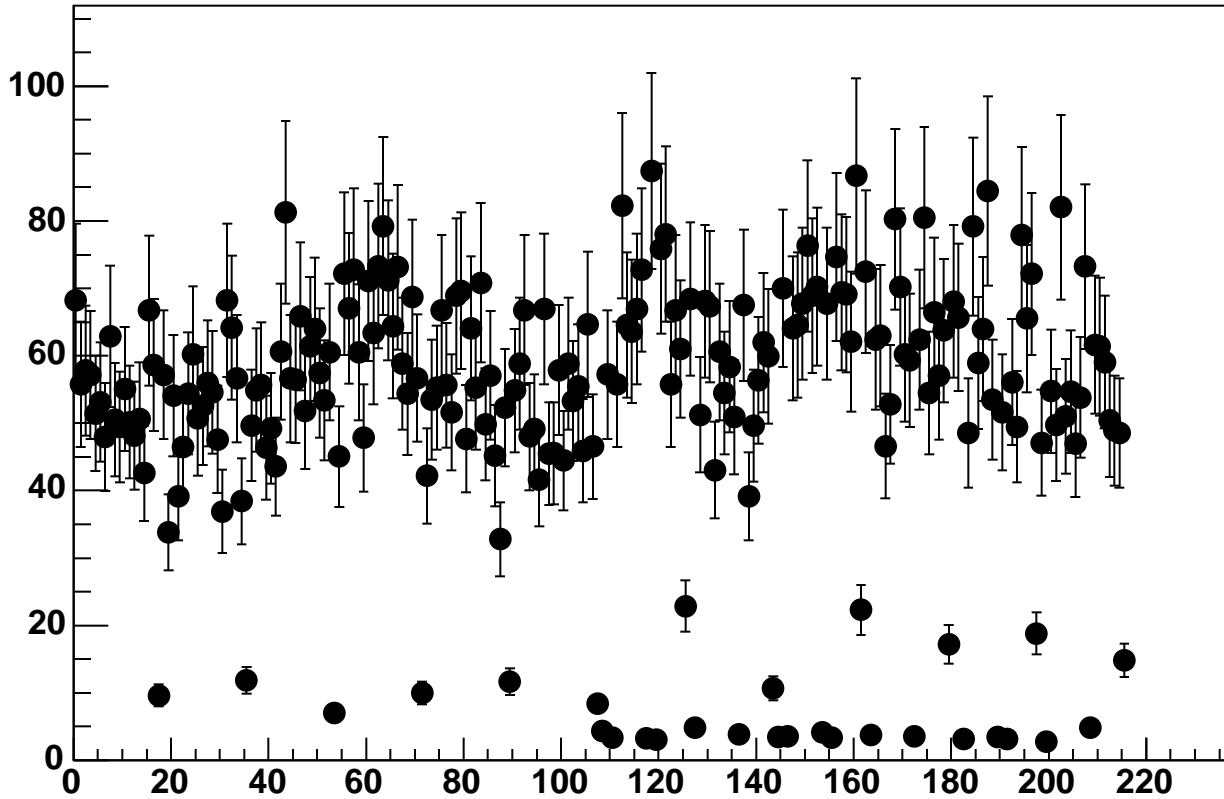
Enable 0, Hold=30, DAC=50, ADC Noise vs 18\*Chip+Chan



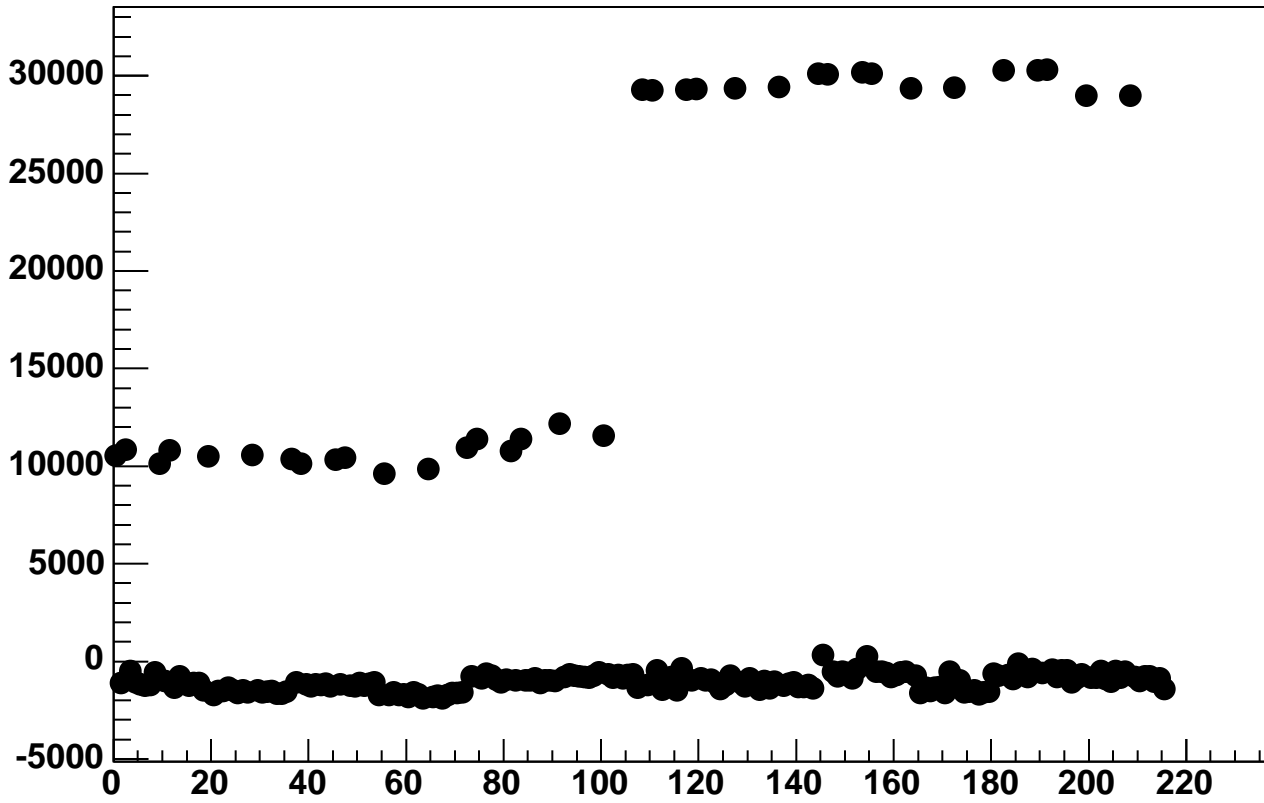
Enable 0, Hold=30, DAC=100, ADC Mean vs 18\*Chip+Chan



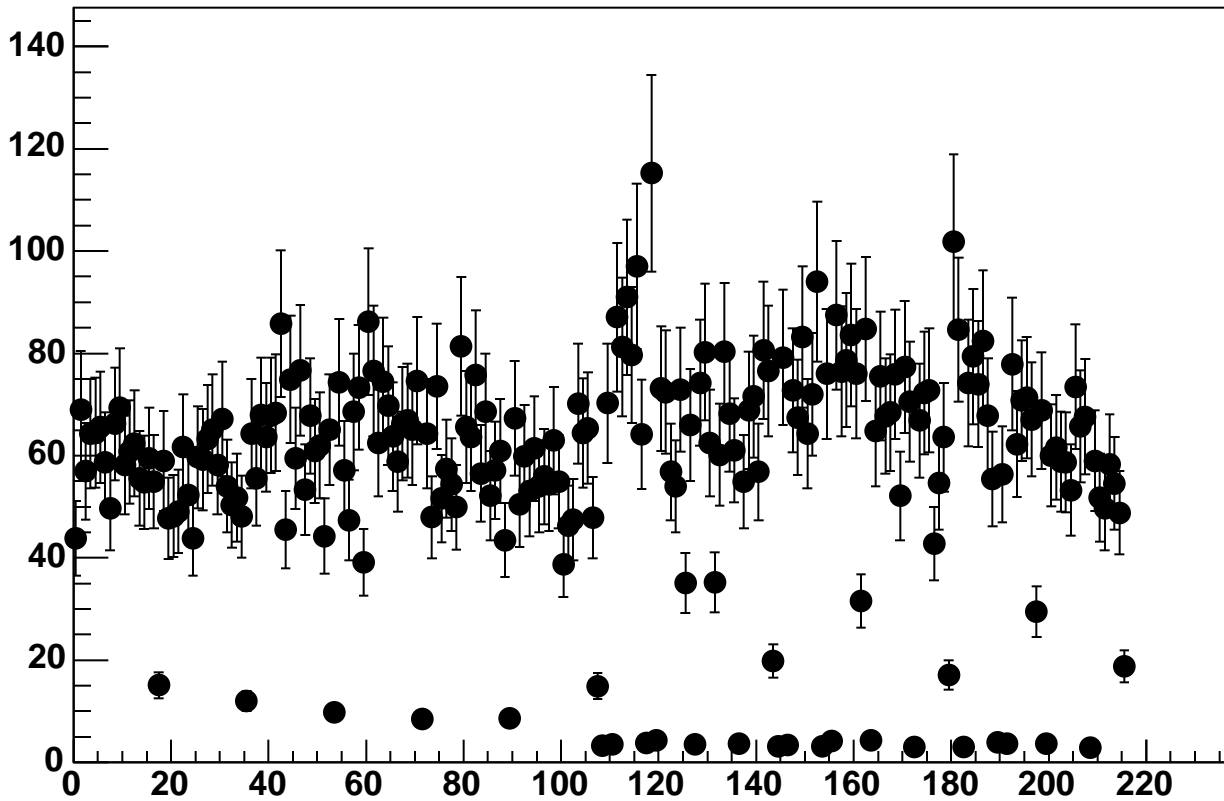
Enable 0, Hold=30, DAC=100, ADC Noise vs 18\*Chip+Chan



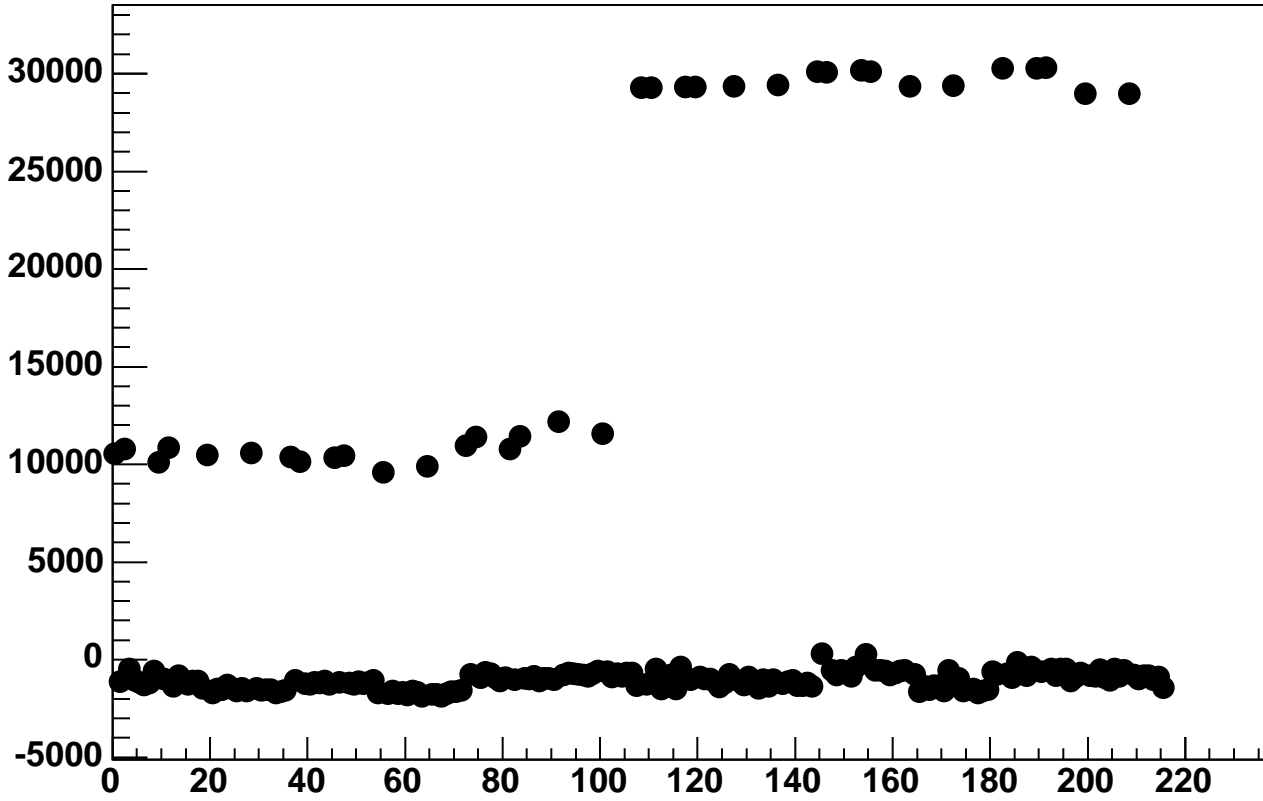
Enable 0, Hold=30, DAC=150, ADC Mean vs 18\*Chip+Chan



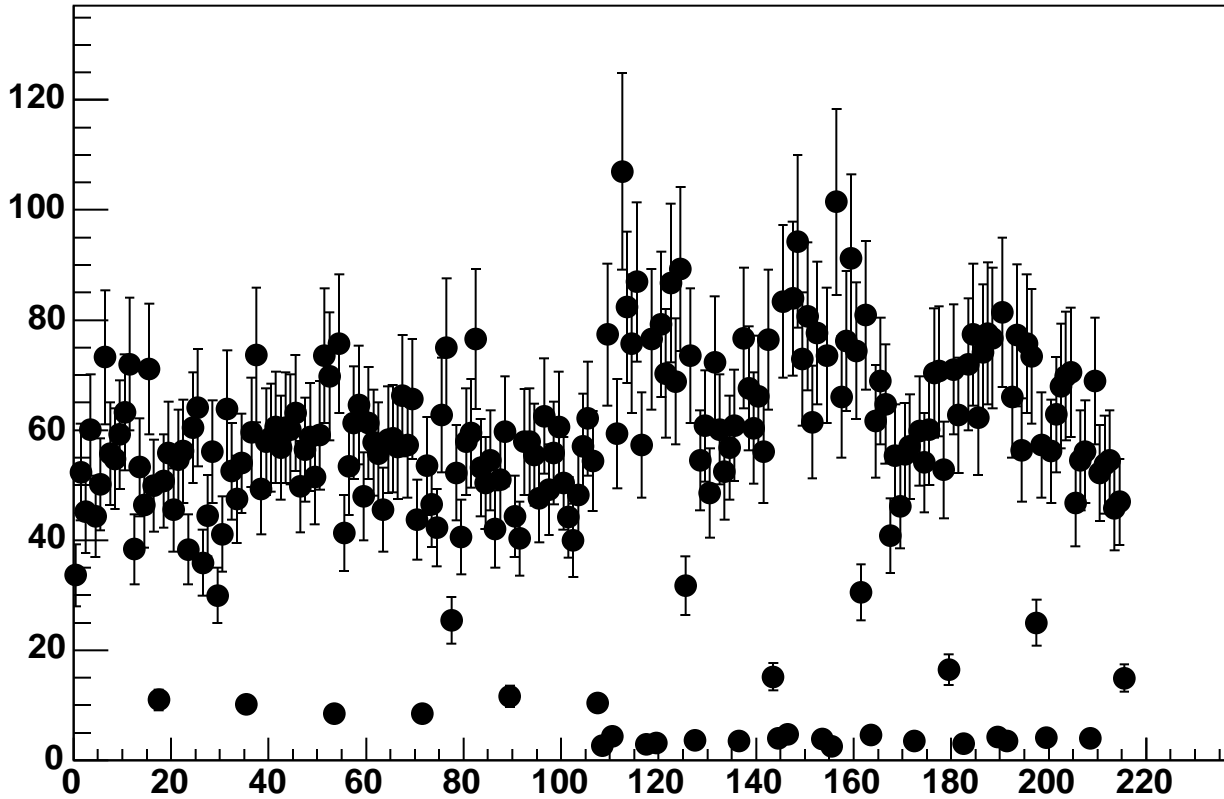
Enable 0, Hold=30, DAC=150, ADC Noise vs 18\*Chip+Chan



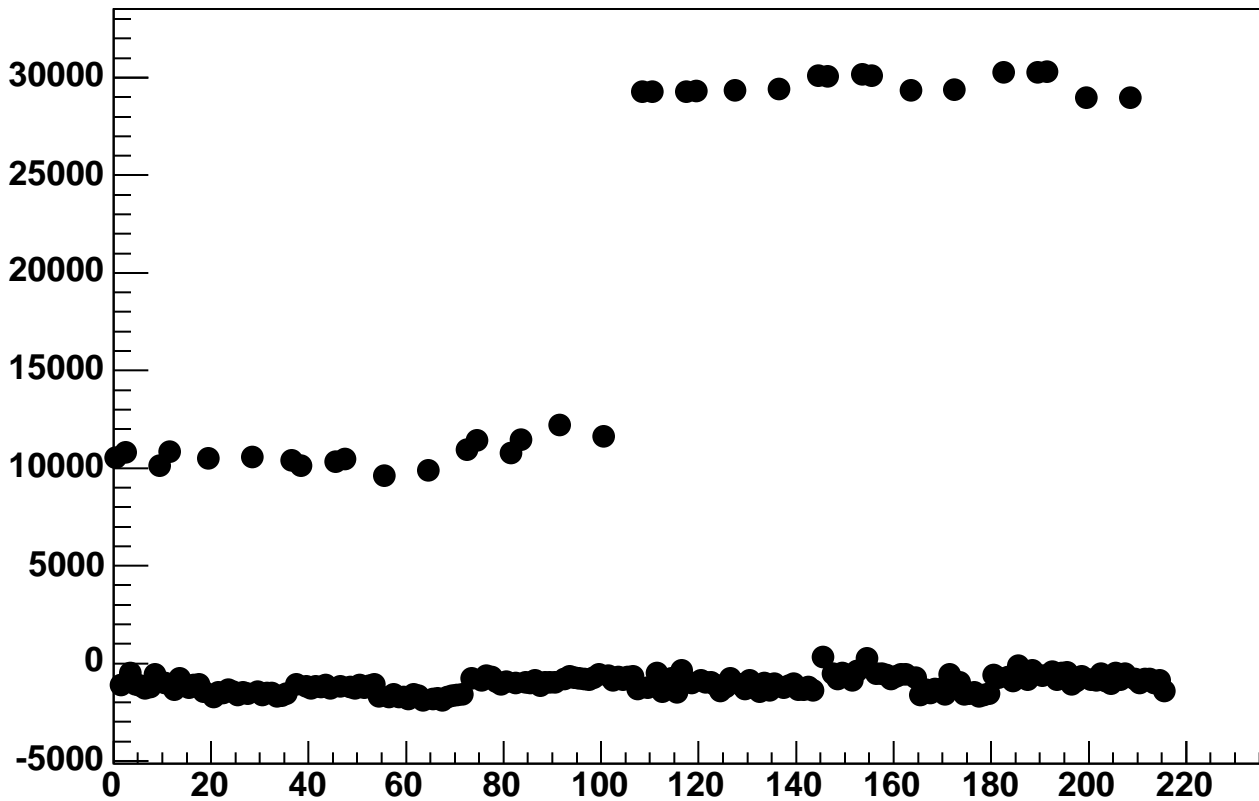
Enable 0, Hold=30, DAC=200, ADC Mean vs 18\*Chip+Chan



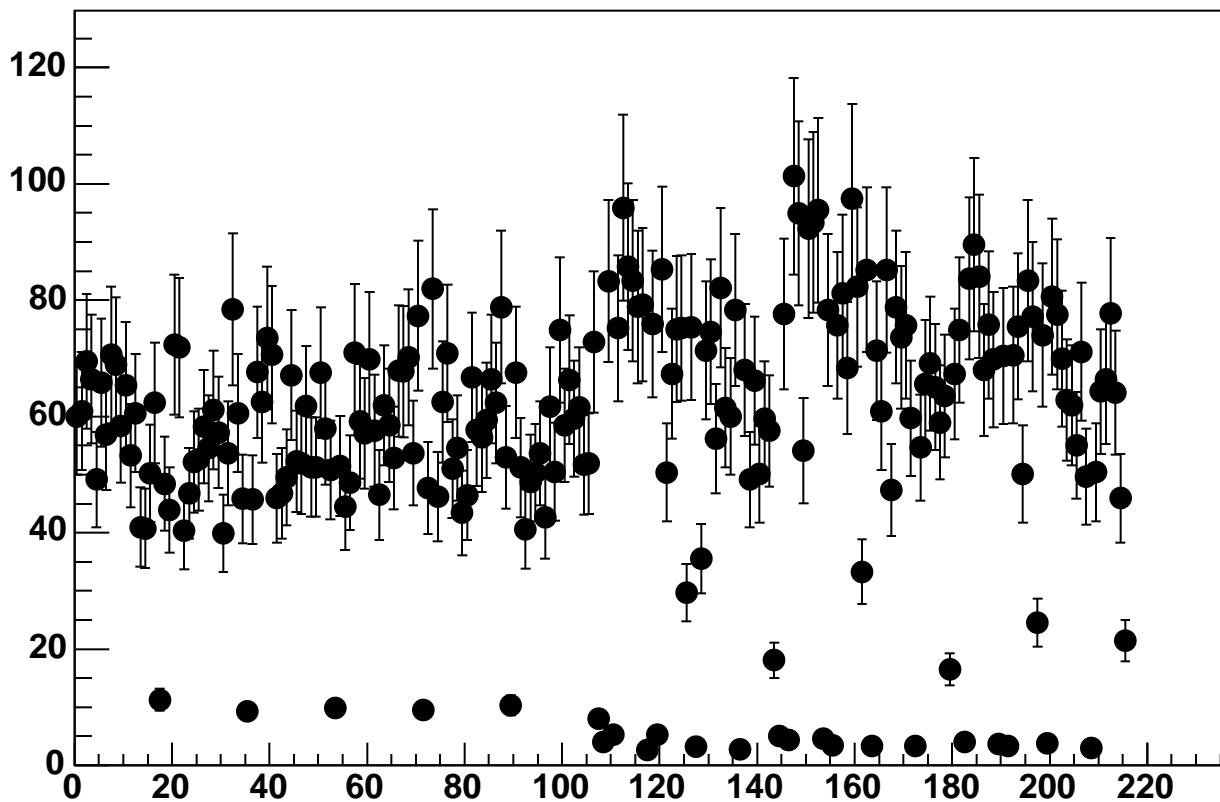
Enable 0, Hold=30, DAC=200, ADC Noise vs 18\*Chip+Chan



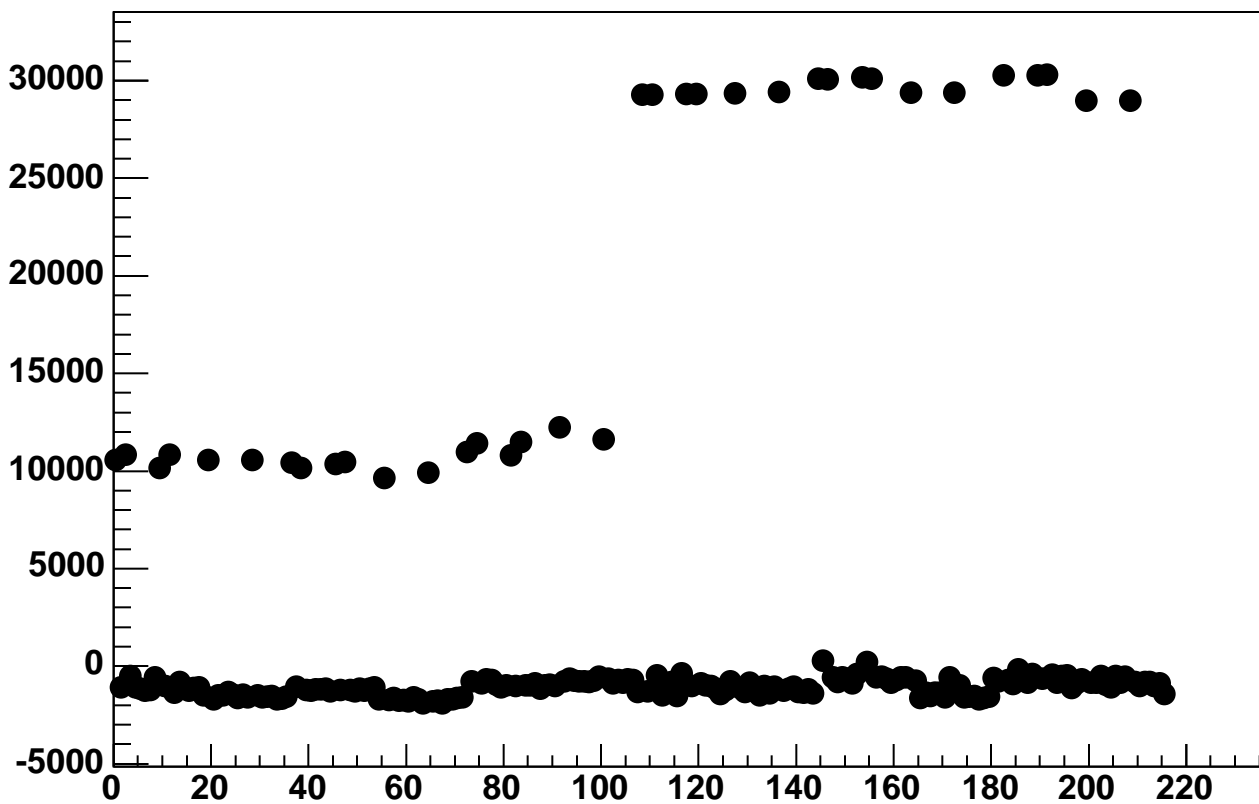
Enable 0, Hold=30, DAC=250, ADC Mean vs 18\*Chip+Chan



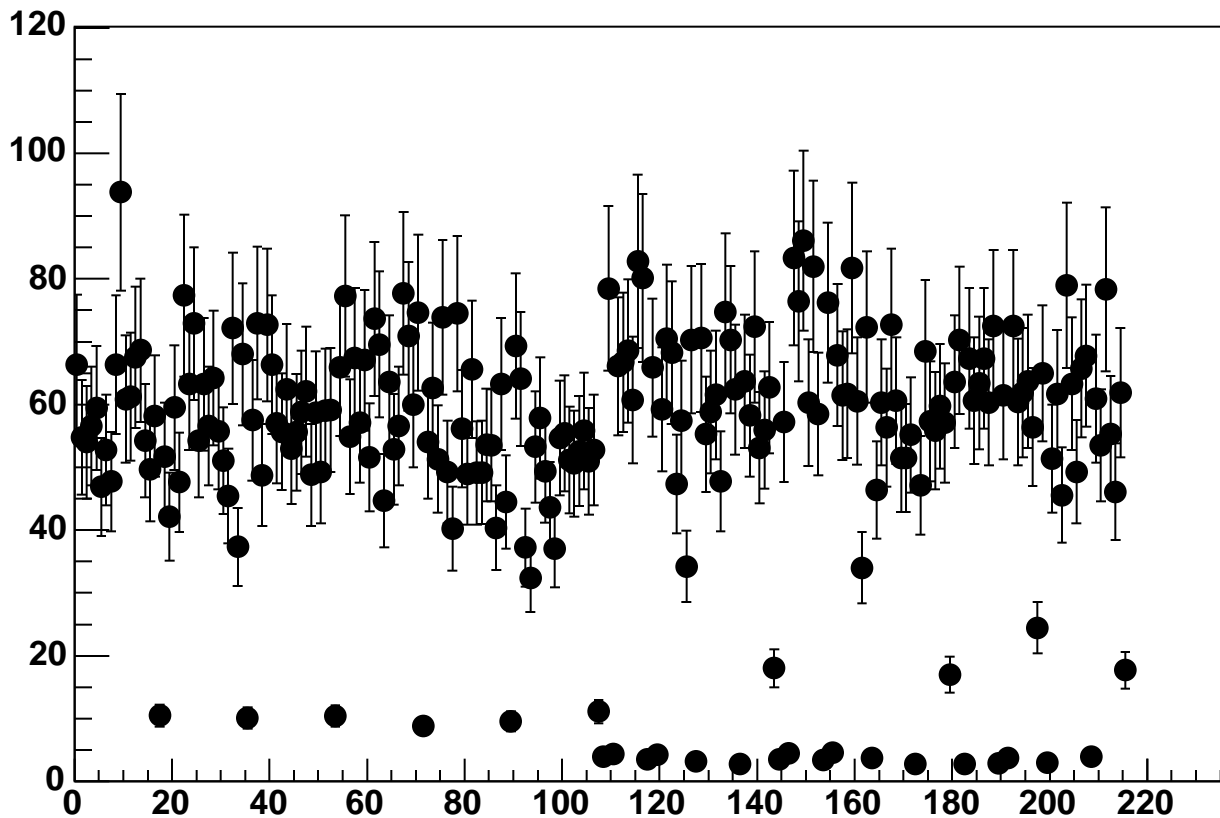
Enable 0, Hold=30, DAC=250, ADC Noise vs 18\*Chip+Chan



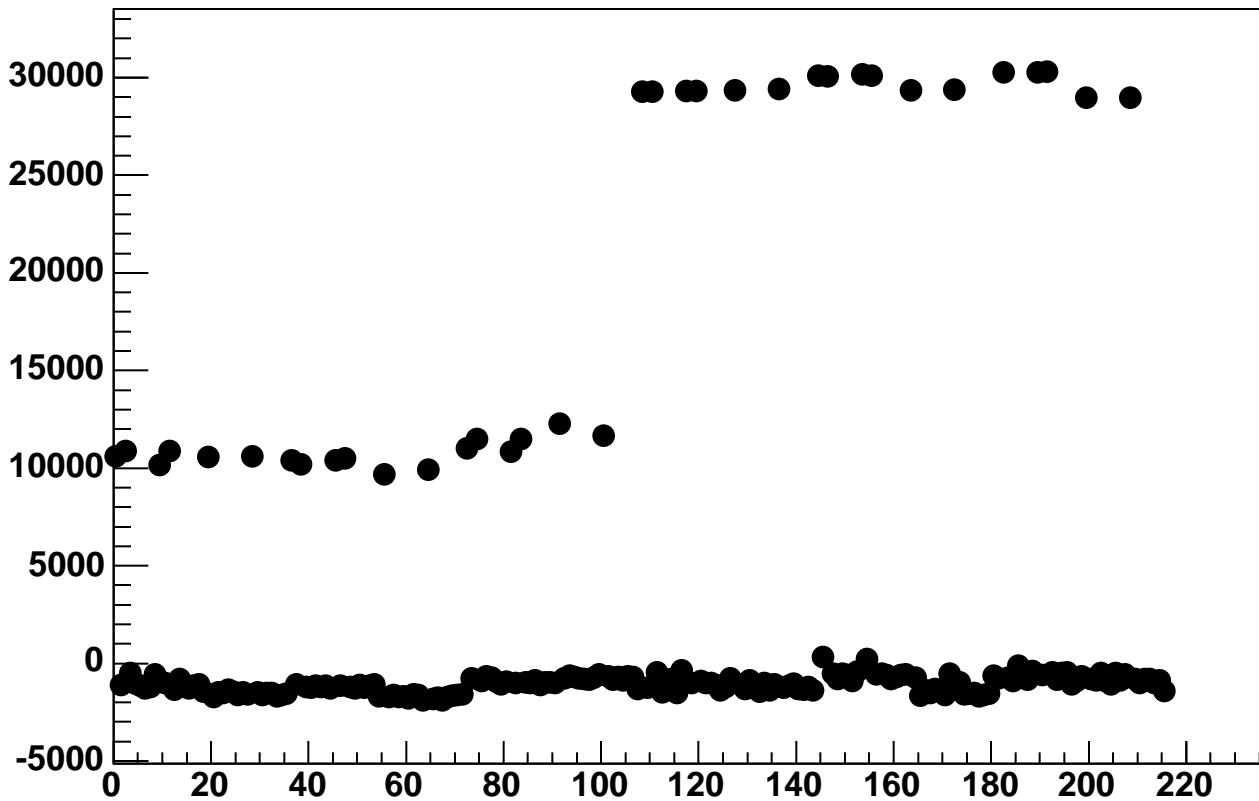
Enable 0, Hold=30, DAC=300, ADC Mean vs 18\*Chip+Chan



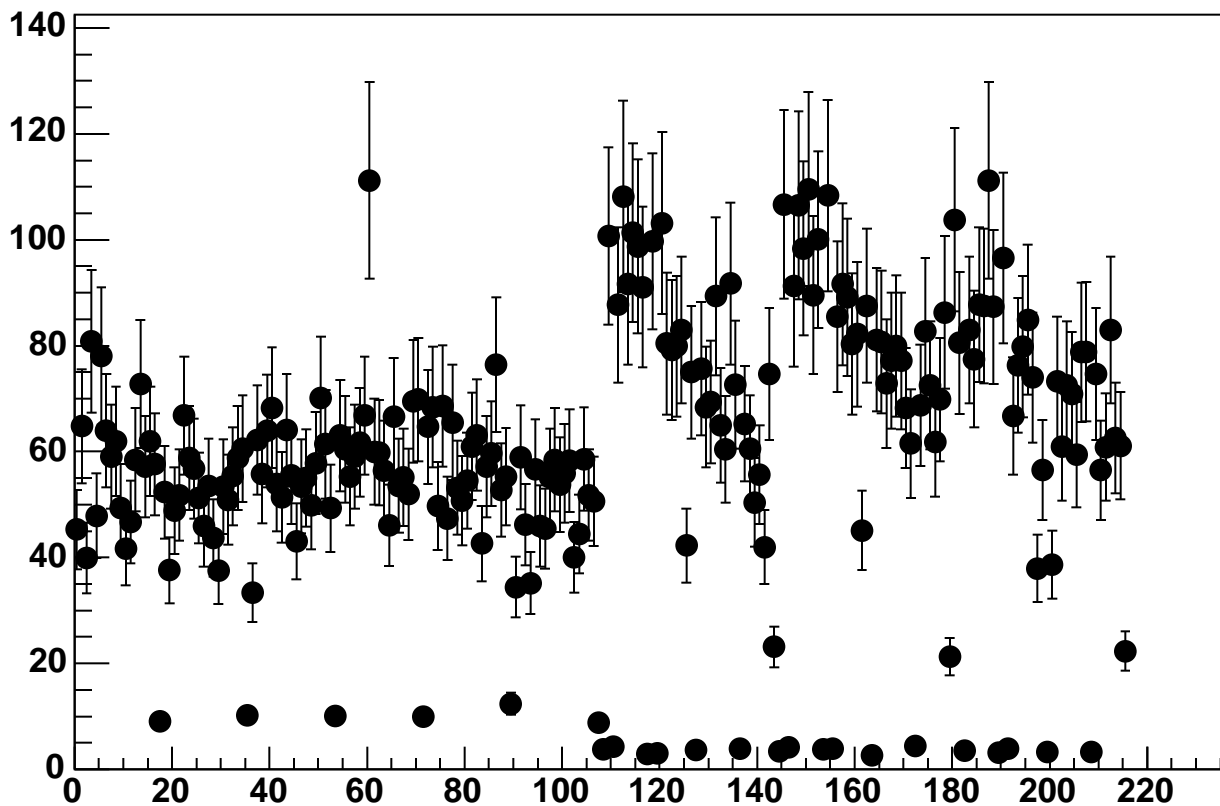
Enable 0, Hold=30, DAC=300, ADC Noise vs 18\*Chip+Chan



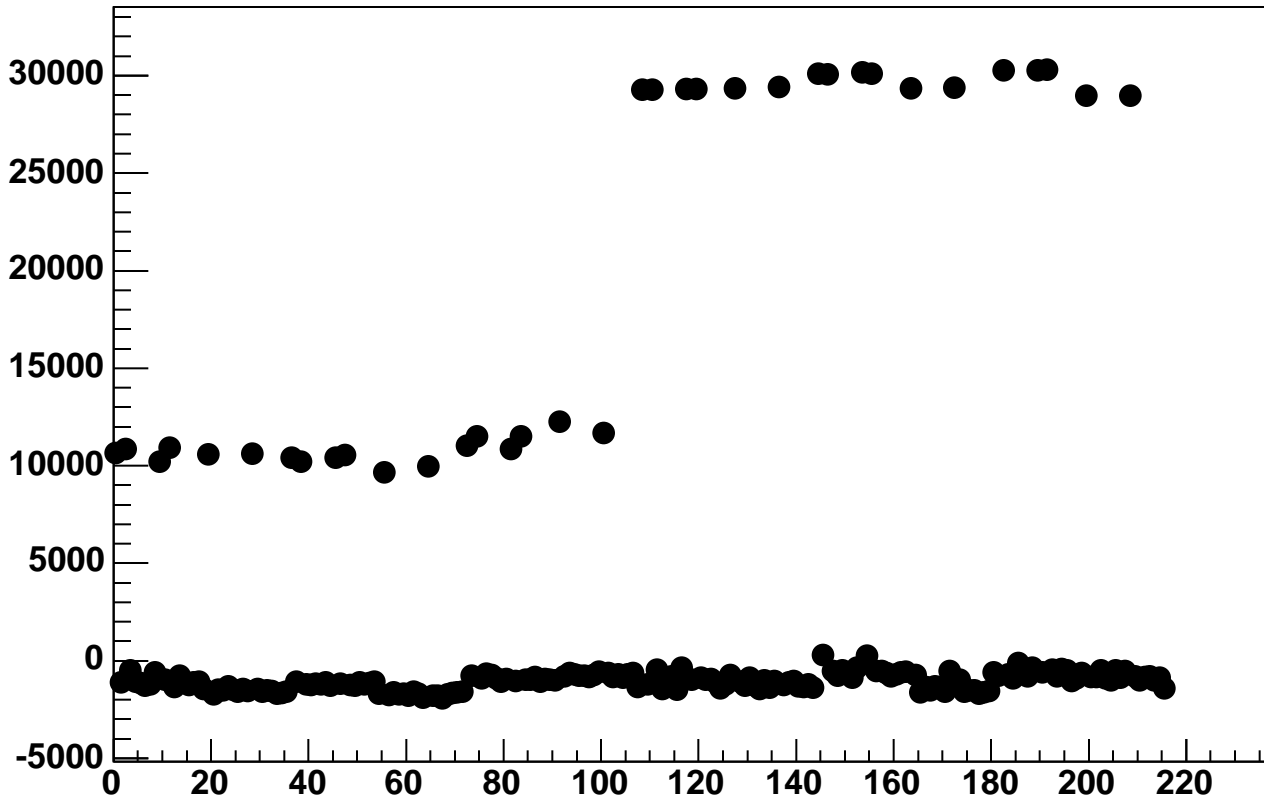
Enable 0, Hold=30, DAC=350, ADC Mean vs 18\*Chip+Chan



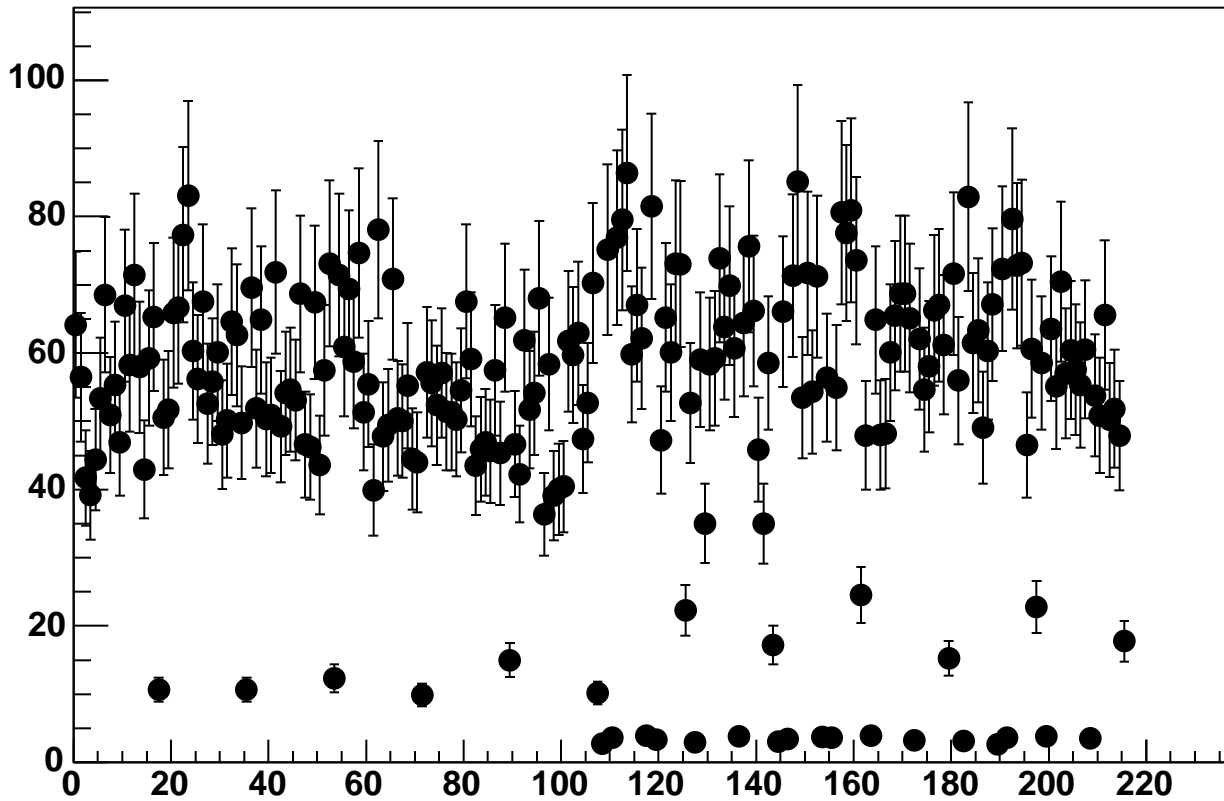
Enable 0, Hold=30, DAC=350, ADC Noise vs 18\*Chip+Chan



Enable 0, Hold=30, DAC=400, ADC Mean vs 18\*Chip+Chan

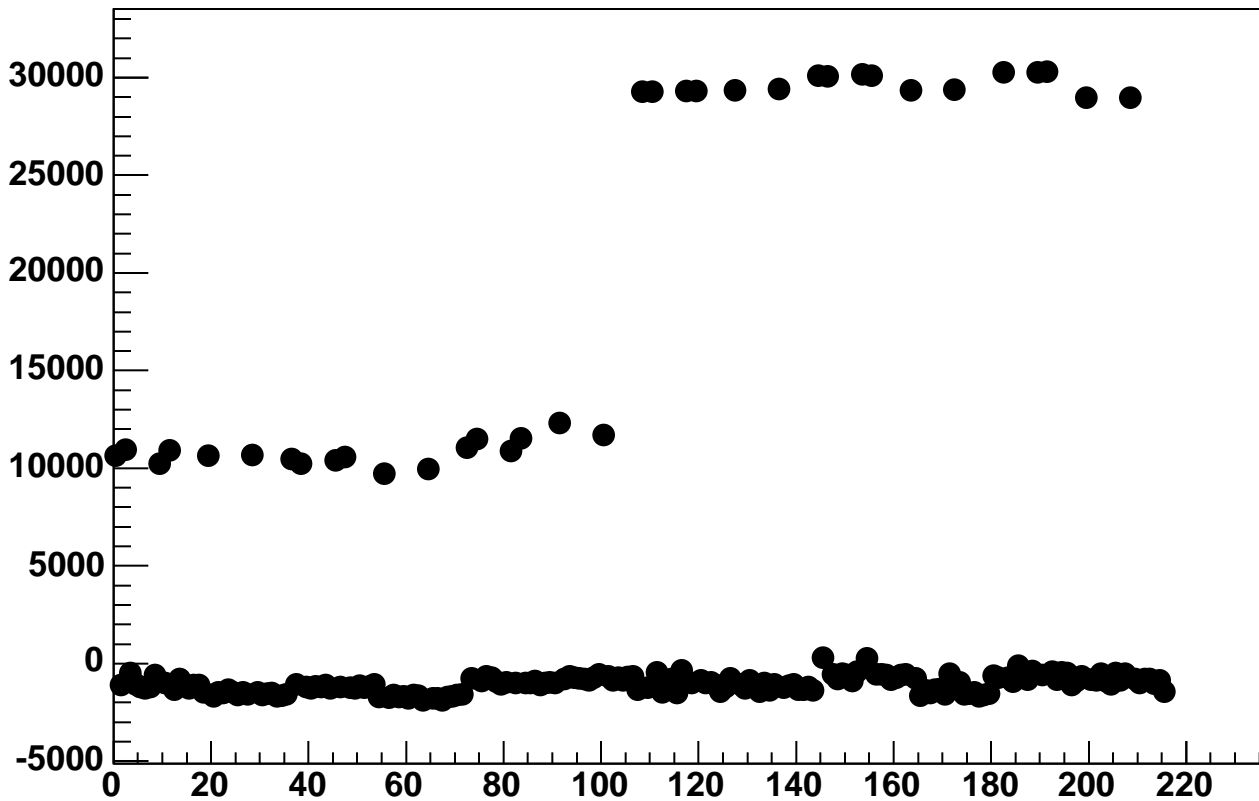


Enable 0, Hold=30, DAC=400, ADC Noise vs 18\*Chip+Chan

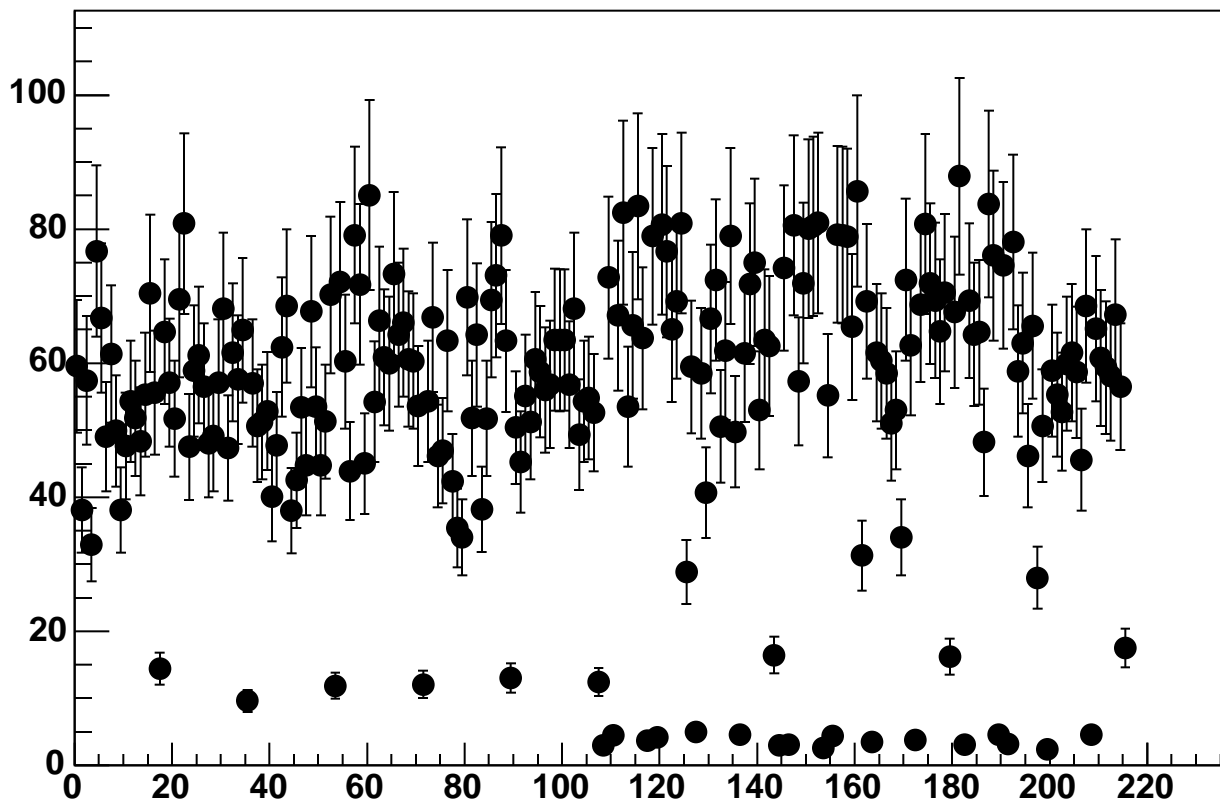




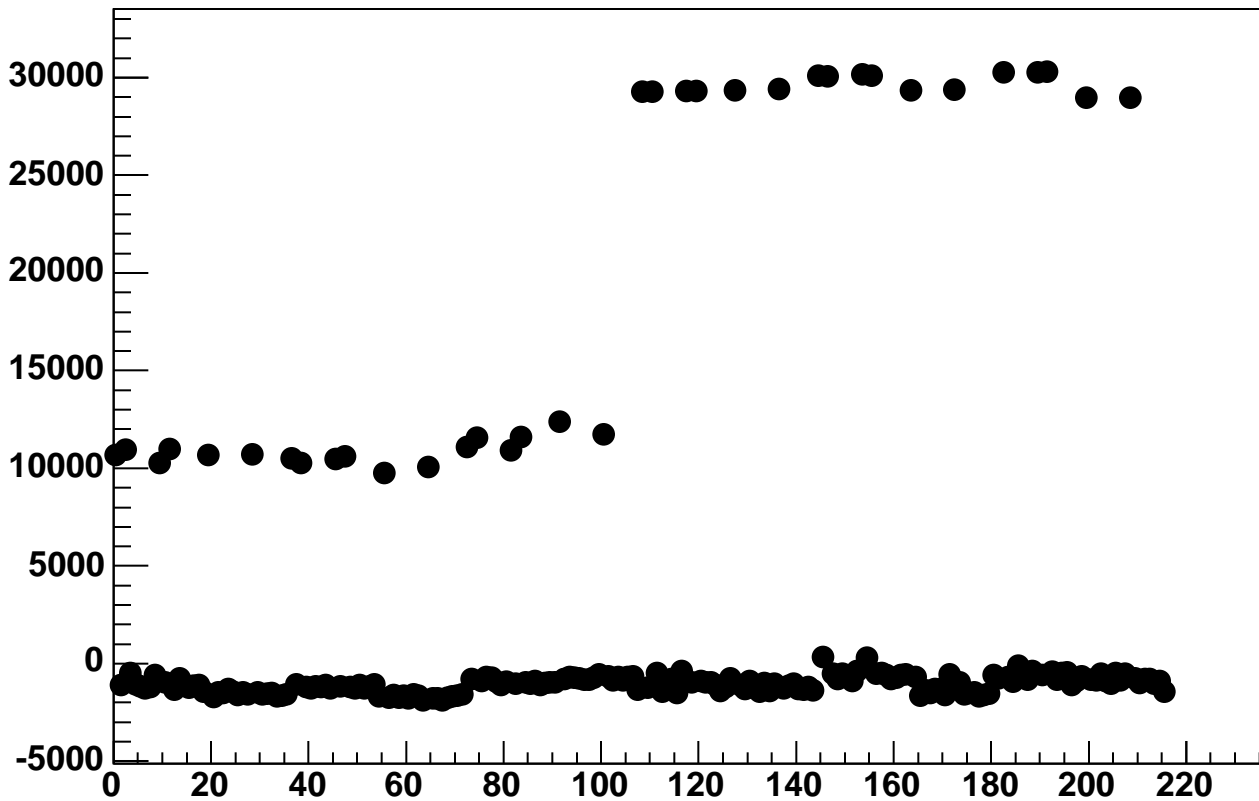
Enable 0, Hold=30, DAC=450, ADC Mean vs 18\*Chip+Chan



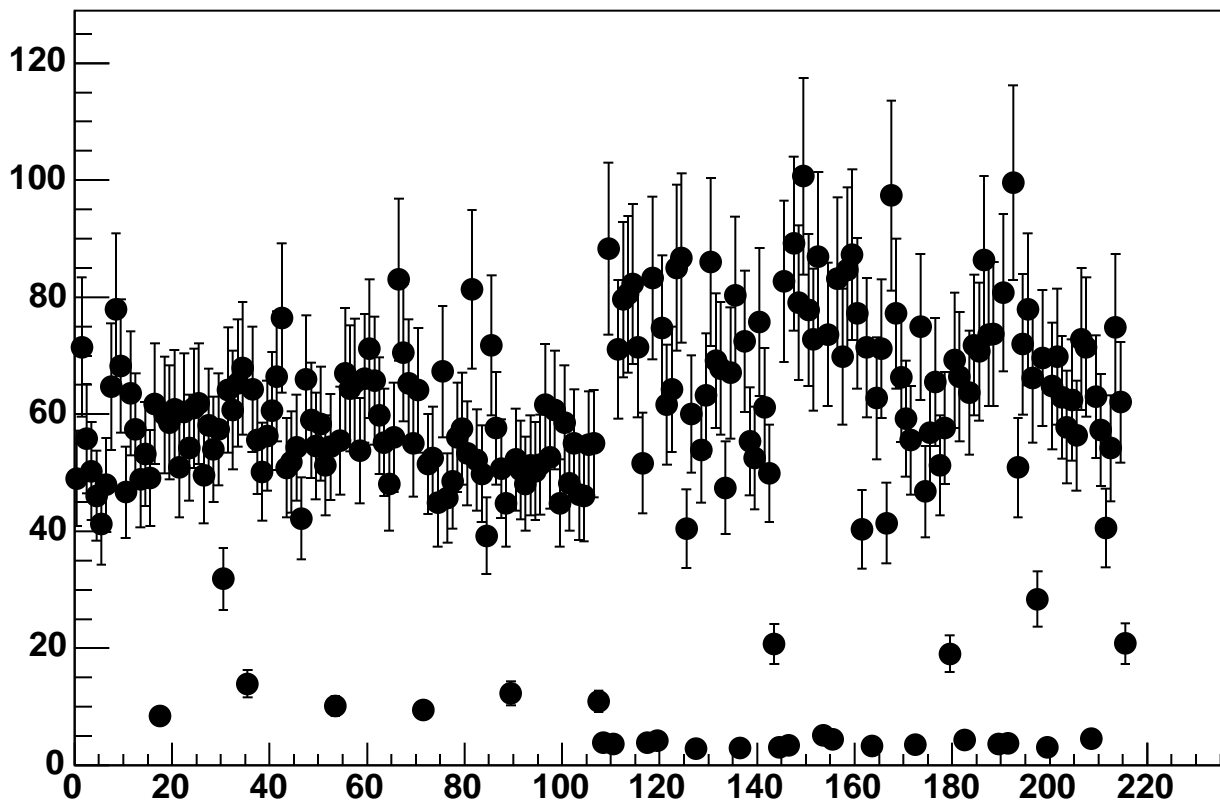
Enable 0, Hold=30, DAC=450, ADC Noise vs 18\*Chip+Chan



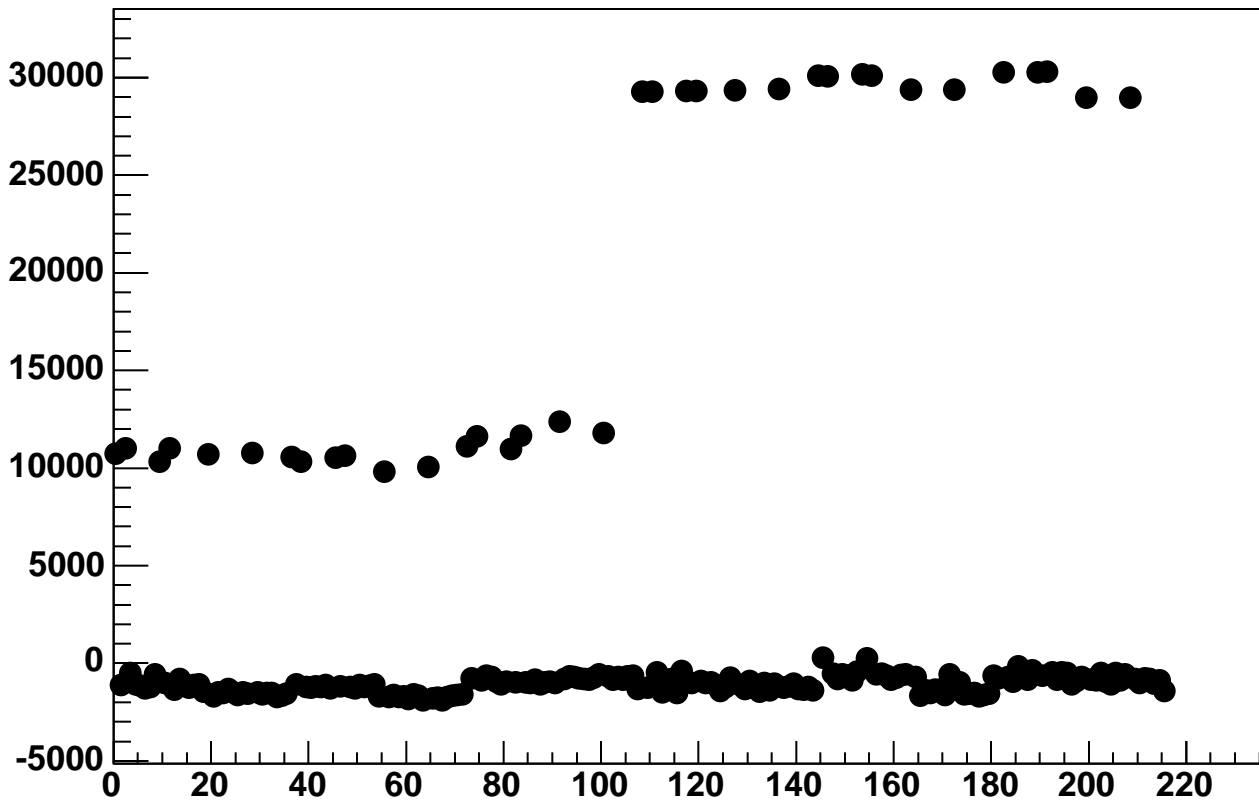
Enable 0, Hold=30, DAC=500, ADC Mean vs 18\*Chip+Chan



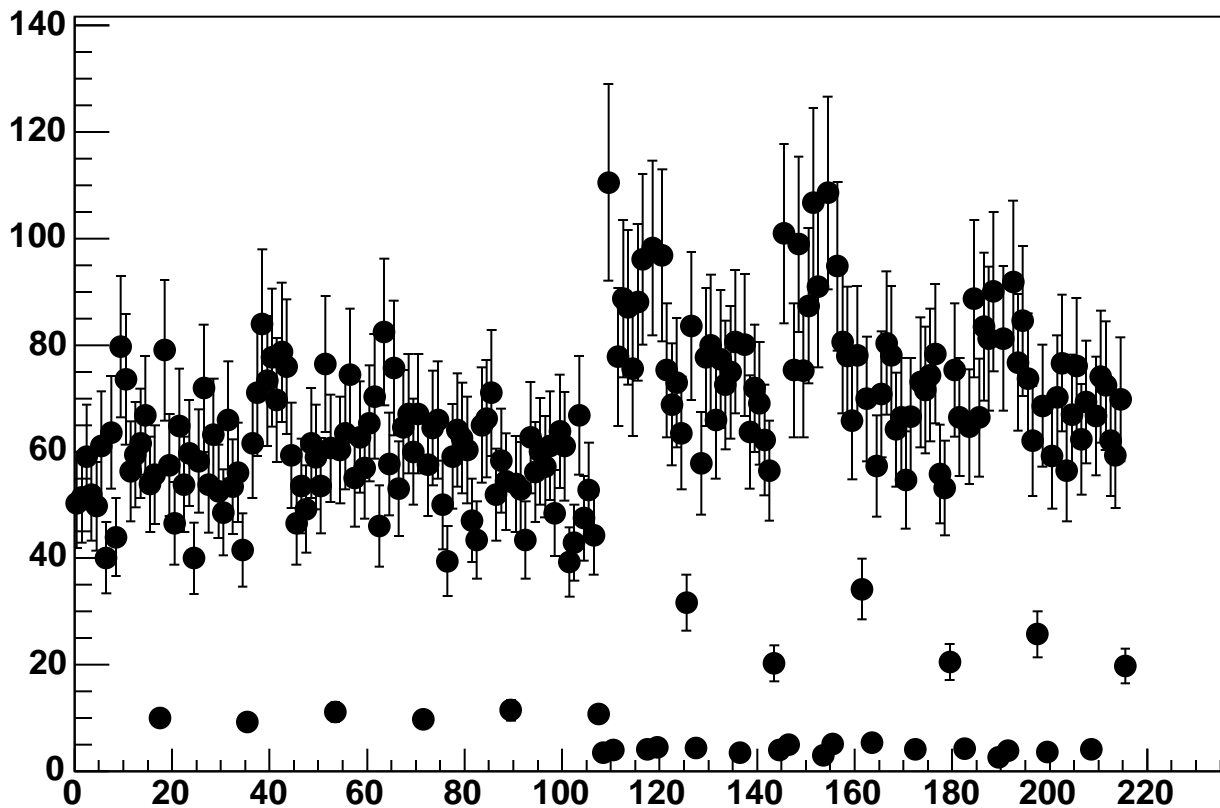
Enable 0, Hold=30, DAC=500, ADC Noise vs 18\*Chip+Chan



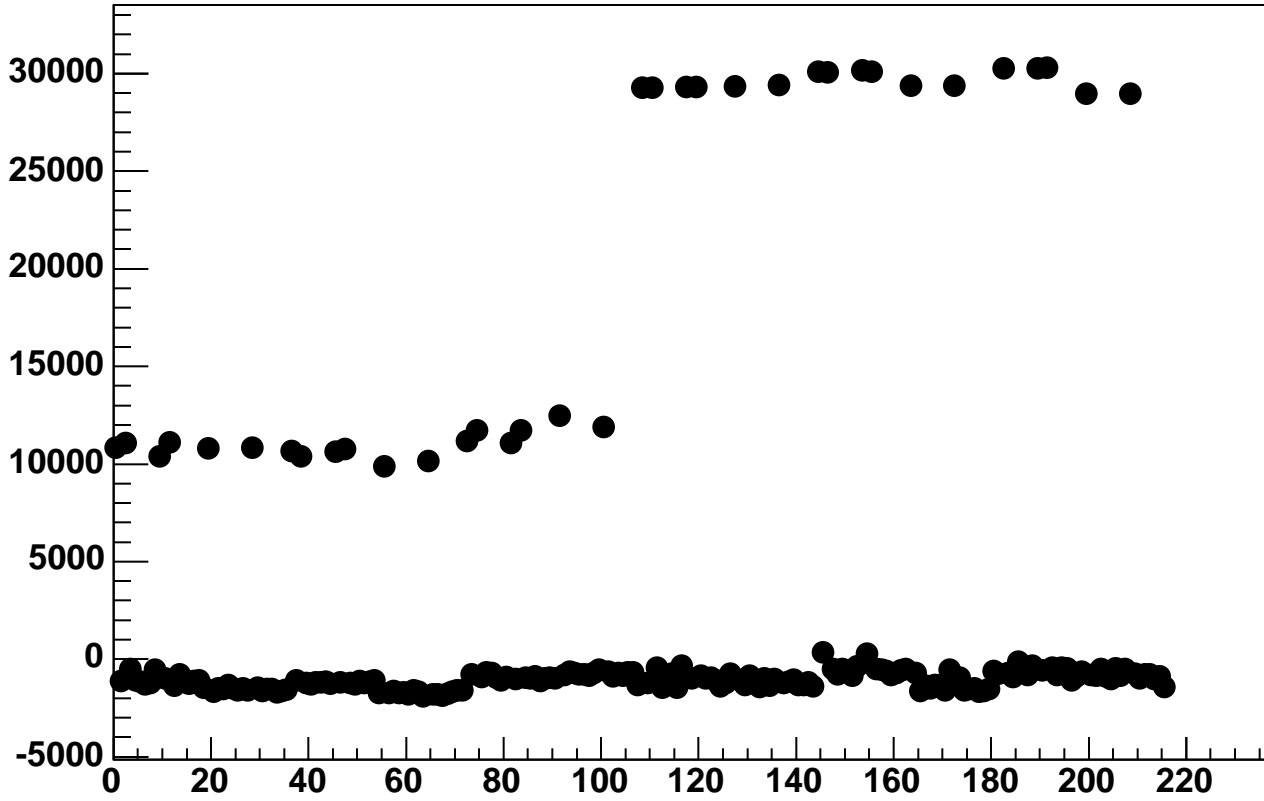
Enable 0, Hold=30, DAC=550, ADC Mean vs 18\*Chip+Chan



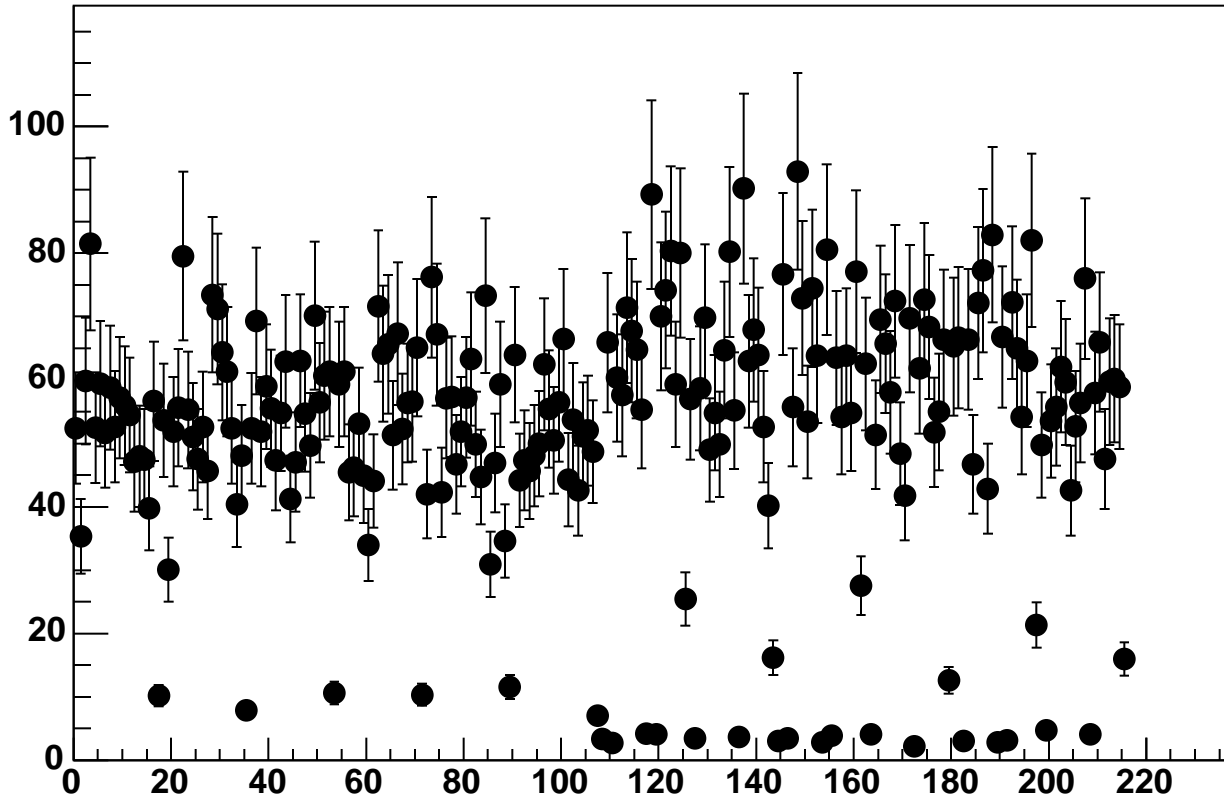
Enable 0, Hold=30, DAC=550, ADC Noise vs 18\*Chip+Chan



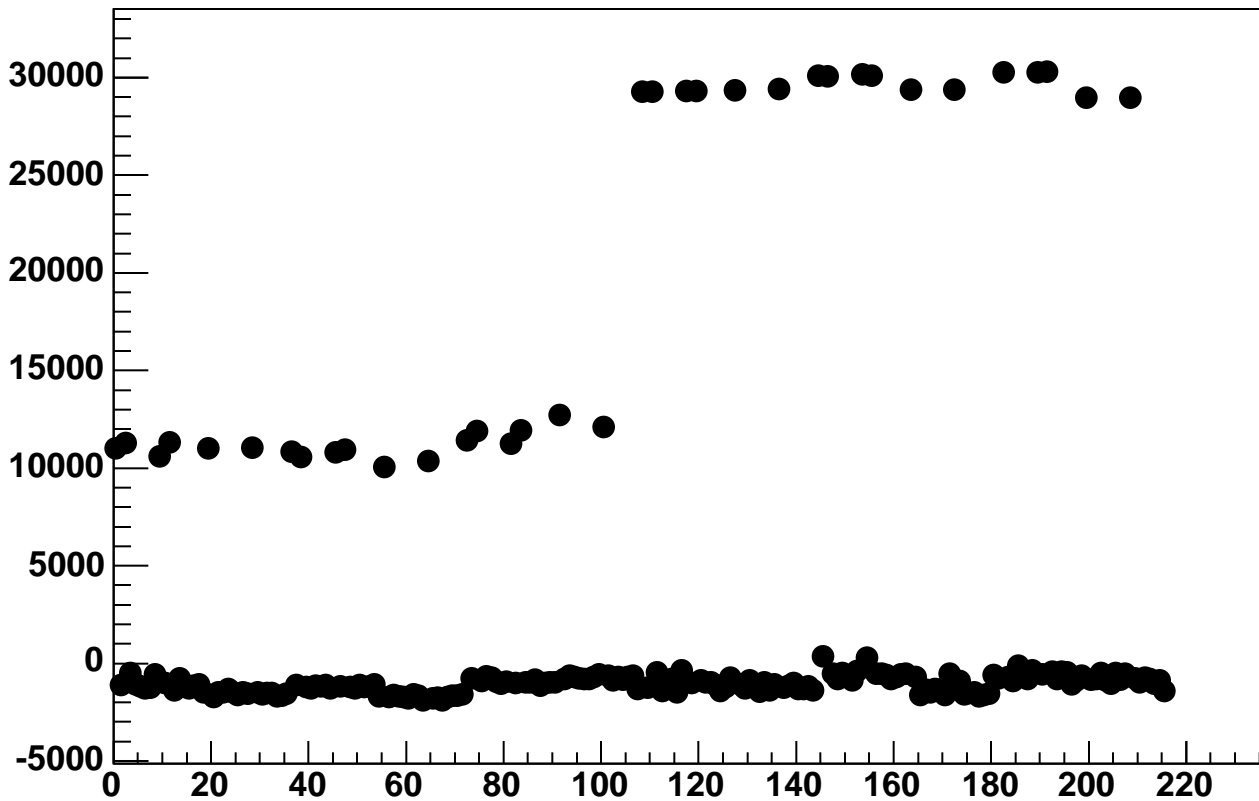
Enable 0, Hold=30, DAC=600, ADC Mean vs 18\*Chip+Chan



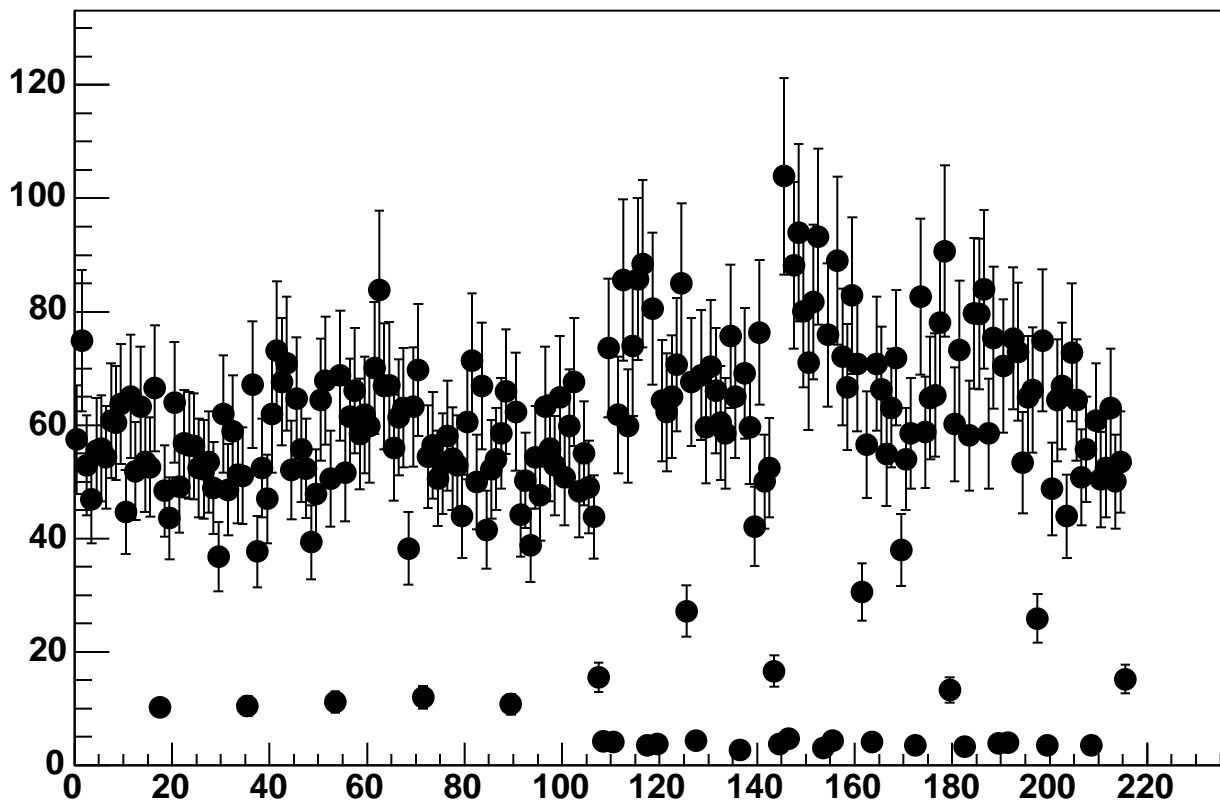
Enable 0, Hold=30, DAC=600, ADC Noise vs 18\*Chip+Chan



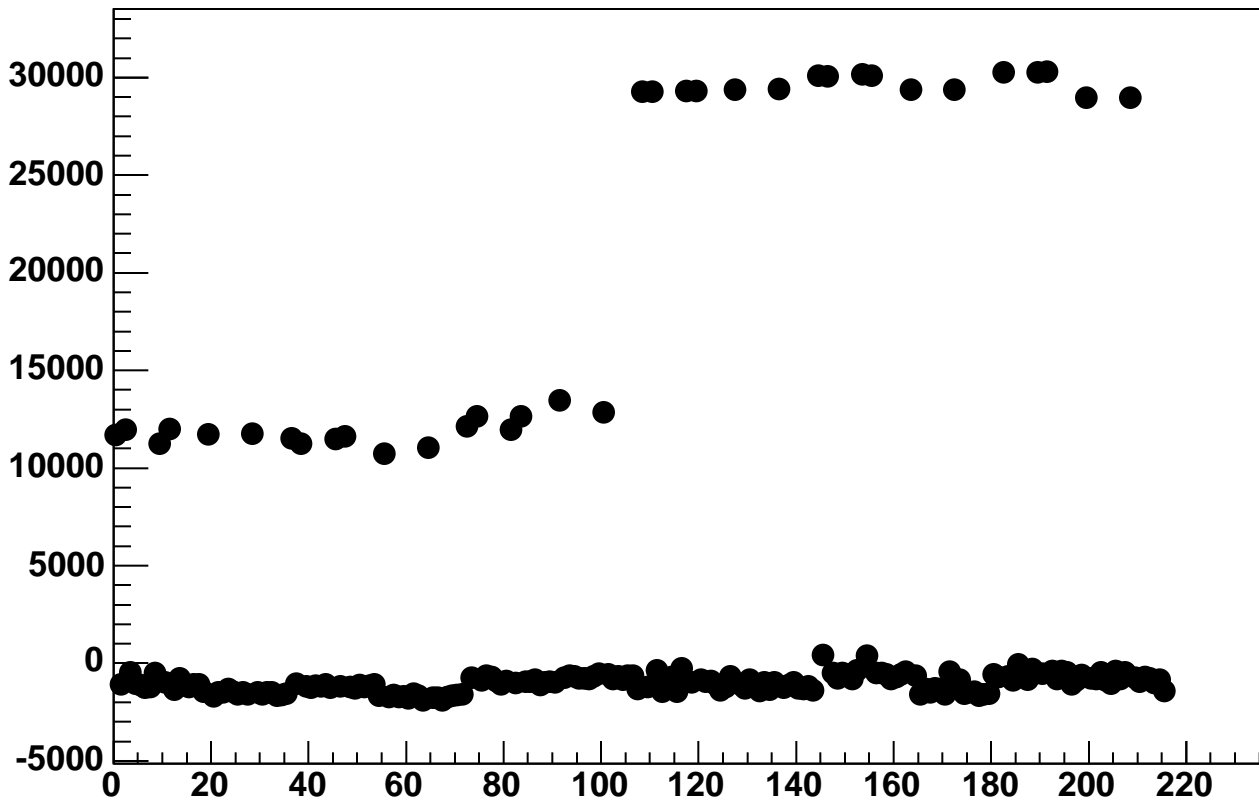
Enable 0, Hold=30, DAC=650, ADC Mean vs 18\*Chip+Chan



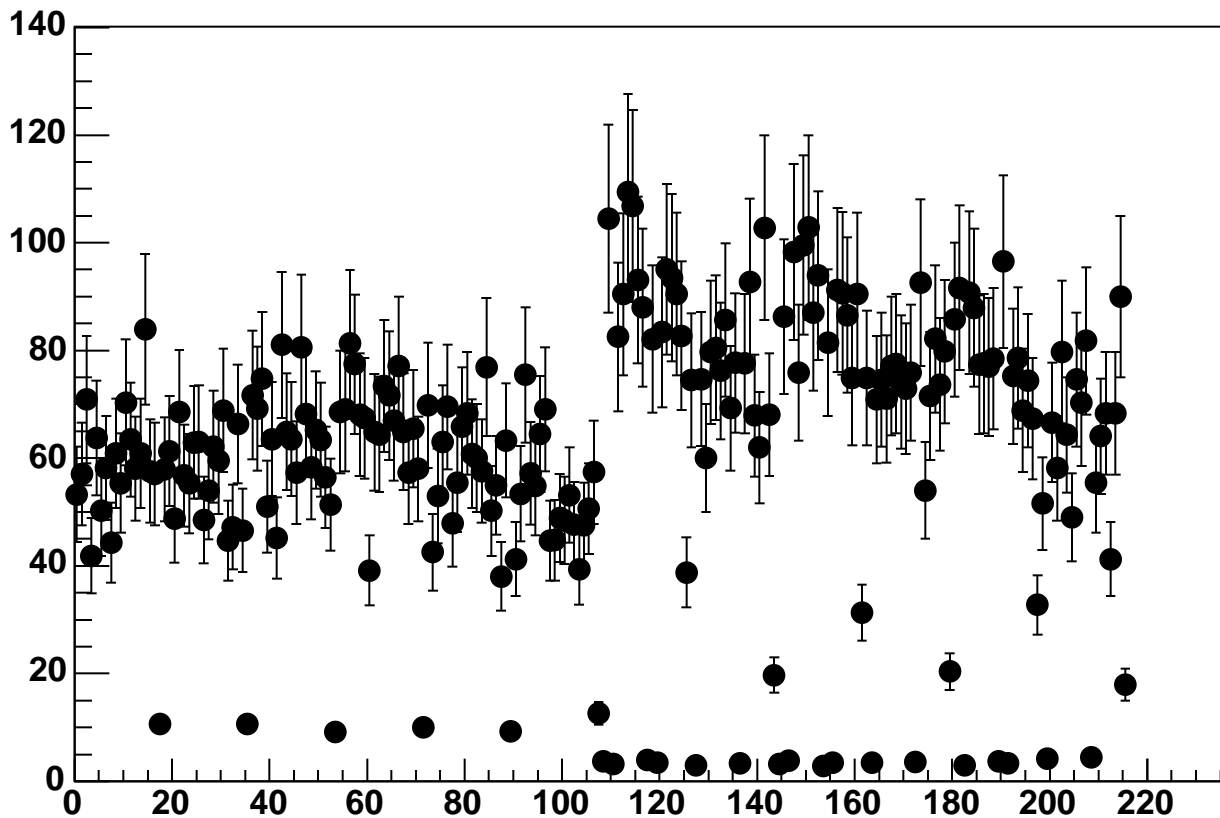
Enable 0, Hold=30, DAC=650, ADC Noise vs 18\*Chip+Chan



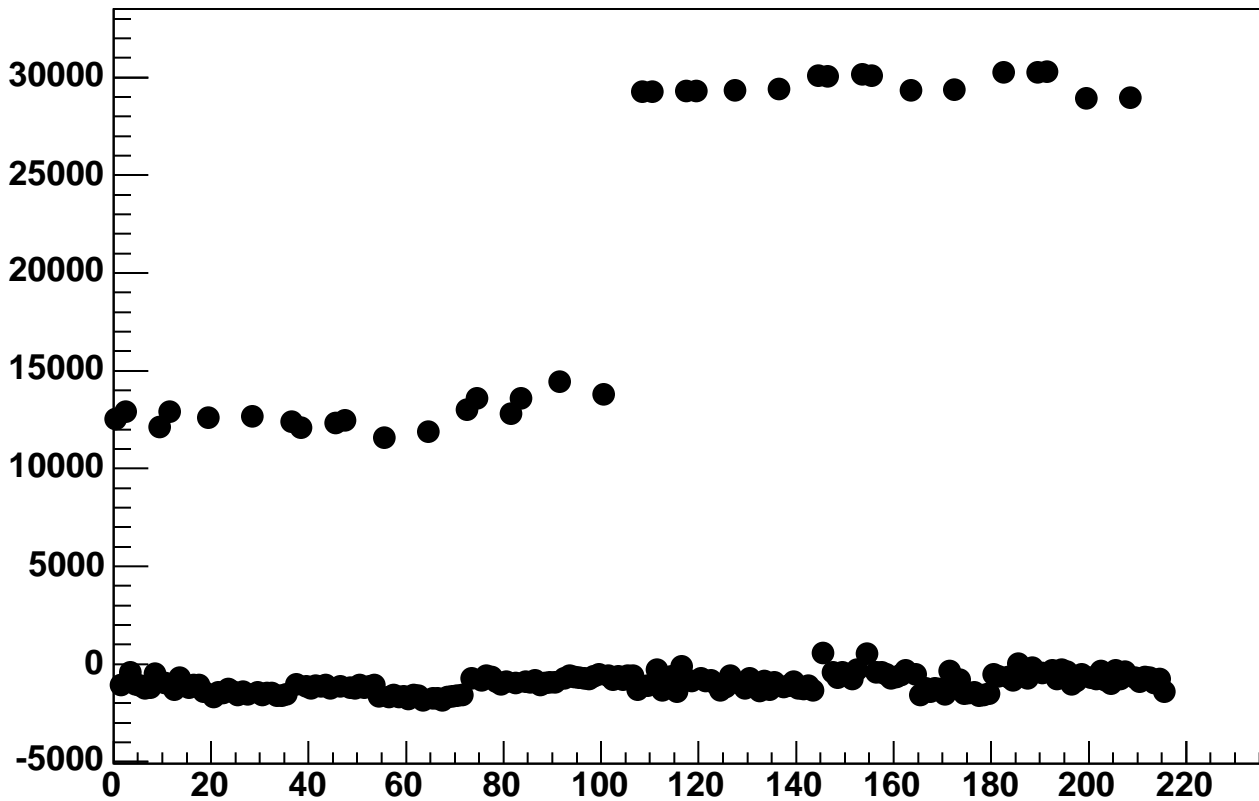
Enable 0, Hold=30, DAC=700, ADC Mean vs 18\*Chip+Chan



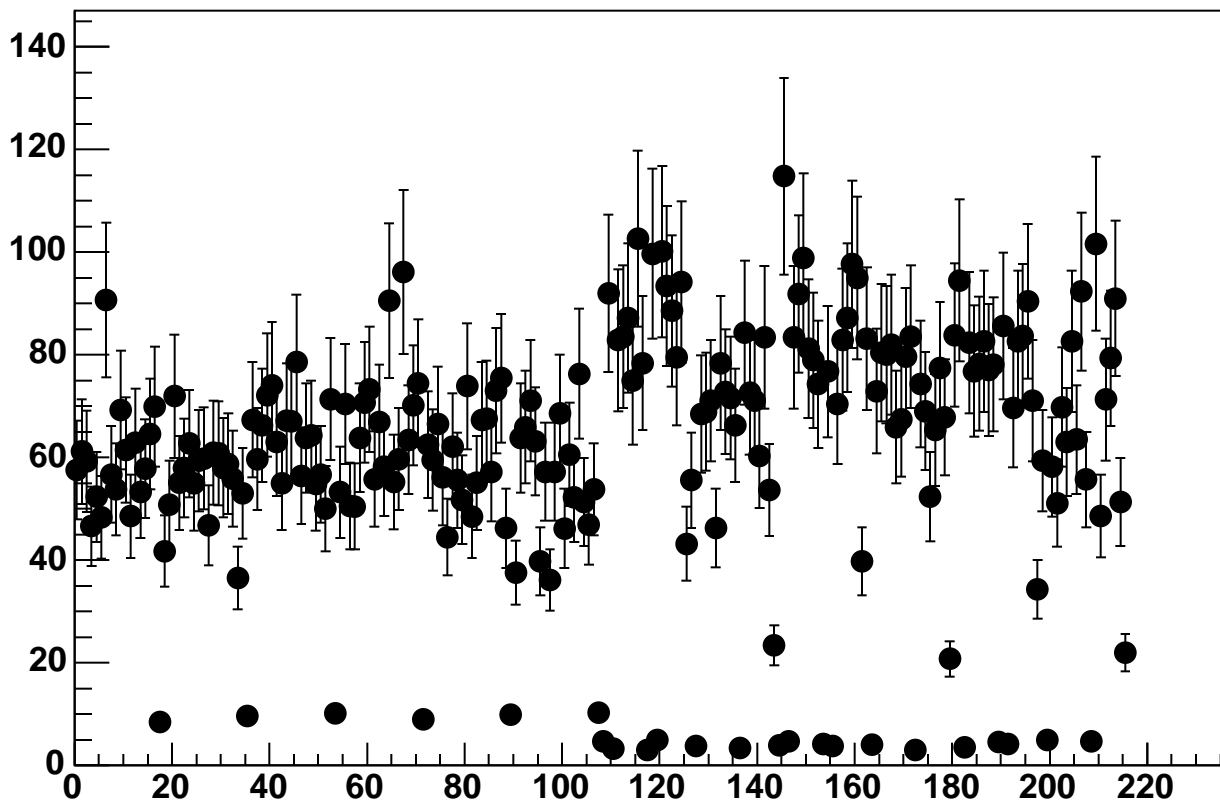
Enable 0, Hold=30, DAC=700, ADC Noise vs 18\*Chip+Chan



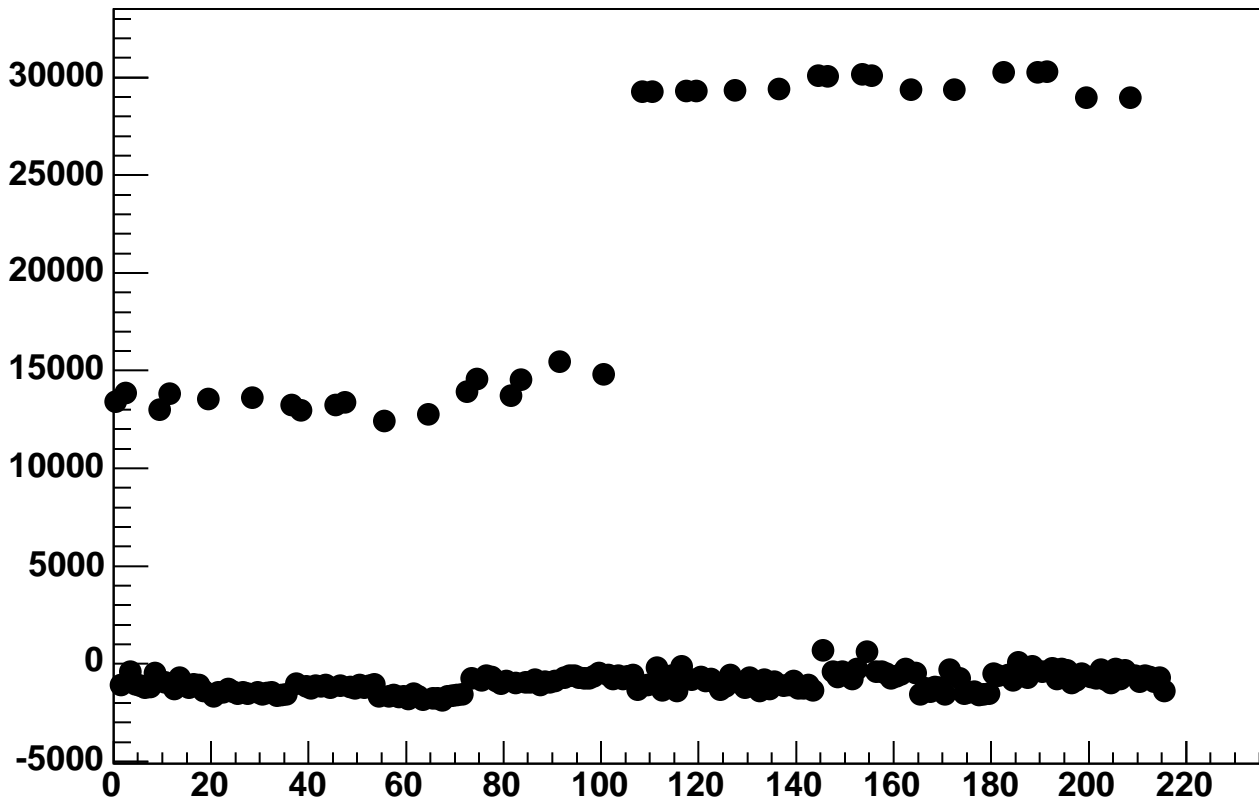
Enable 0, Hold=30, DAC=750, ADC Mean vs 18\*Chip+Chan



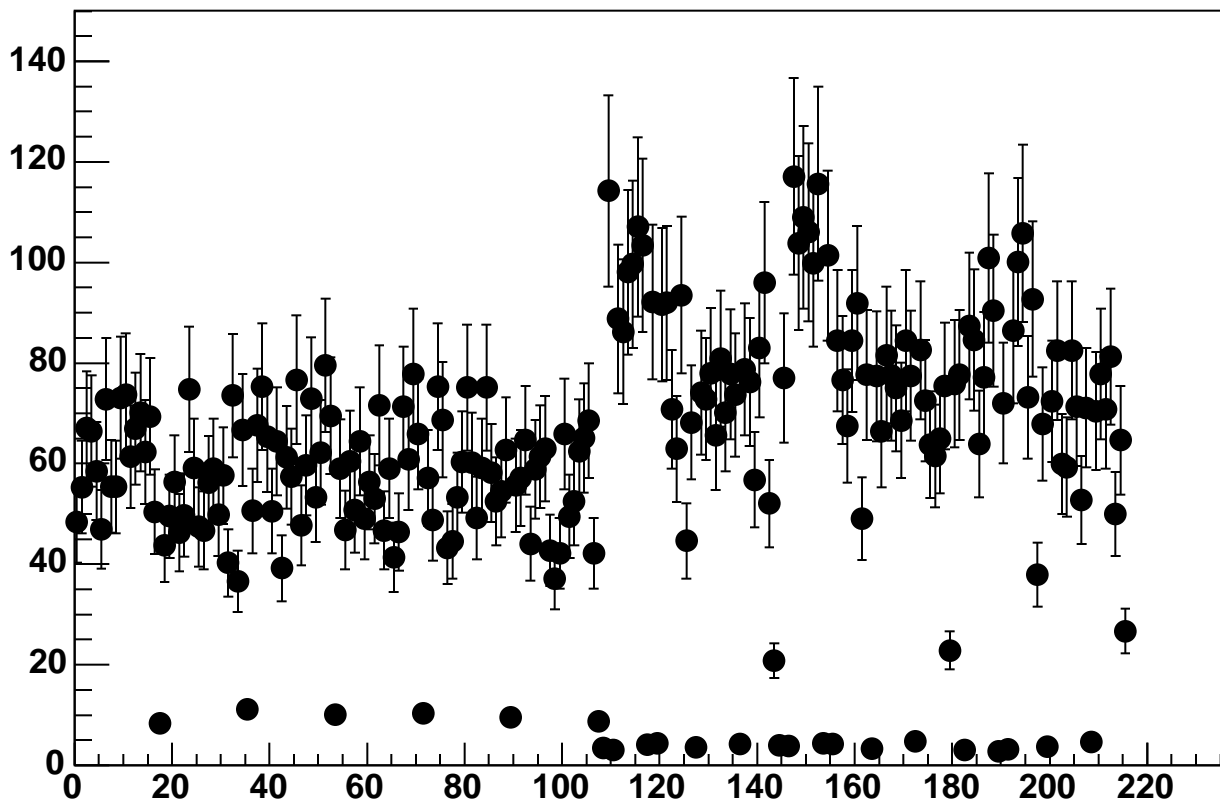
Enable 0, Hold=30, DAC=750, ADC Noise vs 18\*Chip+Chan



Enable 0, Hold=30, DAC=800, ADC Mean vs 18\*Chip+Chan

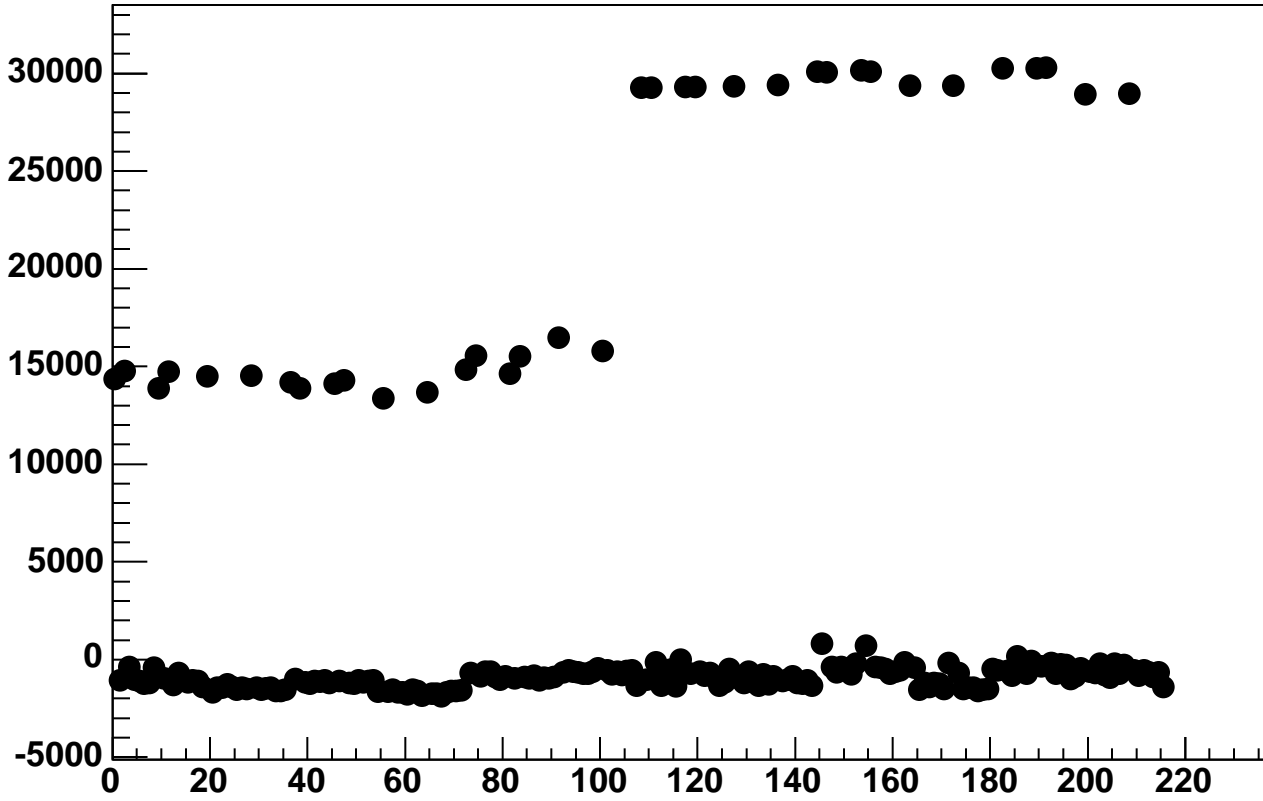


Enable 0, Hold=30, DAC=800, ADC Noise vs 18\*Chip+Chan

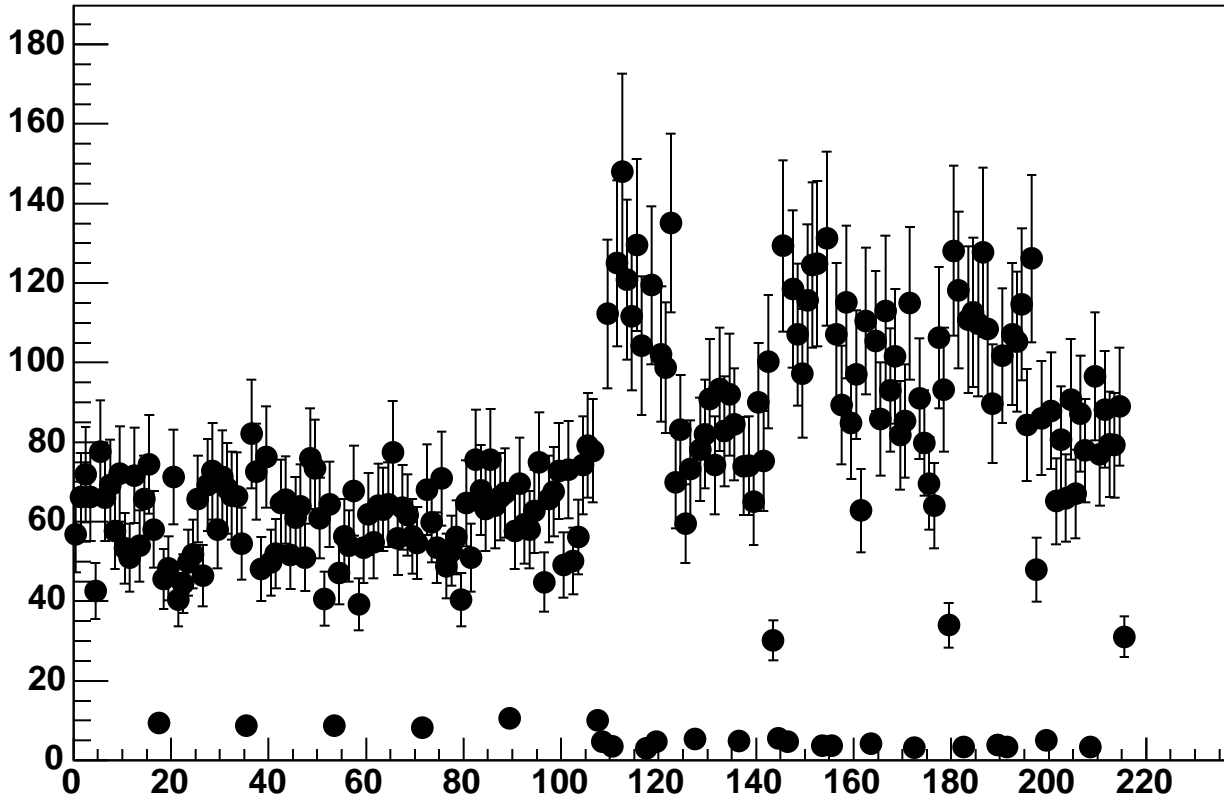




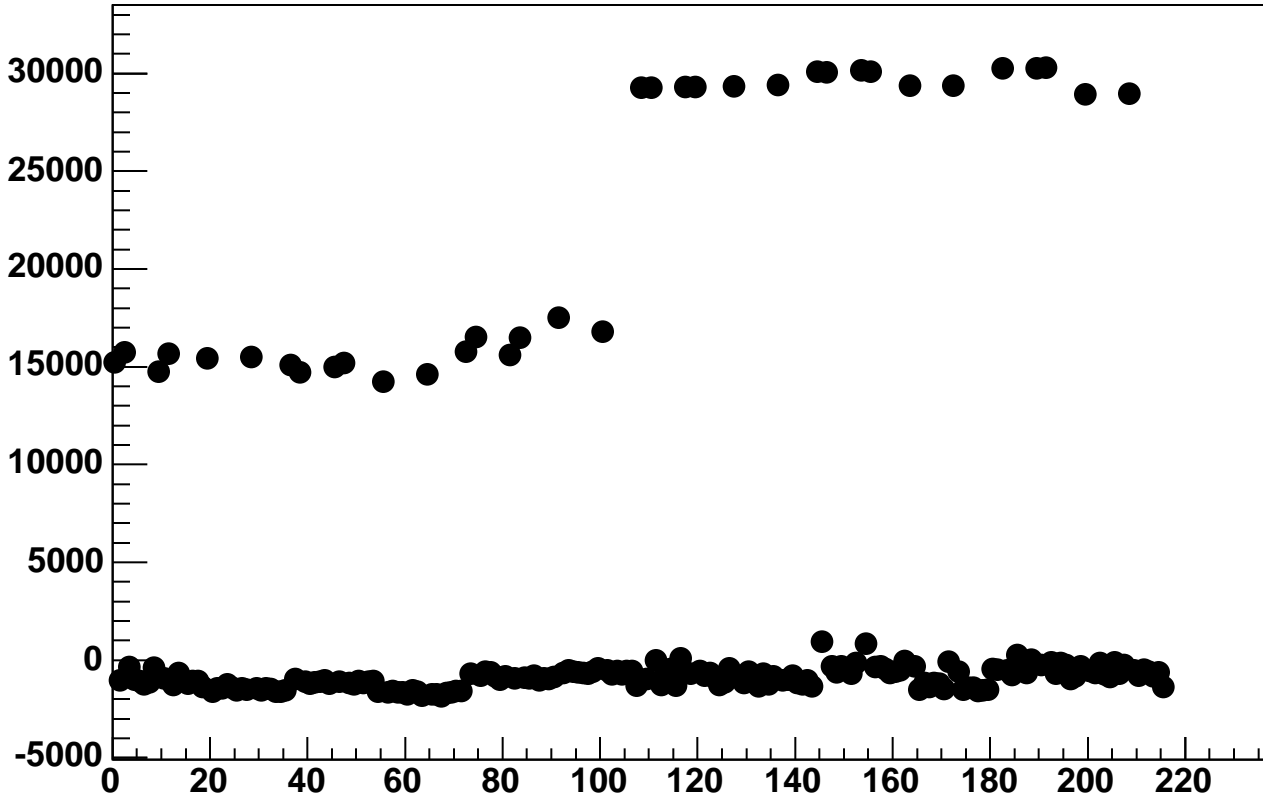
Enable 0, Hold=30, DAC=850, ADC Mean vs 18\*Chip+Chan



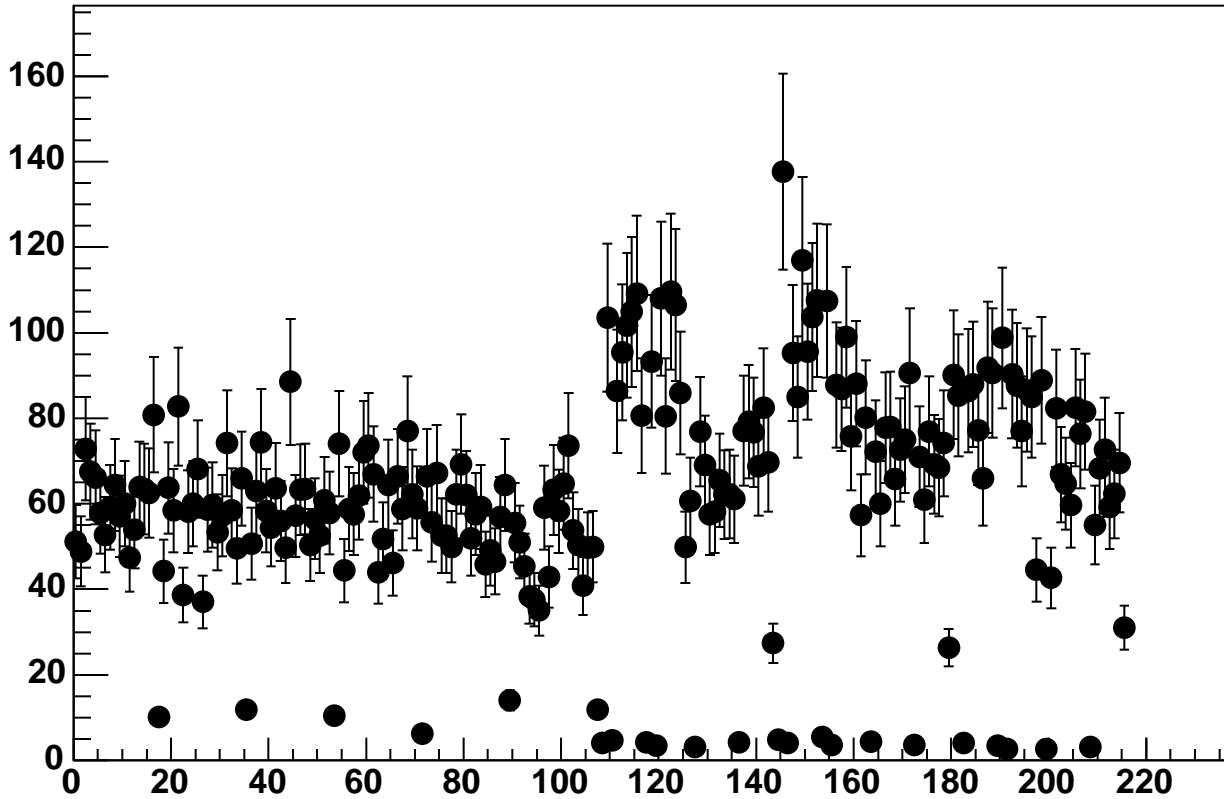
Enable 0, Hold=30, DAC=850, ADC Noise vs 18\*Chip+Chan



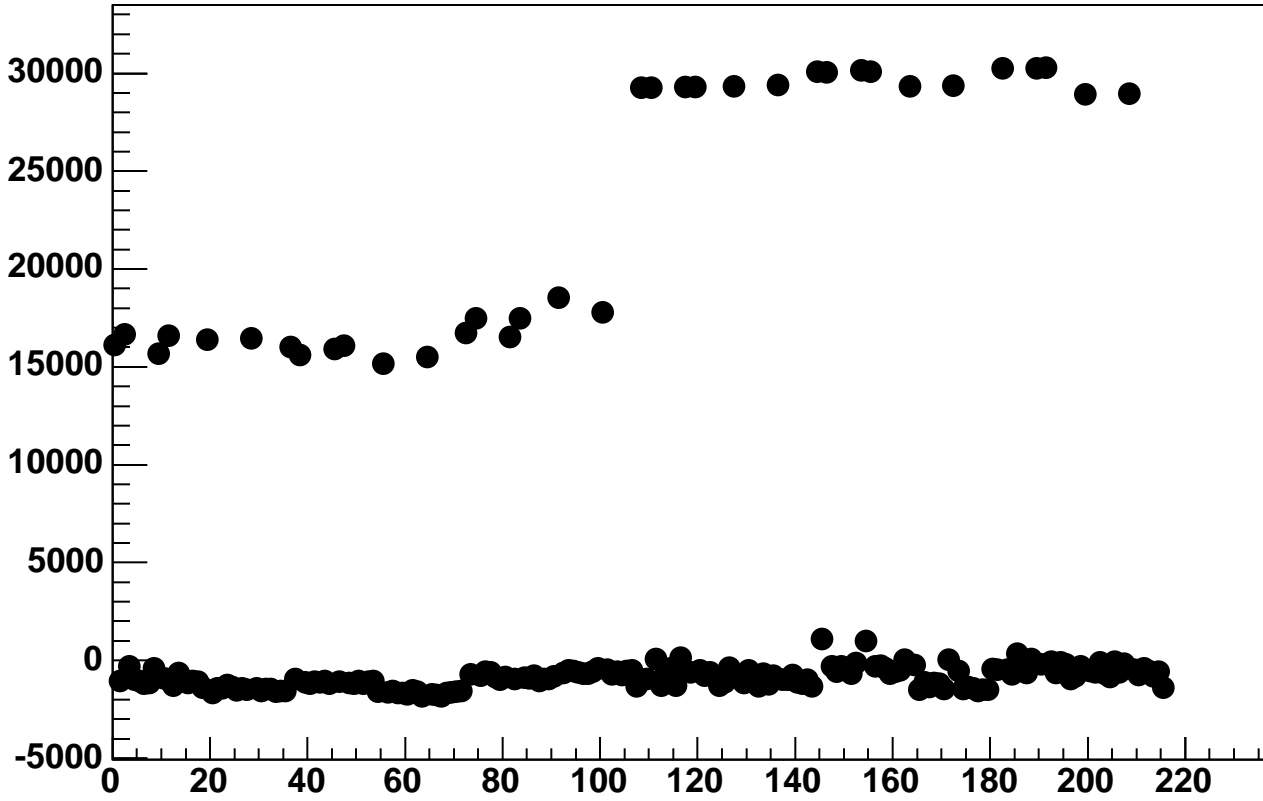
Enable 0, Hold=30, DAC=900, ADC Mean vs 18\*Chip+Chan



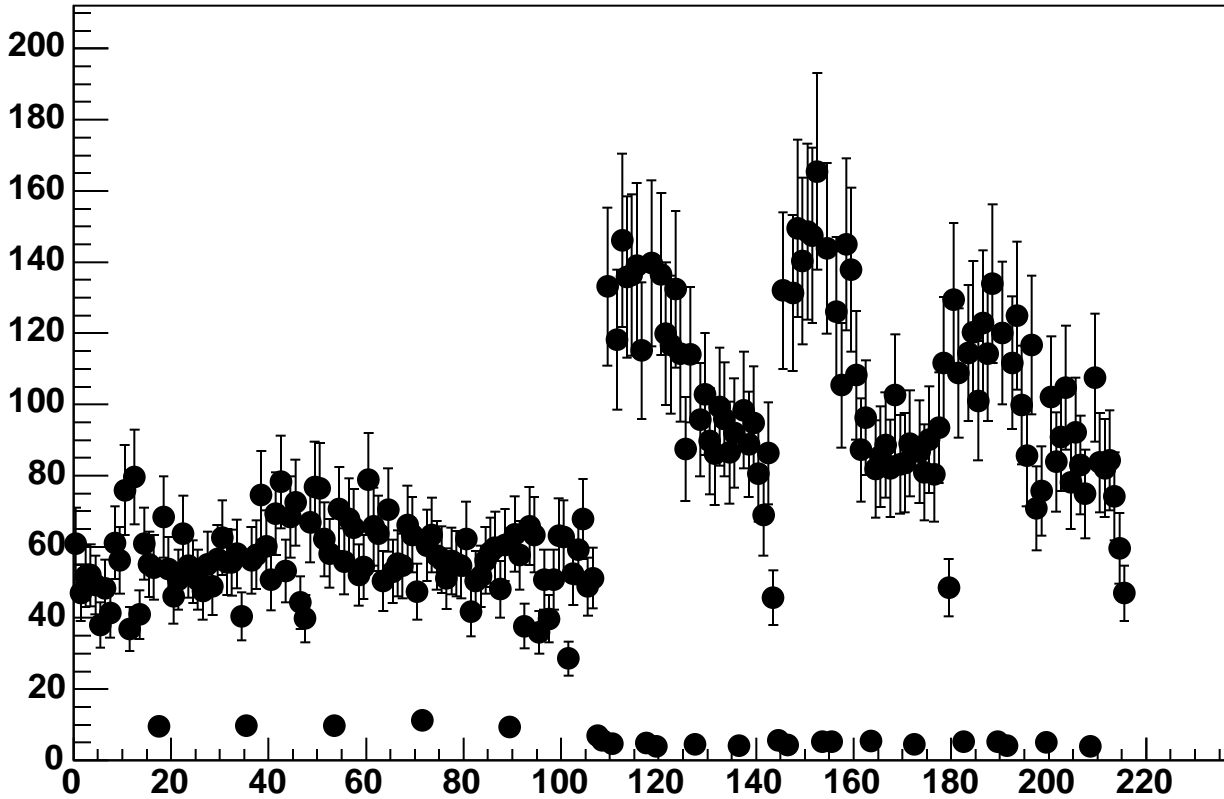
Enable 0, Hold=30, DAC=900, ADC Noise vs 18\*Chip+Chan



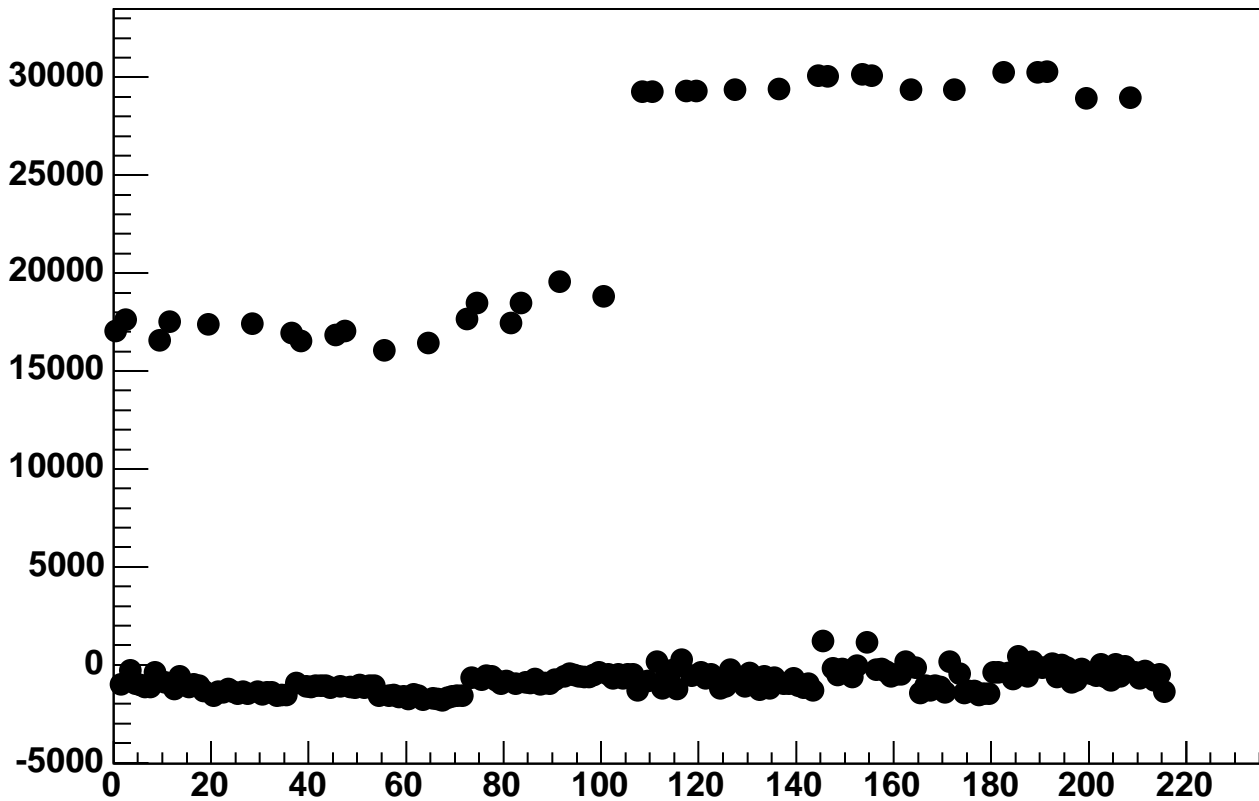
Enable 0, Hold=30, DAC=950, ADC Mean vs 18\*Chip+Chan



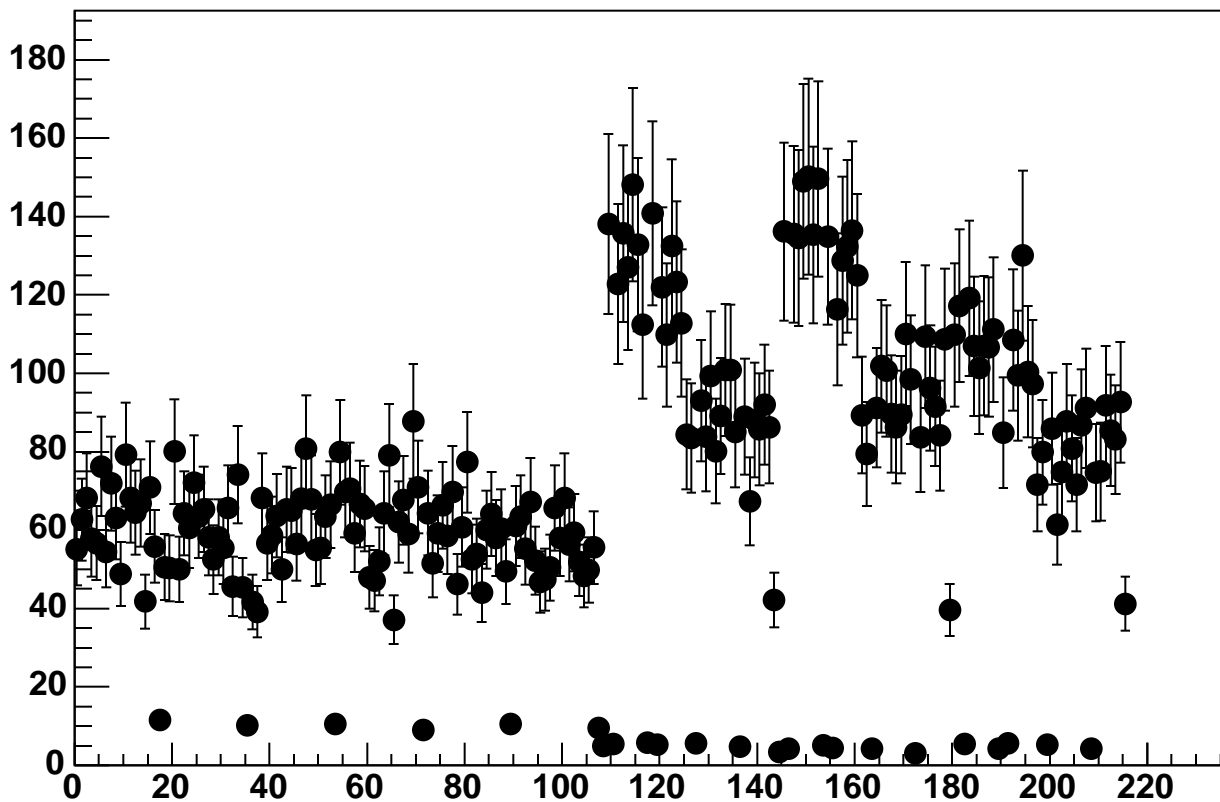
Enable 0, Hold=30, DAC=950, ADC Noise vs 18\*Chip+Chan



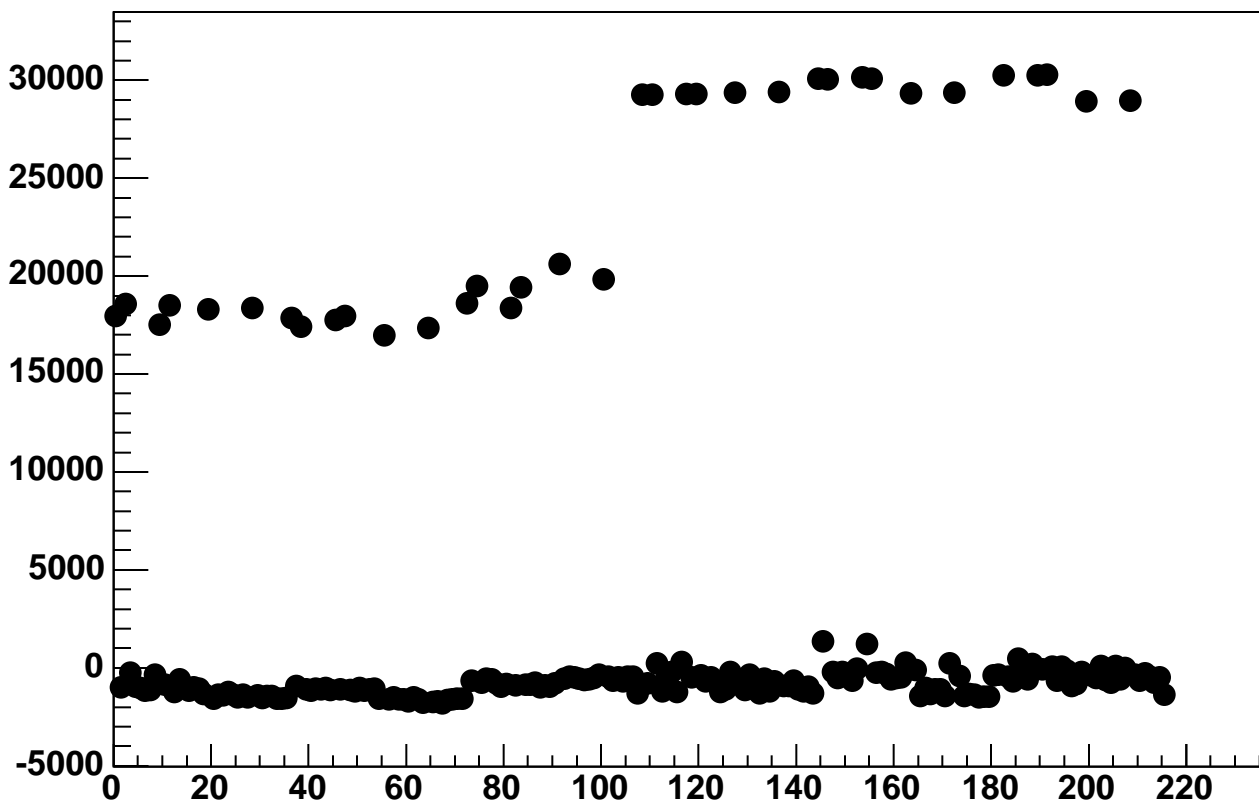
Enable 0, Hold=30, DAC=1000, ADC Mean vs 18\*Chip+Chan



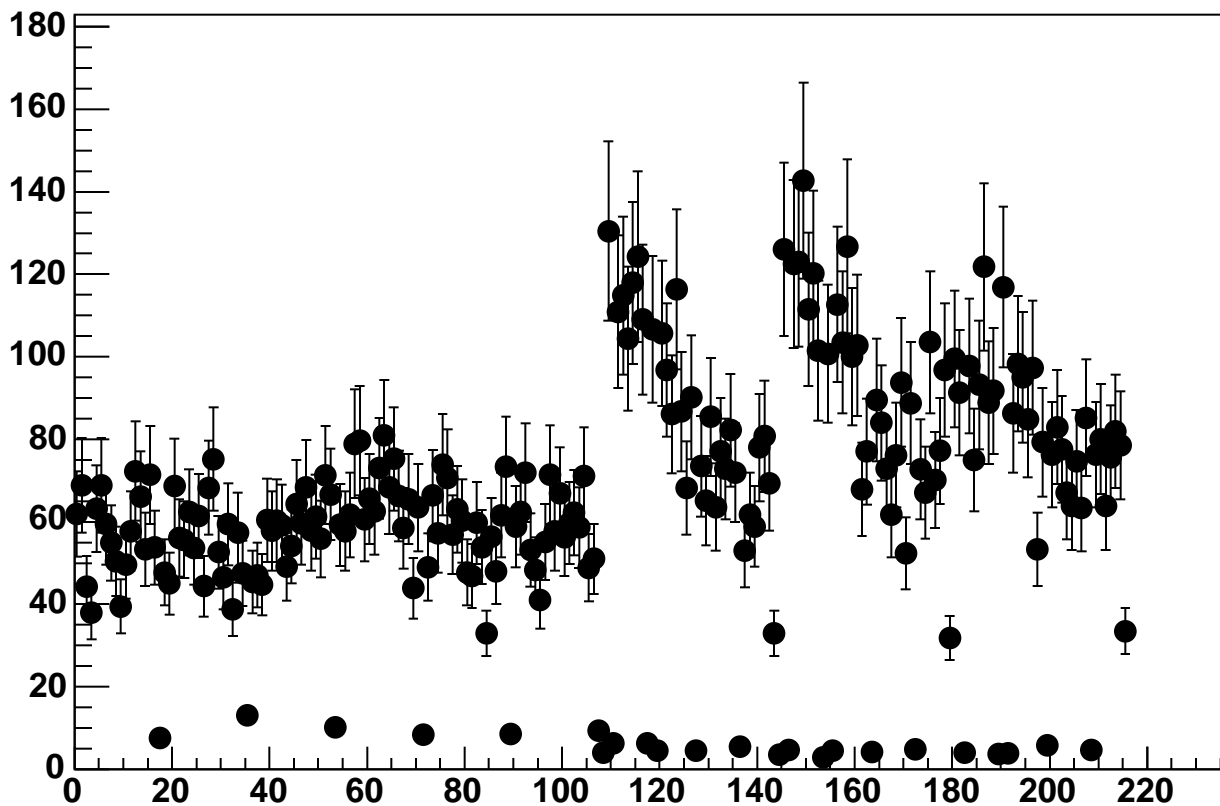
Enable 0, Hold=30, DAC=1000, ADC Noise vs 18\*Chip+Chan



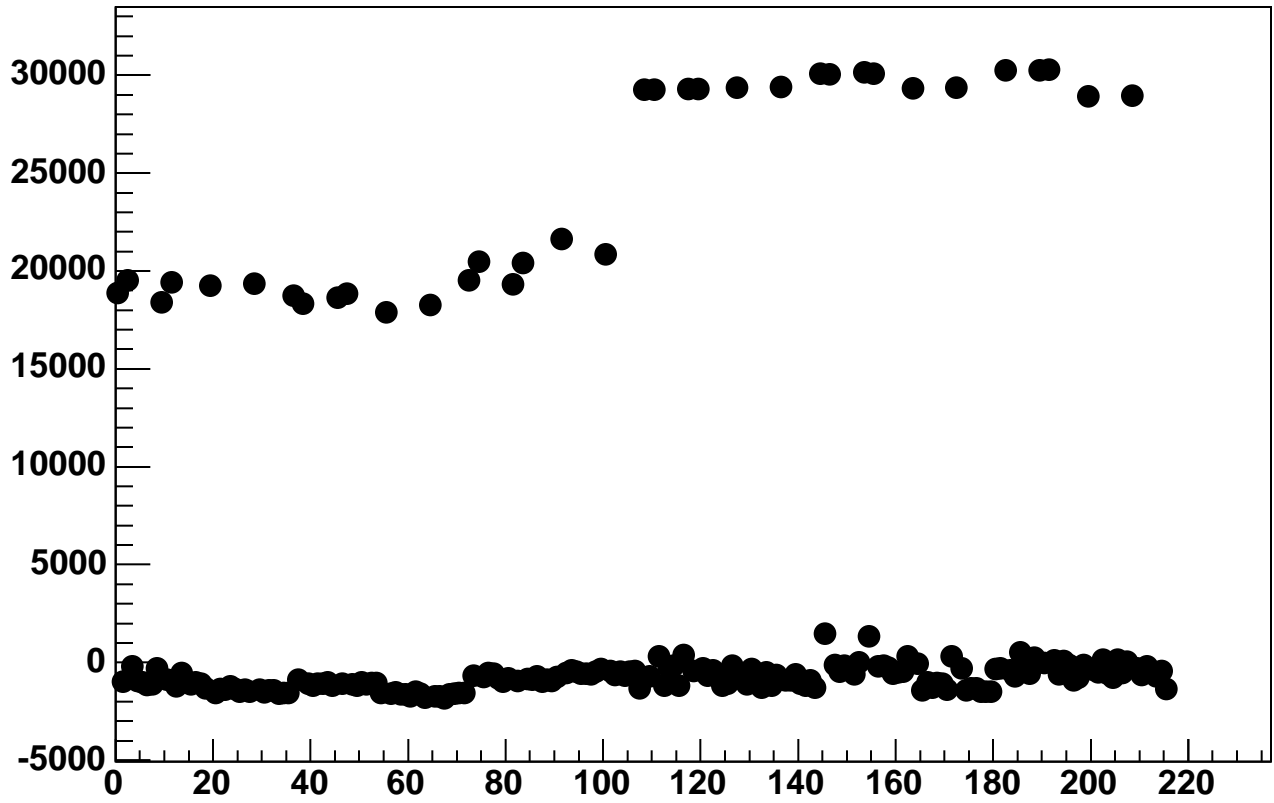
Enable 0, Hold=30, DAC=1050, ADC Mean vs 18\*Chip+Chan



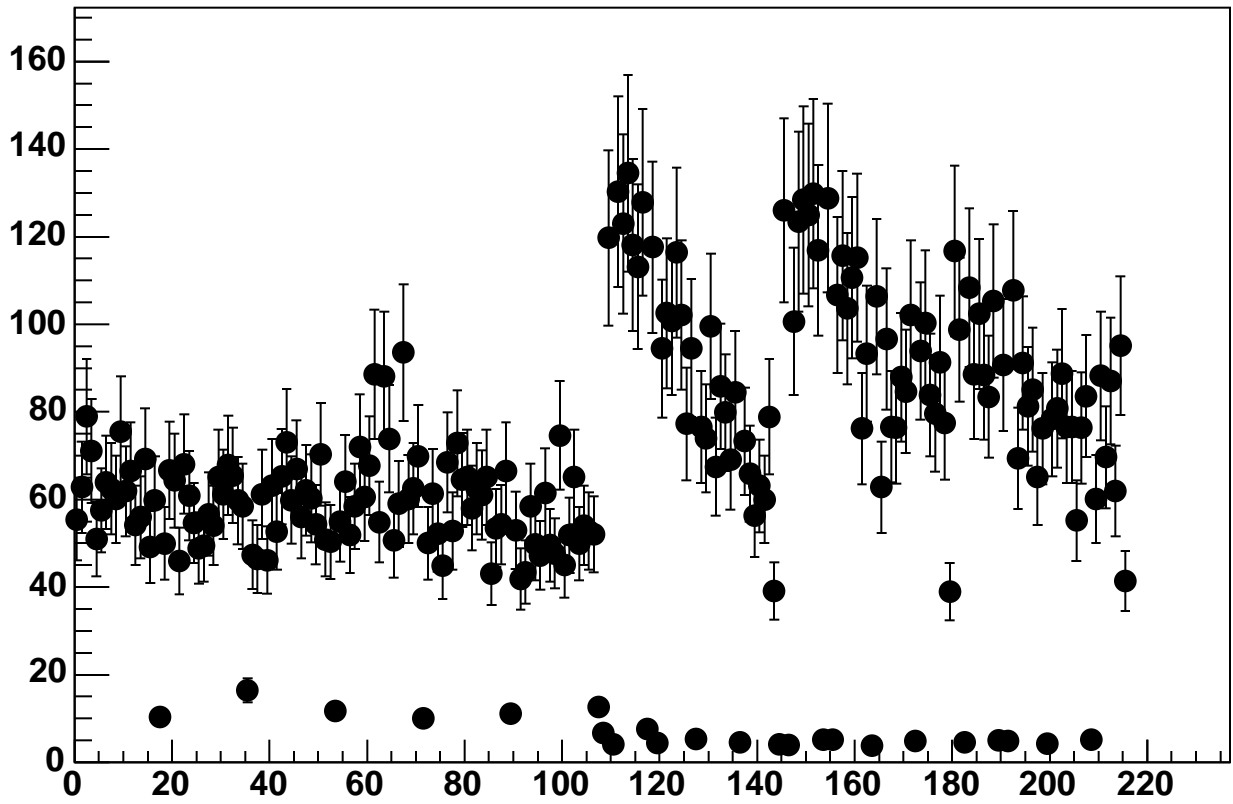
Enable 0, Hold=30, DAC=1050, ADC Noise vs 18\*Chip+Chan



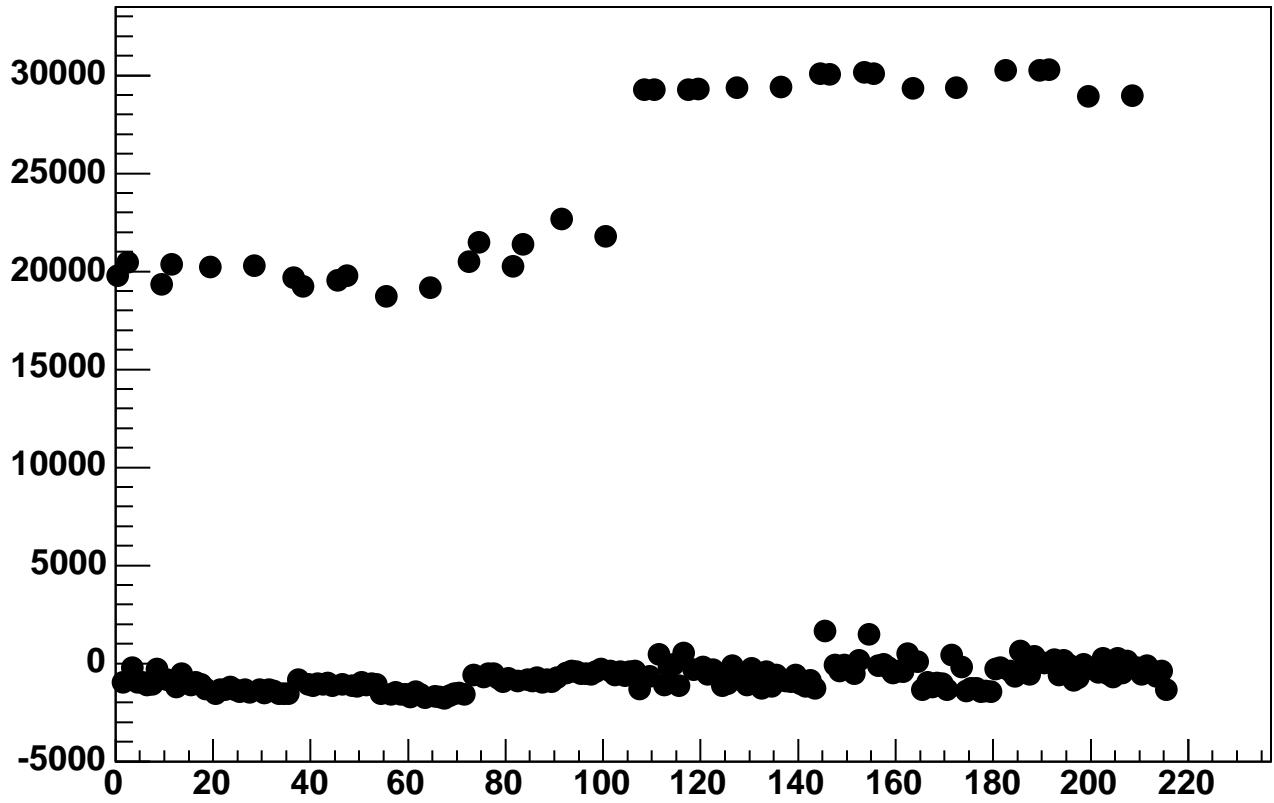
Enable 0, Hold=30, DAC=1100, ADC Mean vs 18\*Chip+Chan



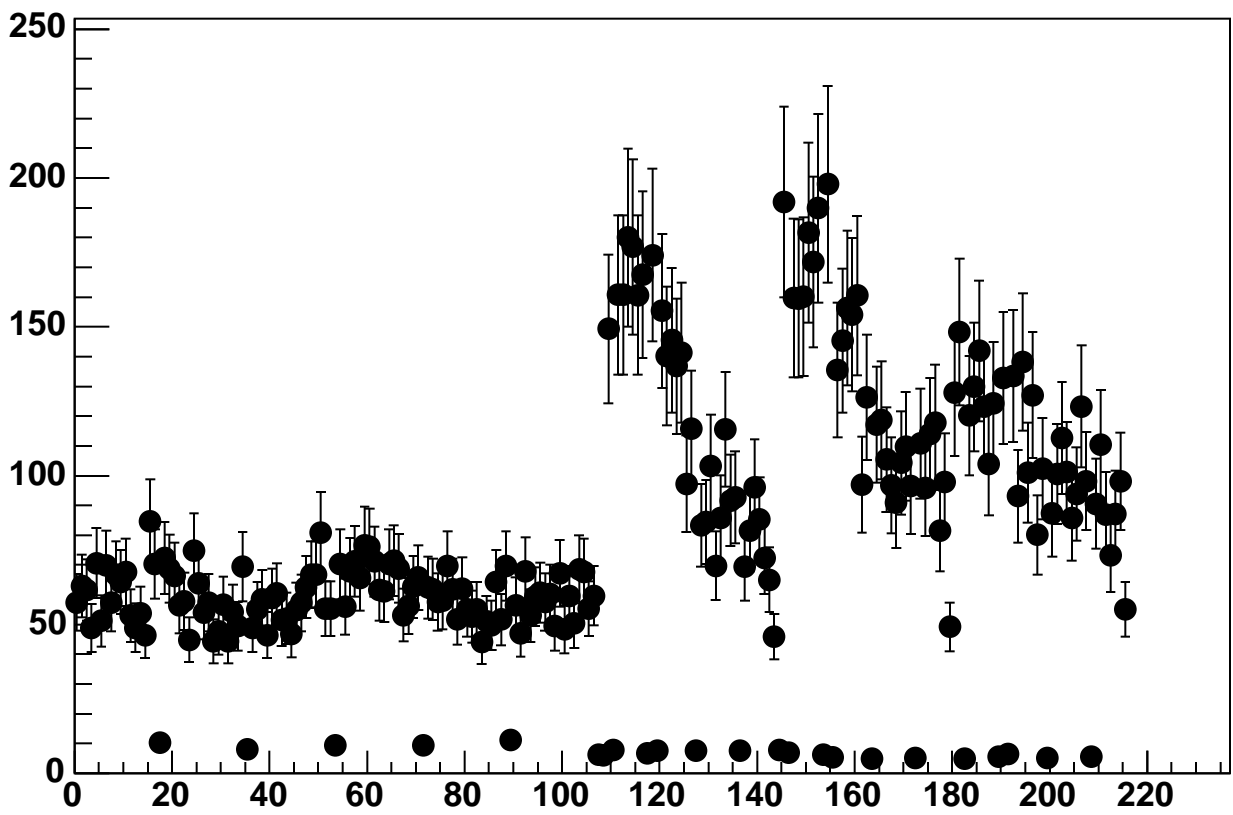
Enable 0, Hold=30, DAC=1100, ADC Noise vs 18\*Chip+Chan



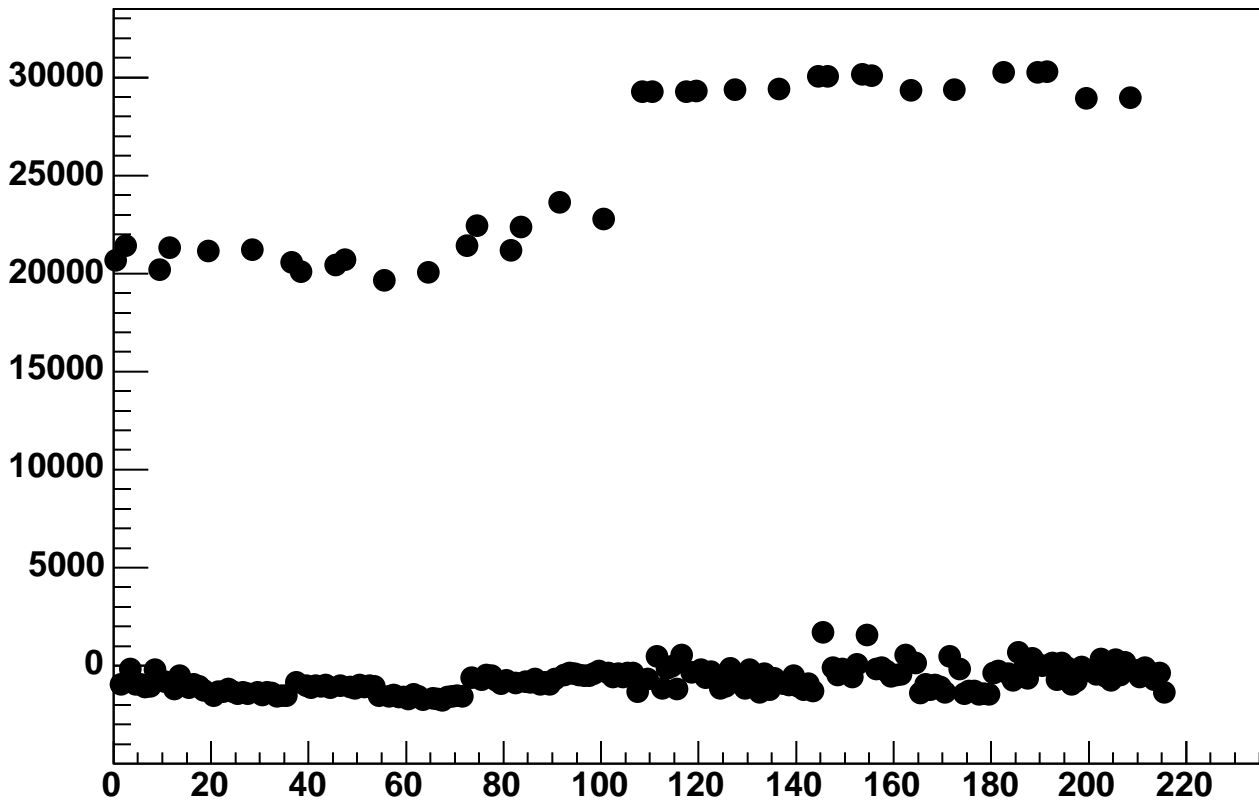
Enable 0, Hold=30, DAC=1150, ADC Mean vs 18\*Chip+Chan



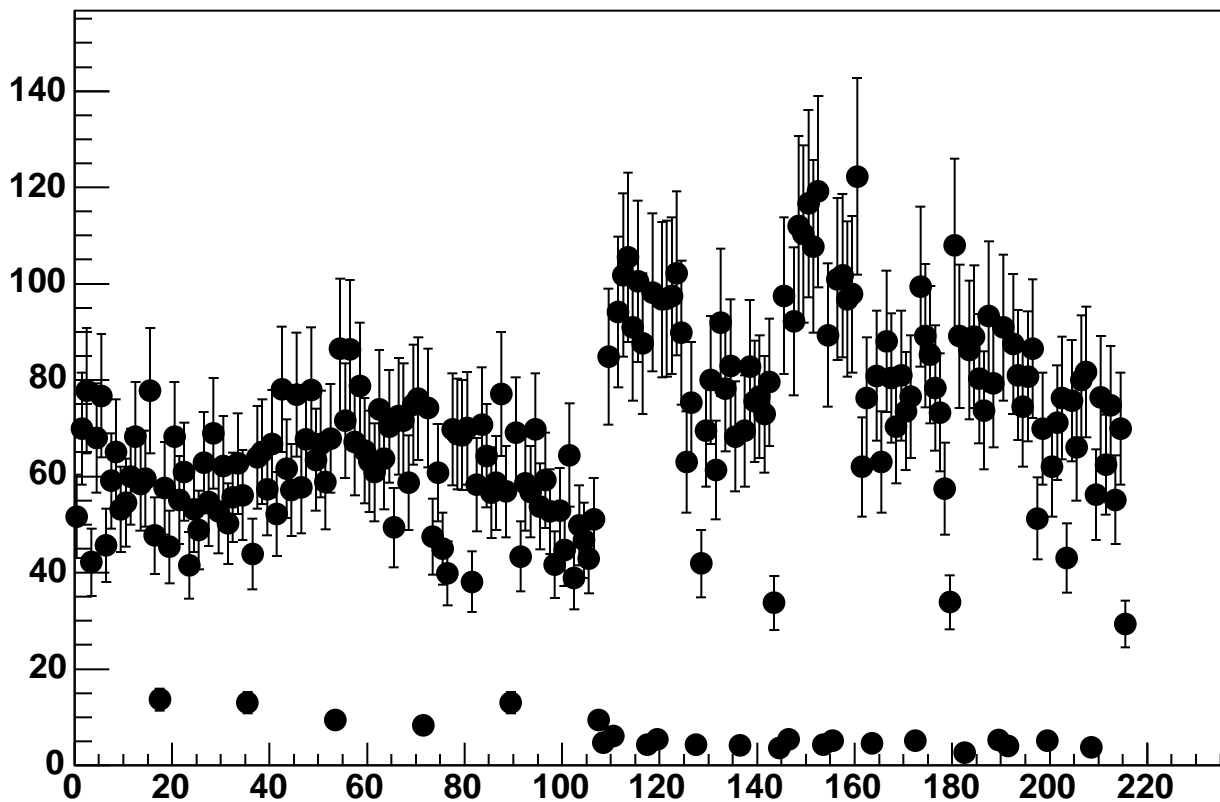
Enable 0, Hold=30, DAC=1150, ADC Noise vs 18\*Chip+Chan



Enable 0, Hold=30, DAC=1200, ADC Mean vs 18\*Chip+Chan

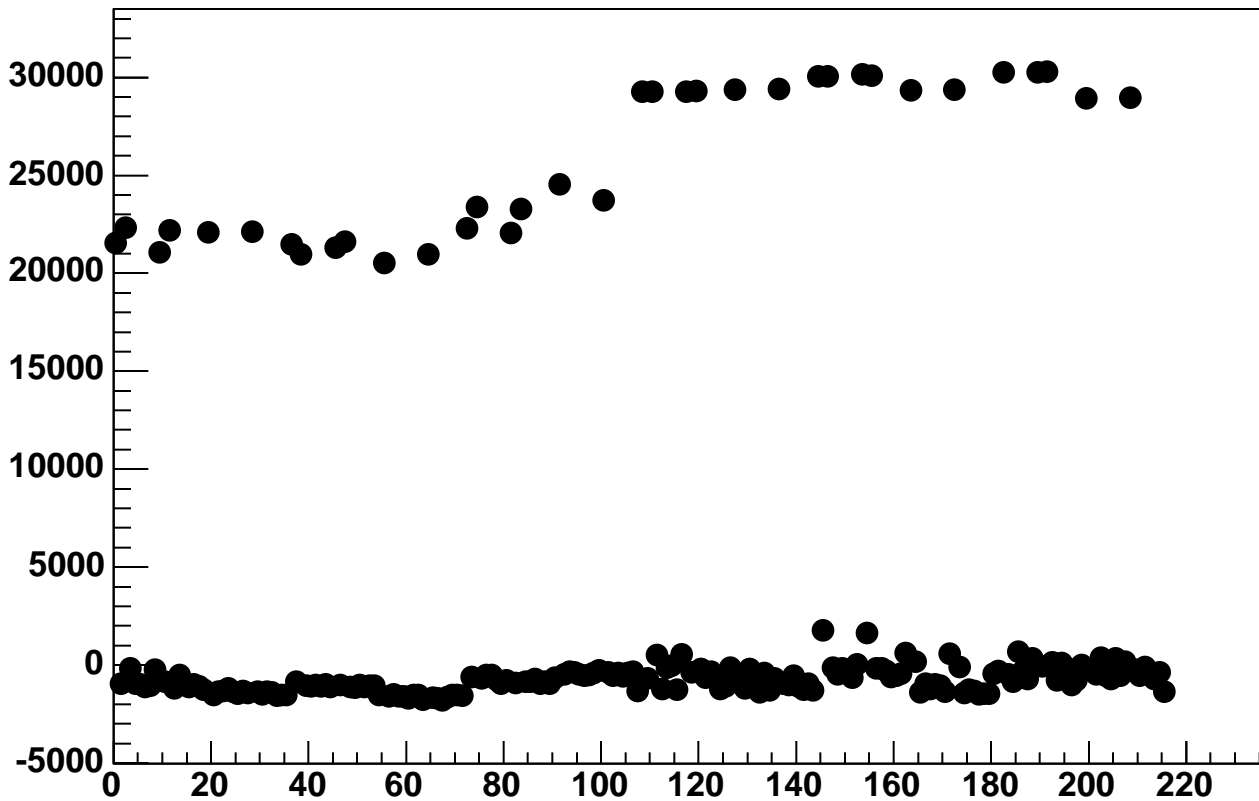


Enable 0, Hold=30, DAC=1200, ADC Noise vs 18\*Chip+Chan

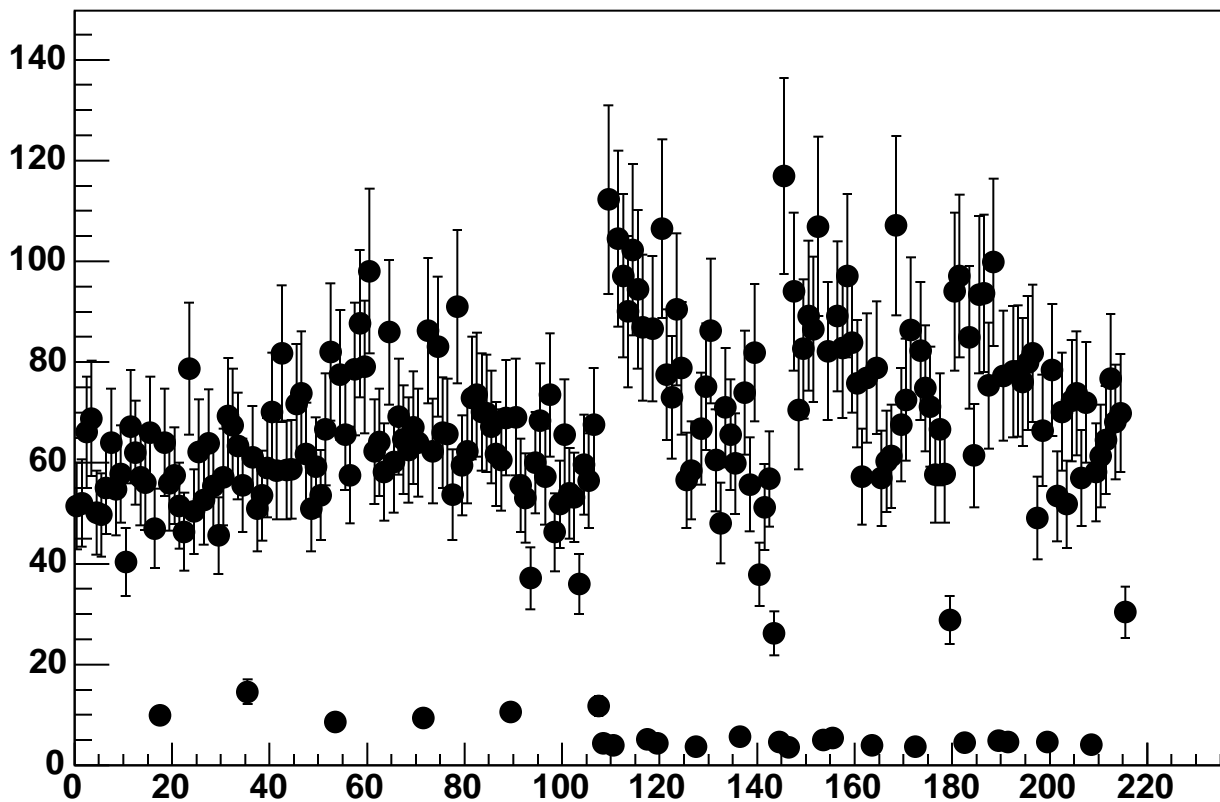




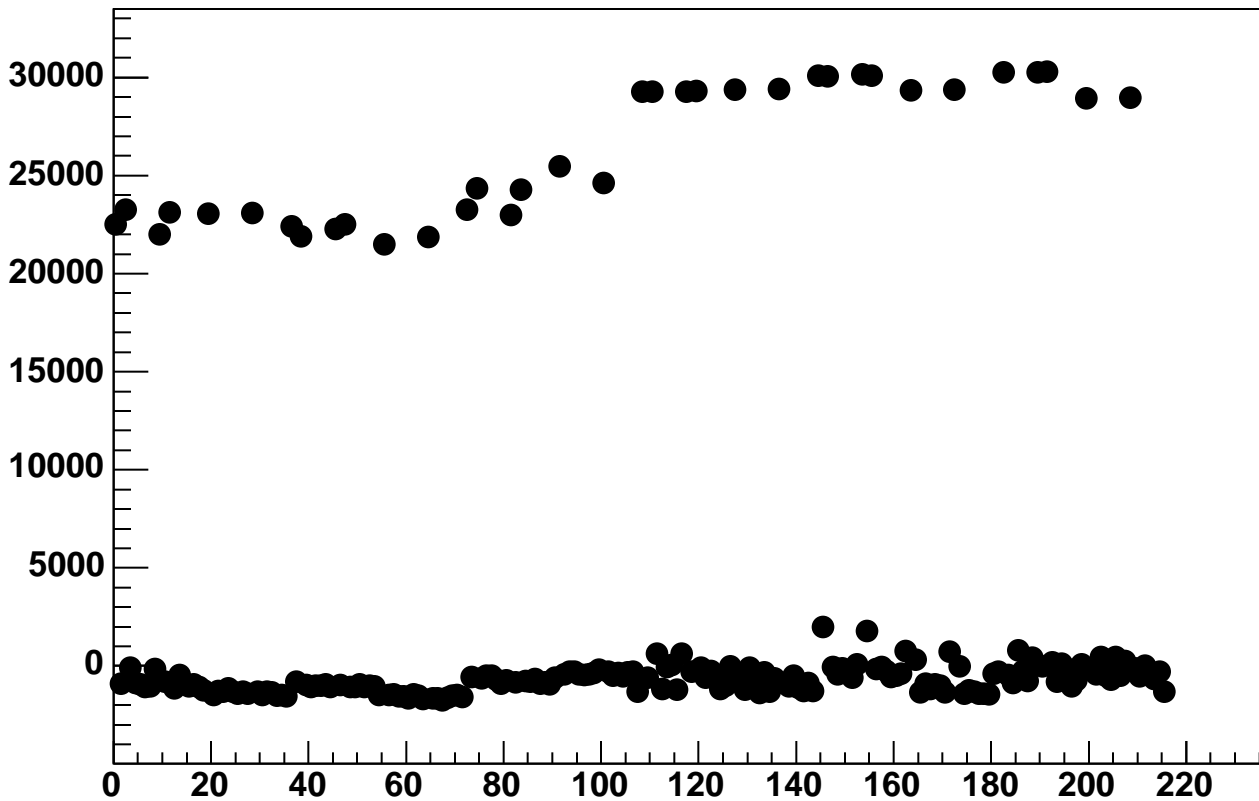
Enable 0, Hold=30, DAC=1250, ADC Mean vs 18\*Chip+Chan



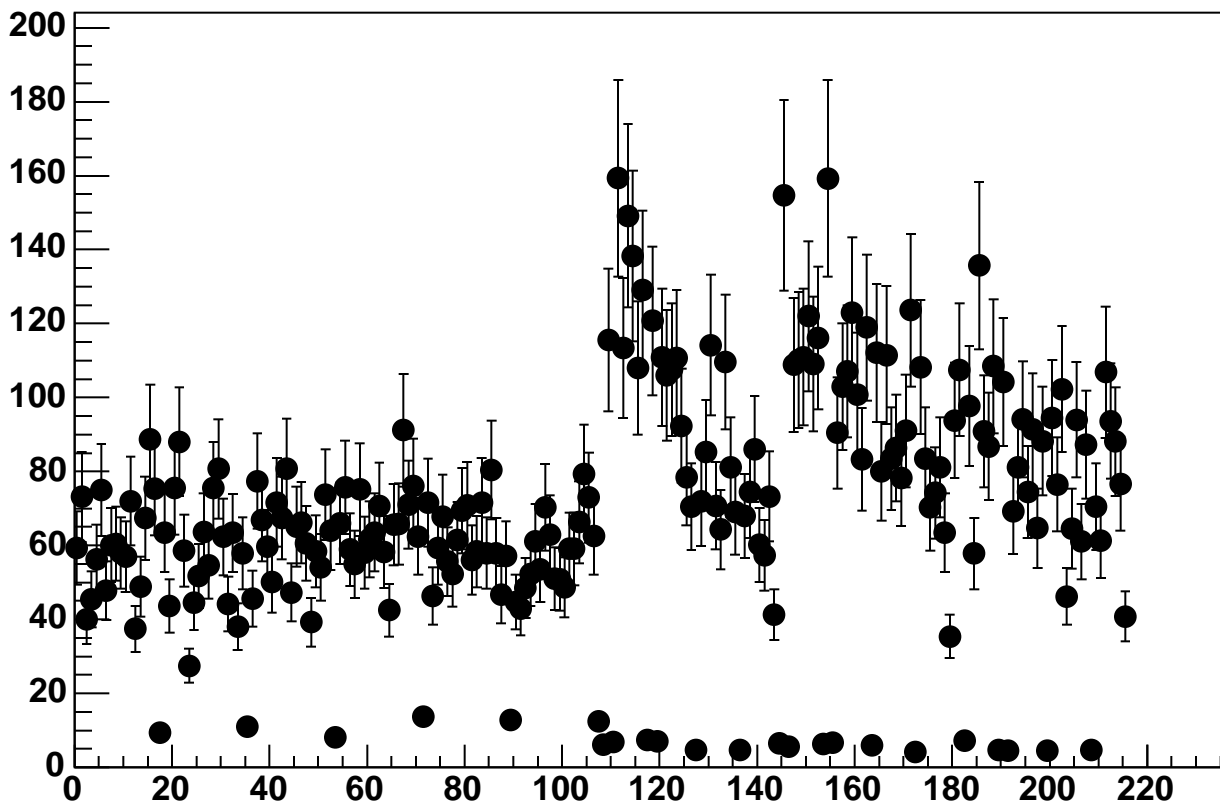
Enable 0, Hold=30, DAC=1250, ADC Noise vs 18\*Chip+Chan



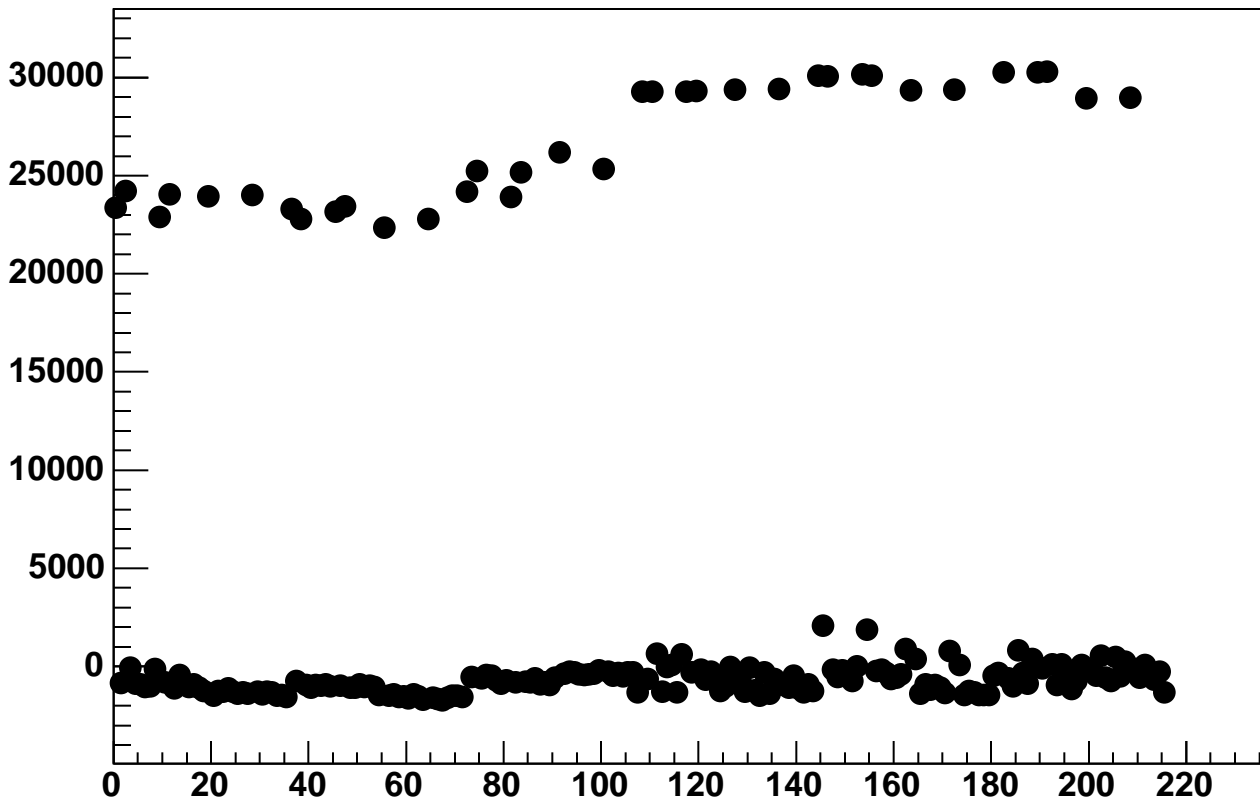
Enable 0, Hold=30, DAC=1300, ADC Mean vs 18\*Chip+Chan



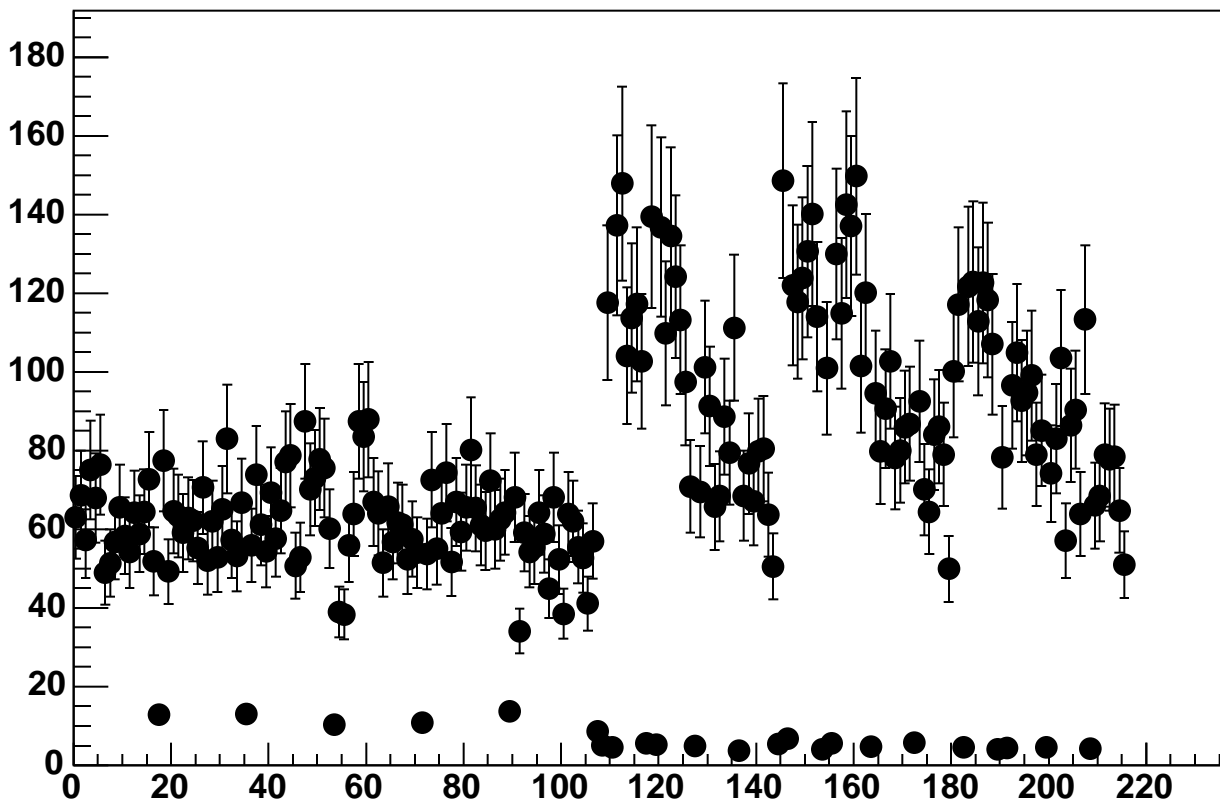
Enable 0, Hold=30, DAC=1300, ADC Noise vs 18\*Chip+Chan



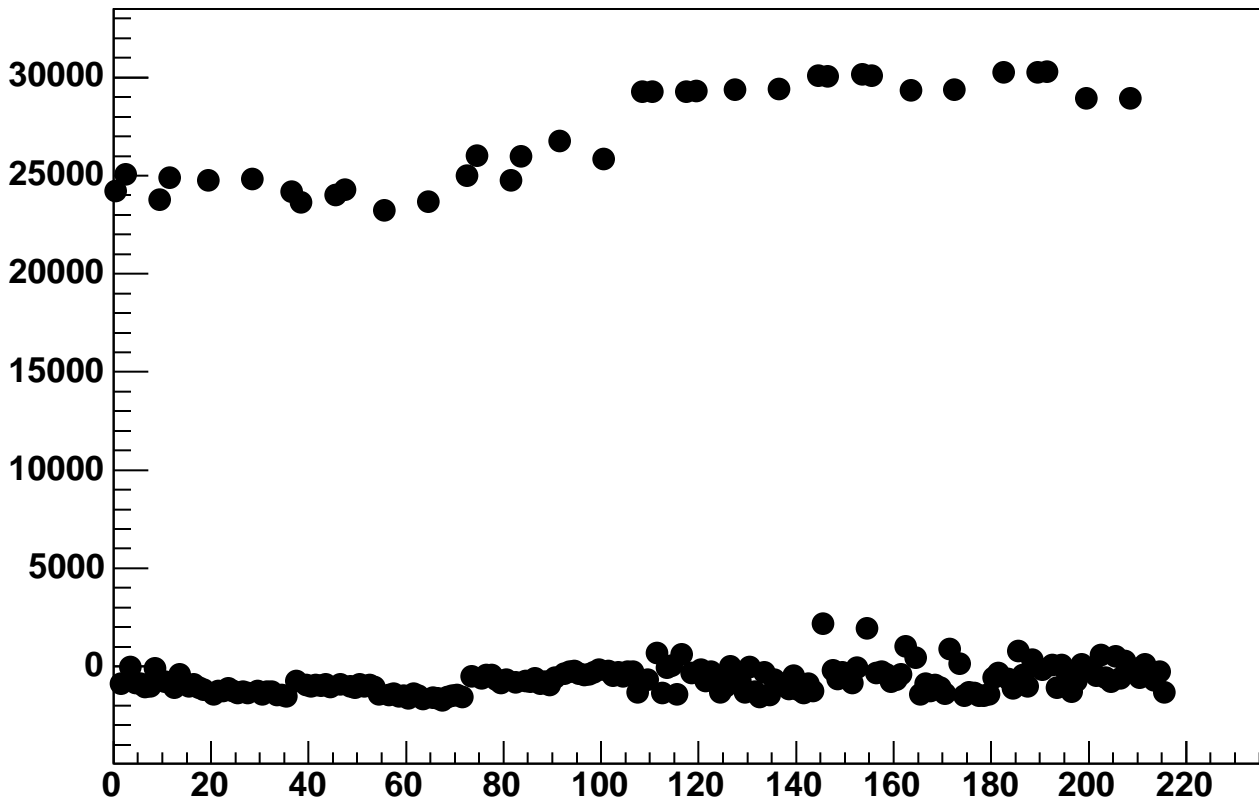
Enable 0, Hold=30, DAC=1350, ADC Mean vs 18\*Chip+Chan



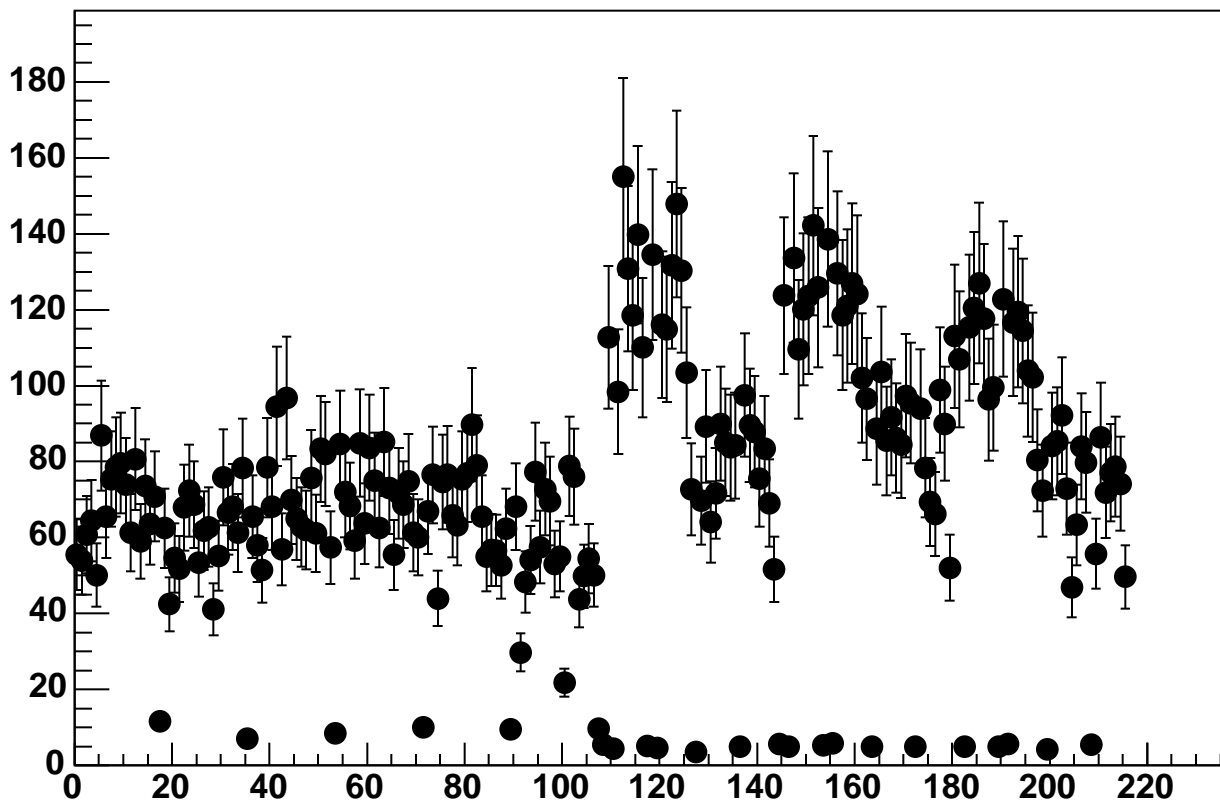
Enable 0, Hold=30, DAC=1350, ADC Noise vs 18\*Chip+Chan



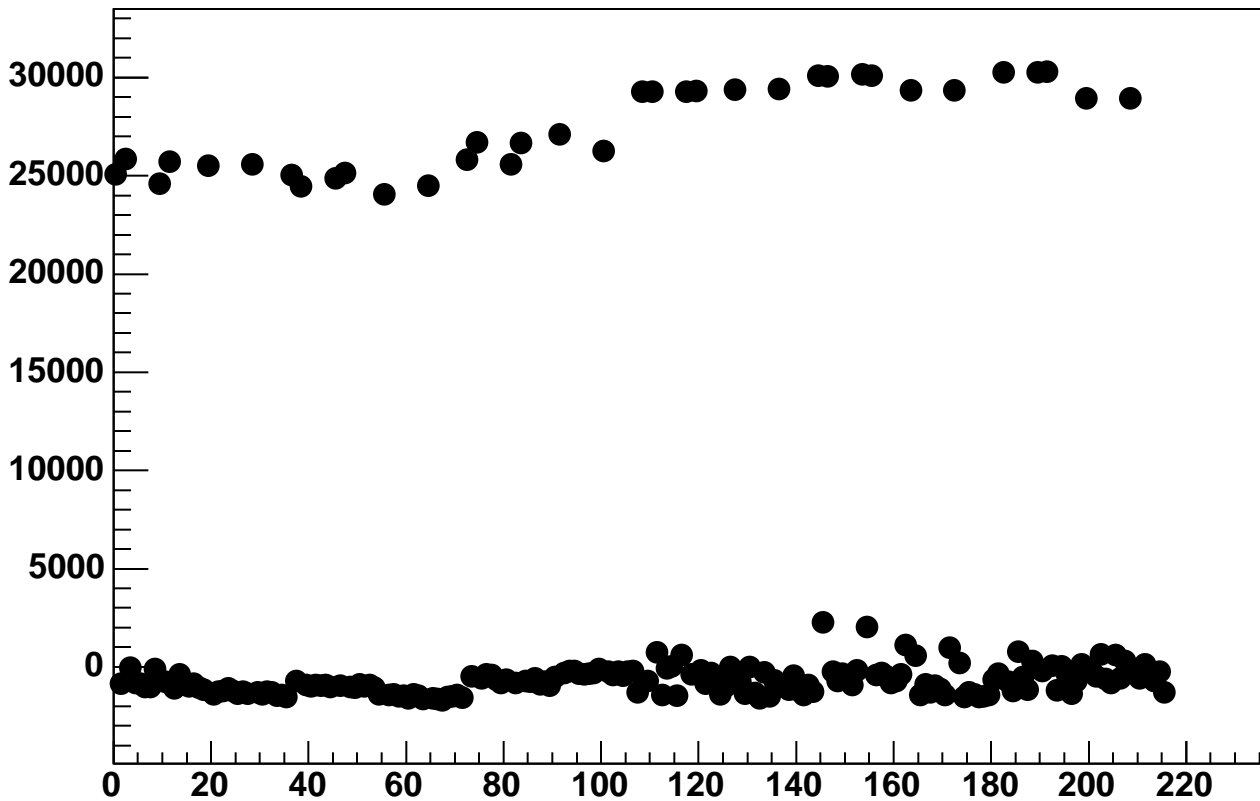
Enable 0, Hold=30, DAC=1400, ADC Mean vs 18\*Chip+Chan



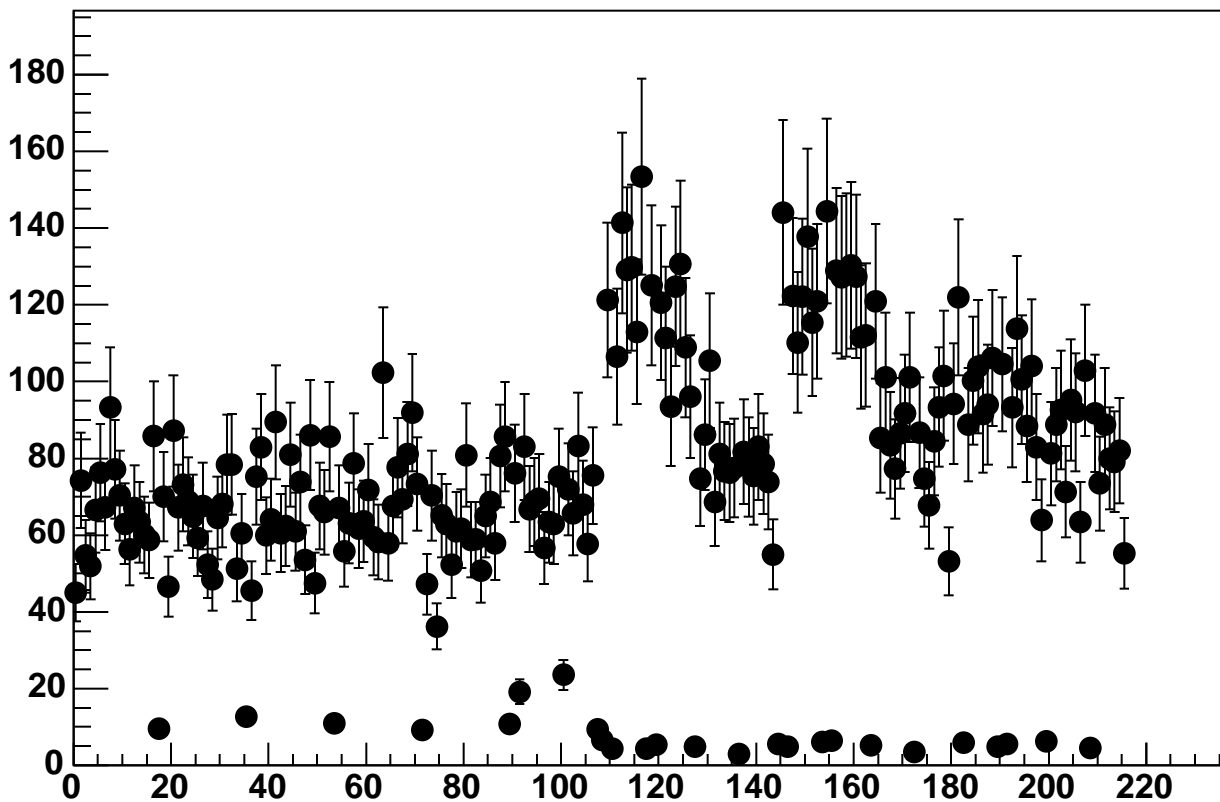
Enable 0, Hold=30, DAC=1400, ADC Noise vs 18\*Chip+Chan



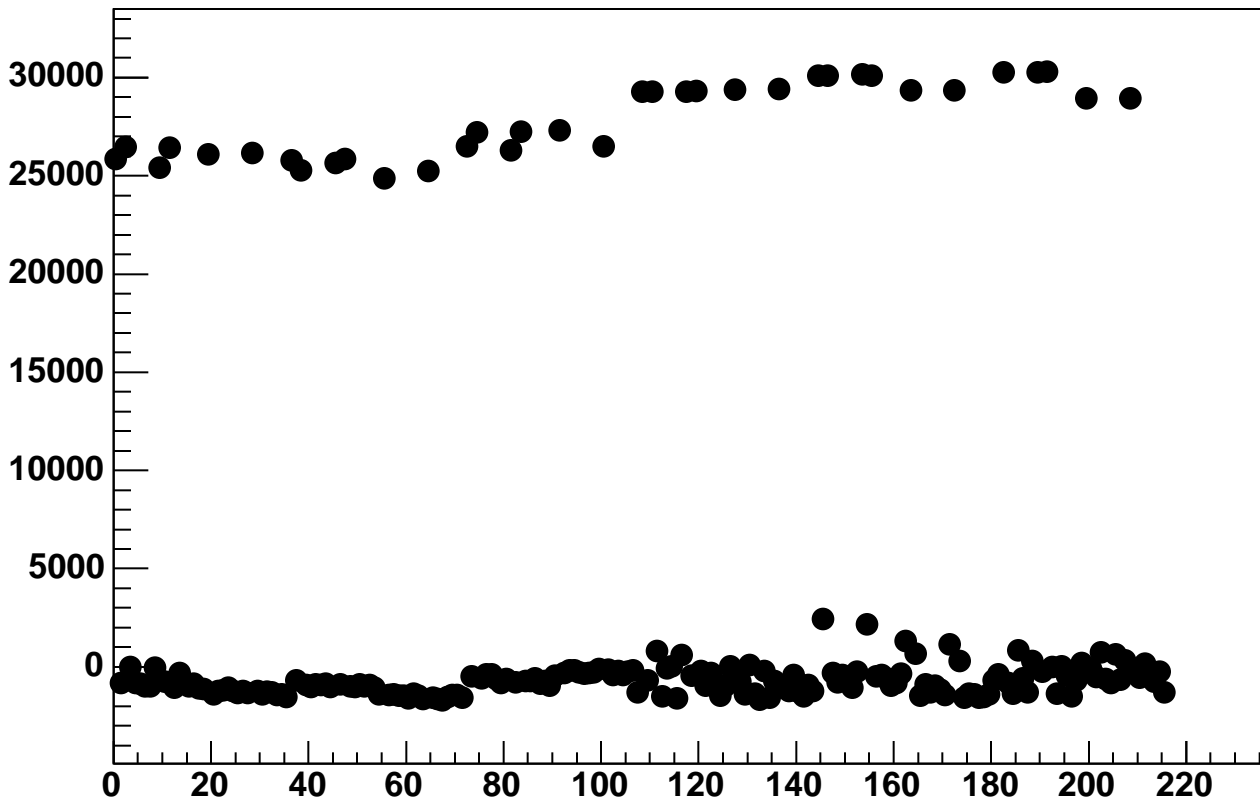
Enable 0, Hold=30, DAC=1450, ADC Mean vs 18\*Chip+Chan



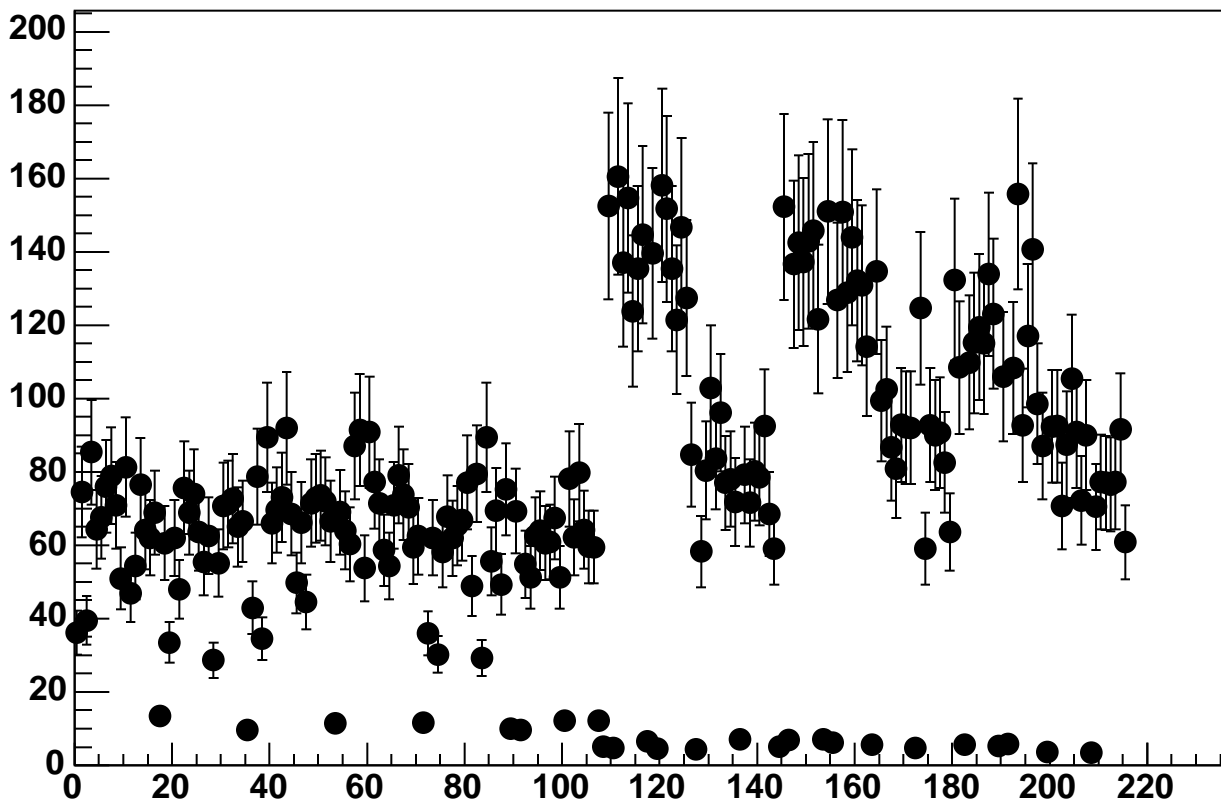
Enable 0, Hold=30, DAC=1450, ADC Noise vs 18\*Chip+Chan



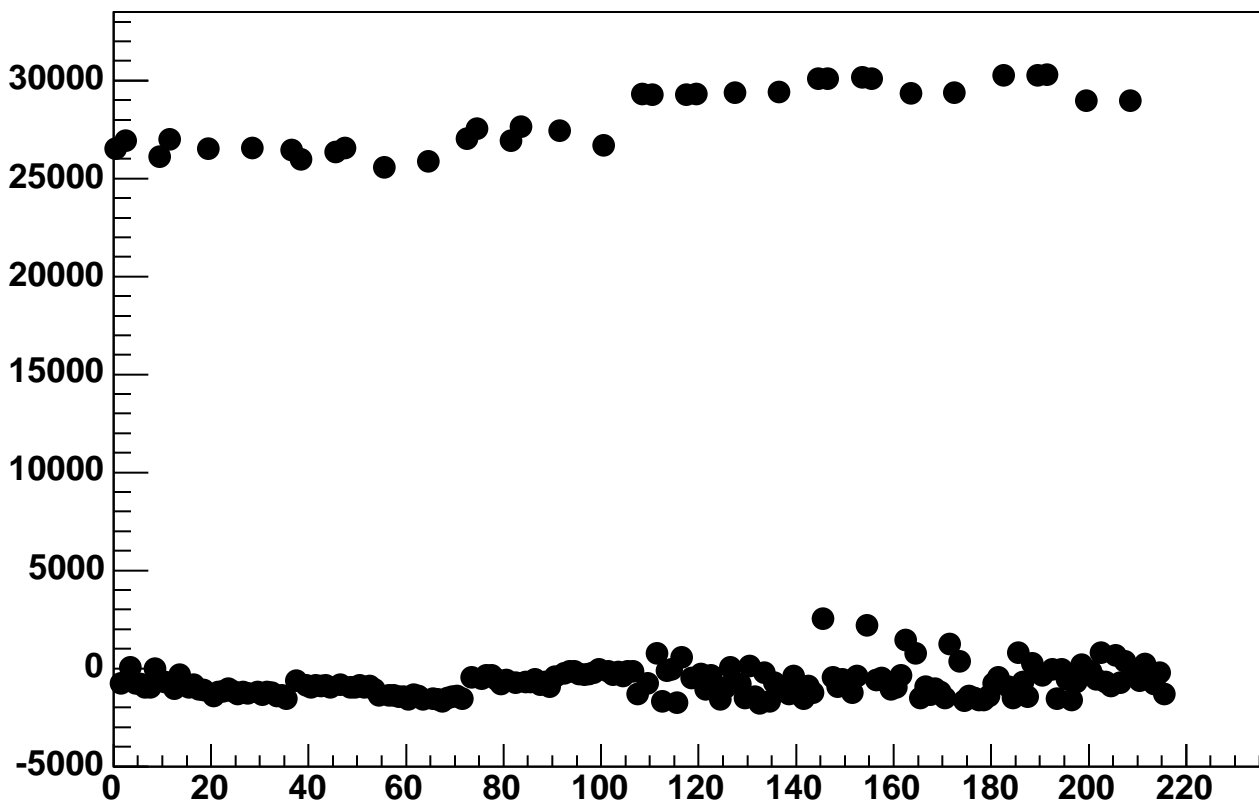
Enable 0, Hold=30, DAC=1500, ADC Mean vs 18\*Chip+Chan



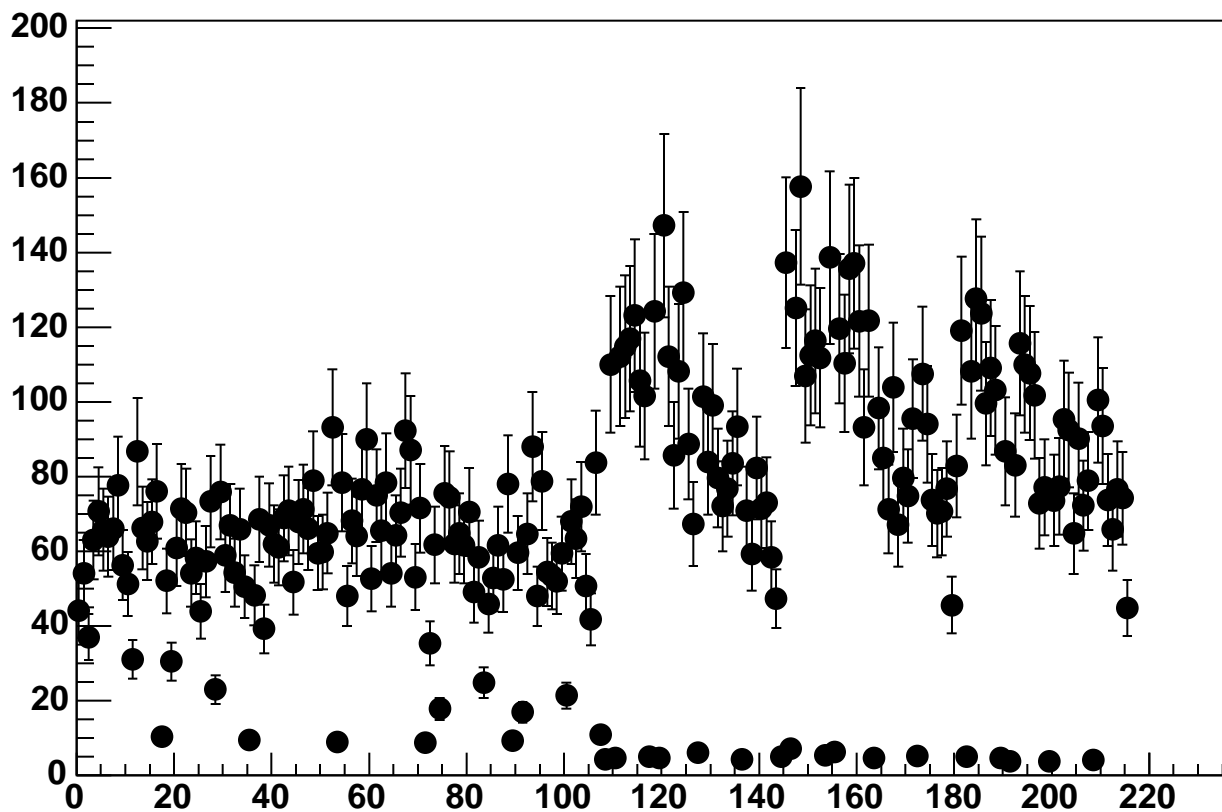
Enable 0, Hold=30, DAC=1500, ADC Noise vs 18\*Chip+Chan



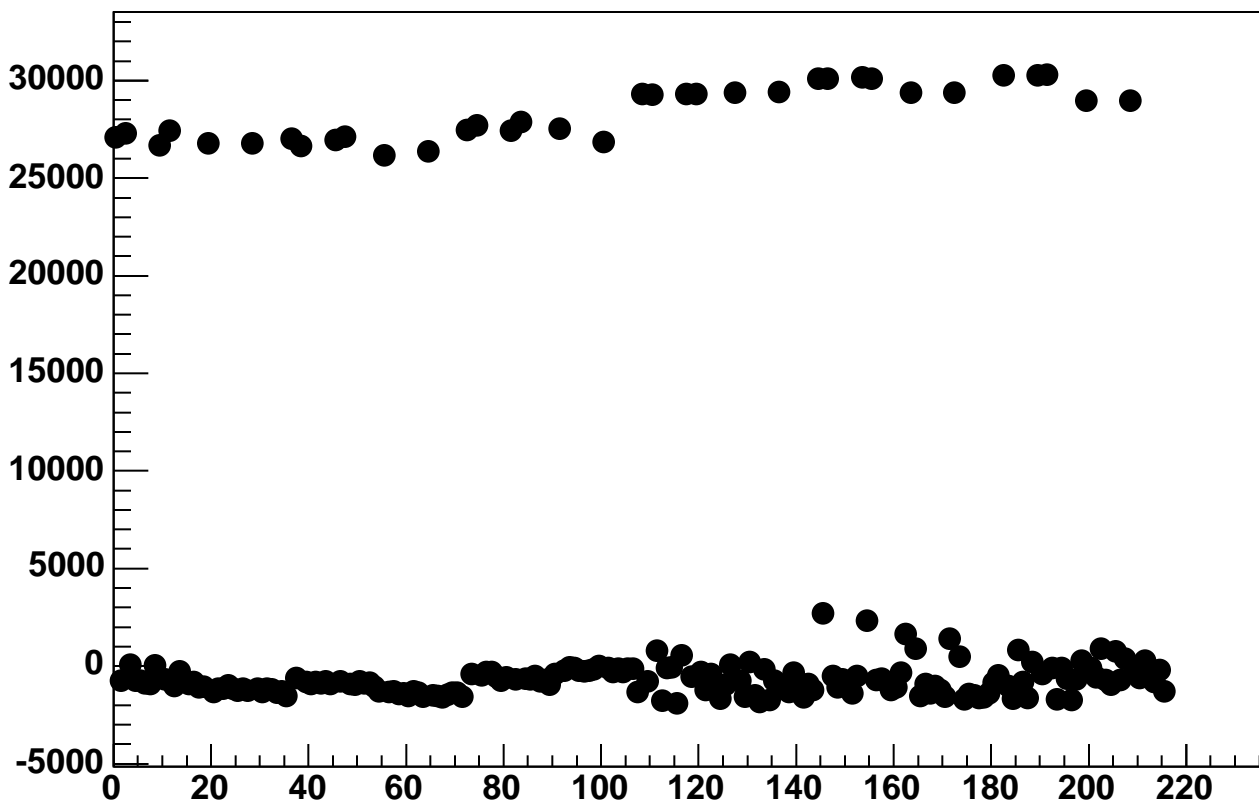
Enable 0, Hold=30, DAC=1550, ADC Mean vs 18\*Chip+Chan



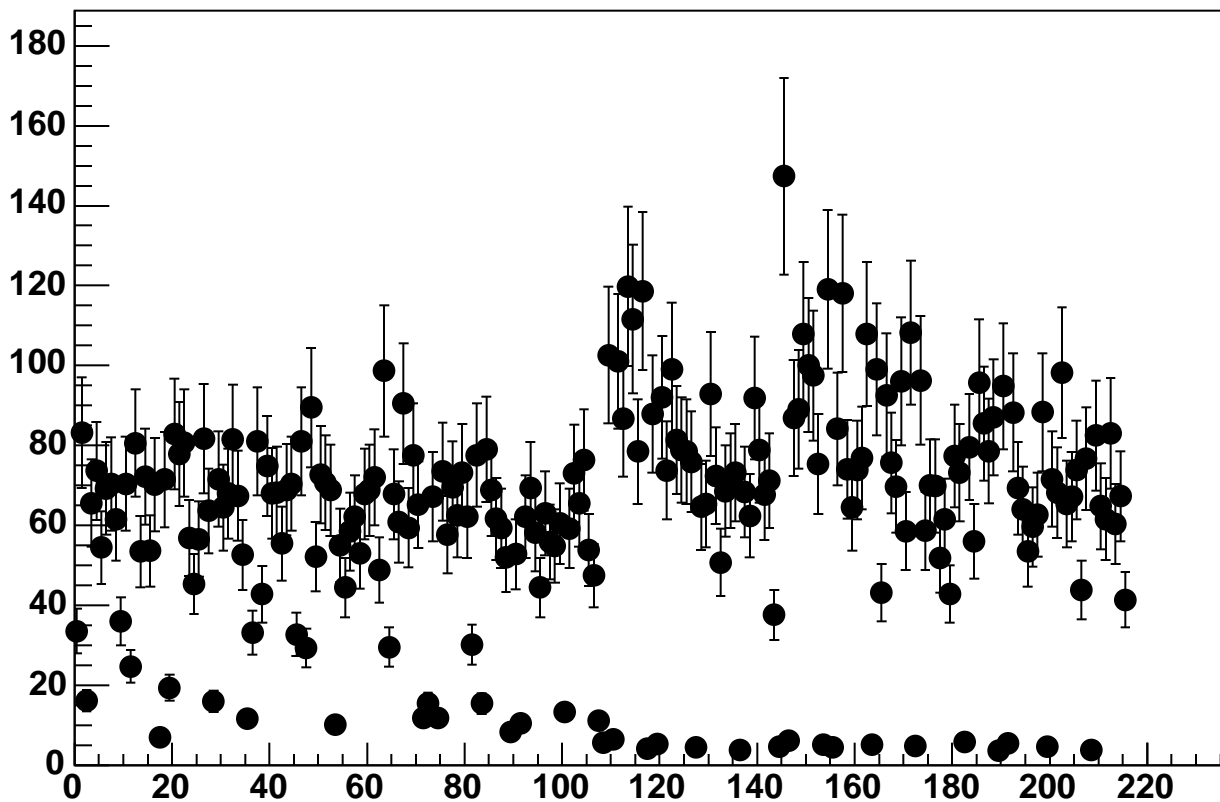
Enable 0, Hold=30, DAC=1550, ADC Noise vs 18\*Chip+Chan



Enable 0, Hold=30, DAC=1600, ADC Mean vs 18\*Chip+Chan

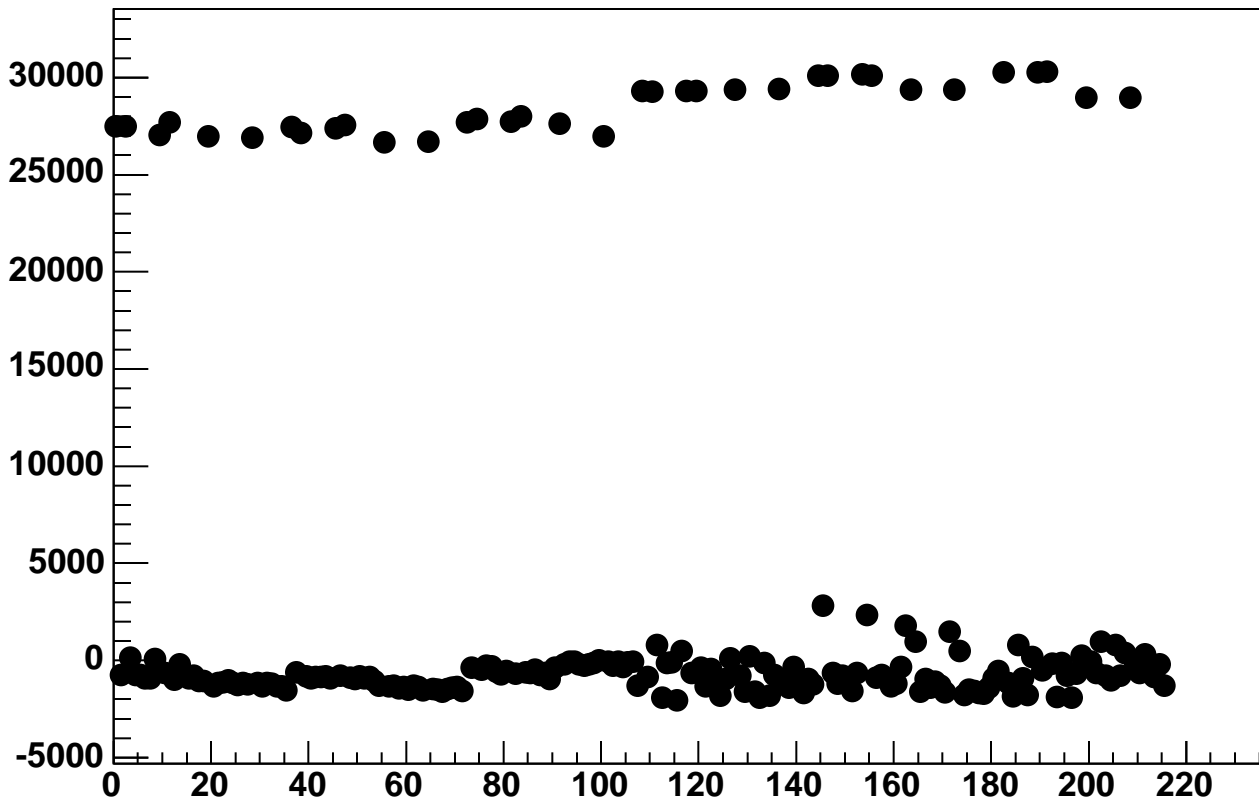


Enable 0, Hold=30, DAC=1600, ADC Noise vs 18\*Chip+Chan

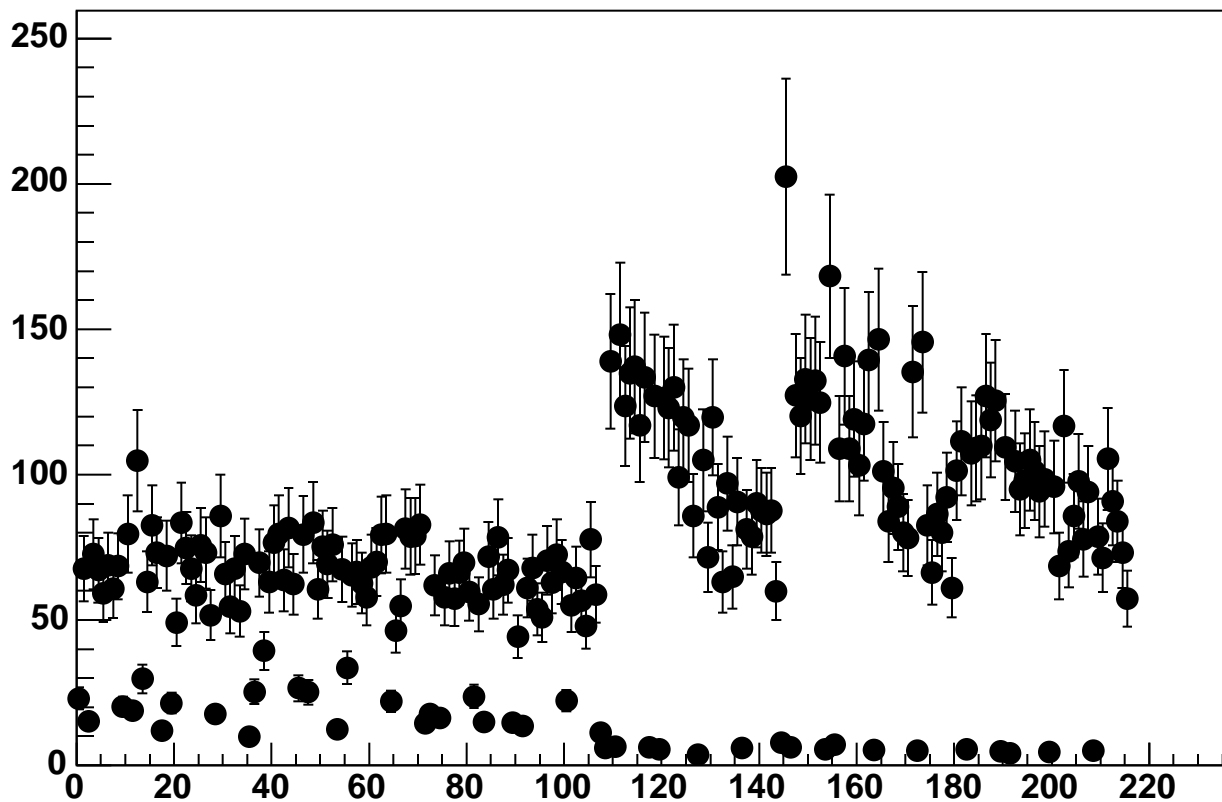




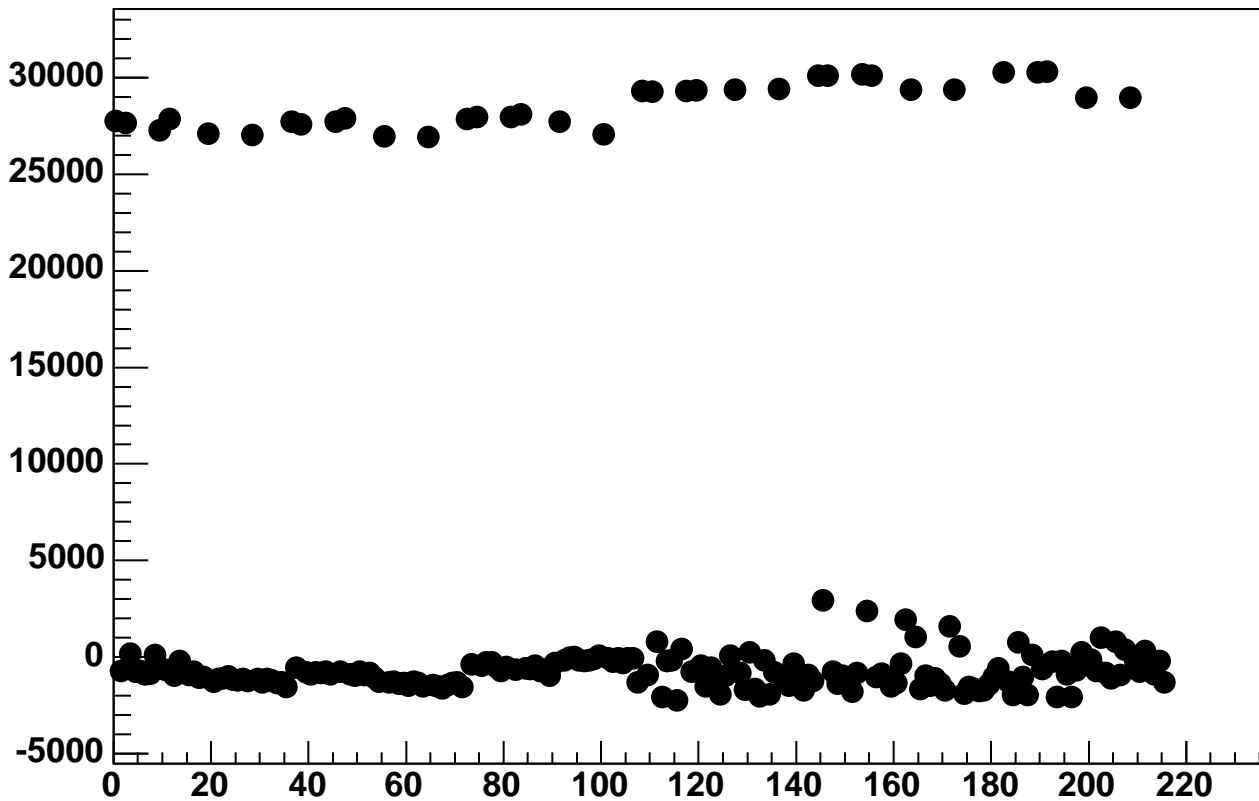
Enable 0, Hold=30, DAC=1650, ADC Mean vs 18\*Chip+Chan



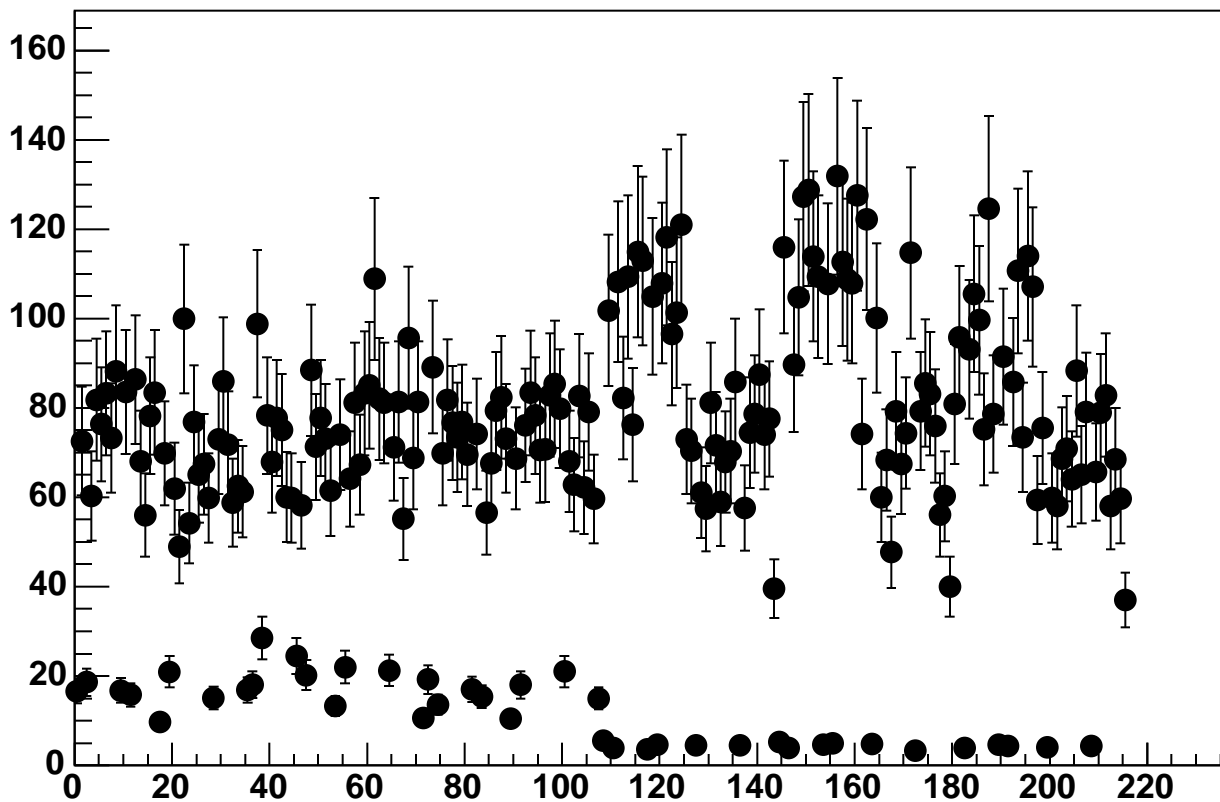
Enable 0, Hold=30, DAC=1650, ADC Noise vs 18\*Chip+Chan



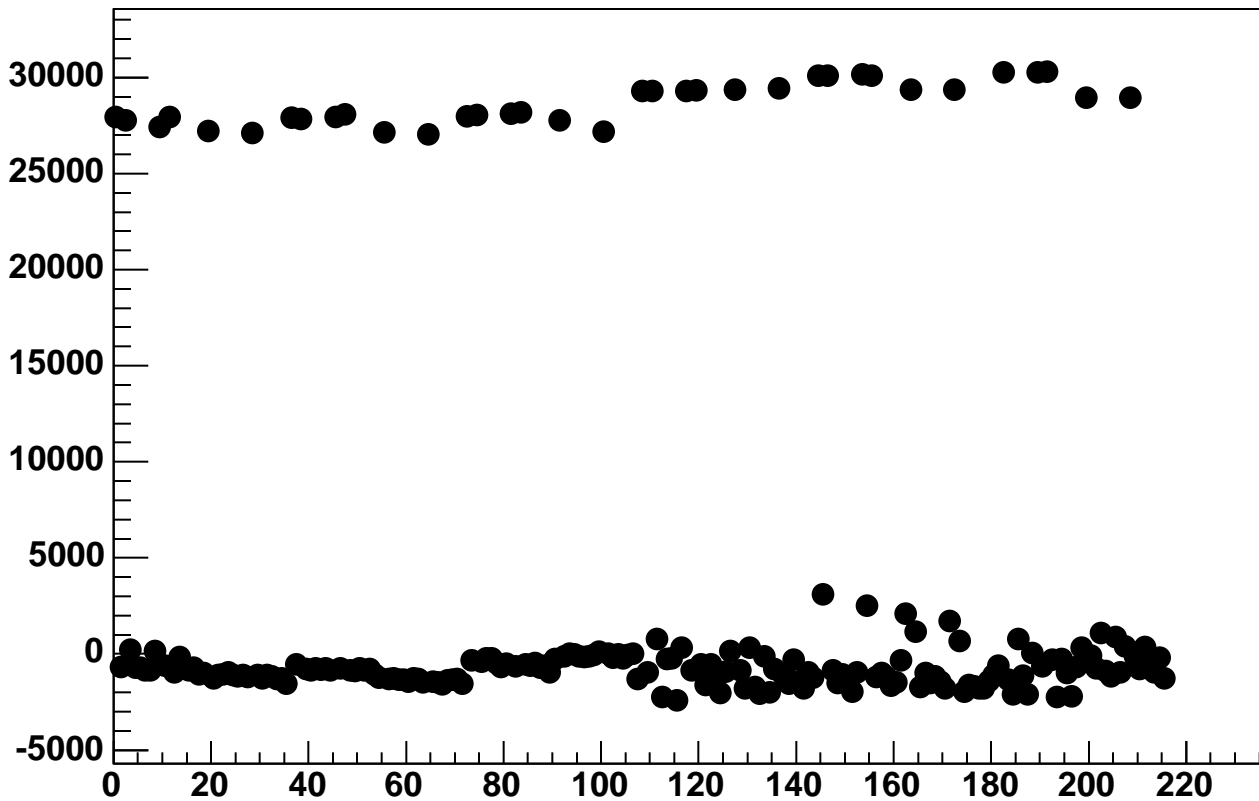
Enable 0, Hold=30, DAC=1700, ADC Mean vs 18\*Chip+Chan



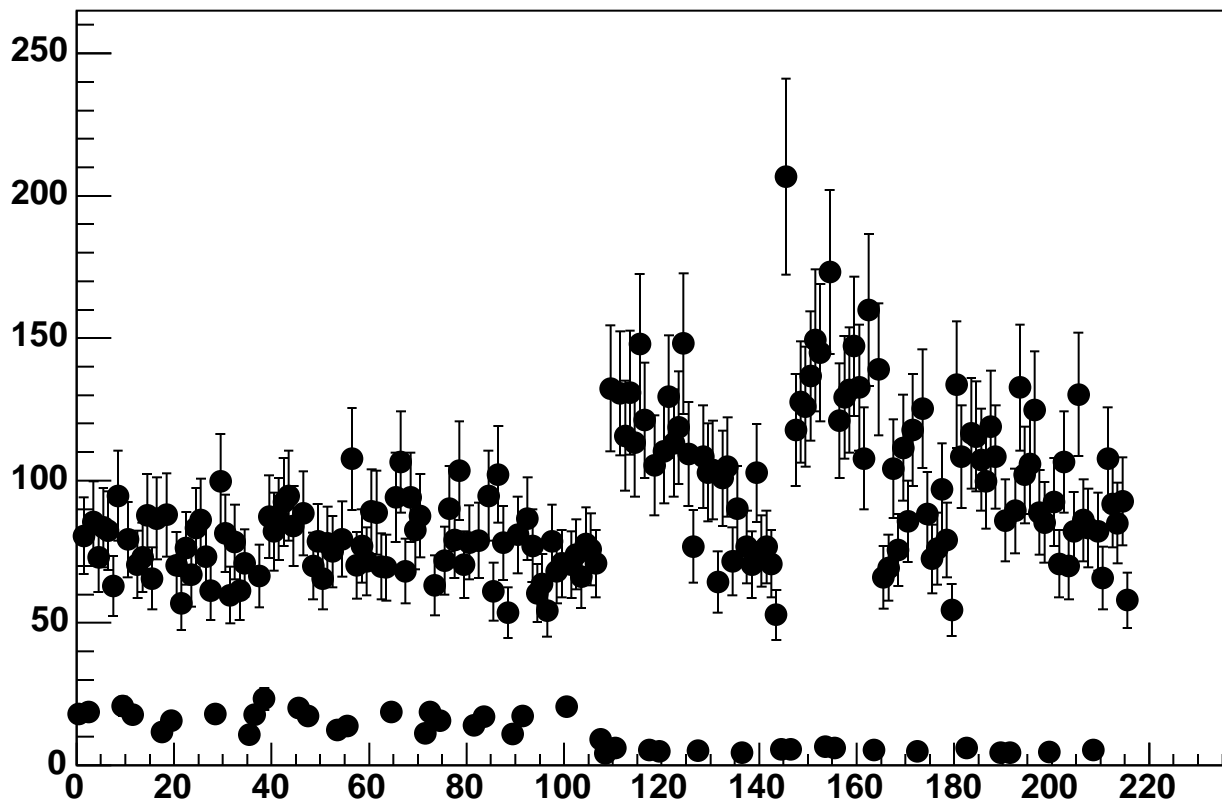
Enable 0, Hold=30, DAC=1700, ADC Noise vs 18\*Chip+Chan



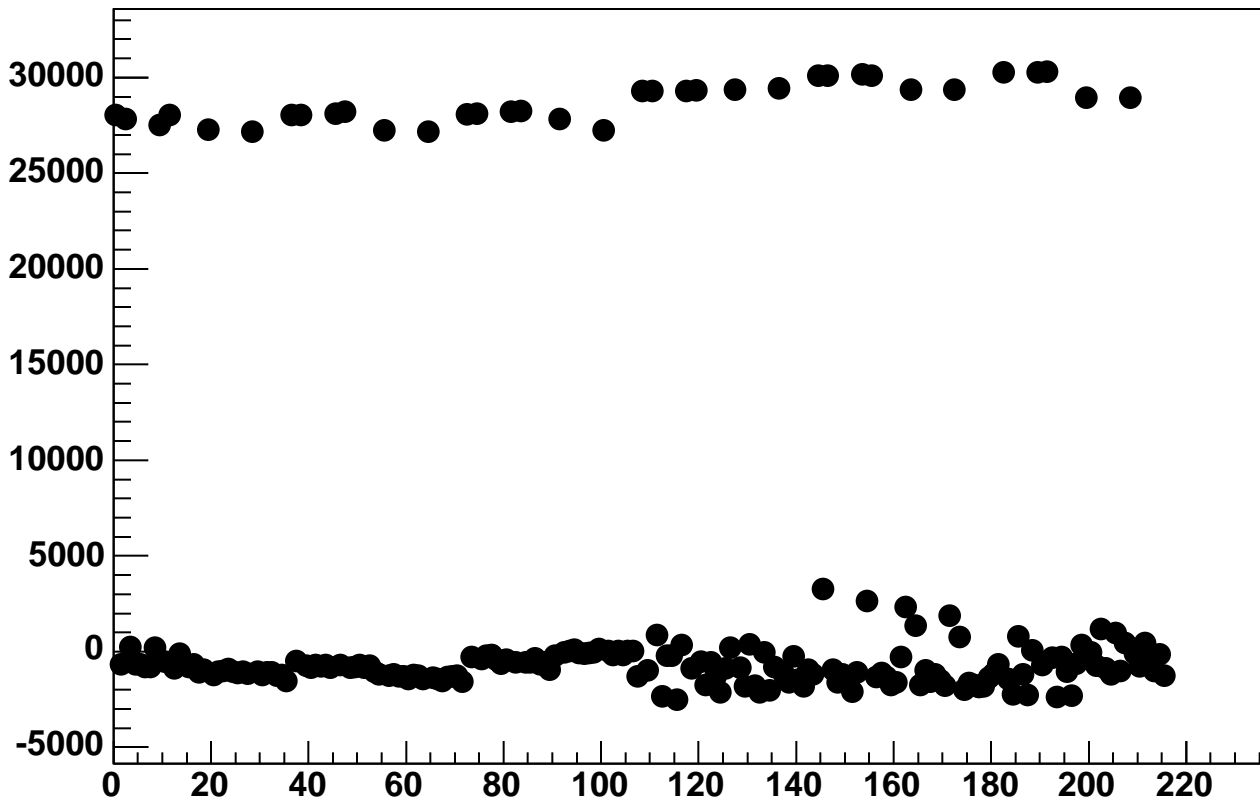
Enable 0, Hold=30, DAC=1750, ADC Mean vs 18\*Chip+Chan



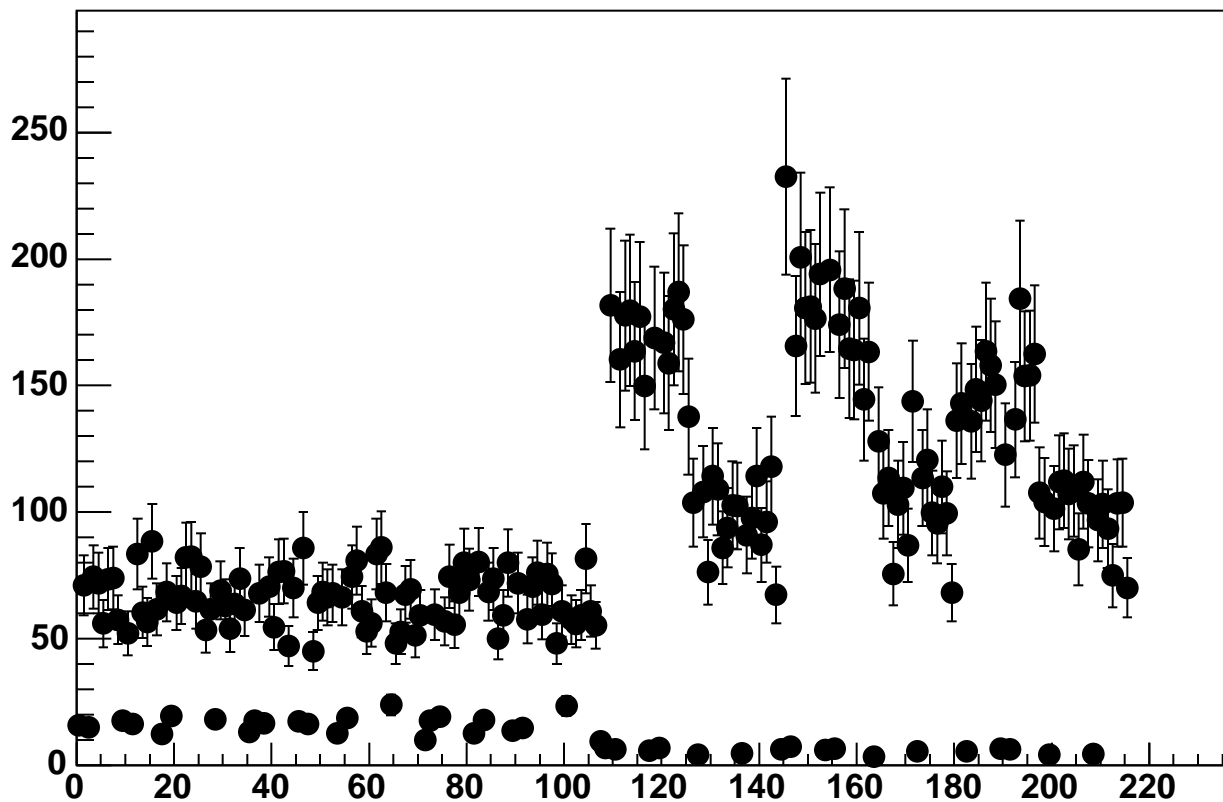
Enable 0, Hold=30, DAC=1750, ADC Noise vs 18\*Chip+Chan



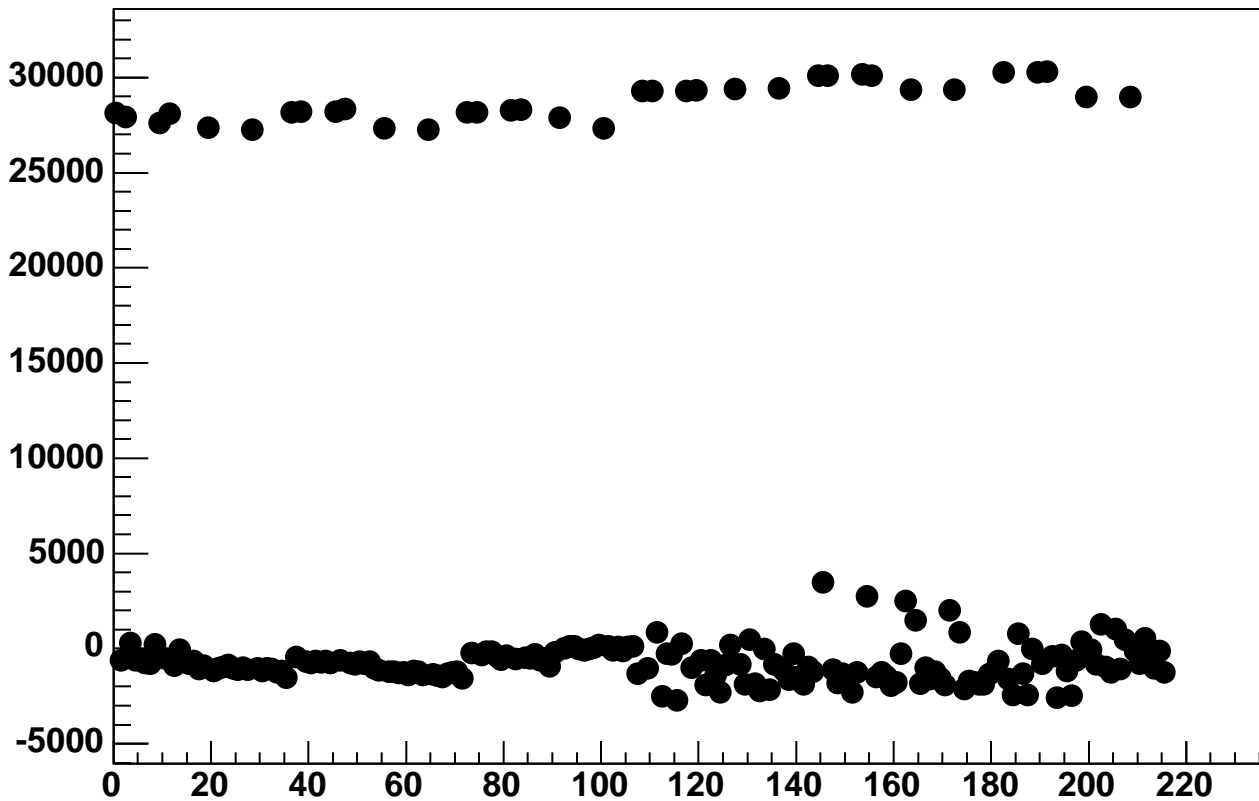
Enable 0, Hold=30, DAC=1800, ADC Mean vs 18\*Chip+Chan



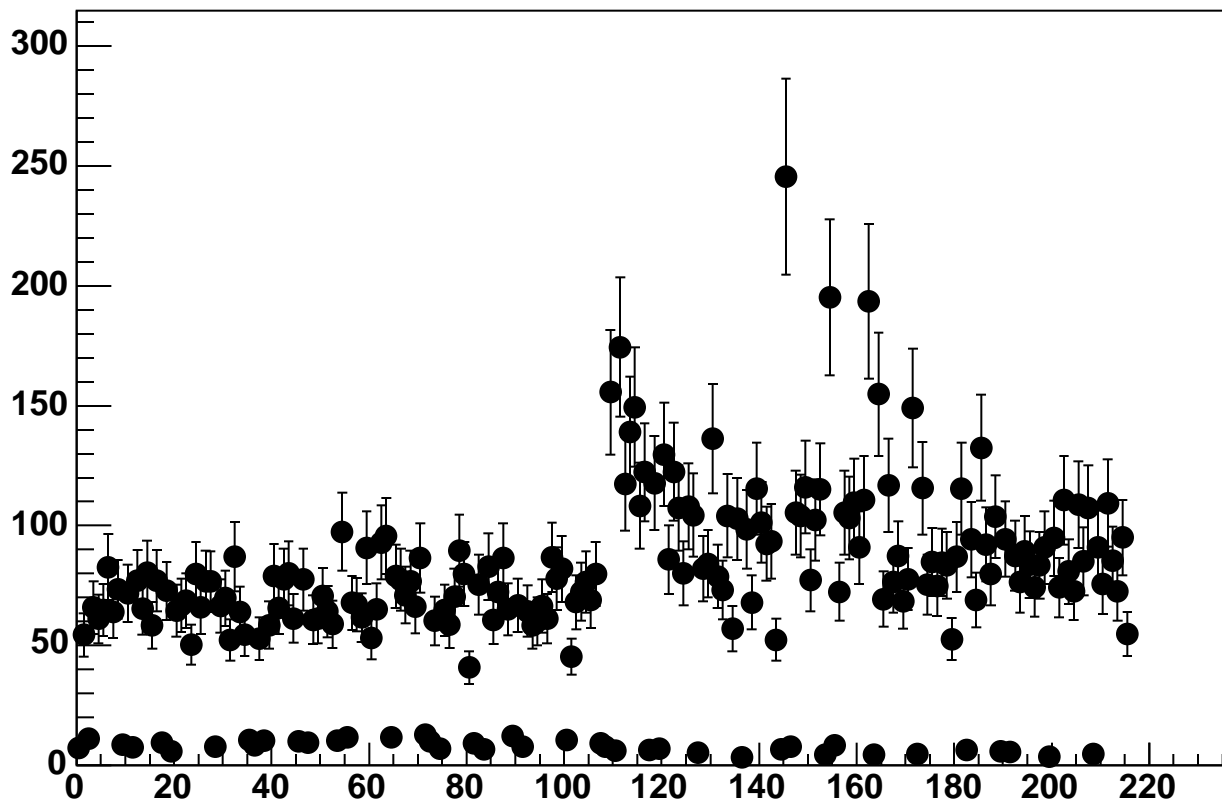
Enable 0, Hold=30, DAC=1800, ADC Noise vs 18\*Chip+Chan



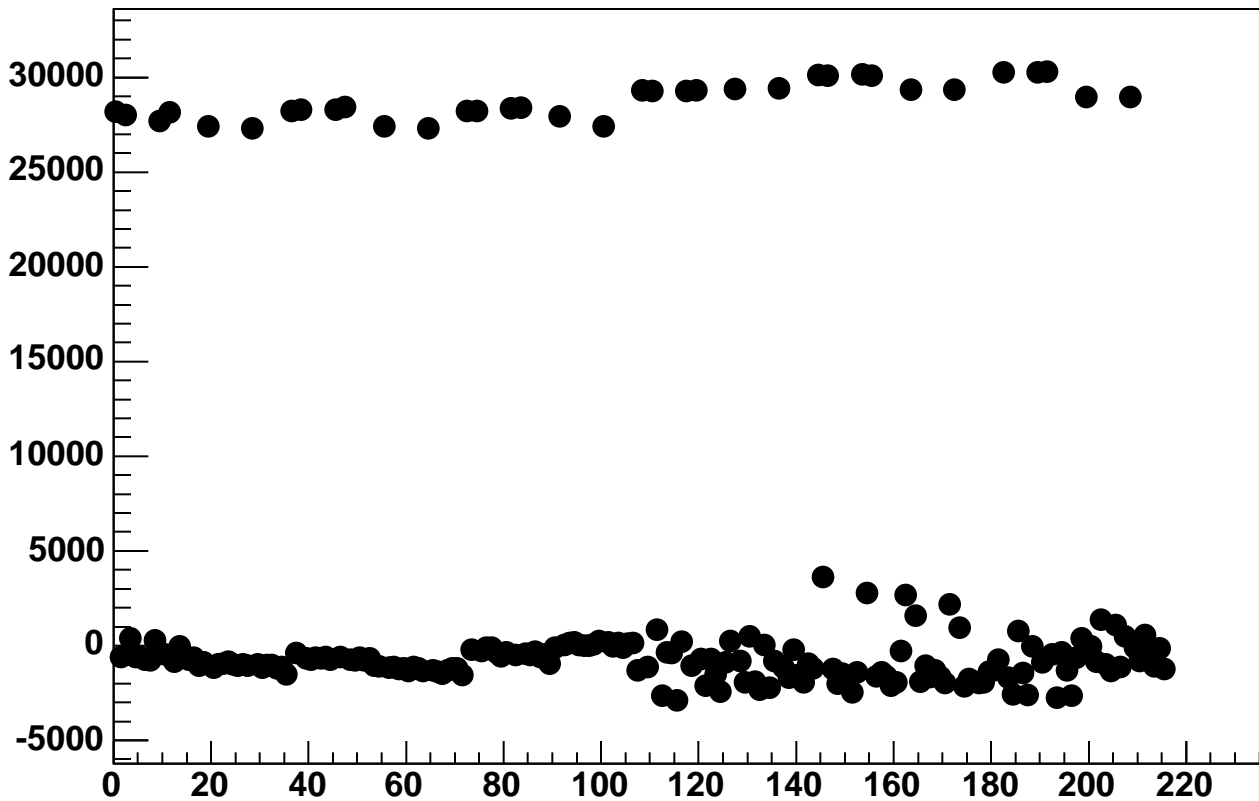
Enable 0, Hold=30, DAC=1850, ADC Mean vs 18\*Chip+Chan



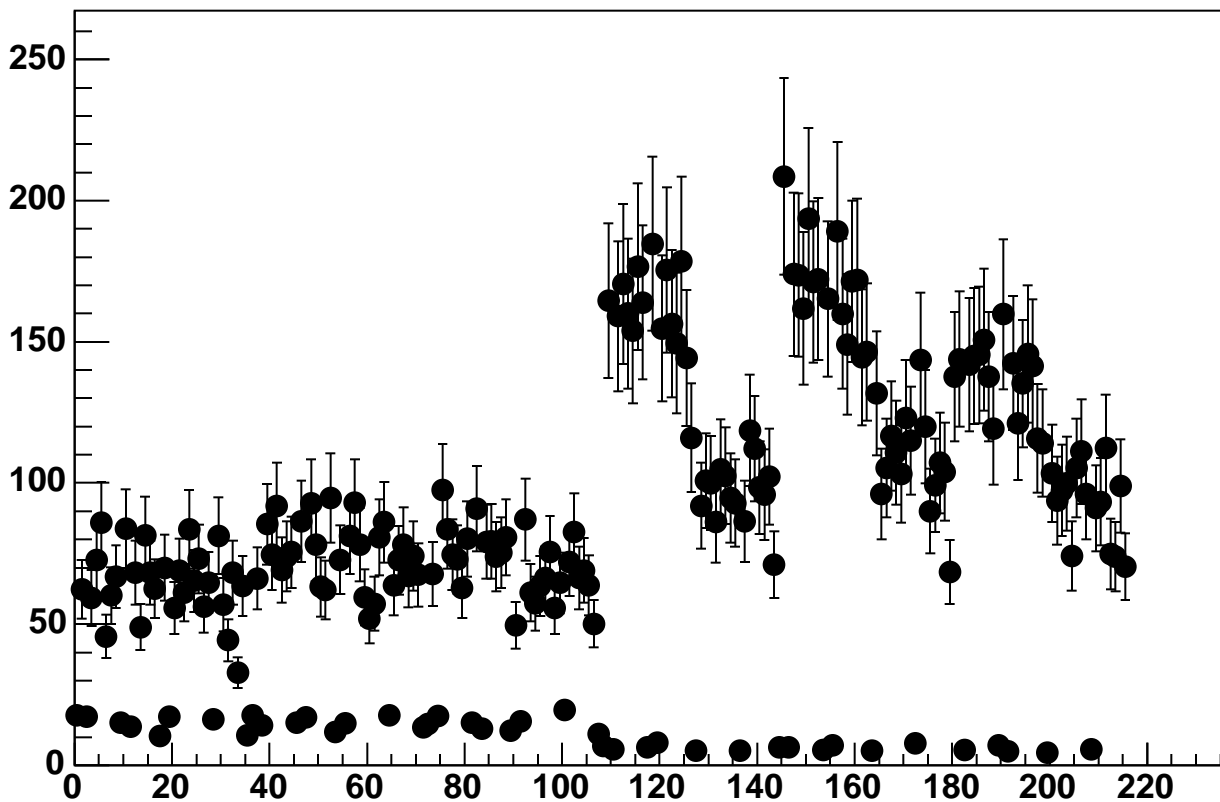
Enable 0, Hold=30, DAC=1850, ADC Noise vs 18\*Chip+Chan



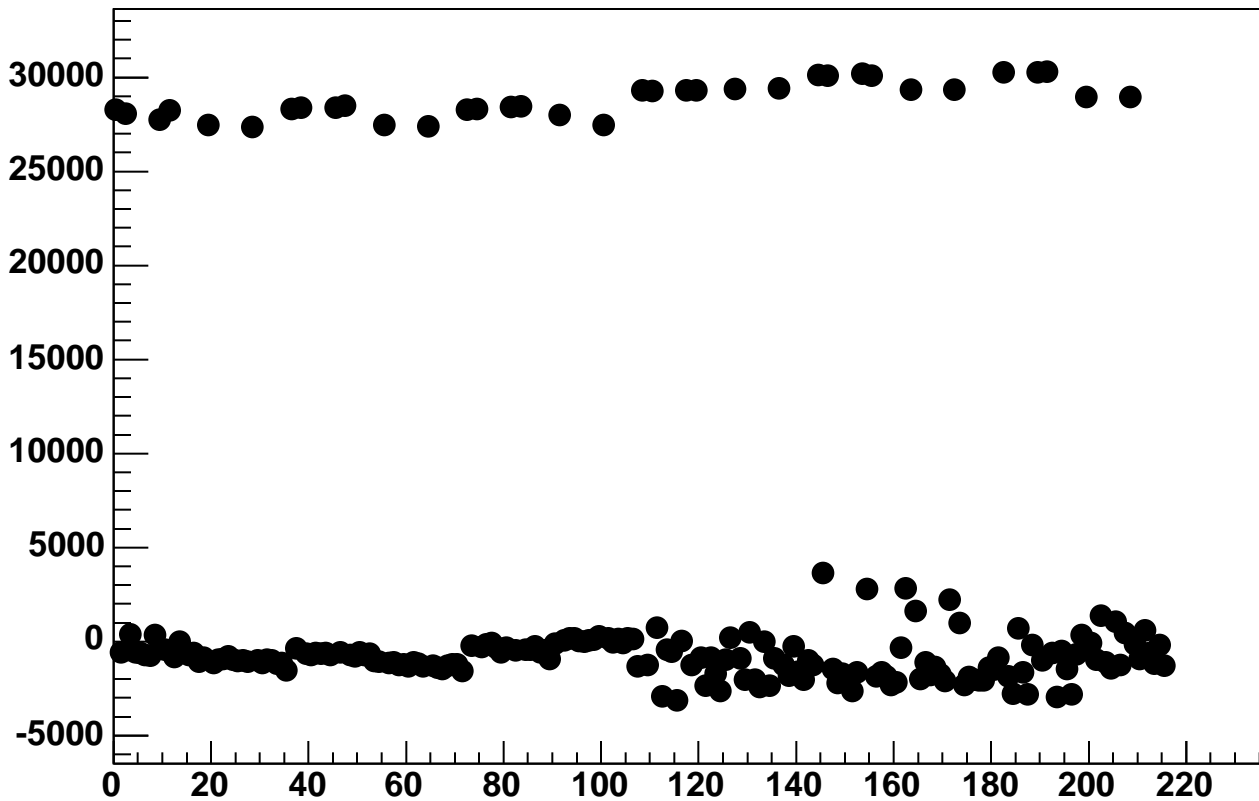
Enable 0, Hold=30, DAC=1900, ADC Mean vs 18\*Chip+Chan



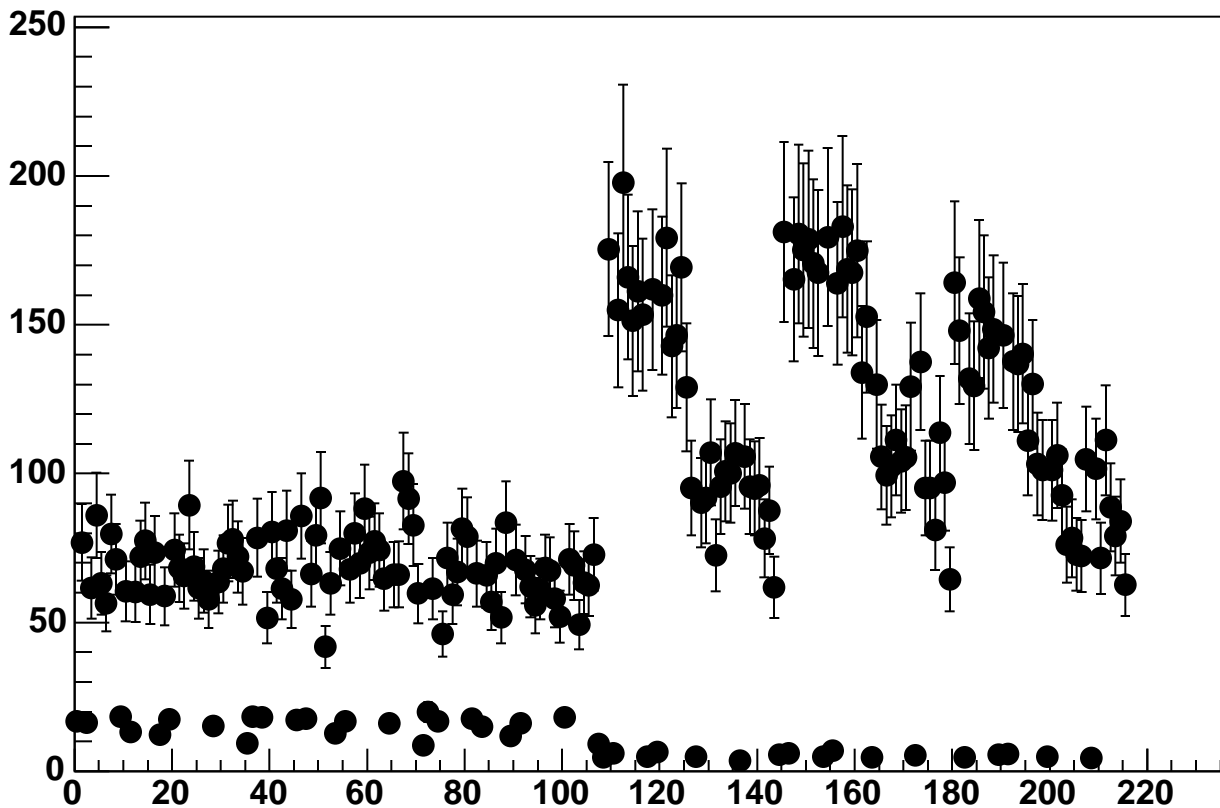
Enable 0, Hold=30, DAC=1900, ADC Noise vs 18\*Chip+Chan



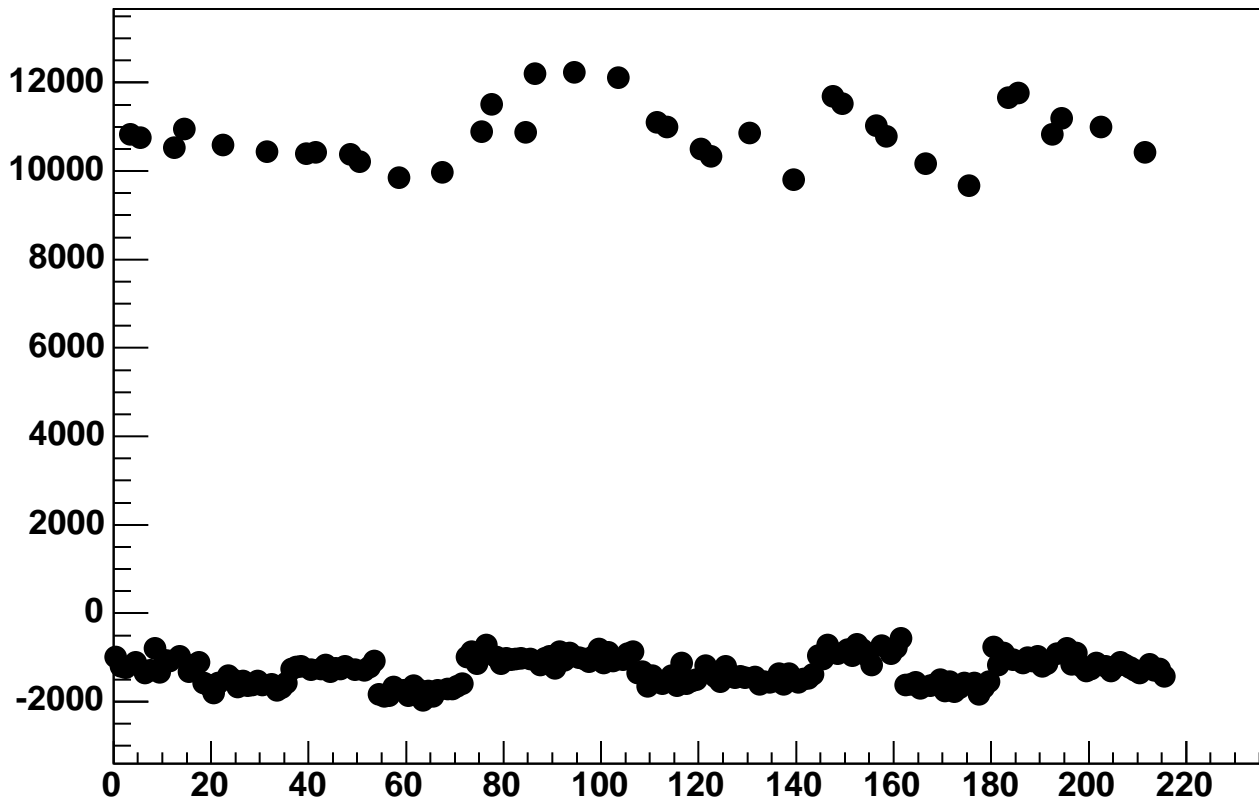
Enable 0, Hold=30, DAC=1950, ADC Mean vs 18\*Chip+Chan



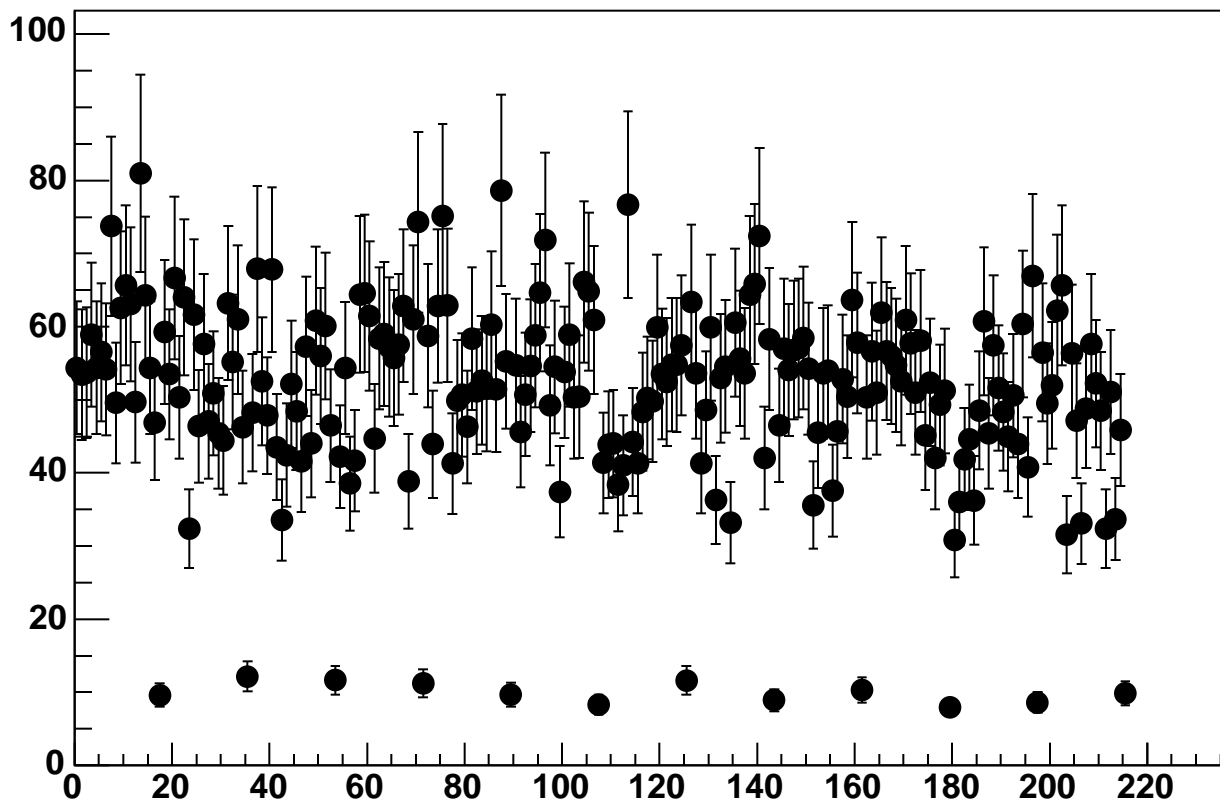
Enable 0, Hold=30, DAC=1950, ADC Noise vs 18\*Chip+Chan



Enable 1, Hold=30, DAC=0, ADC Mean vs 18\*Chip+Chan

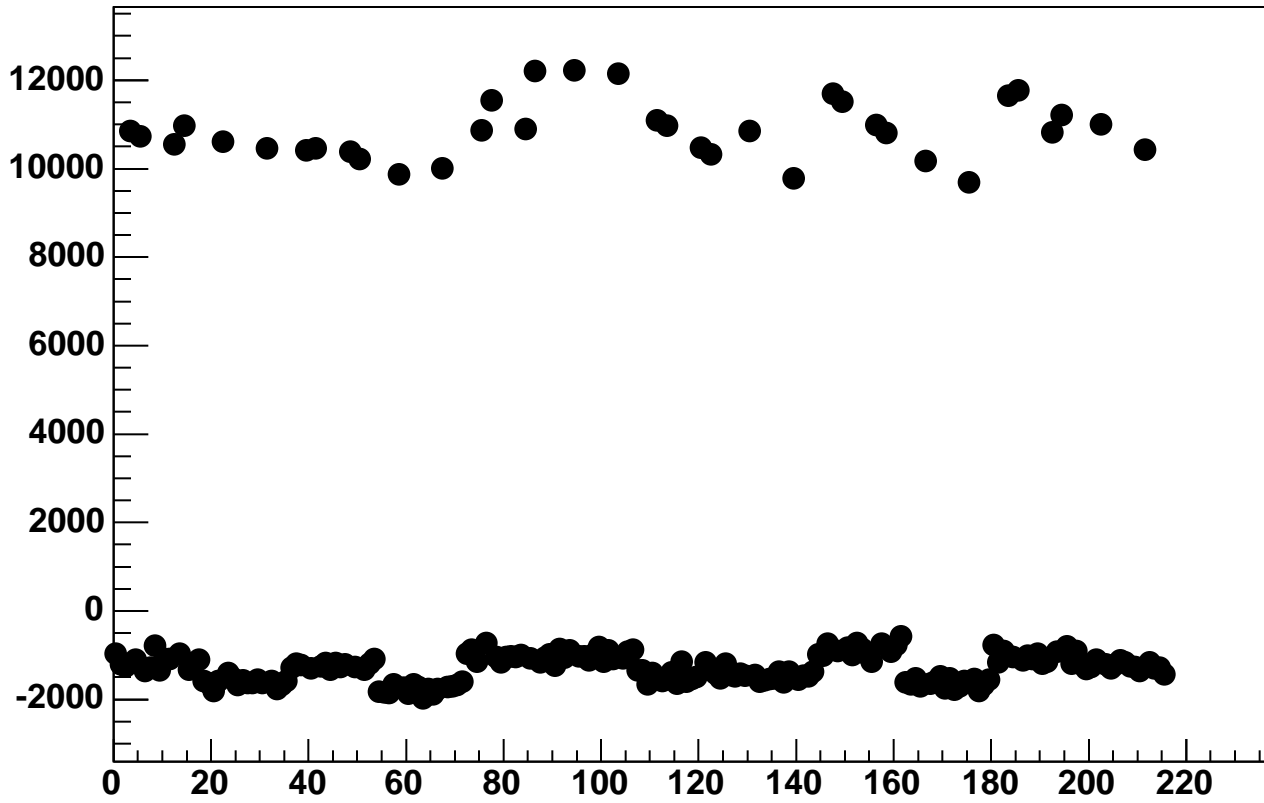


Enable 1, Hold=30, DAC=0, ADC Noise vs 18\*Chip+Chan

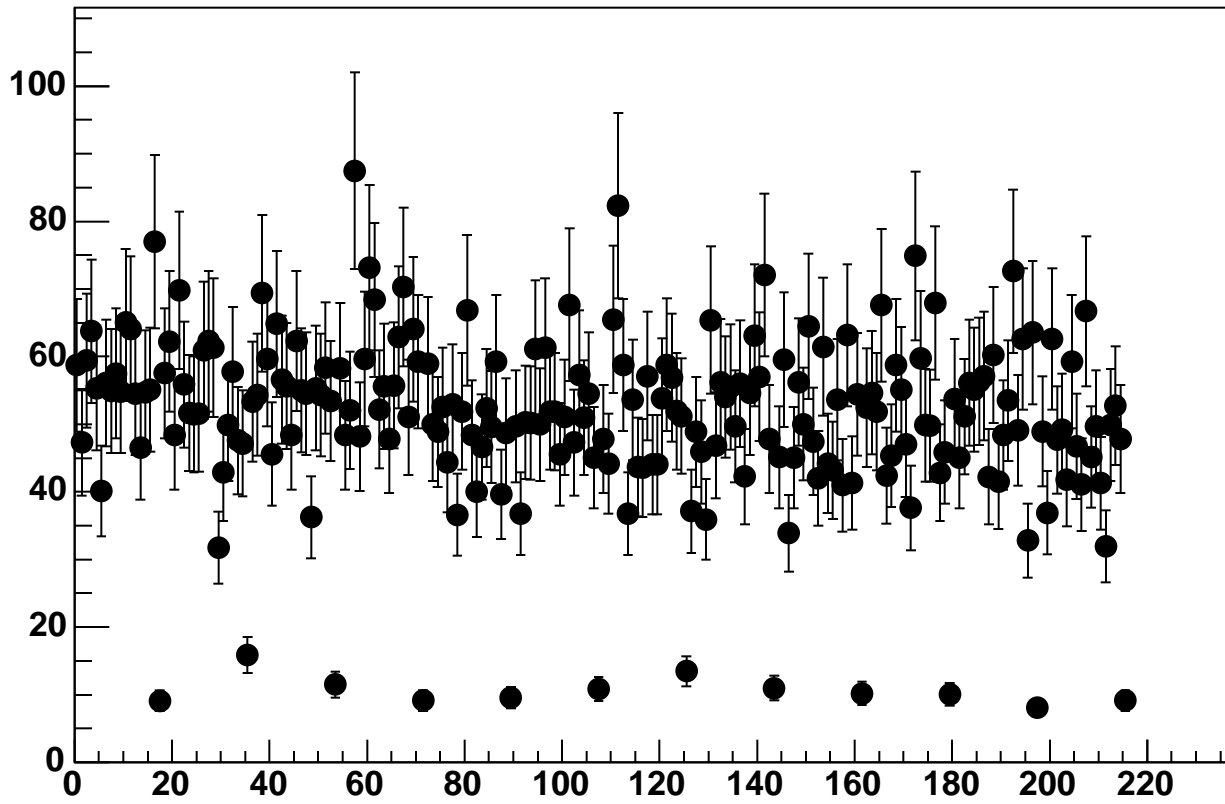




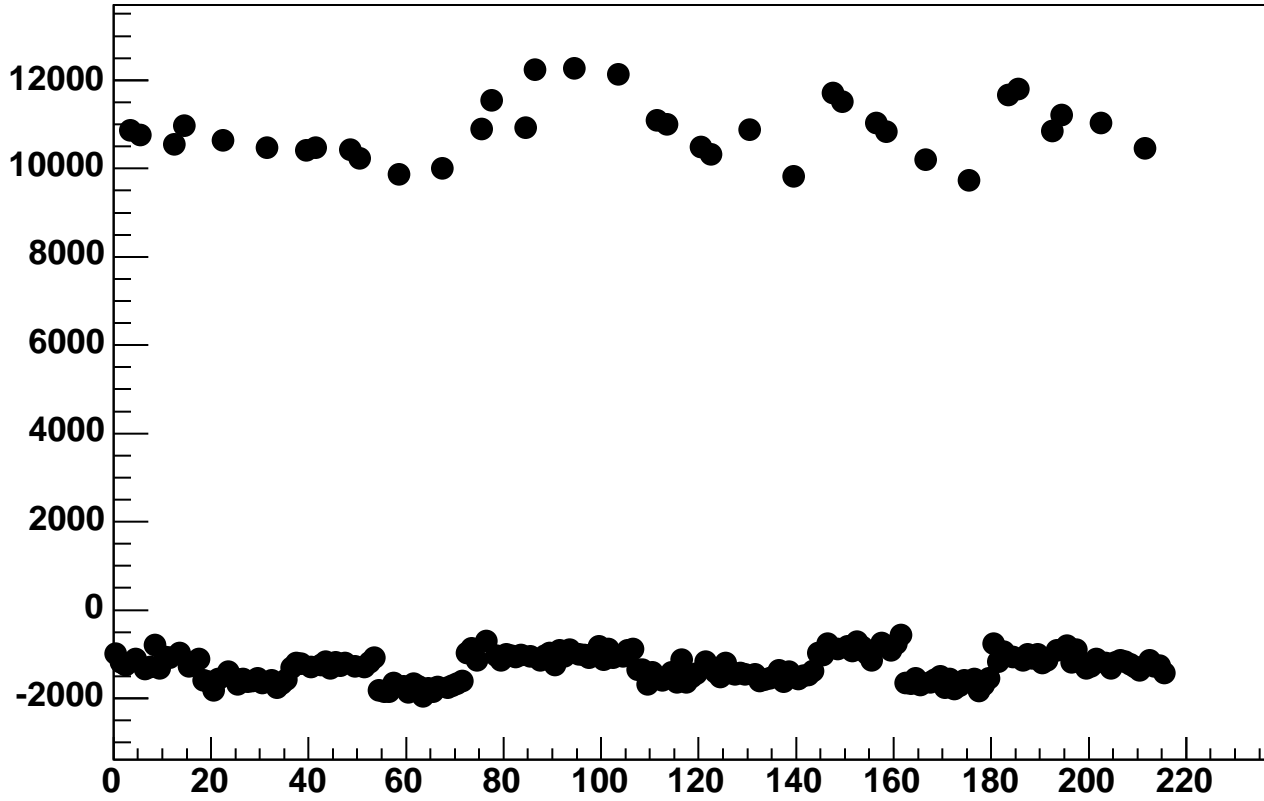
Enable 1, Hold=30, DAC=50, ADC Mean vs 18\*Chip+Chan



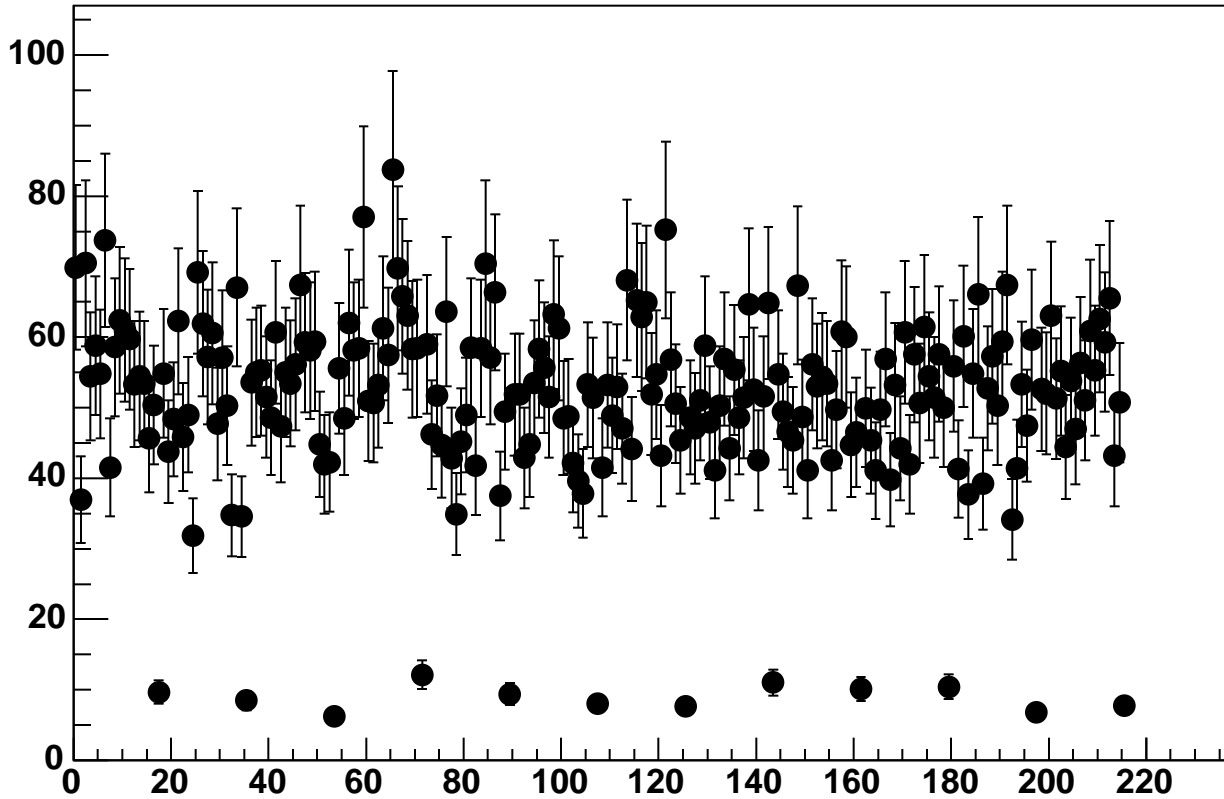
Enable 1, Hold=30, DAC=50, ADC Noise vs 18\*Chip+Chan



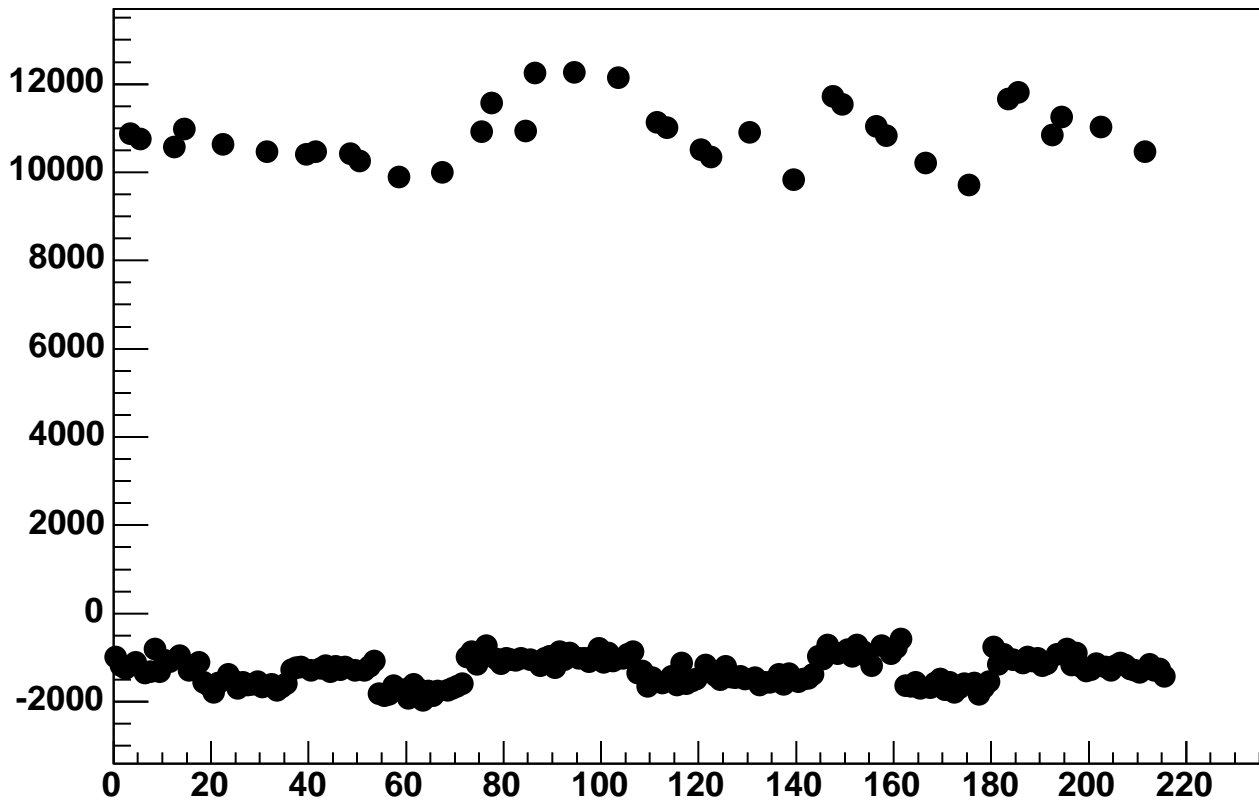
Enable 1, Hold=30, DAC=100, ADC Mean vs 18\*Chip+Chan



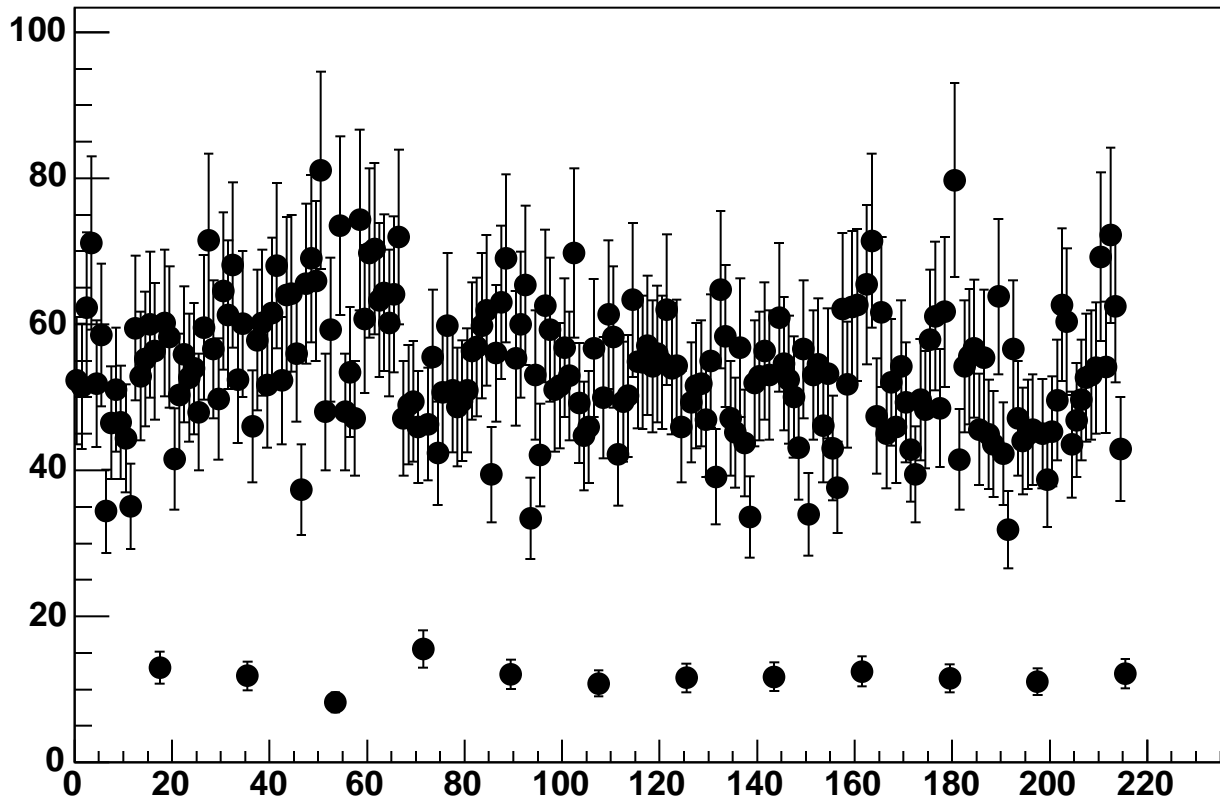
Enable 1, Hold=30, DAC=100, ADC Noise vs 18\*Chip+Chan



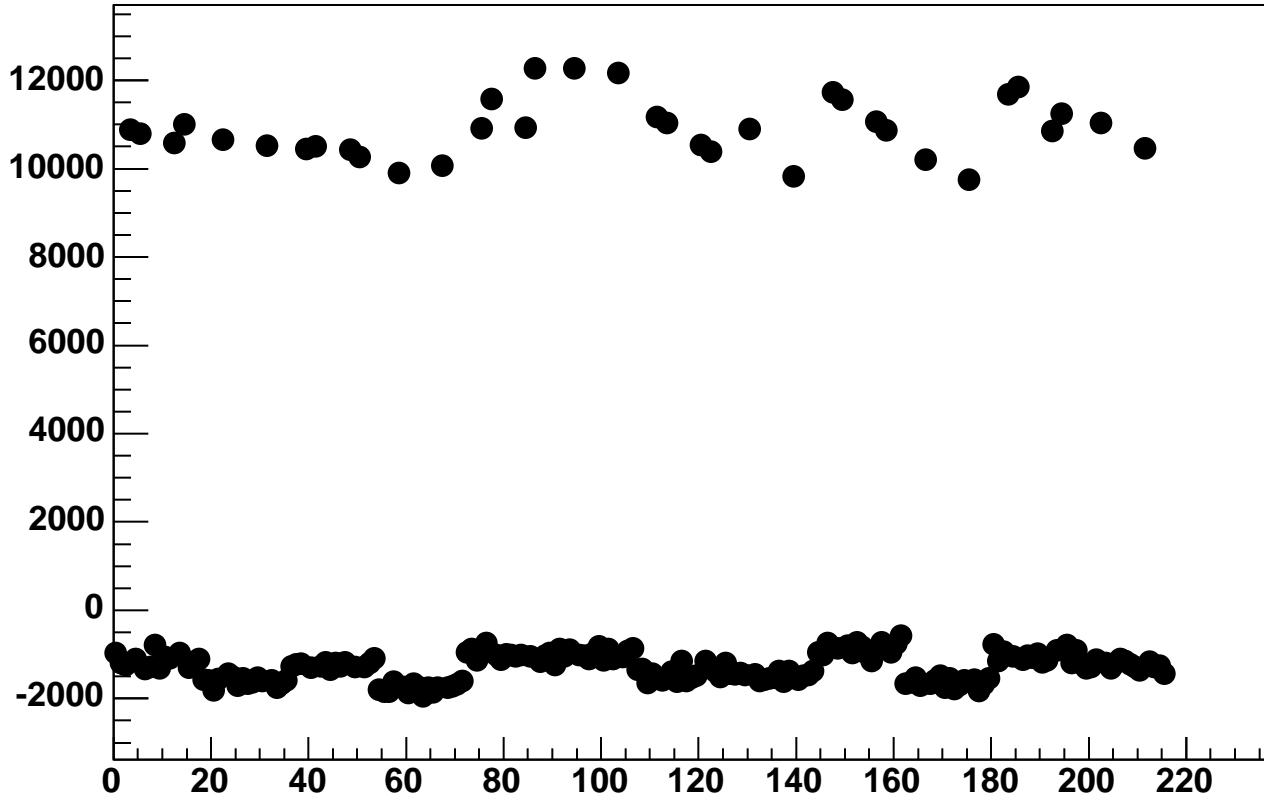
Enable 1, Hold=30, DAC=150, ADC Mean vs 18\*Chip+Chan



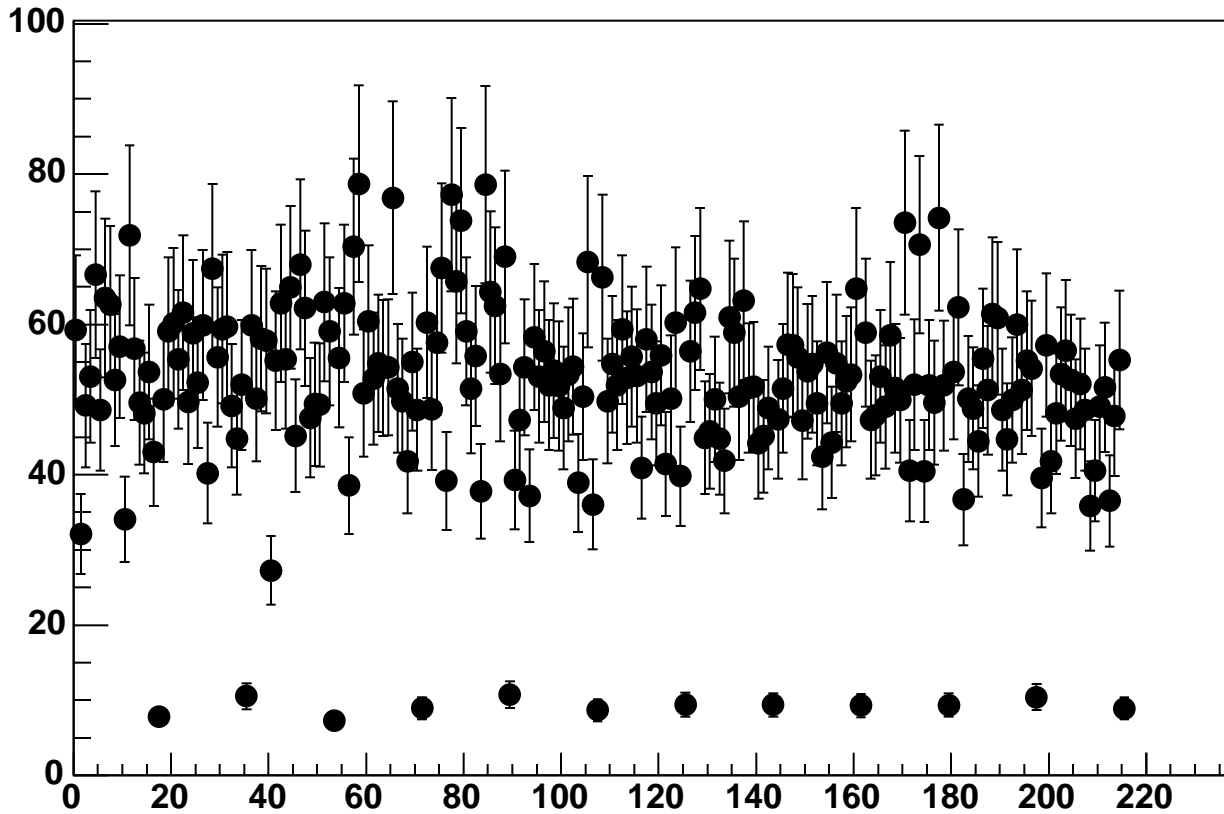
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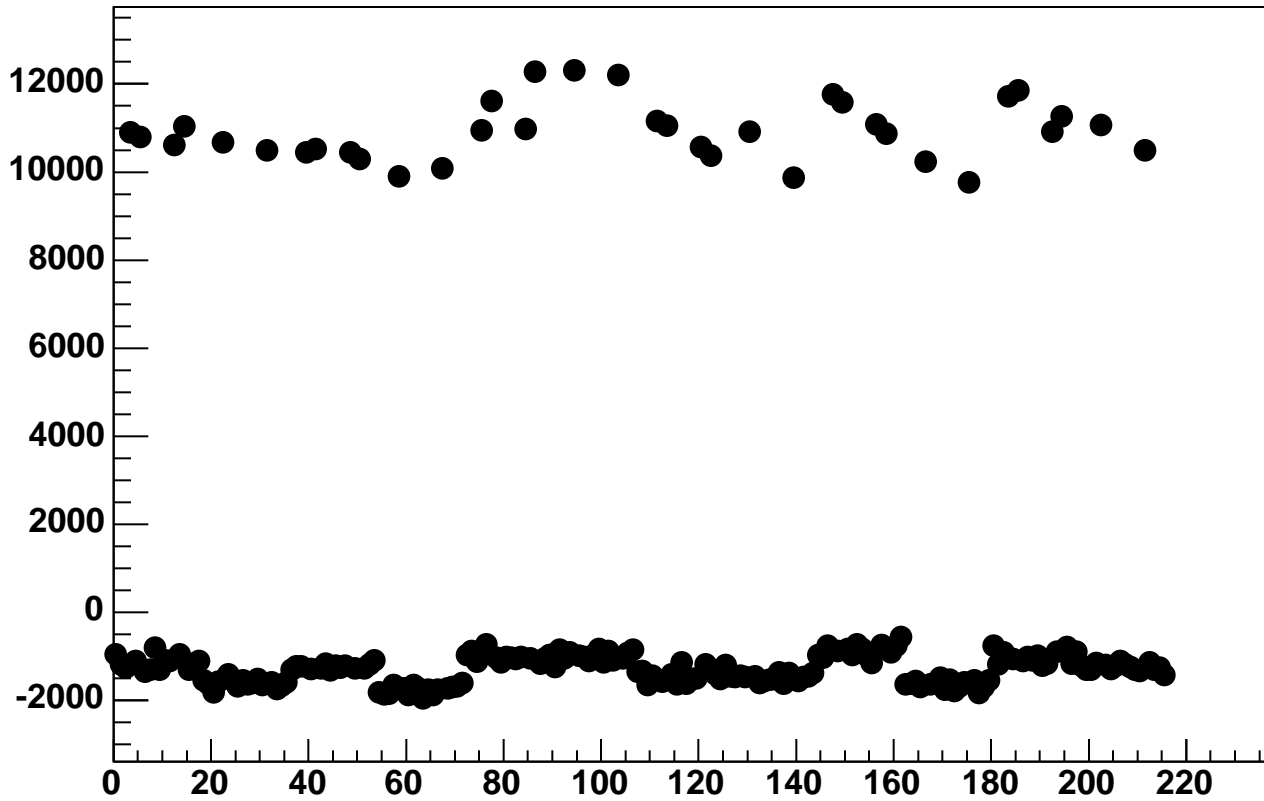
Enable 1, Hold=30, DAC=200, ADC Mean vs 18\*Chip+Chan



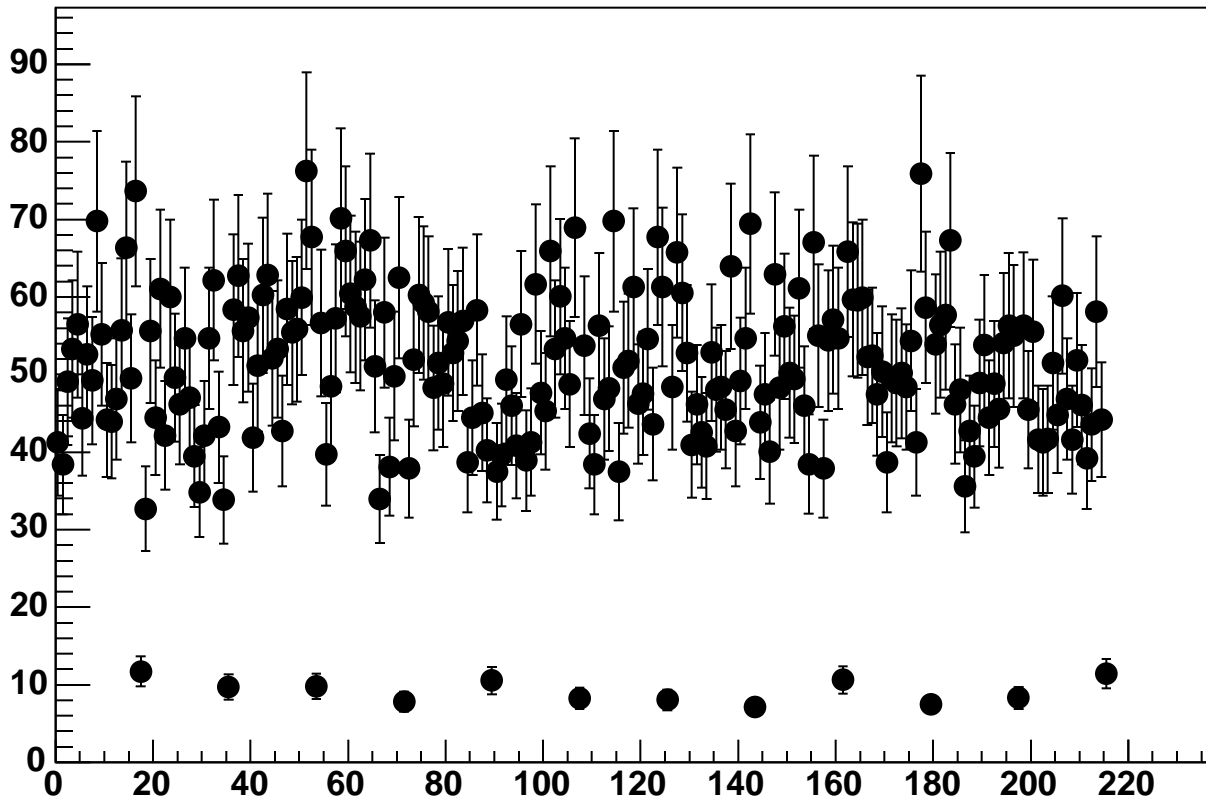
Enable 1, Hold=30, DAC=200, ADC Noise vs 18\*Chip+Chan



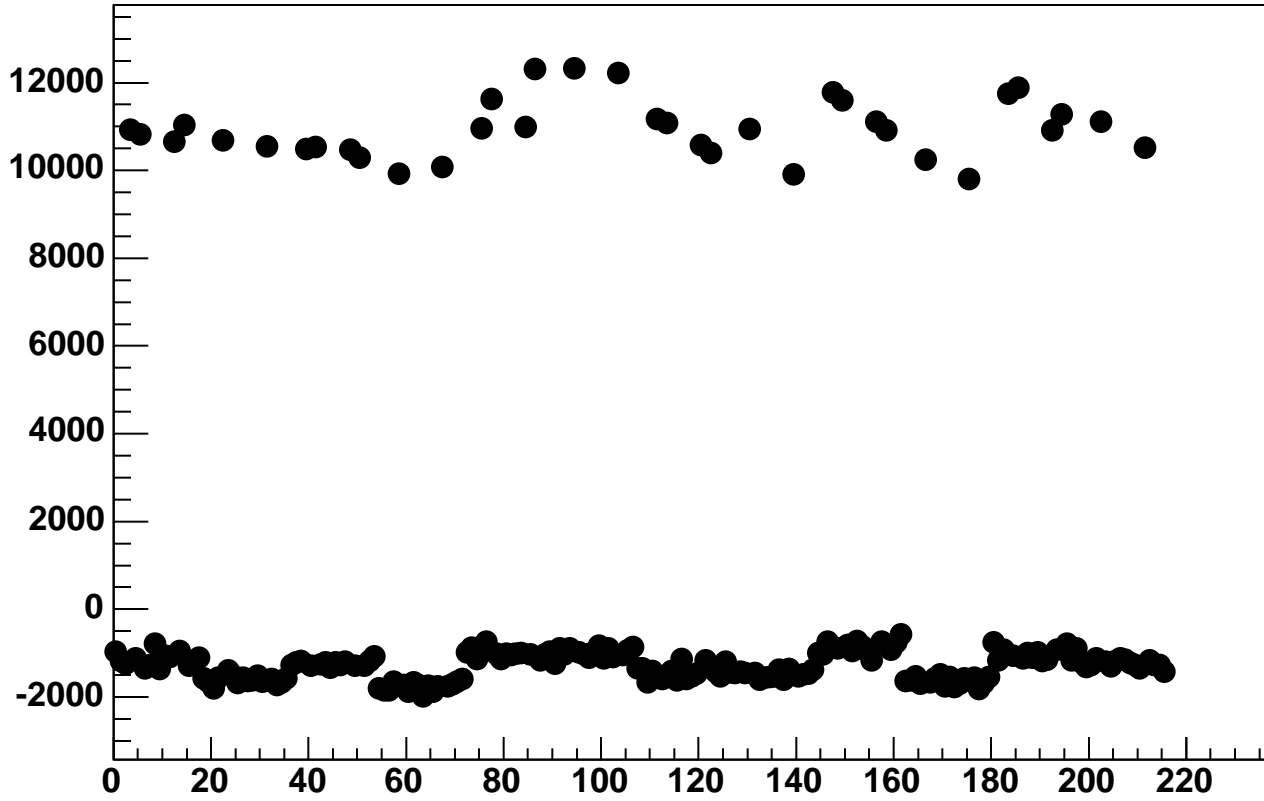
Enable 1, Hold=30, DAC=250, ADC Mean vs 18\*Chip+Chan



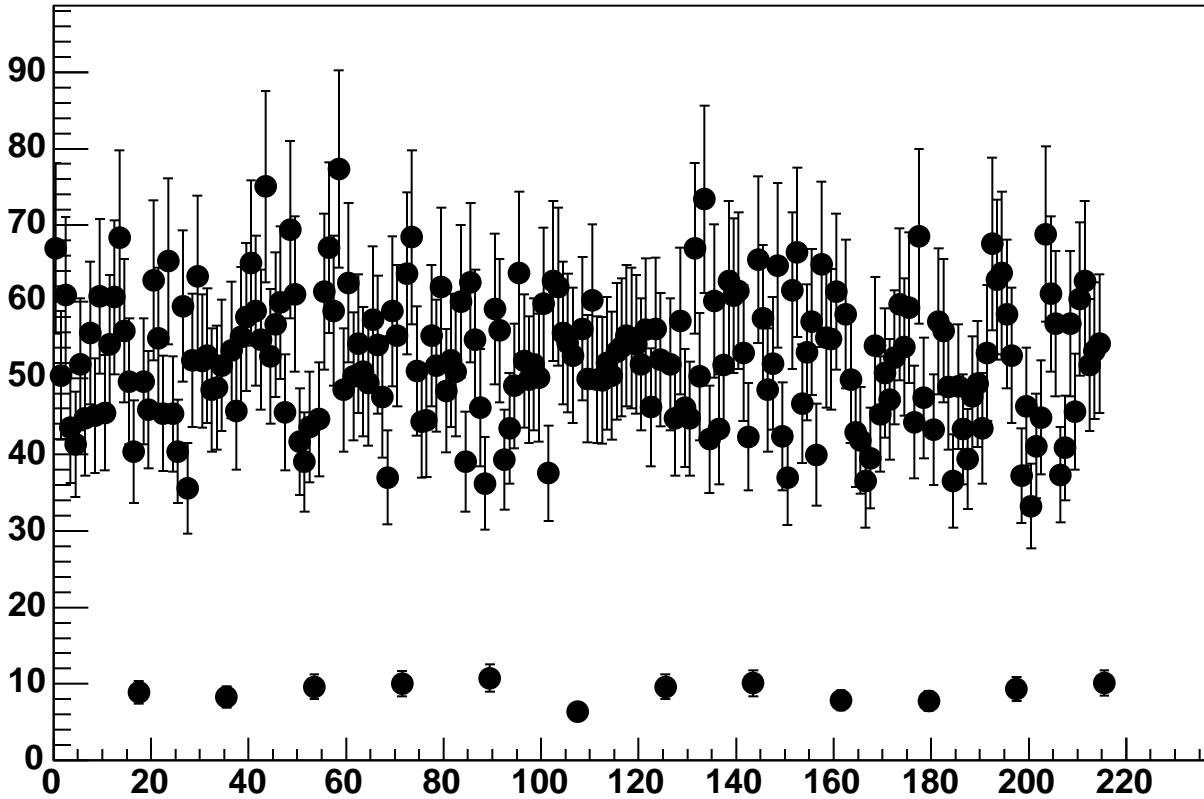
Enable 1, Hold=30, DAC=250, ADC Noise vs 18\*Chip+Chan



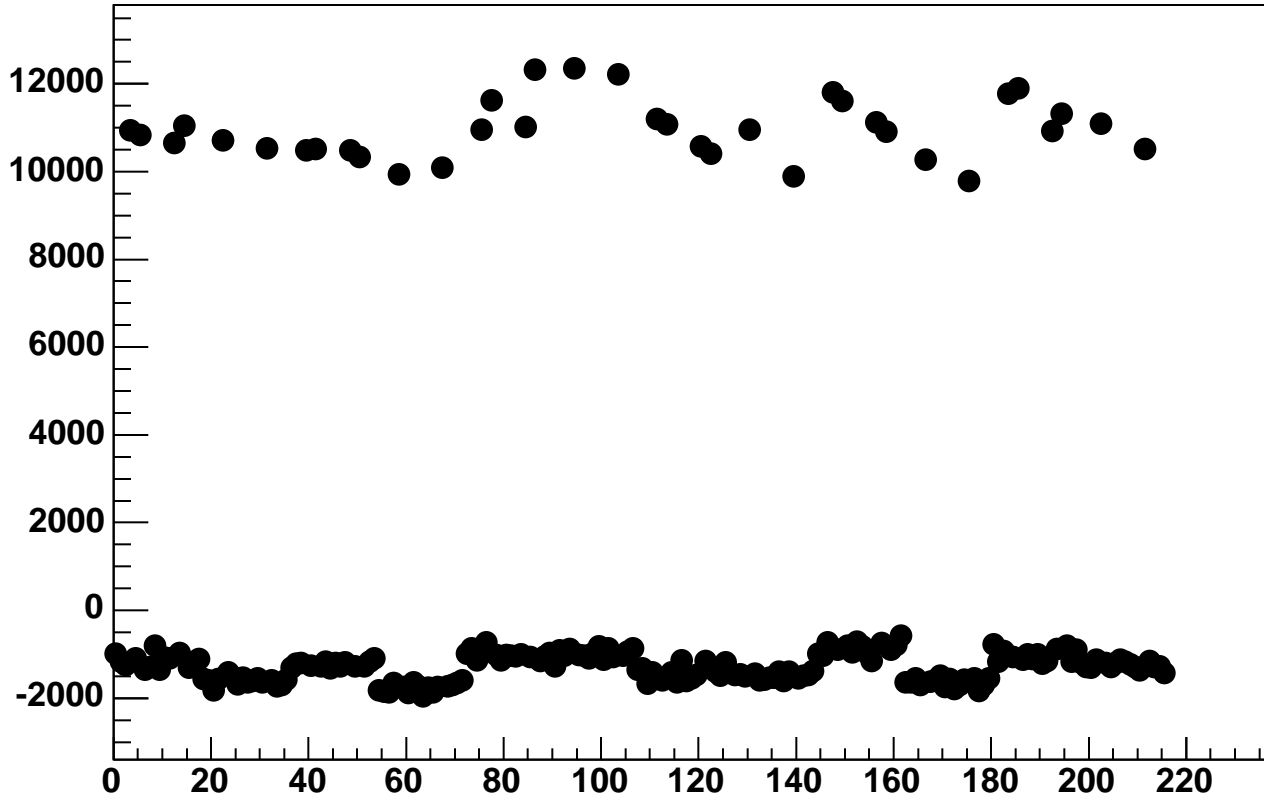
Enable 1, Hold=30, DAC=300, ADC Mean vs 18\*Chip+Chan



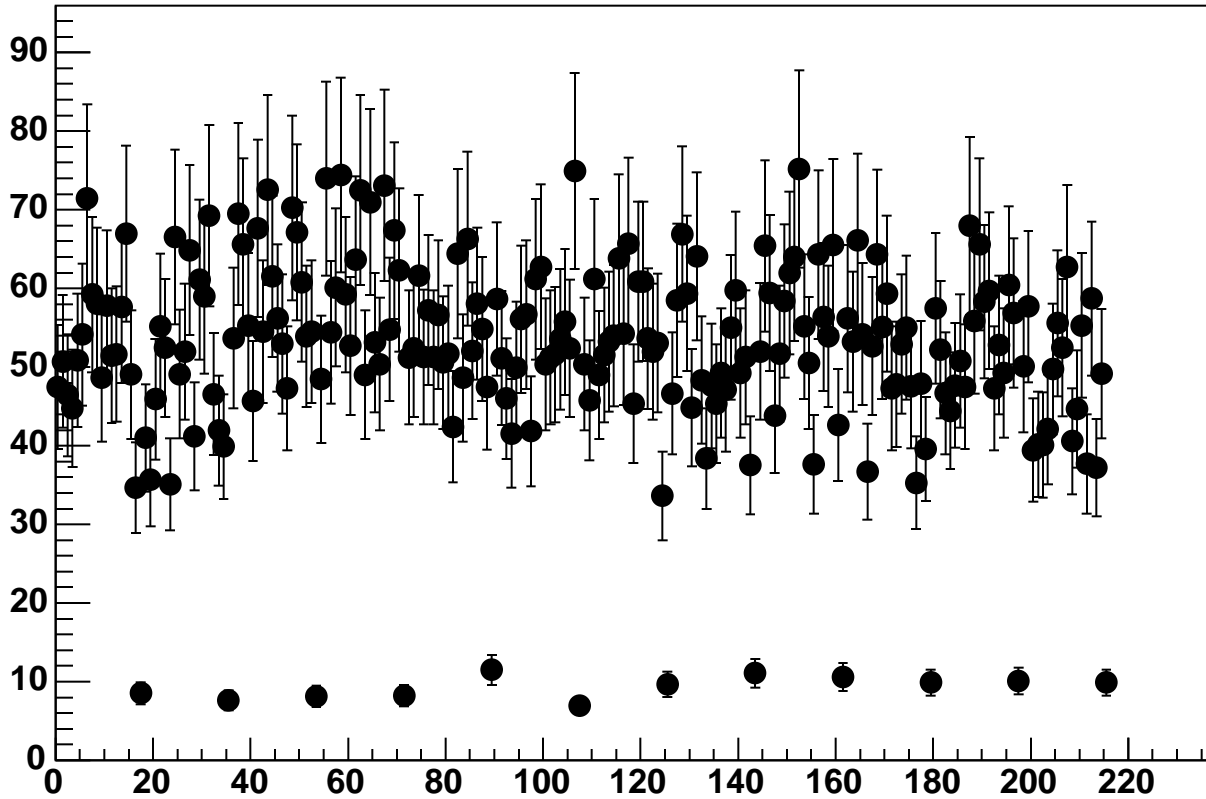
Enable 1, Hold=30, DAC=300, ADC Noise vs 18\*Chip+Chan



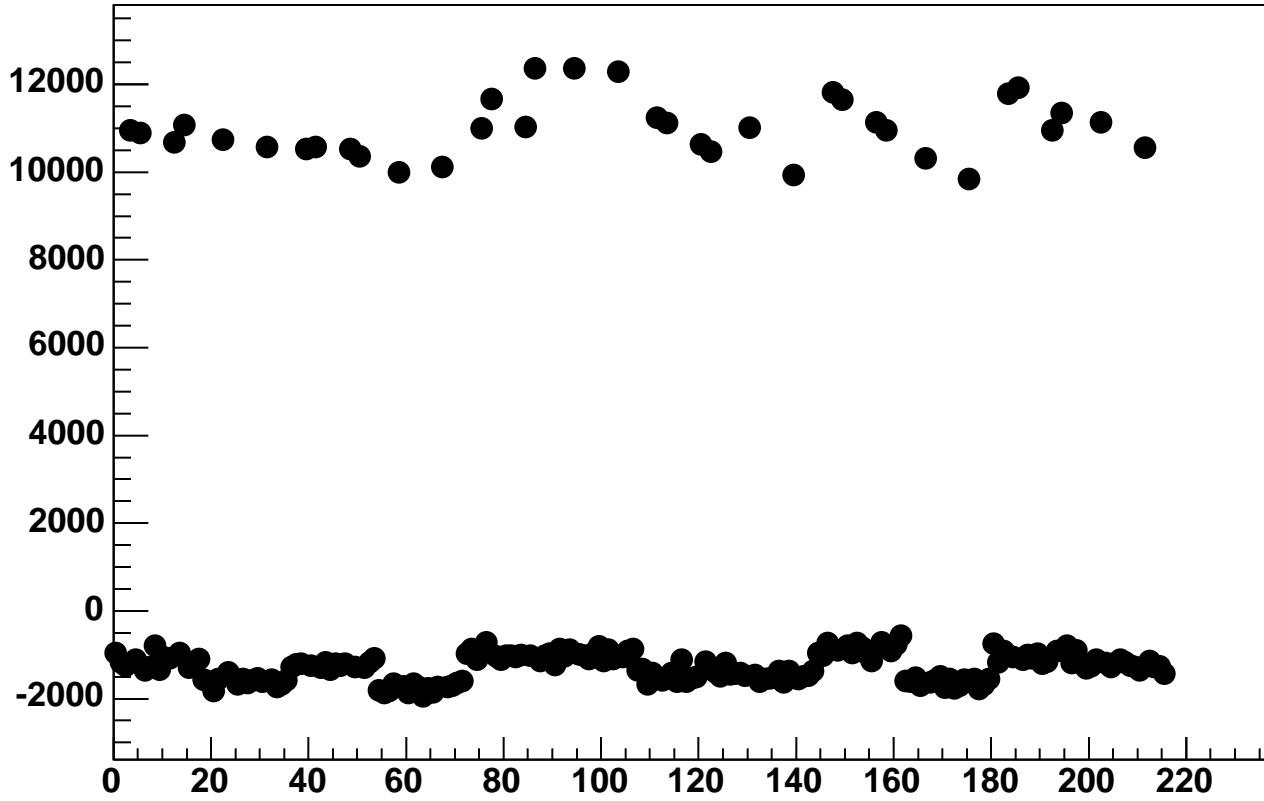
Enable 1, Hold=30, DAC=350, ADC Mean vs 18\*Chip+Chan



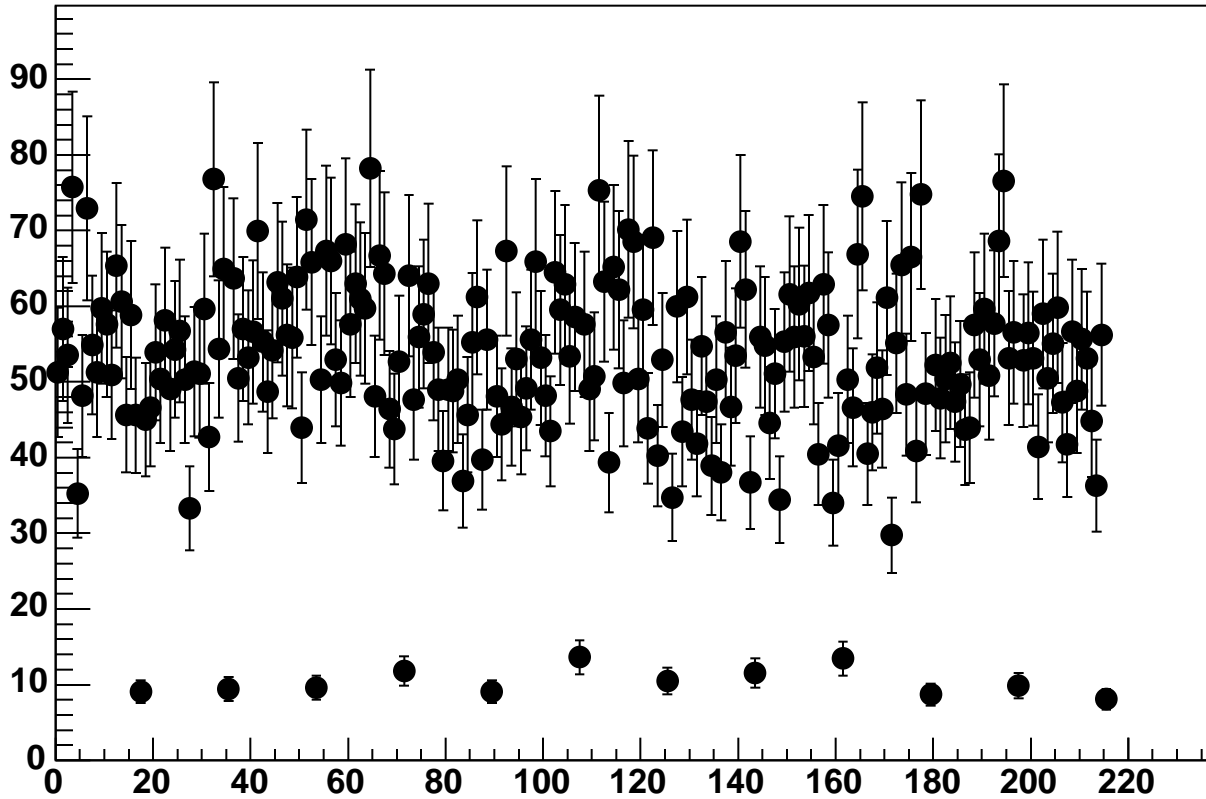
Enable 1, Hold=30, DAC=350, ADC Noise vs 18\*Chip+Chan



Enable 1, Hold=30, DAC=400, ADC Mean vs 18\*Chip+Chan

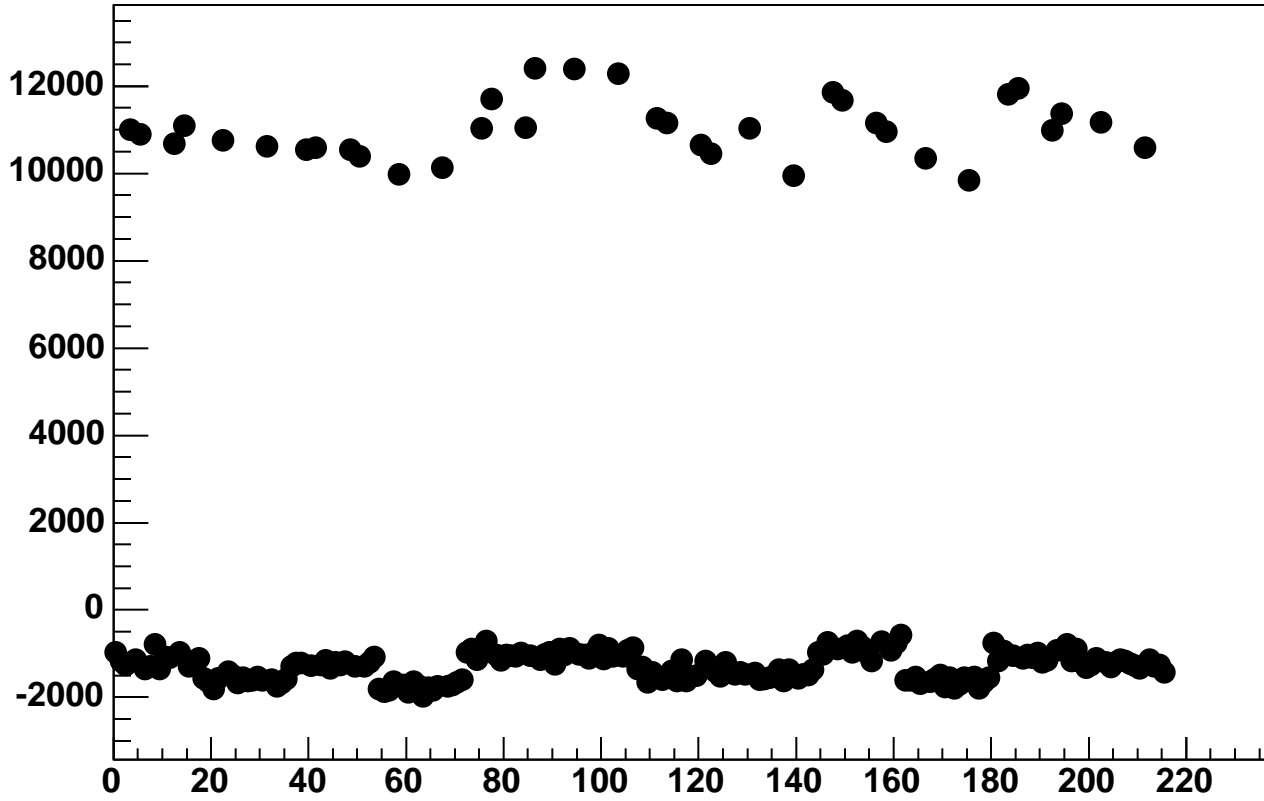


Enable 1, Hold=30, DAC=400, ADC Noise vs 18\*Chip+Chan

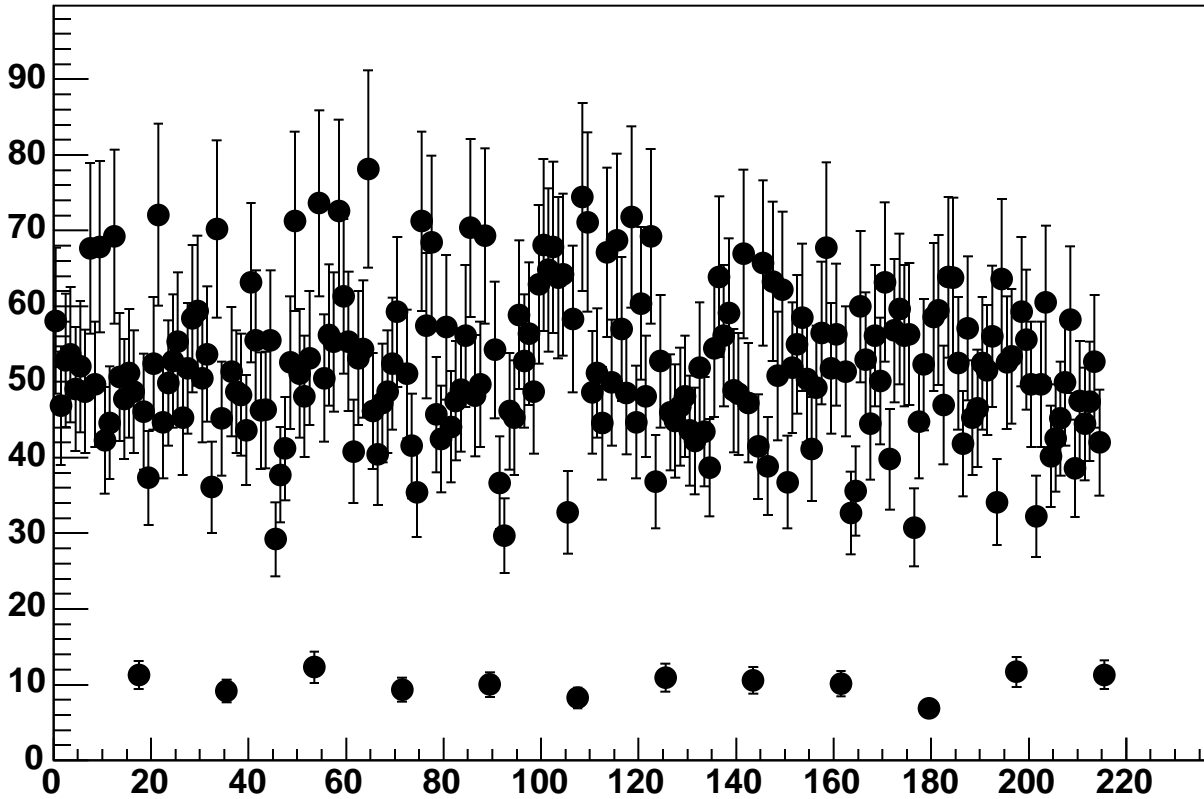




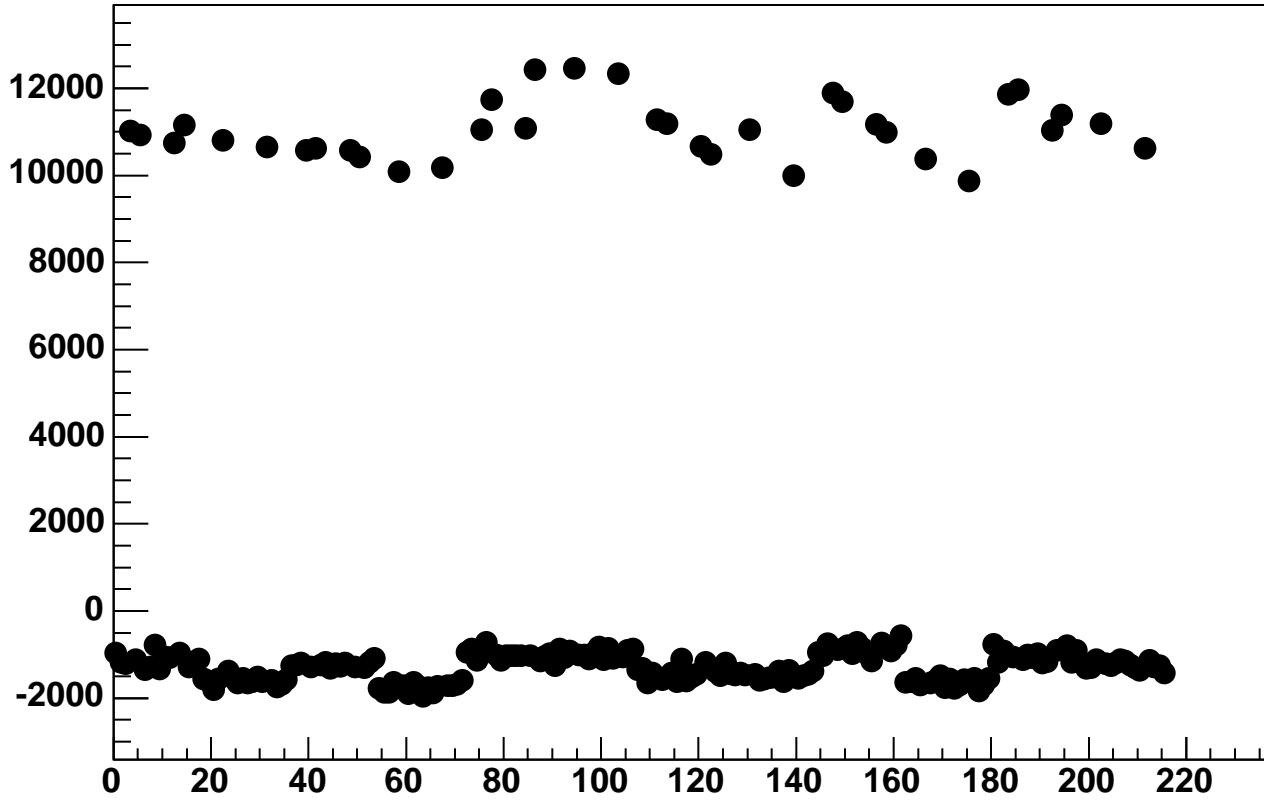
Enable 1, Hold=30, DAC=450, ADC Mean vs 18\*Chip+Chan



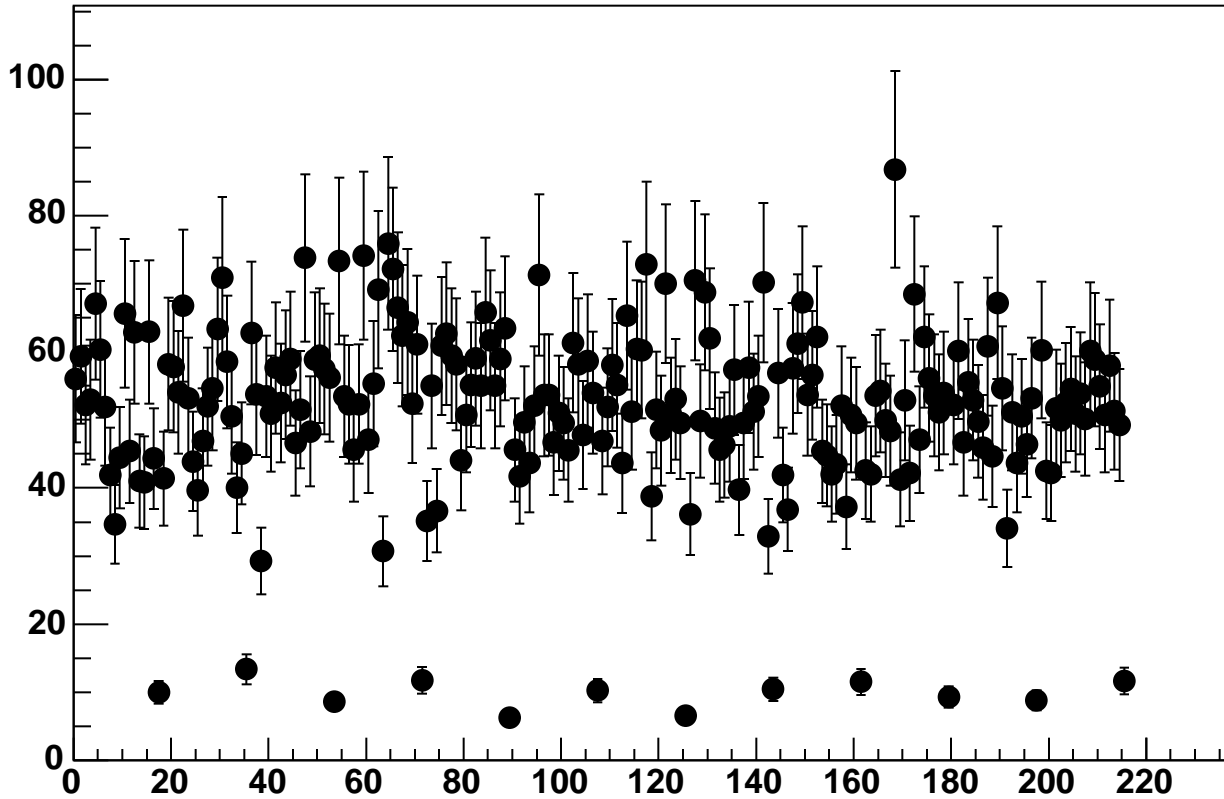
Enable 1, Hold=30, DAC=450, ADC Noise vs 18\*Chip+Chan



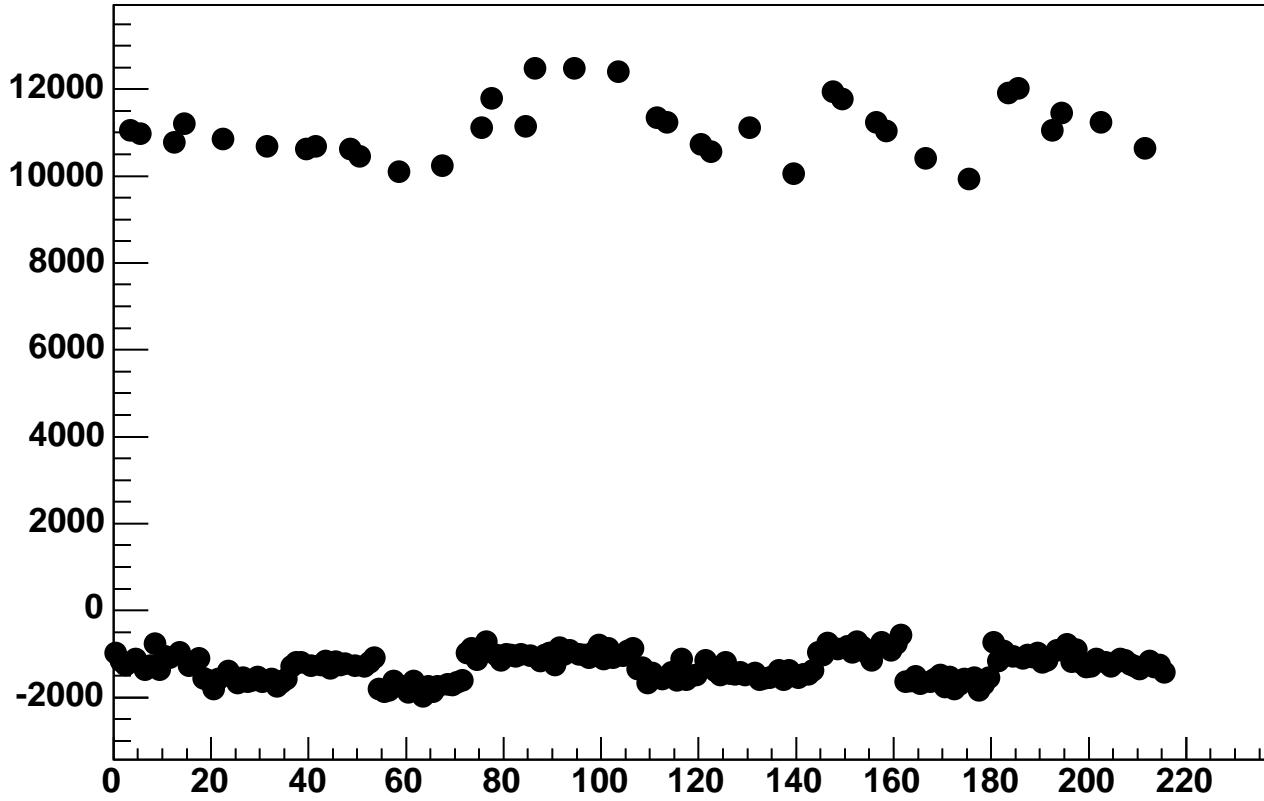
Enable 1, Hold=30, DAC=500, ADC Mean vs 18\*Chip+Chan



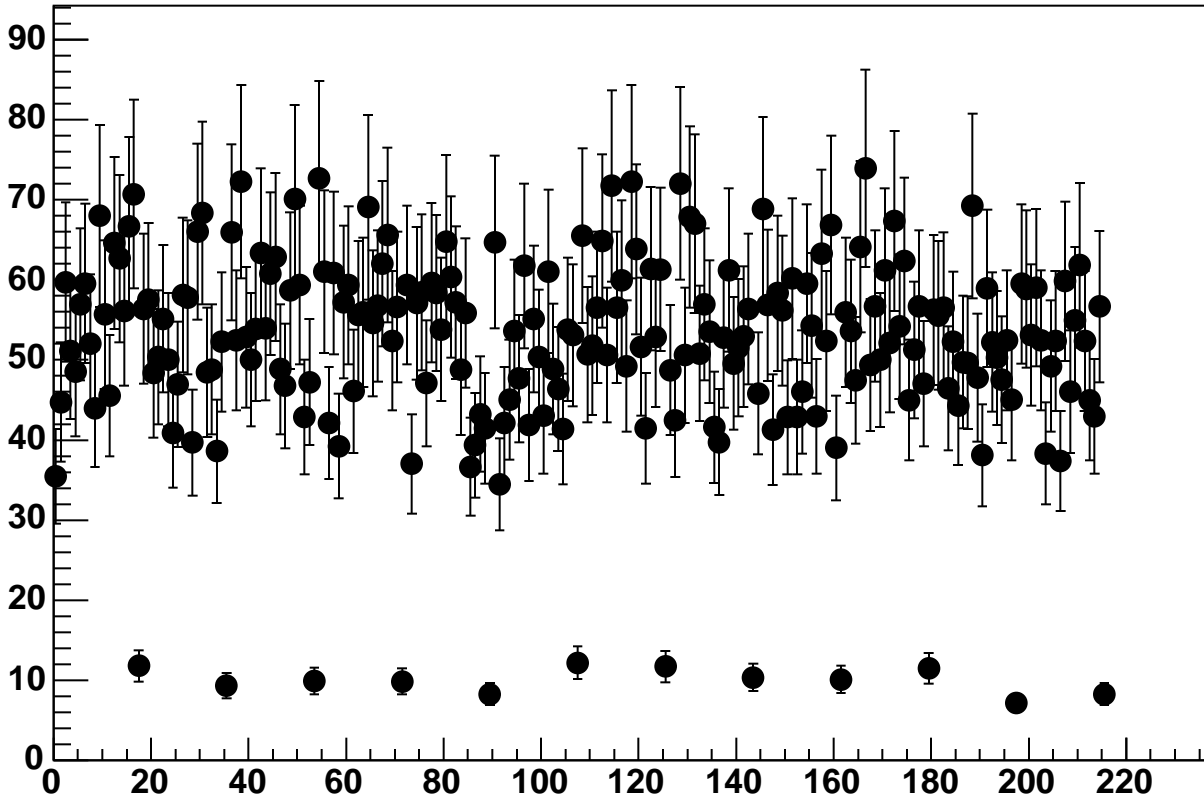
Enable 1, Hold=30, DAC=500, ADC Noise vs 18\*Chip+Chan



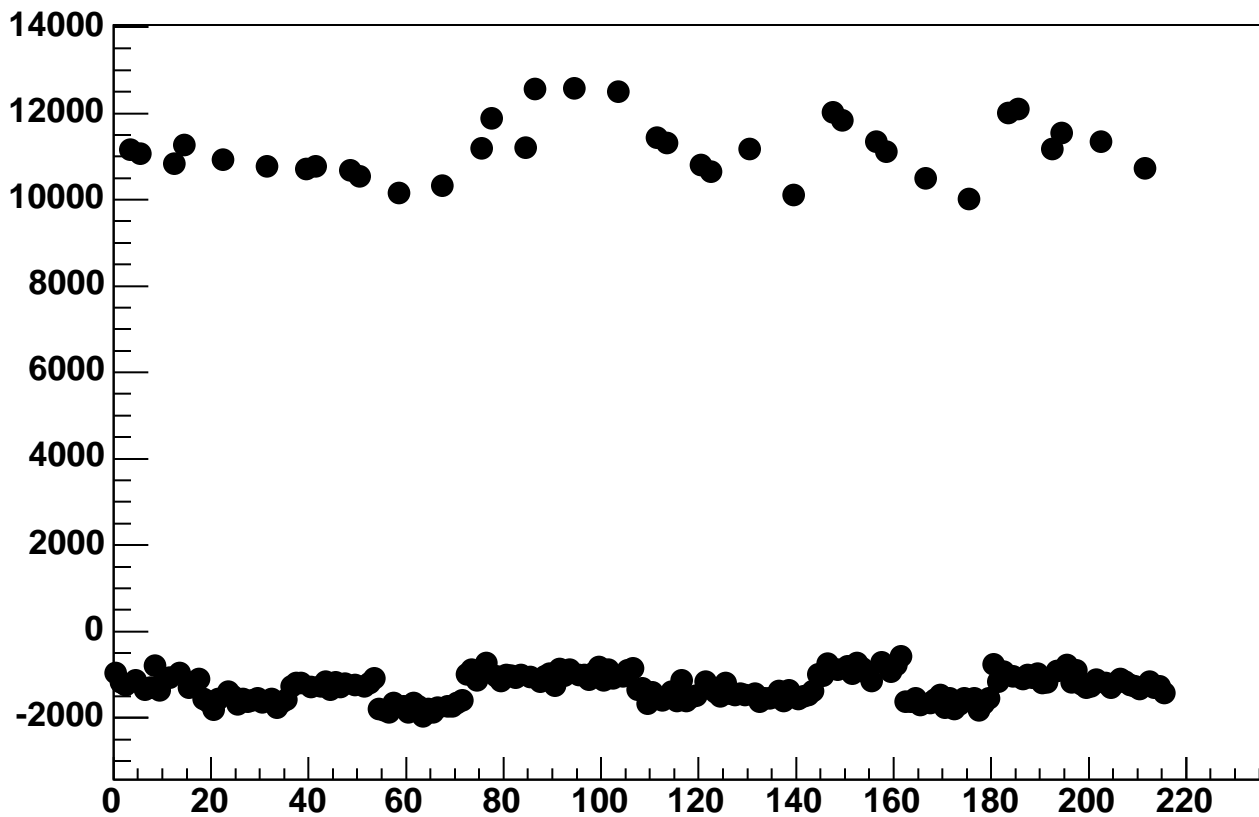
Enable 1, Hold=30, DAC=550, ADC Mean vs 18\*Chip+Chan



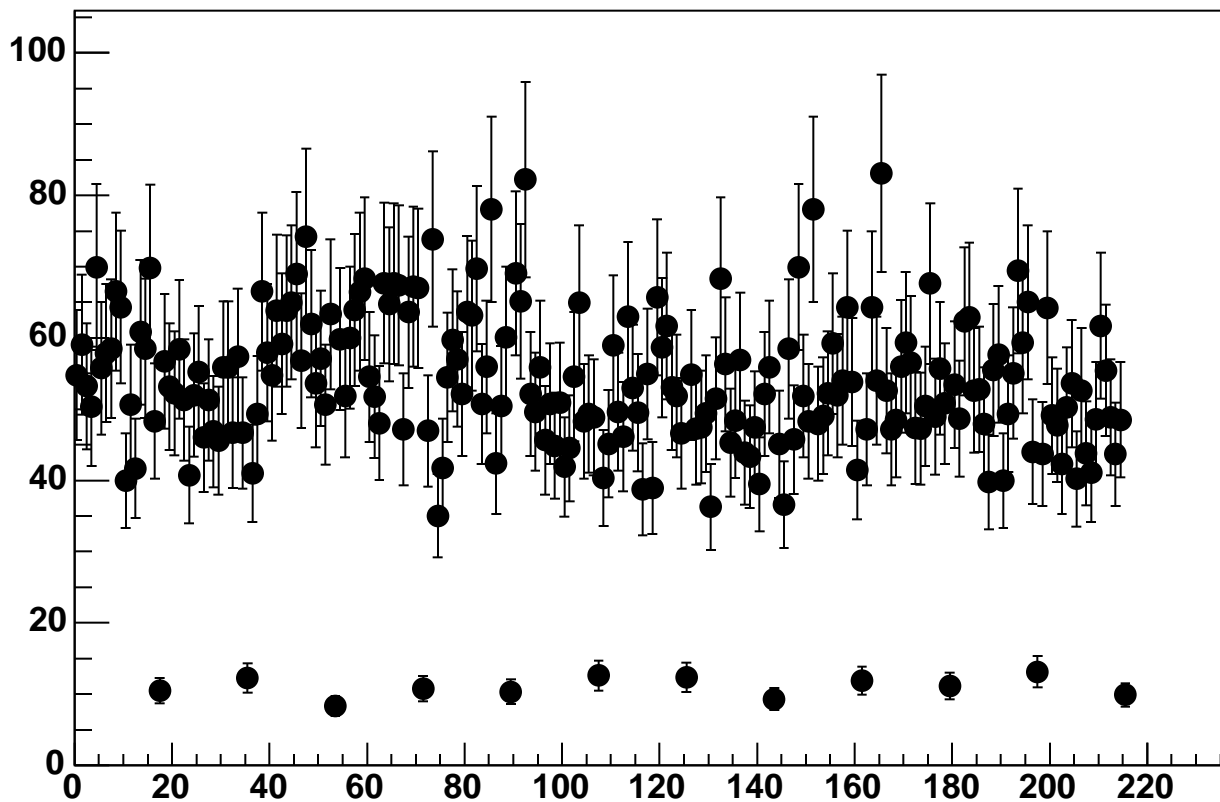
Enable 1, Hold=30, DAC=550, ADC Noise vs 18\*Chip+Chan



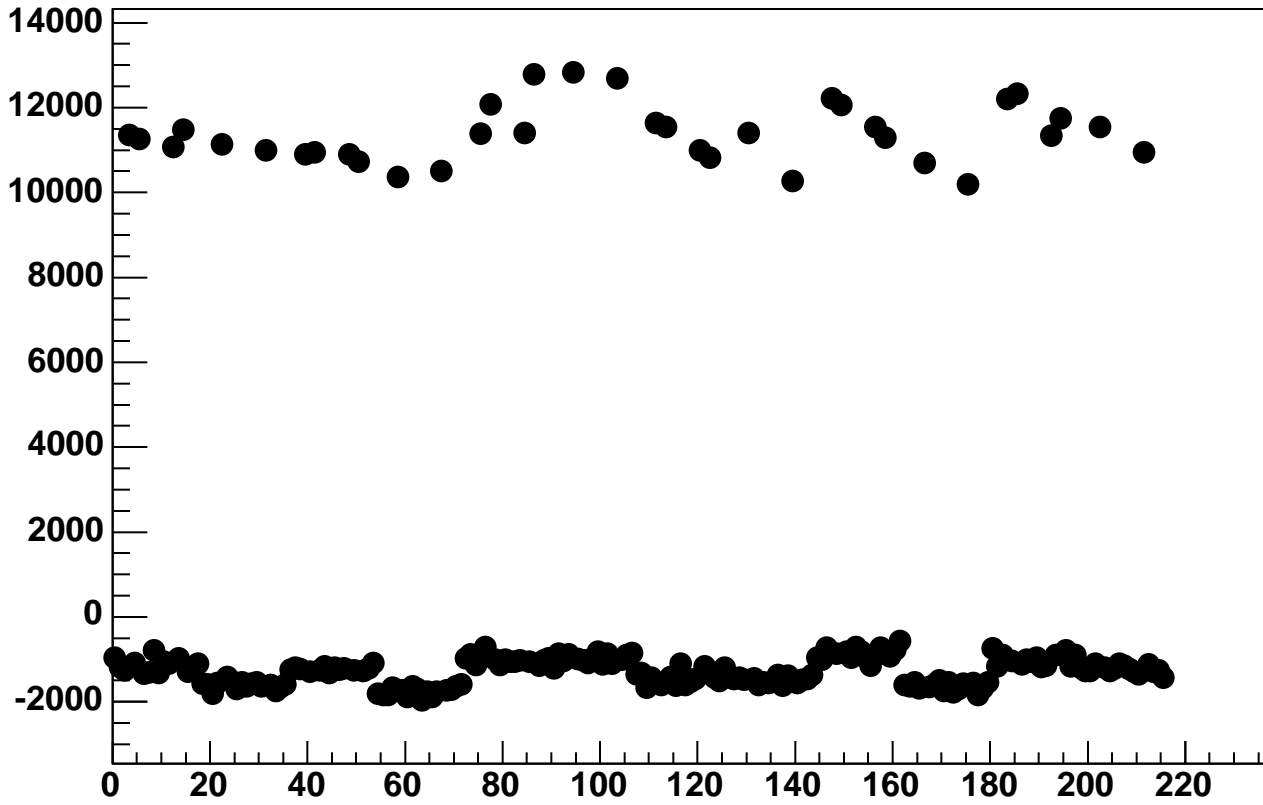
Enable 1, Hold=30, DAC=600, ADC Mean vs 18\*Chip+Chan



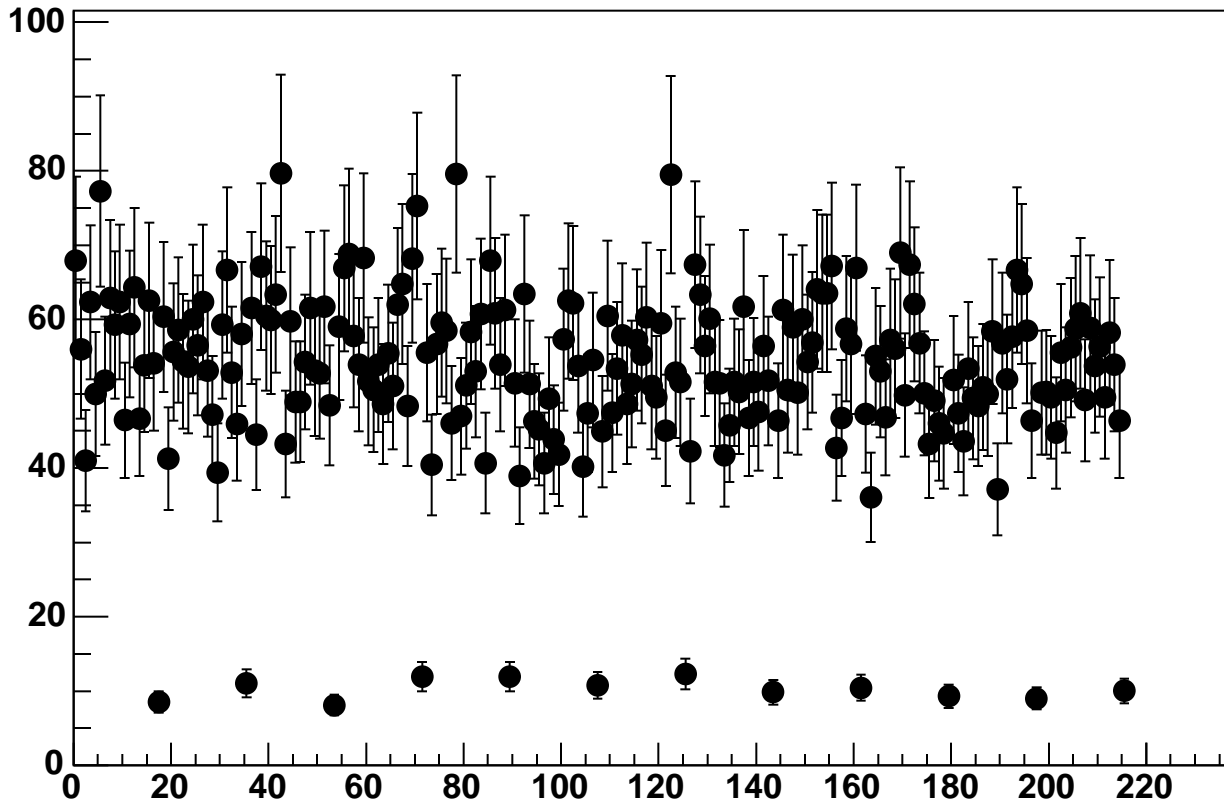
Enable 1, Hold=30, DAC=600, ADC Noise vs 18\*Chip+Chan



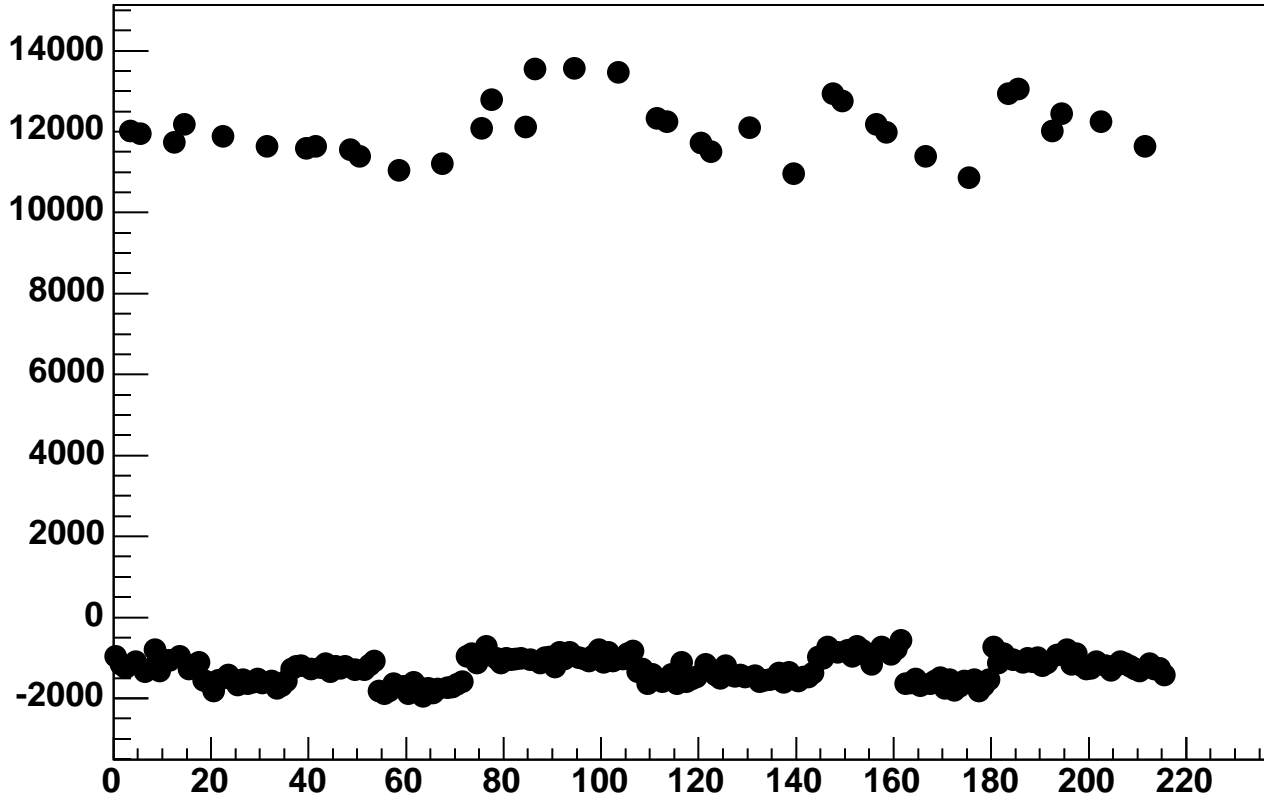
Enable 1, Hold=30, DAC=650, ADC Mean vs 18\*Chip+Chan



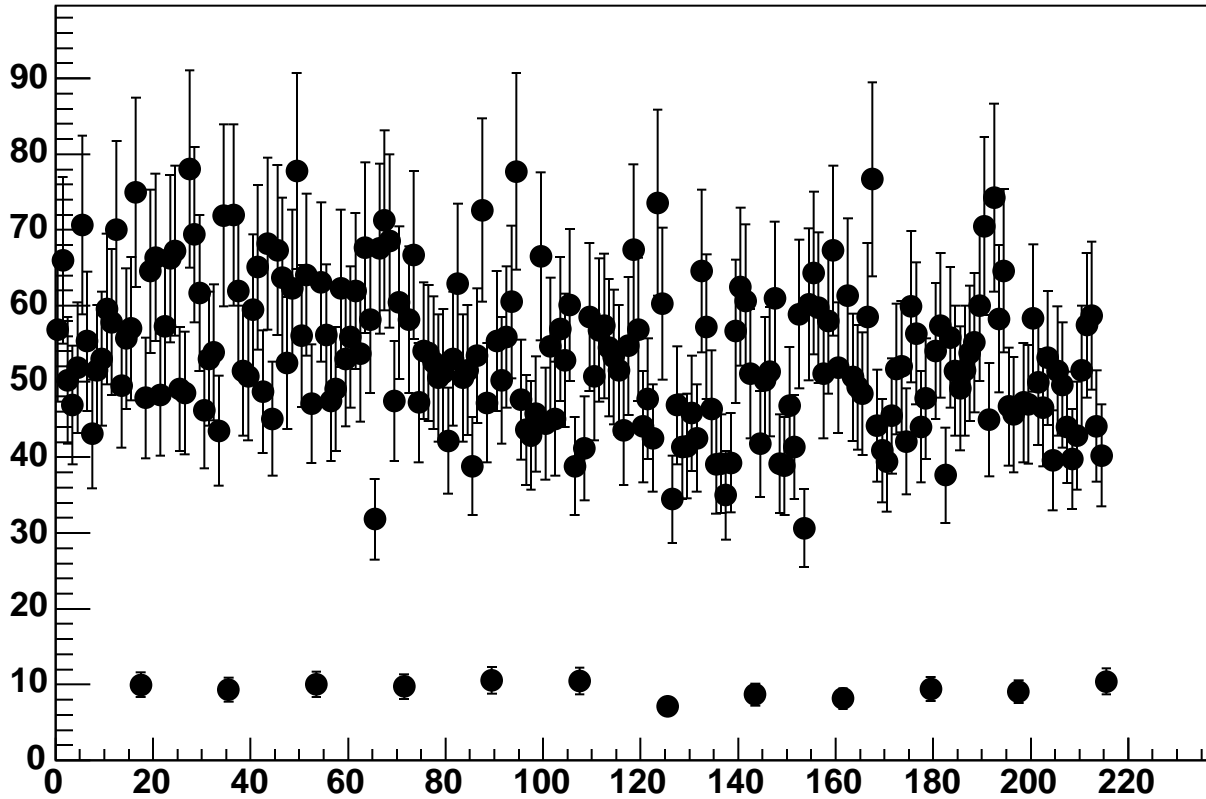
Enable 1, Hold=30, DAC=650, ADC Noise vs 18\*Chip+Chan



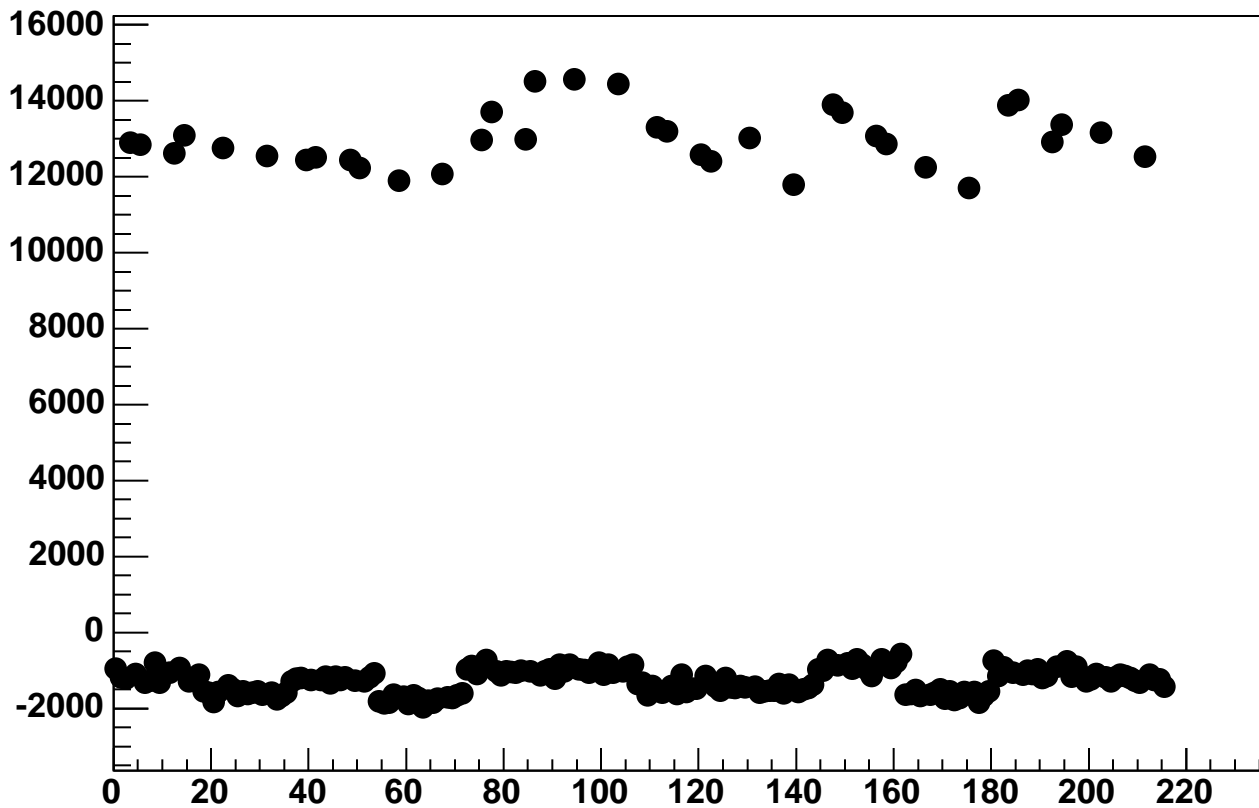
Enable 1, Hold=30, DAC=700, ADC Mean vs 18\*Chip+Chan



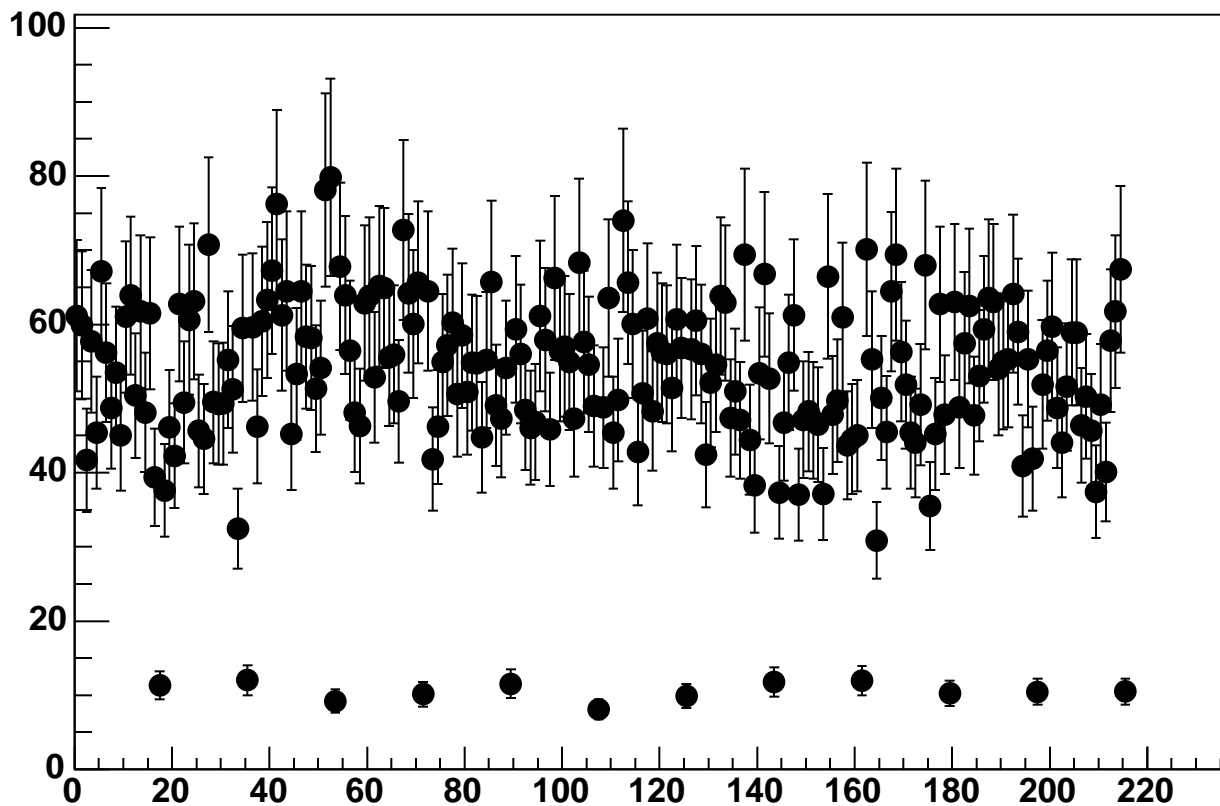
Enable 1, Hold=30, DAC=700, ADC Noise vs 18\*Chip+Chan



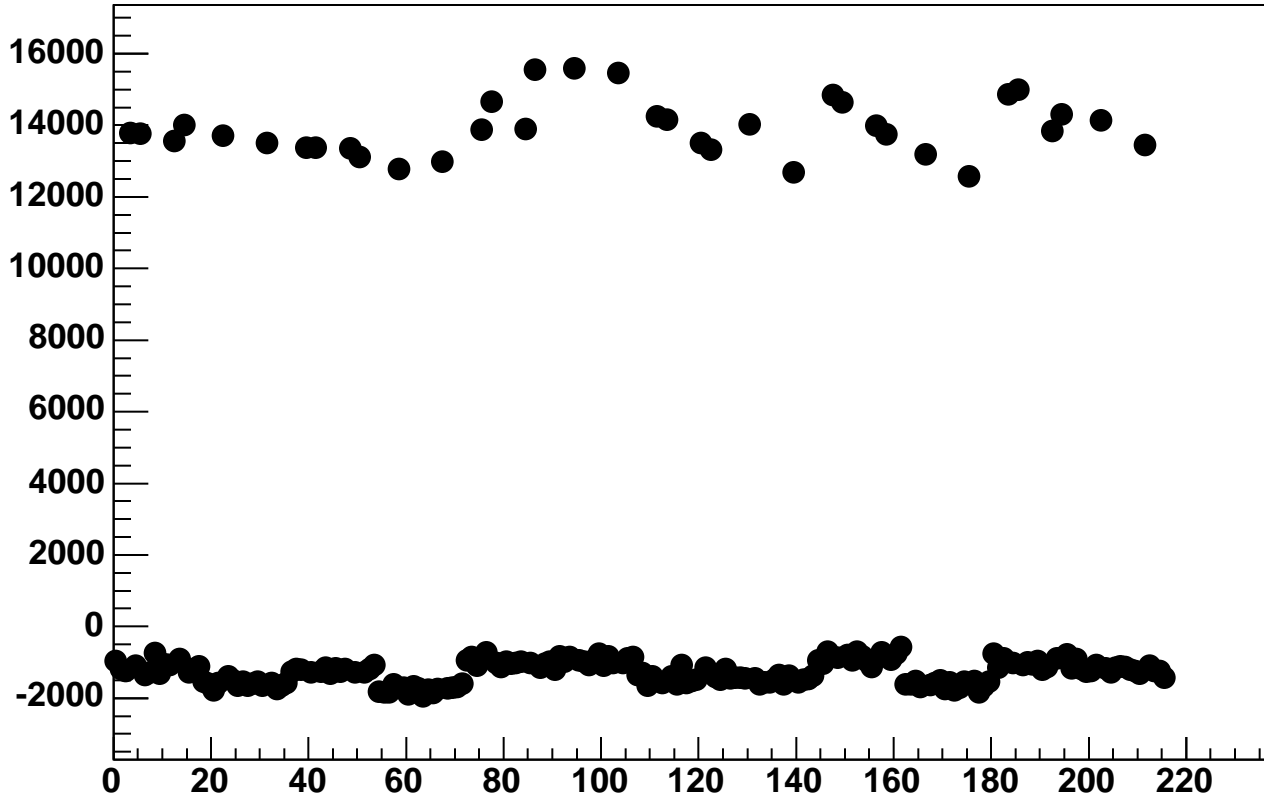
Enable 1, Hold=30, DAC=750, ADC Mean vs 18\*Chip+Chan



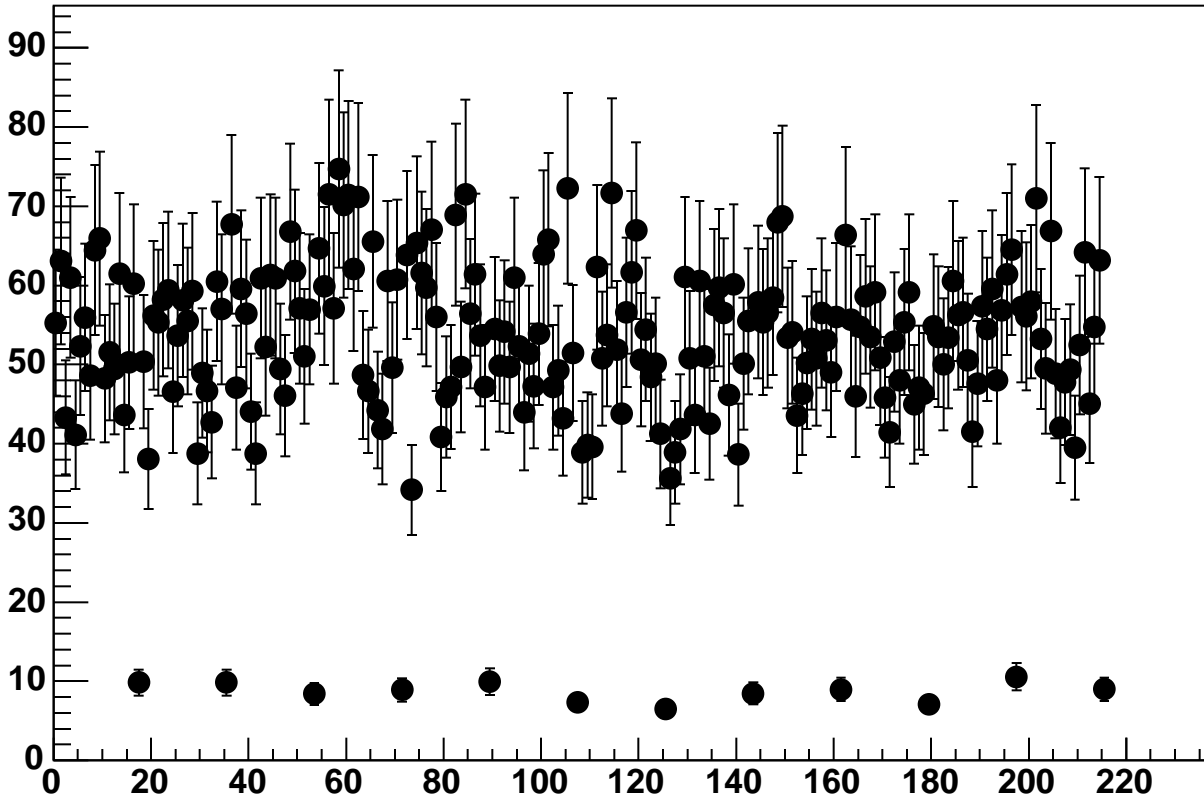
Enable 1, Hold=30, DAC=750, ADC Noise vs 18\*Chip+Chan



Enable 1, Hold=30, DAC=800, ADC Mean vs 18\*Chip+Chan

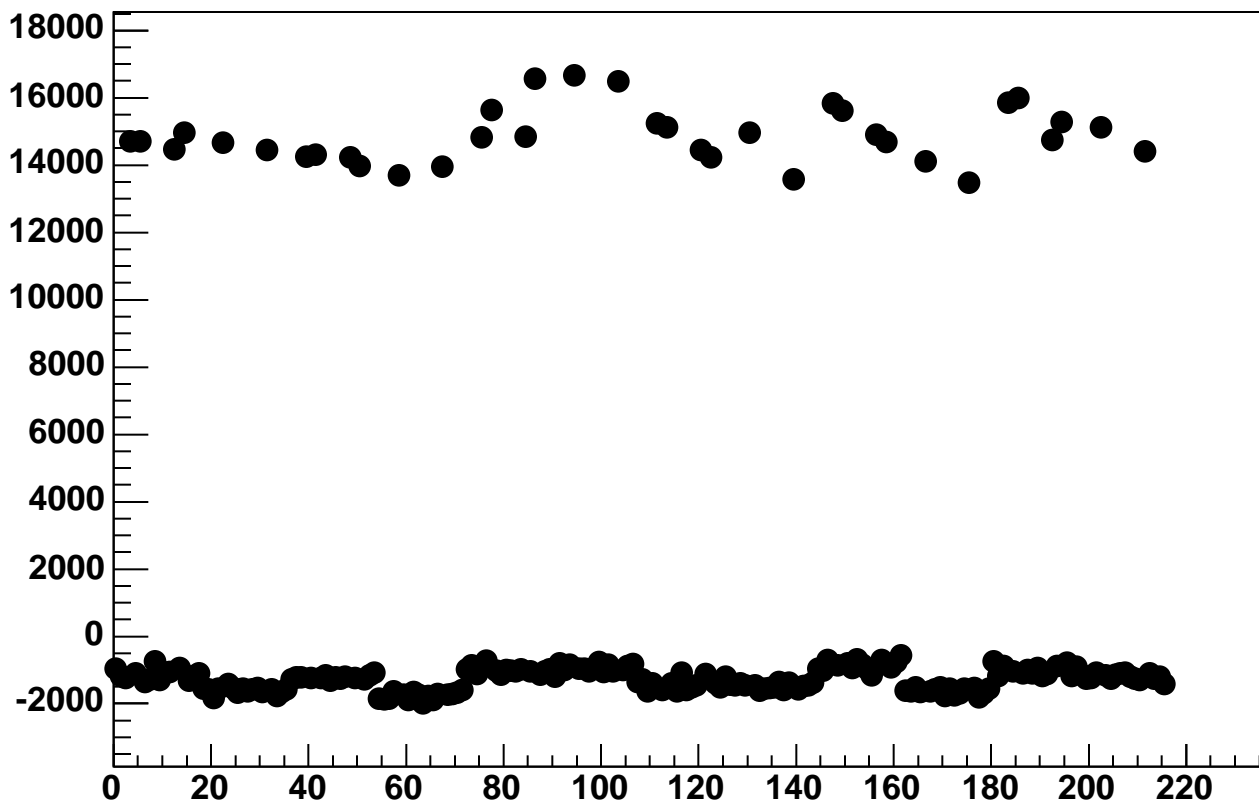


Enable 1, Hold=30, DAC=800, ADC Noise vs 18\*Chip+Chan

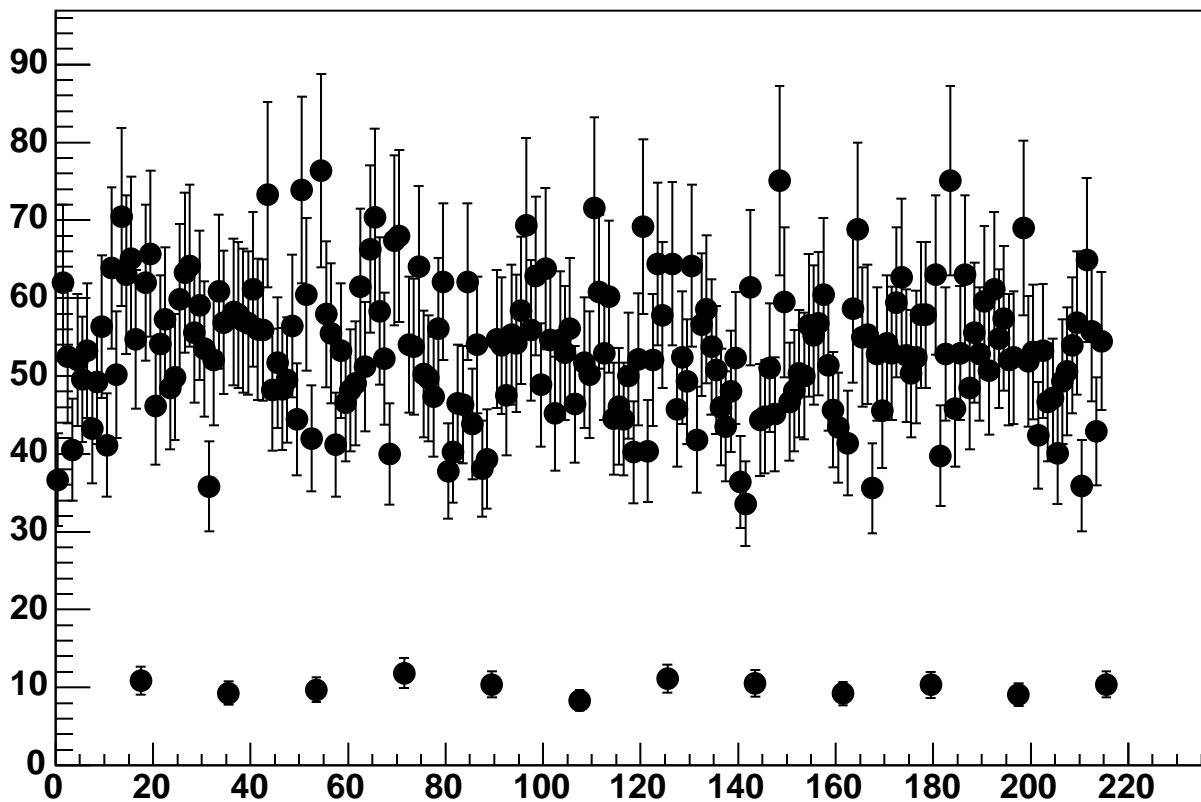




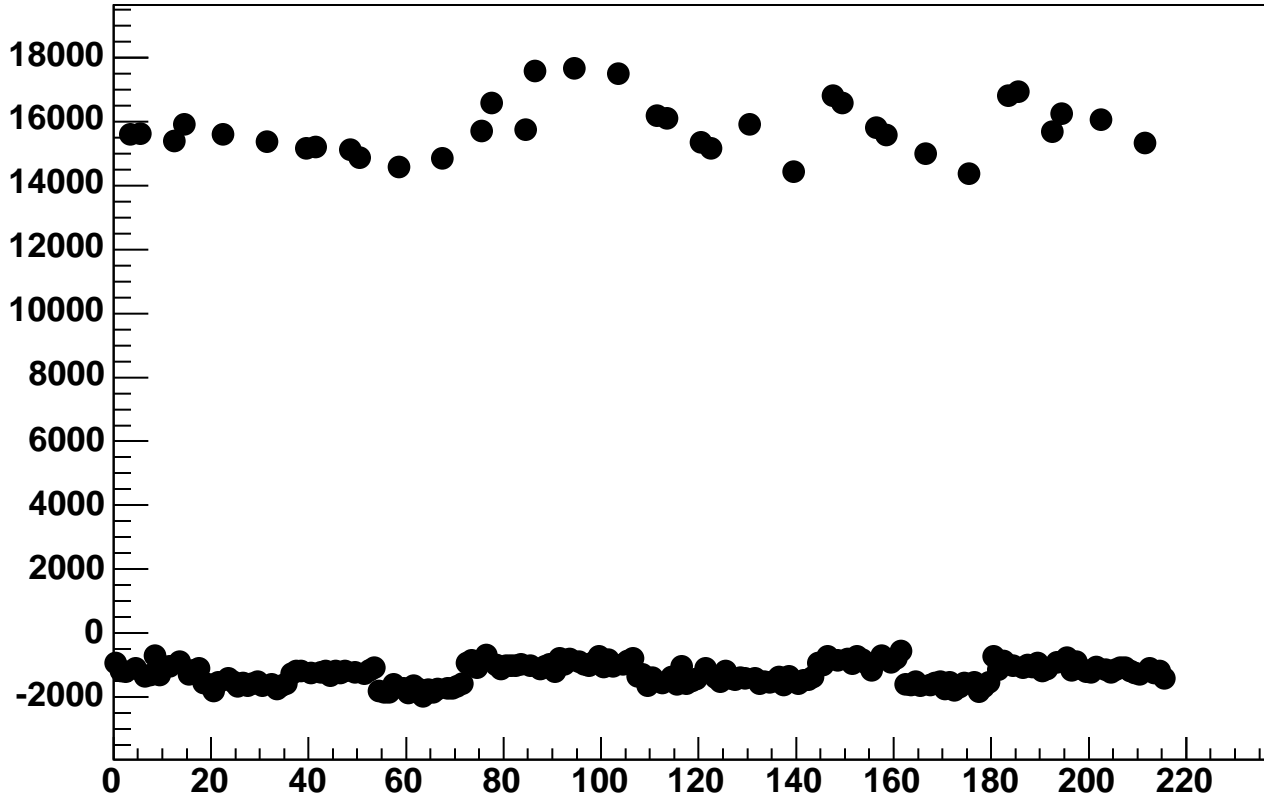
Enable 1, Hold=30, DAC=850, ADC Mean vs 18\*Chip+Chan



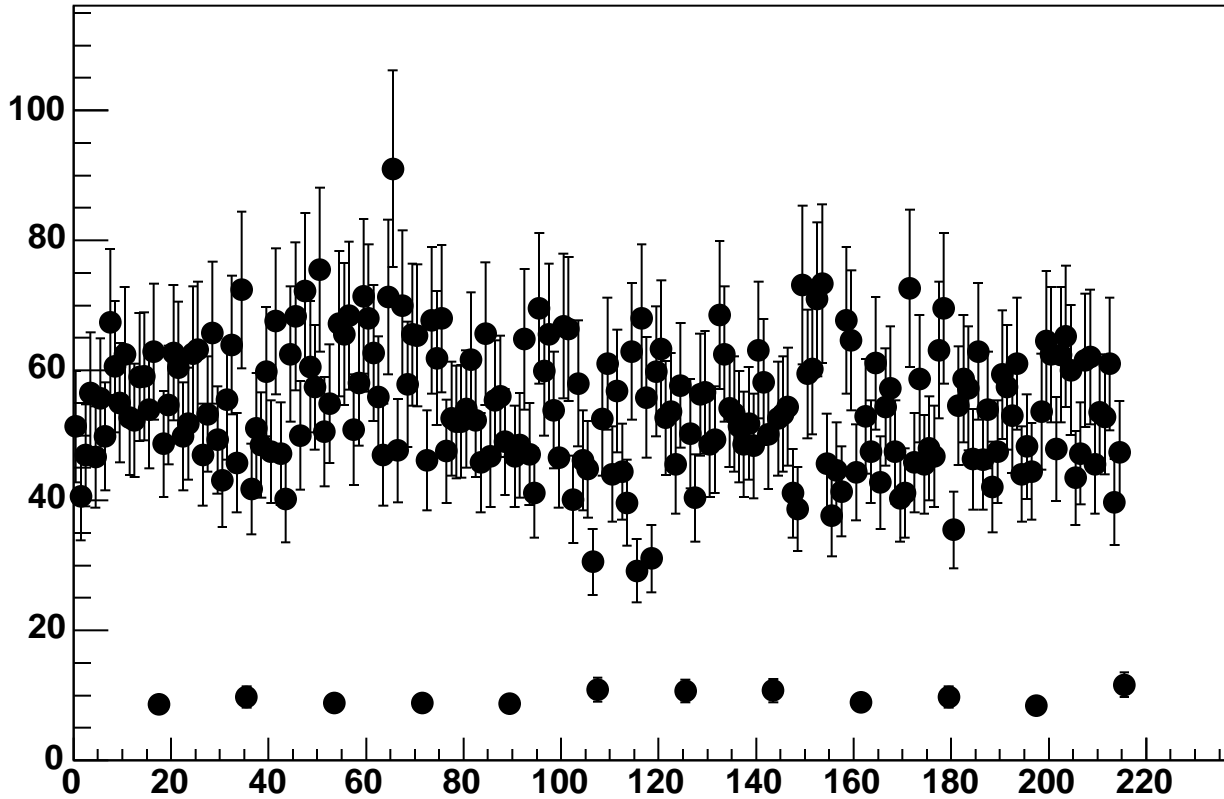
Enable 1, Hold=30, DAC=850, ADC Noise vs 18\*Chip+Chan



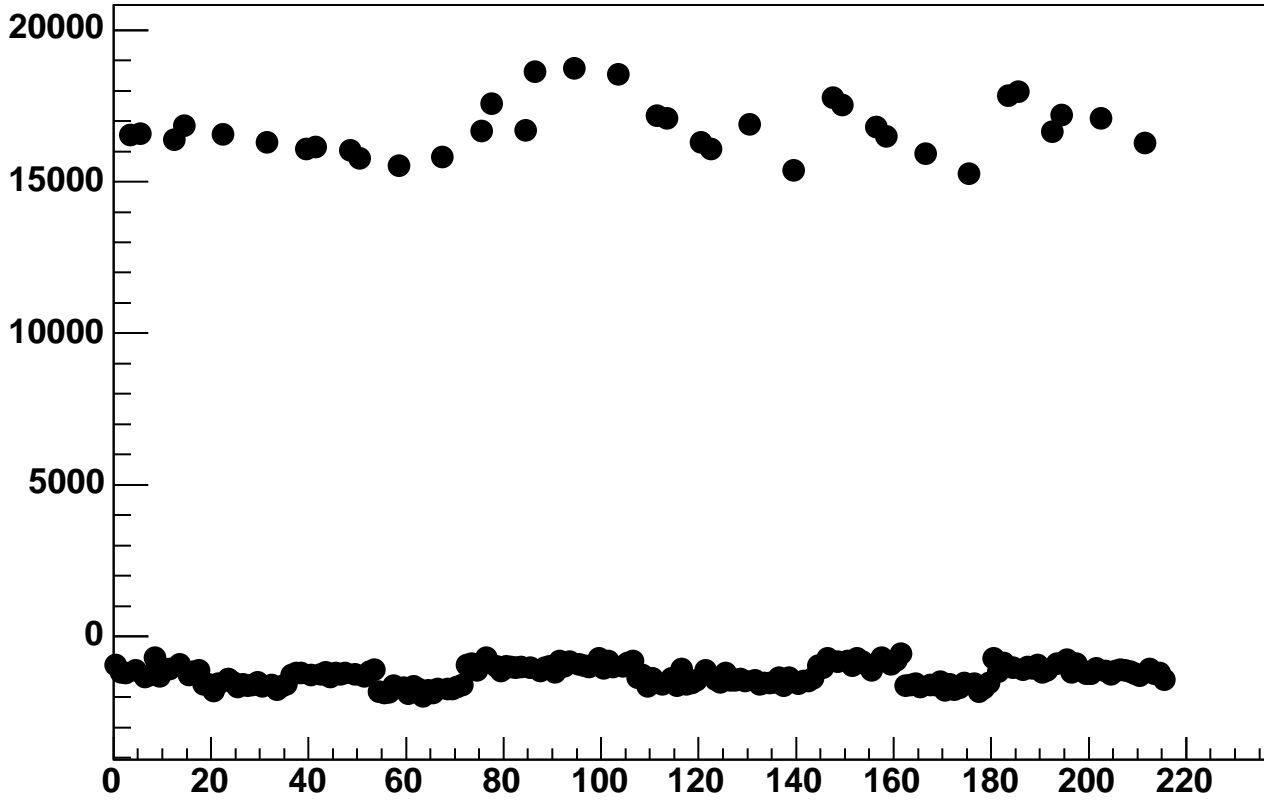
Enable 1, Hold=30, DAC=900, ADC Mean vs 18\*Chip+Chan



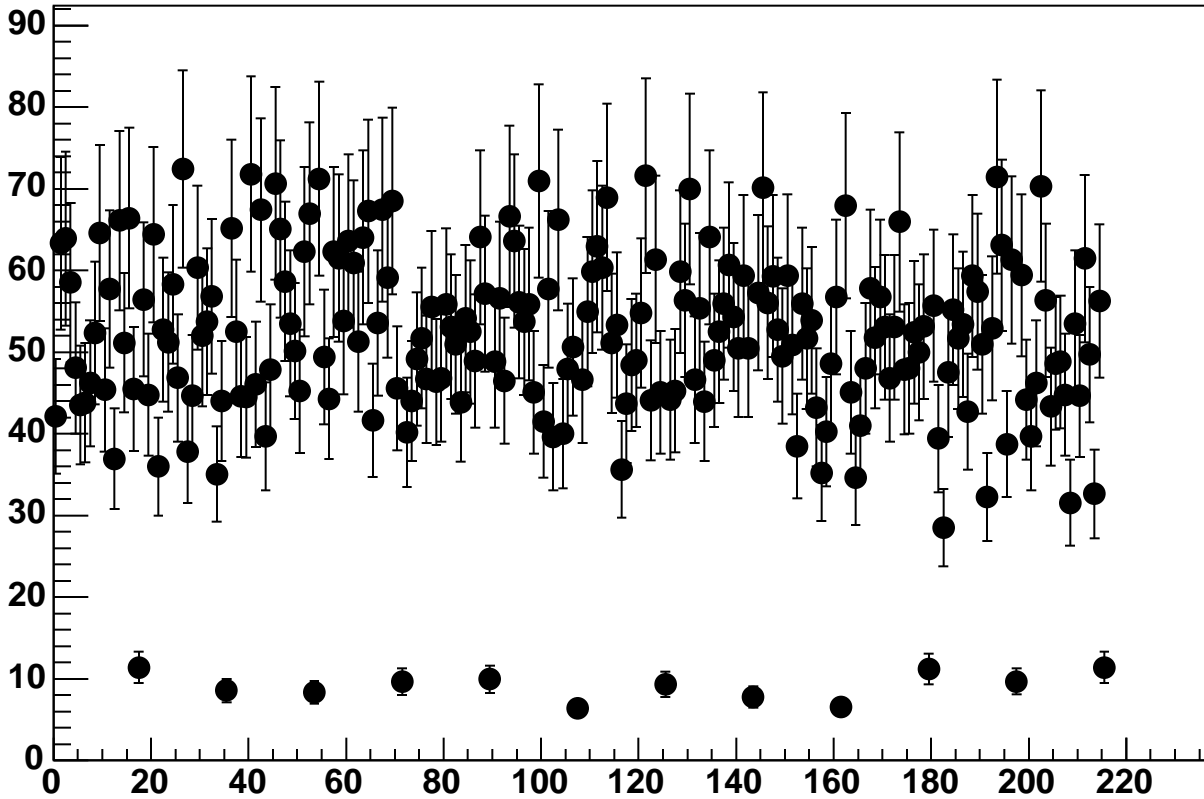
Enable 1, Hold=30, DAC=900, ADC Noise vs 18\*Chip+Chan



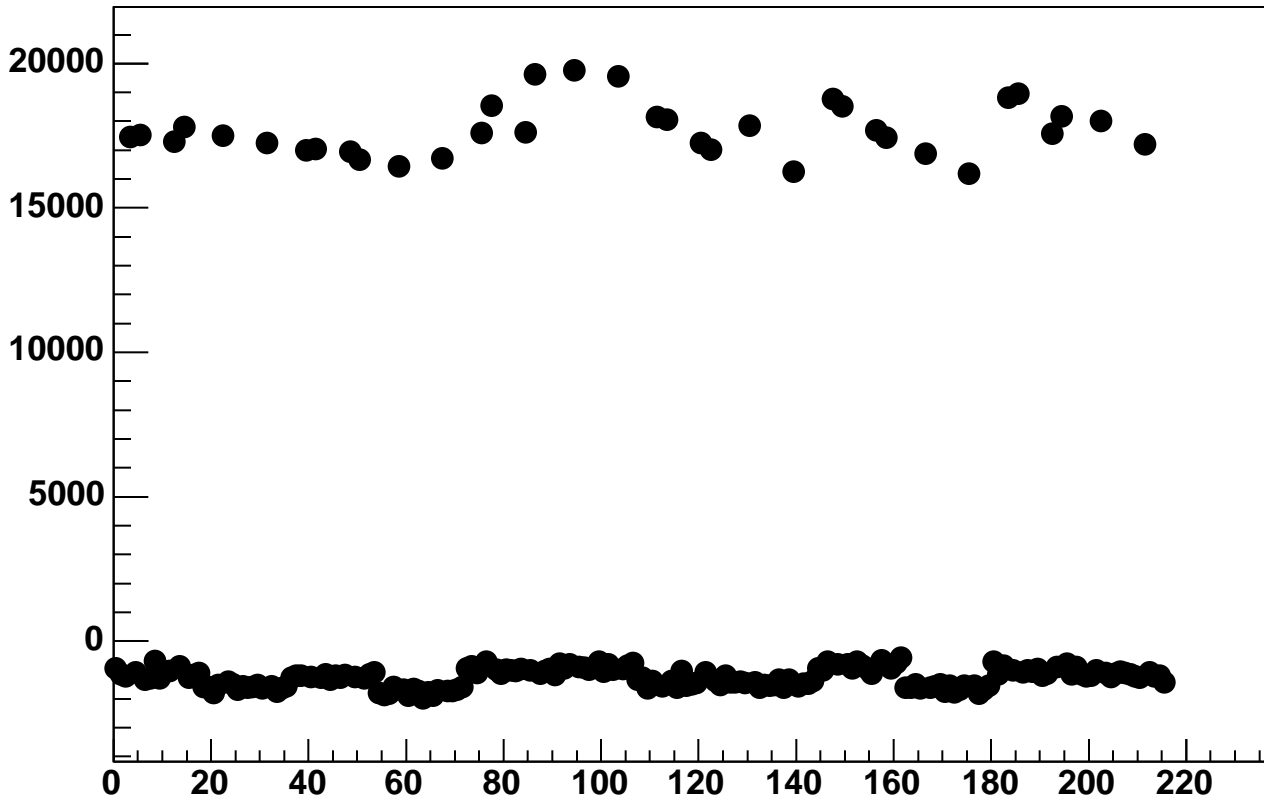
Enable 1, Hold=30, DAC=950, ADC Mean vs 18\*Chip+Chan



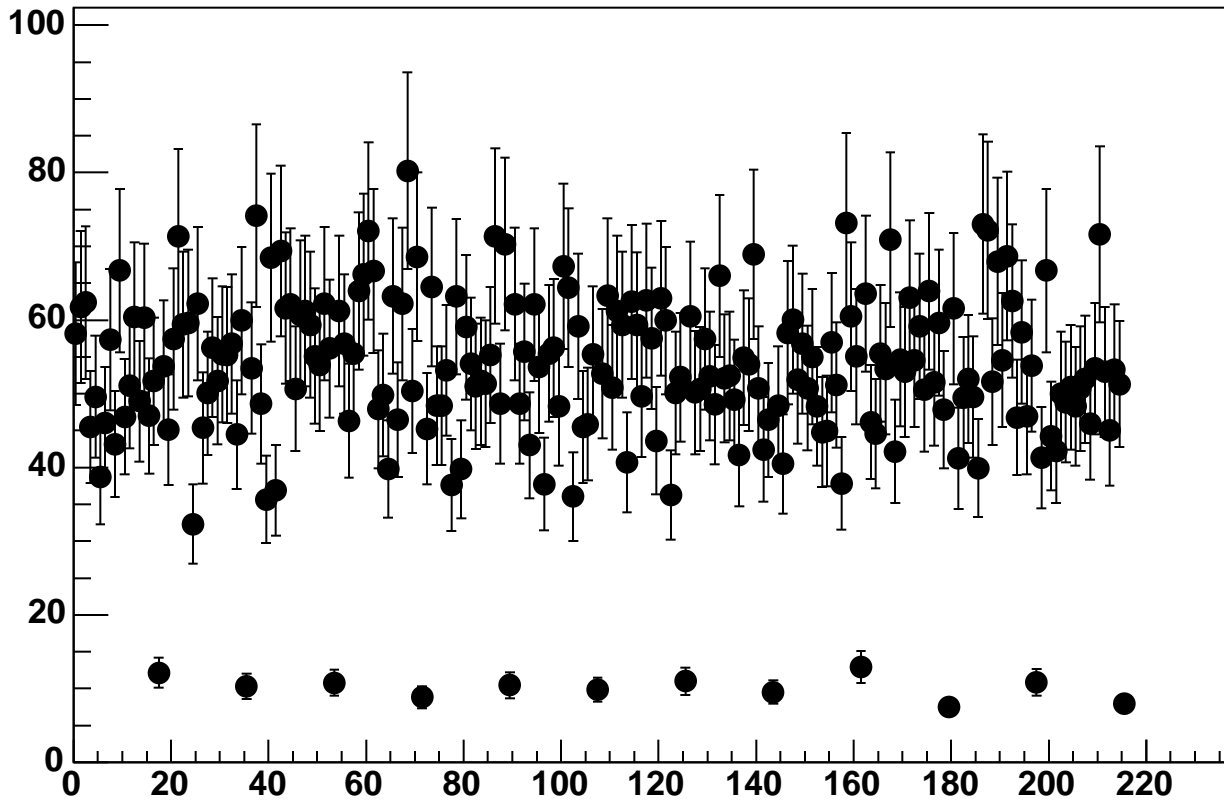
Enable 1, Hold=30, DAC=950, ADC Noise vs 18\*Chip+Chan



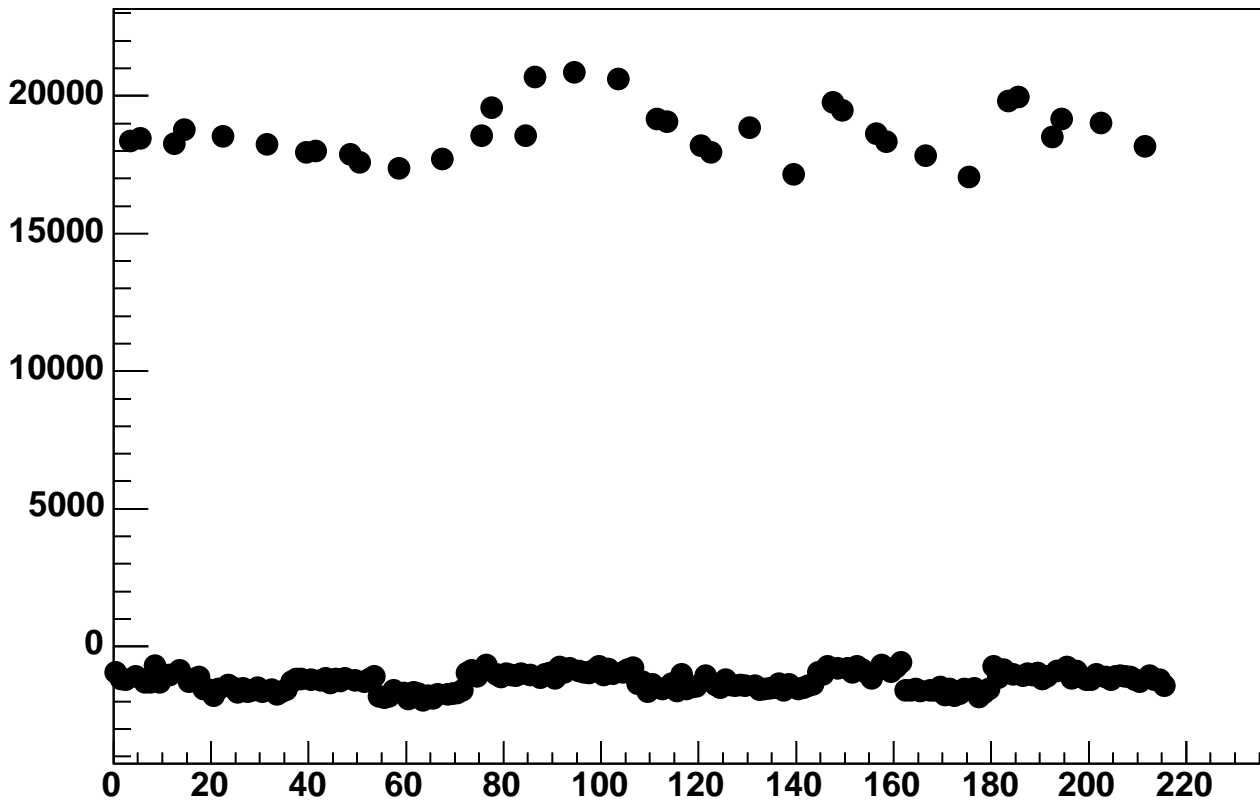
Enable 1, Hold=30, DAC=1000, ADC Mean vs 18\*Chip+Chan



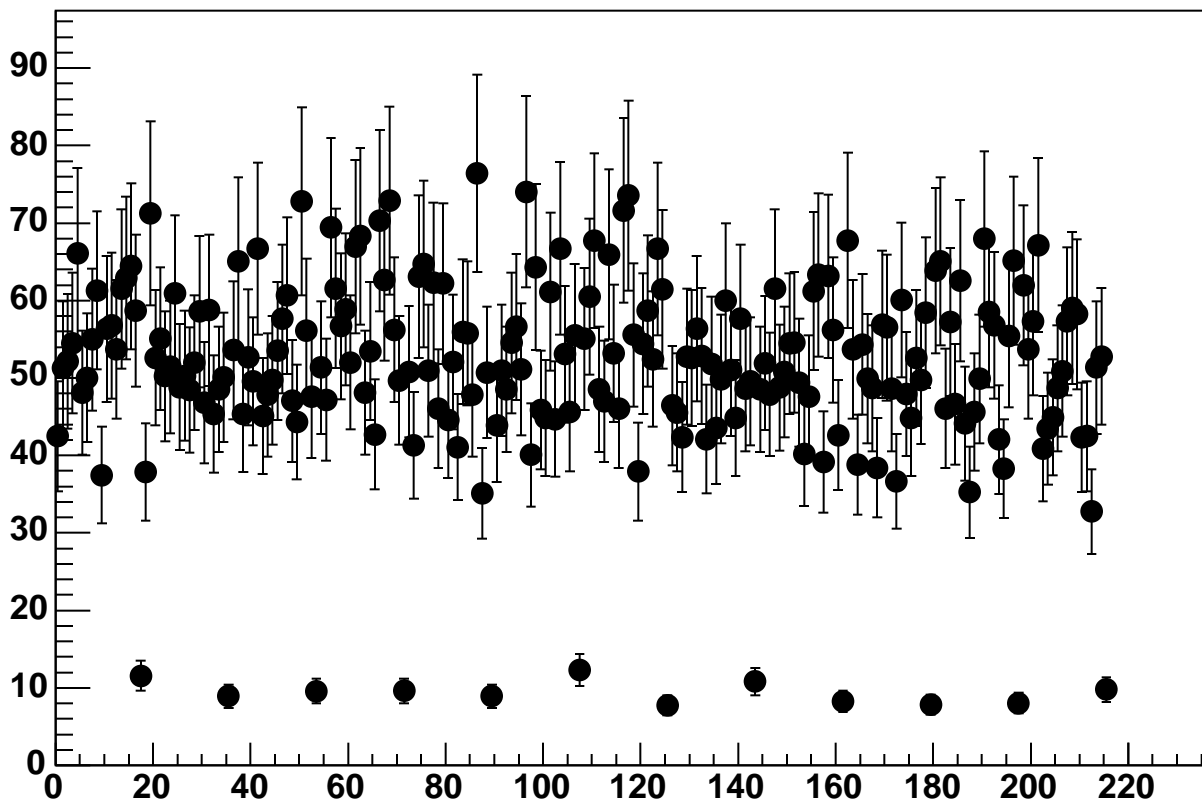
Enable 1, Hold=30, DAC=1000, ADC Noise vs 18\*Chip+Chan



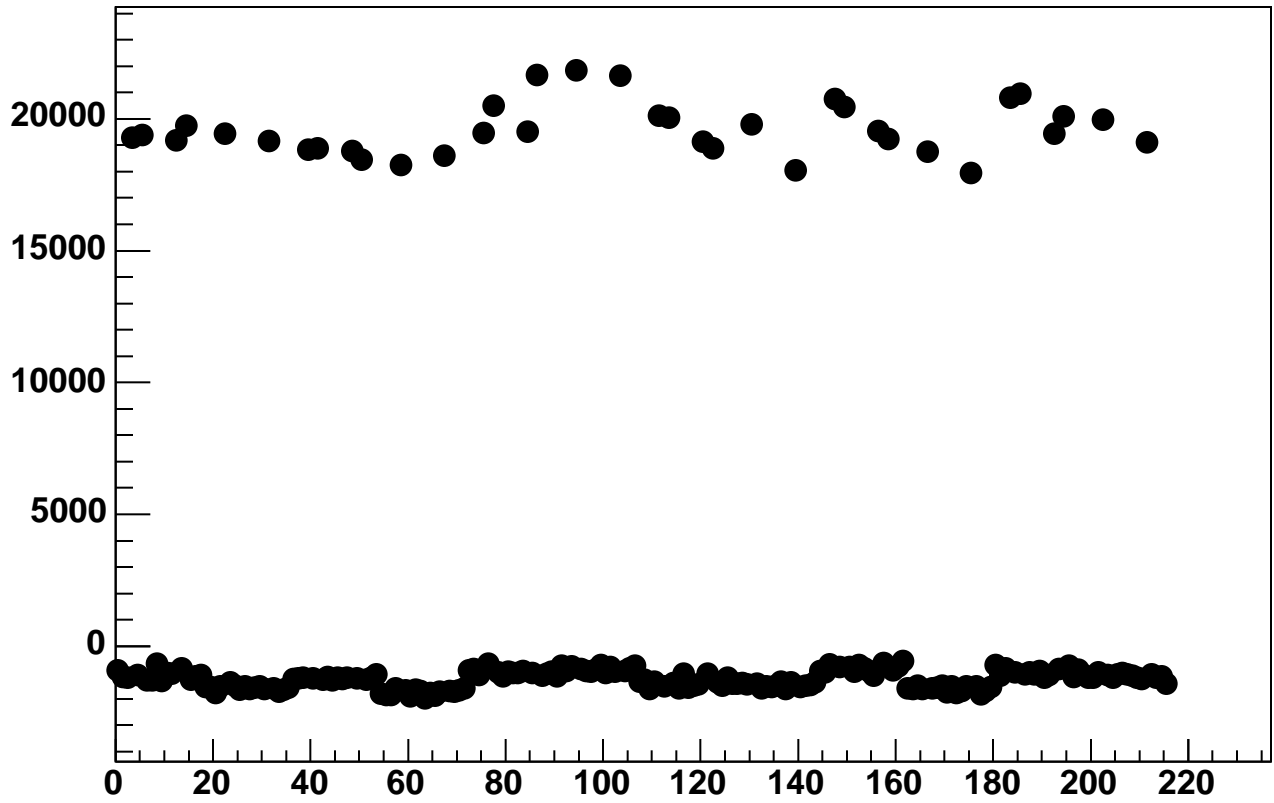
Enable 1, Hold=30, DAC=1050, ADC Mean vs 18\*Chip+Chan



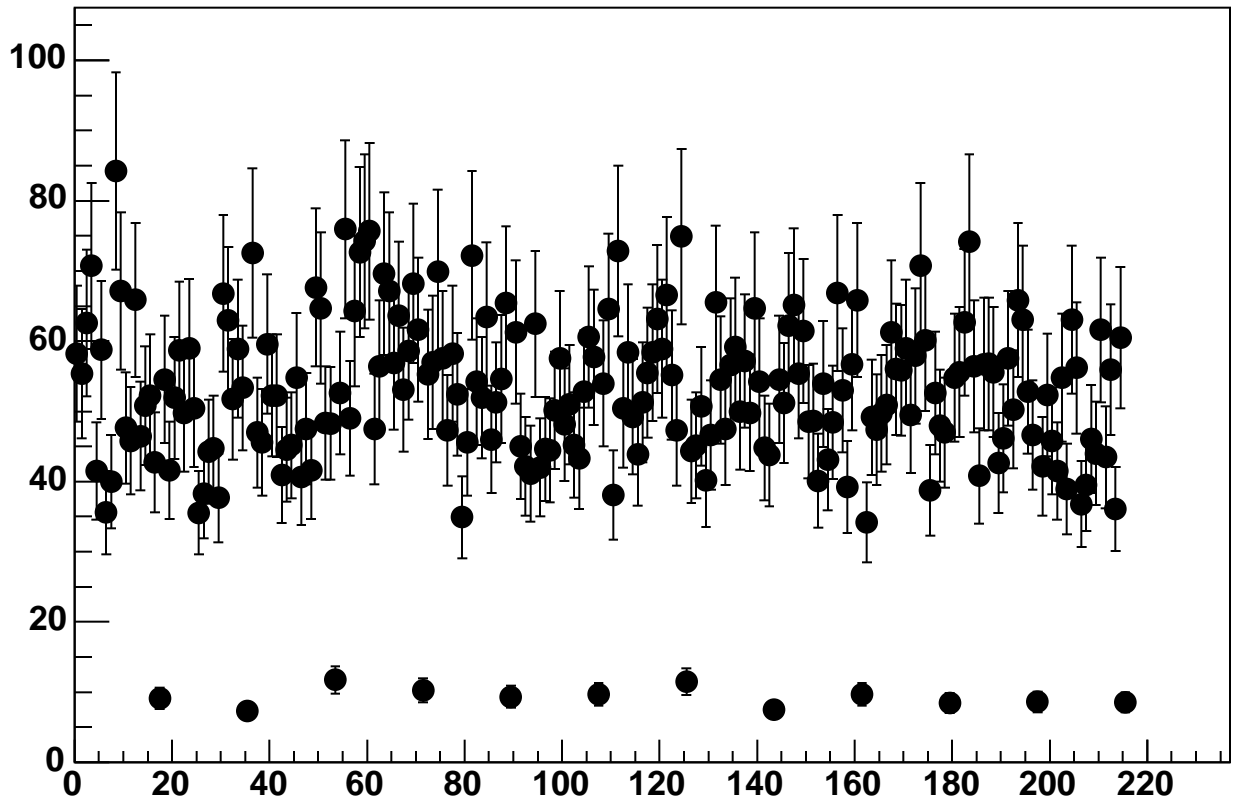
Enable 1, Hold=30, DAC=1050, ADC Noise vs 18\*Chip+Chan



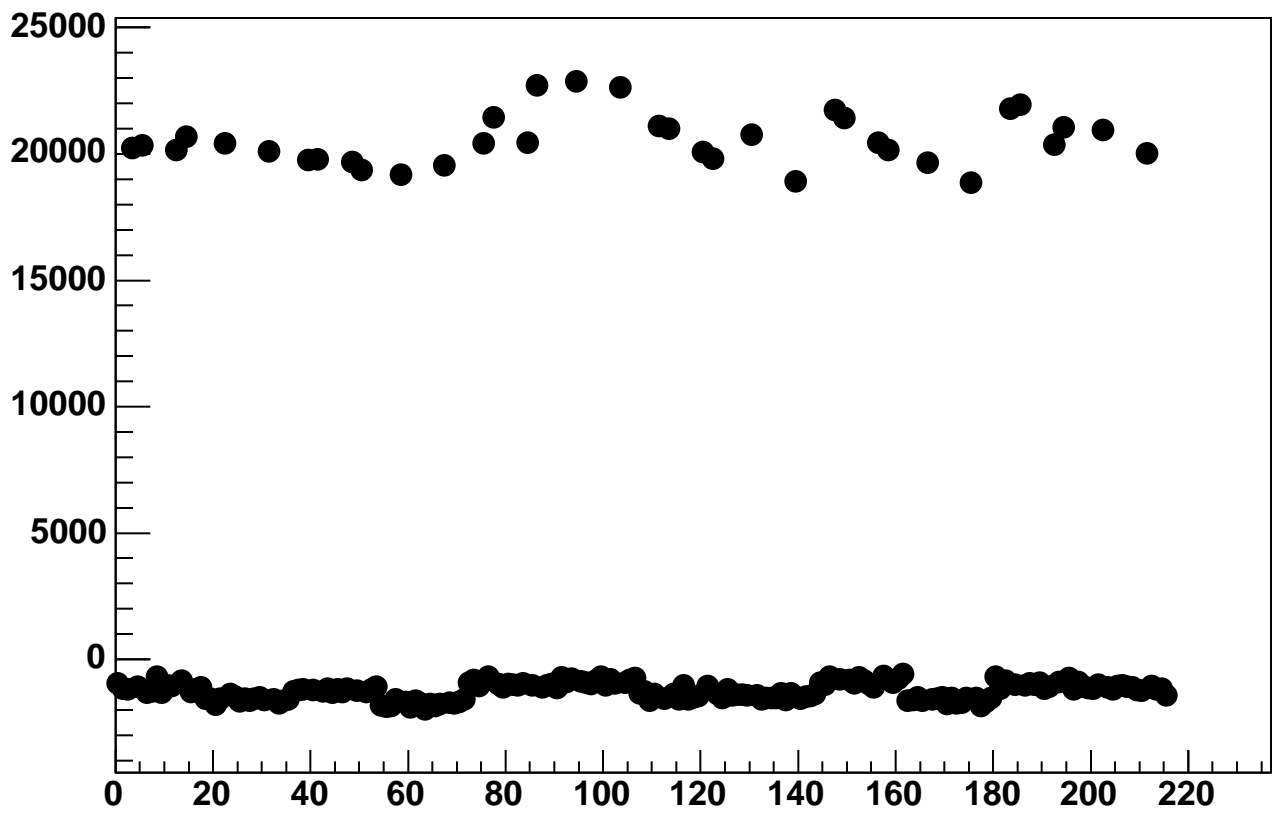
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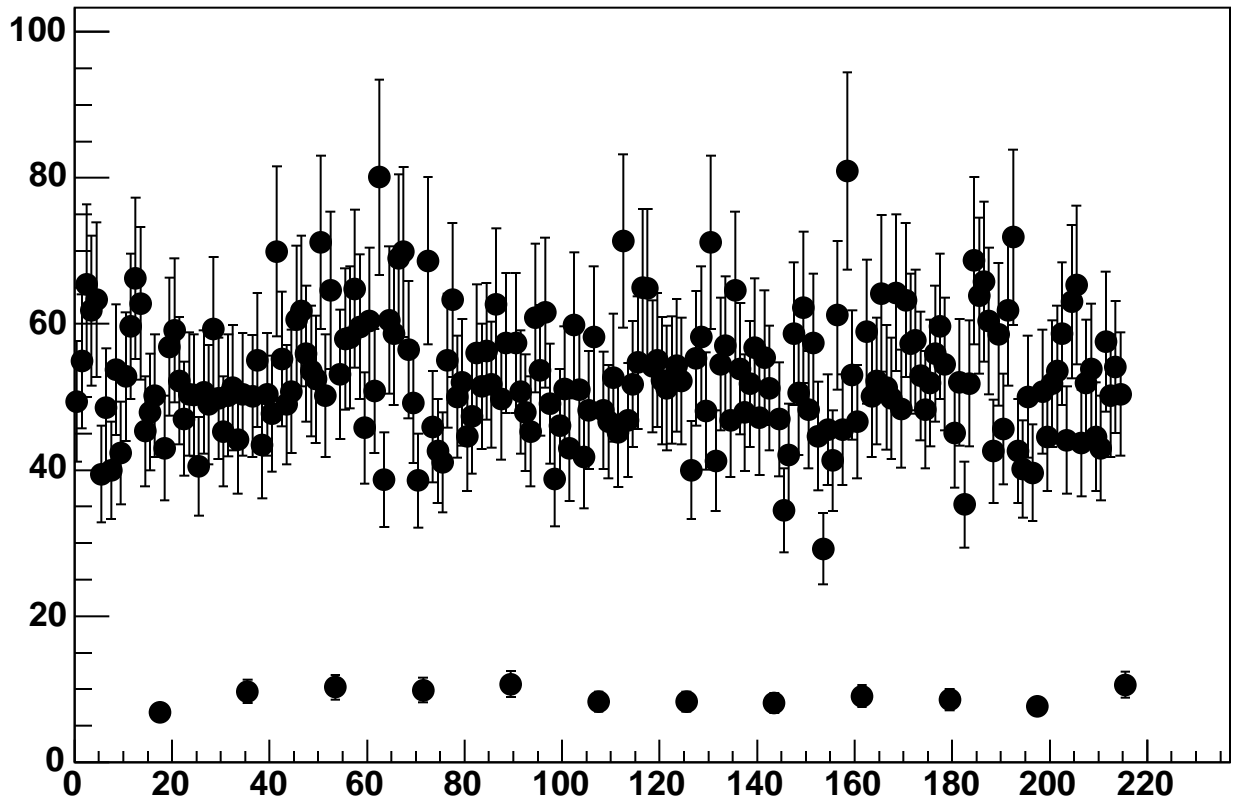
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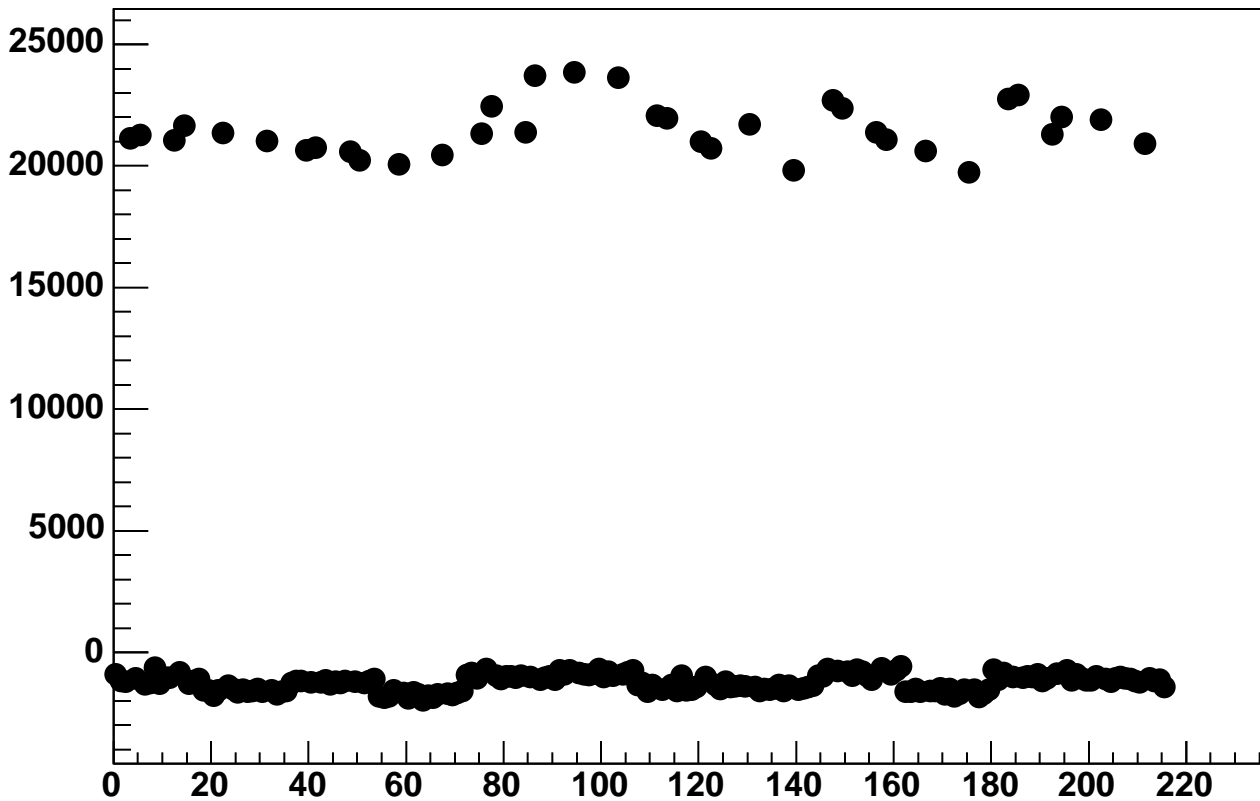
Enable 1, Hold=30, DAC=1150, ADC Mean vs 18\*Chip+Chan



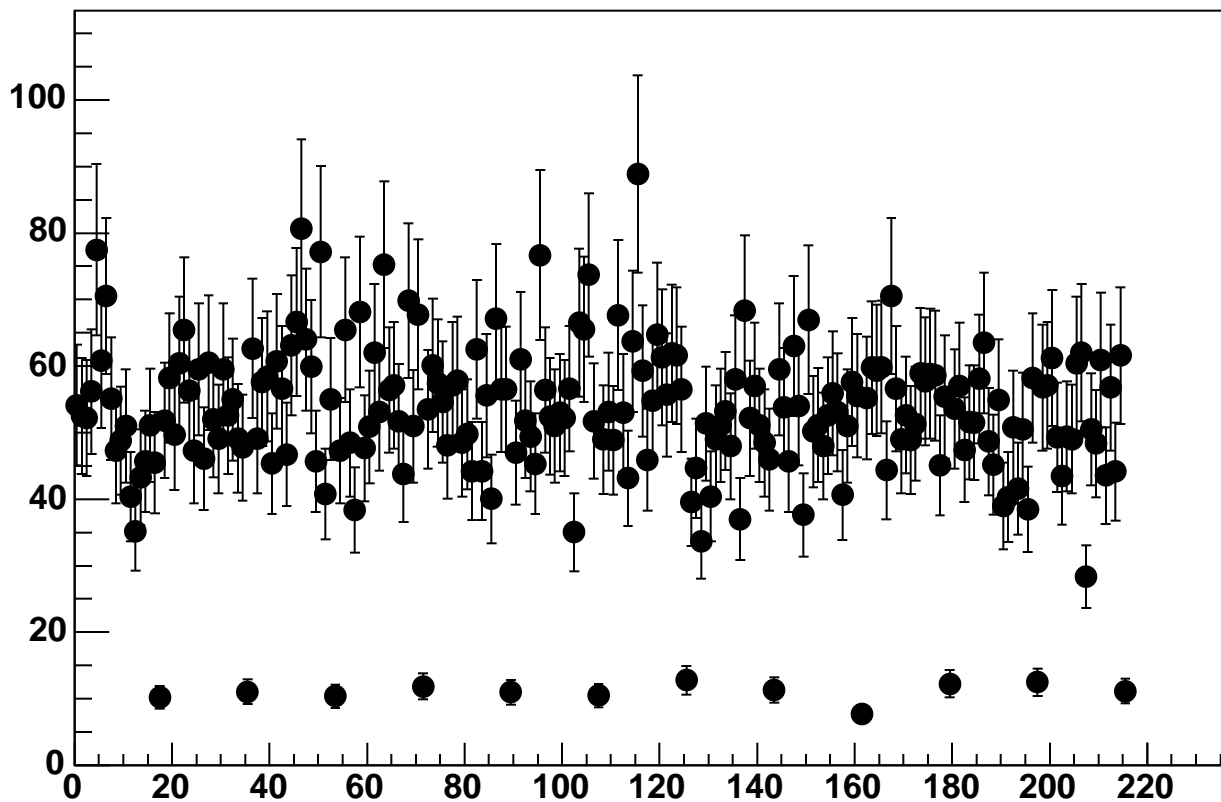
Enable 1, Hold=30, DAC=1150, ADC Noise vs 18\*Chip+Chan



Enable 1, Hold=30, DAC=1200, ADC Mean vs 18\*Chip+Chan

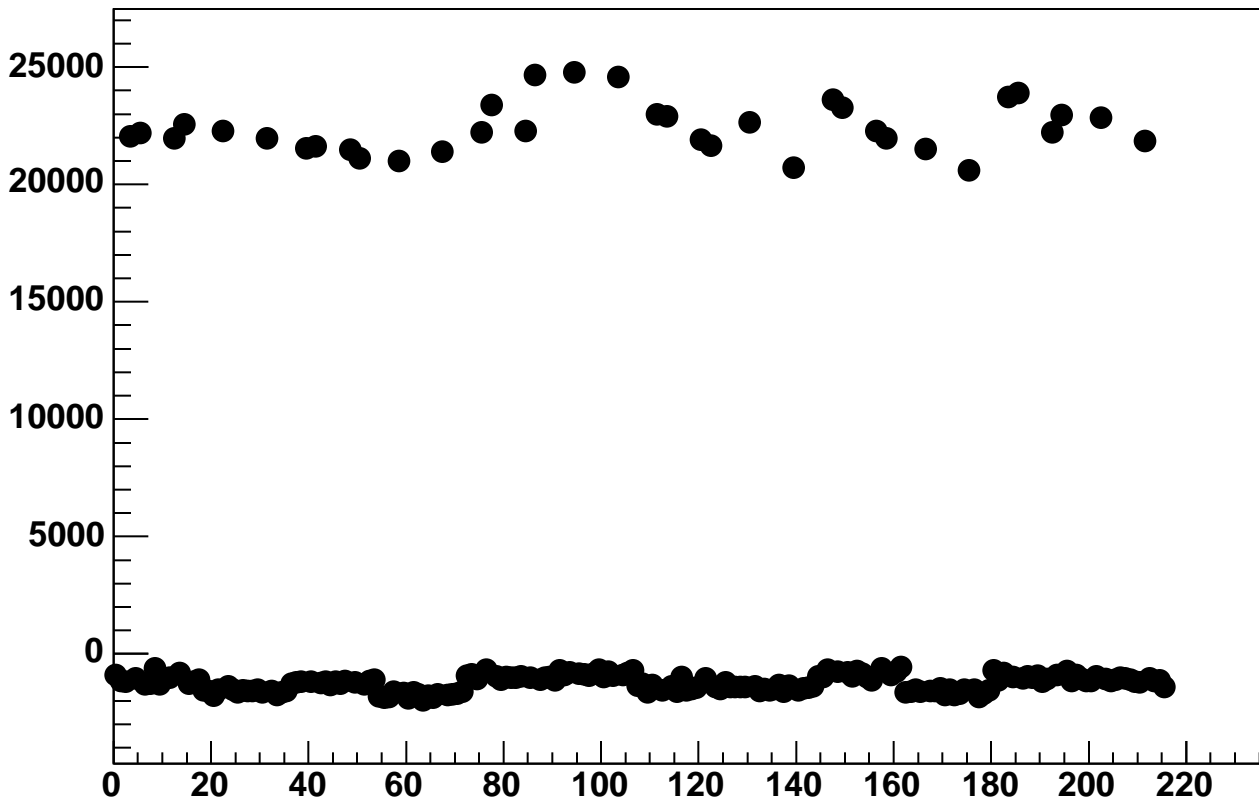


Enable 1, Hold=30, DAC=1200, ADC Noise vs 18\*Chip+Chan

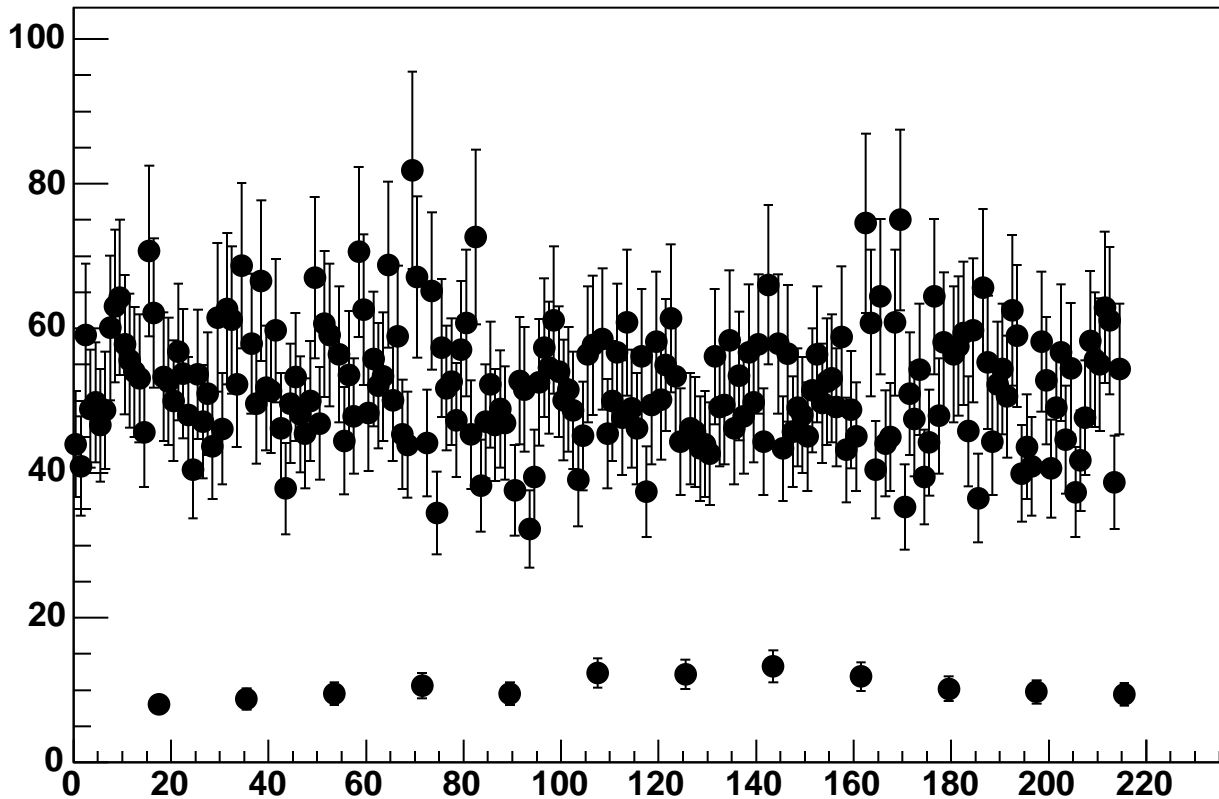




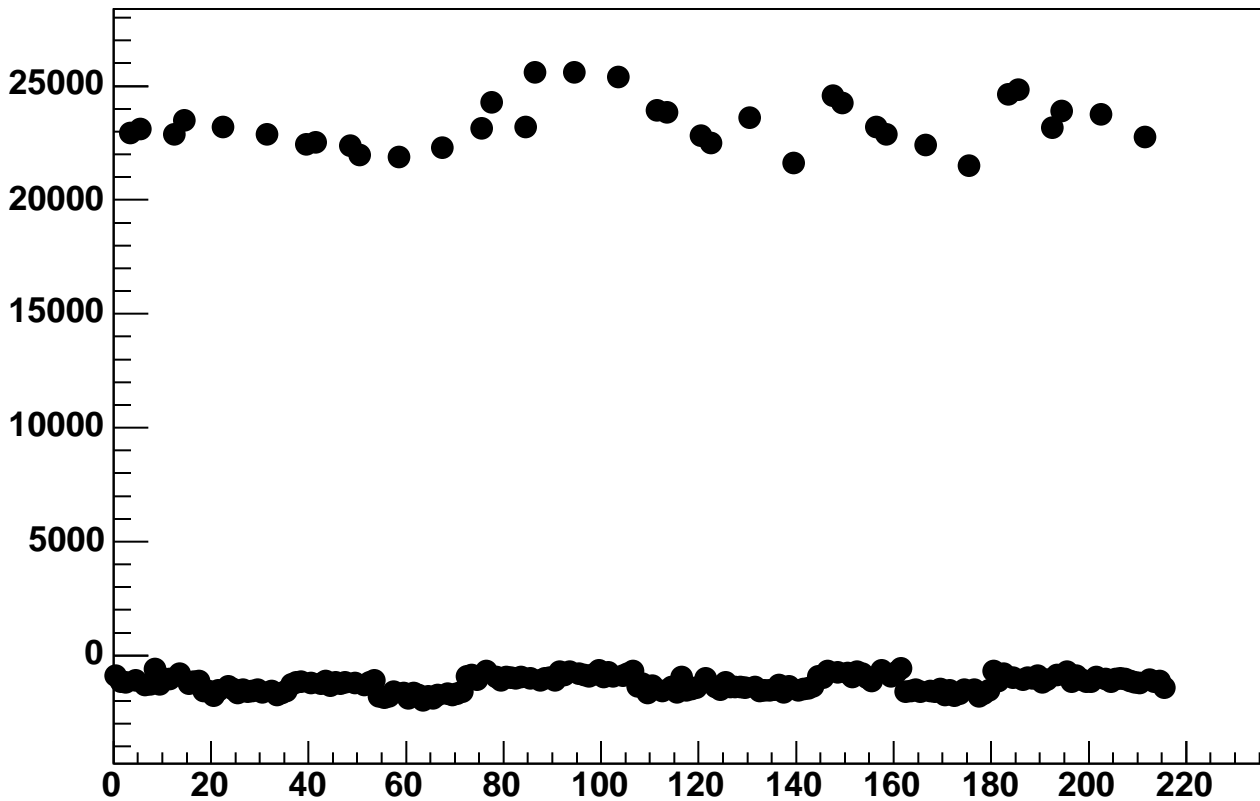
Enable 1, Hold=30, DAC=1250, ADC Mean vs 18\*Chip+Chan



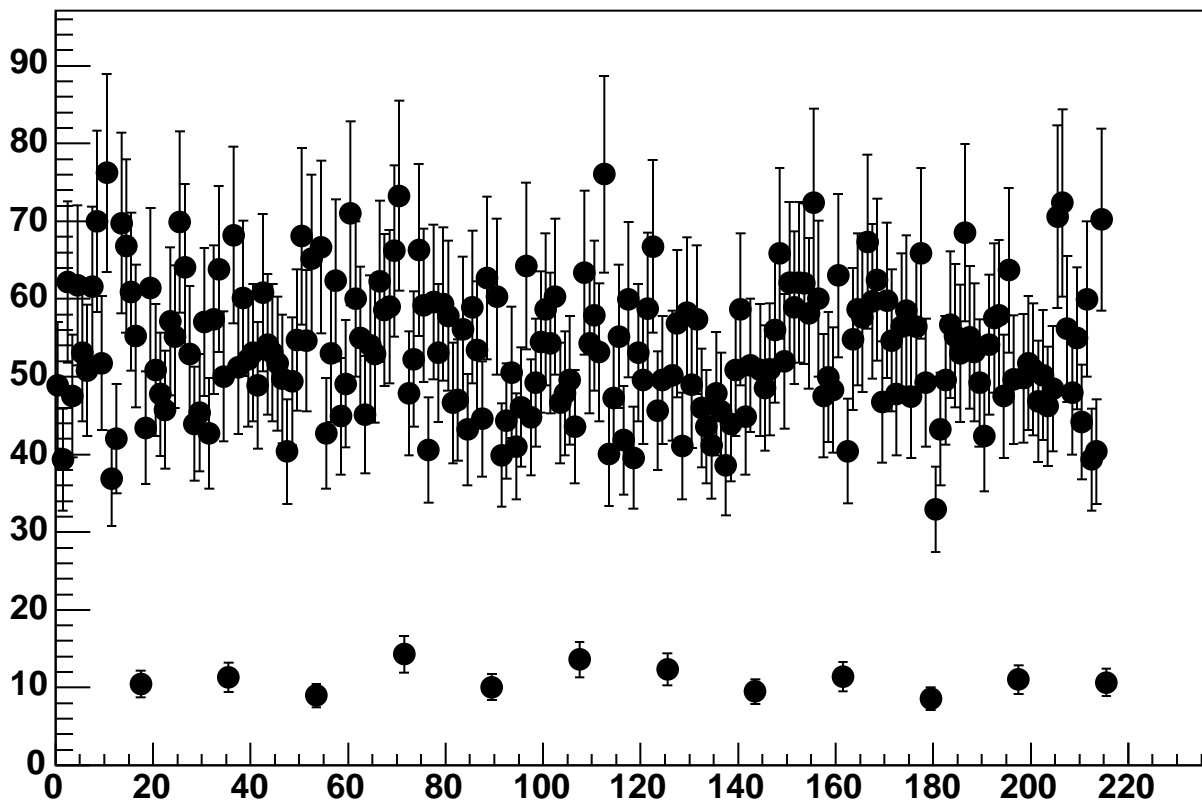
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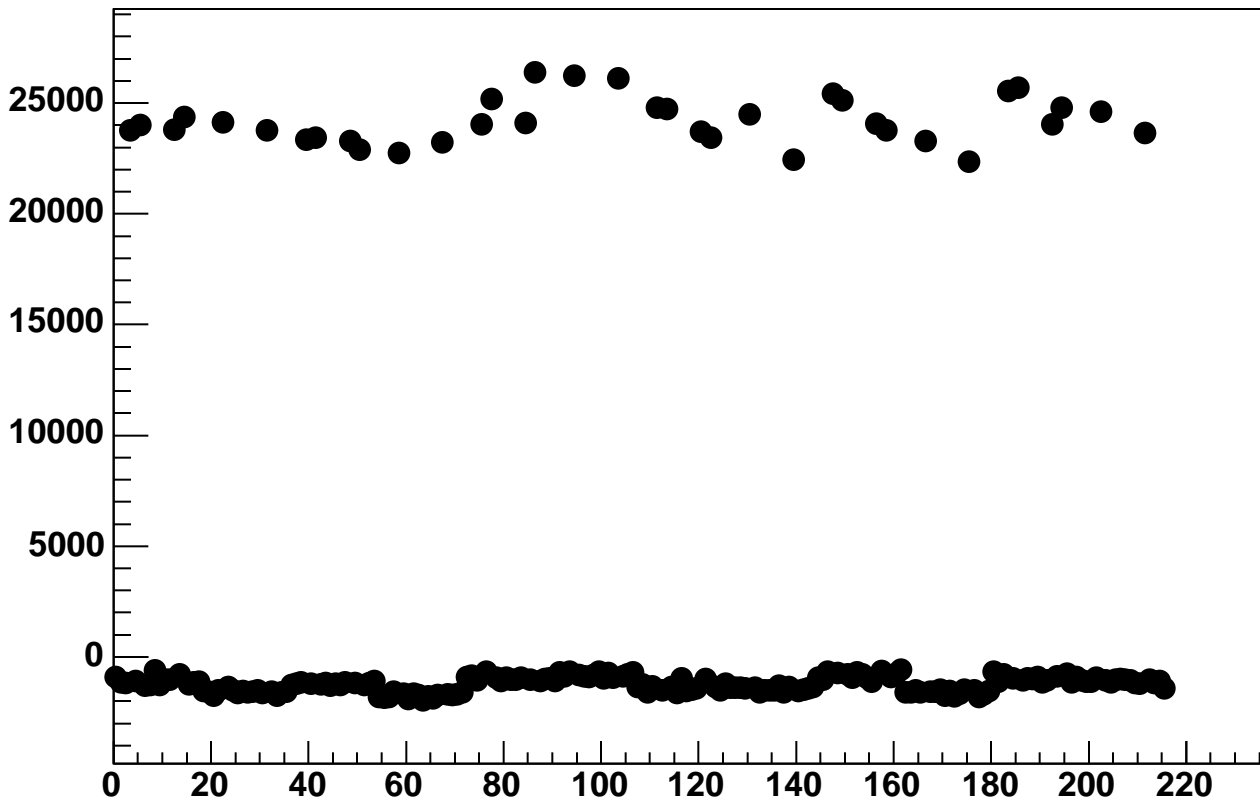
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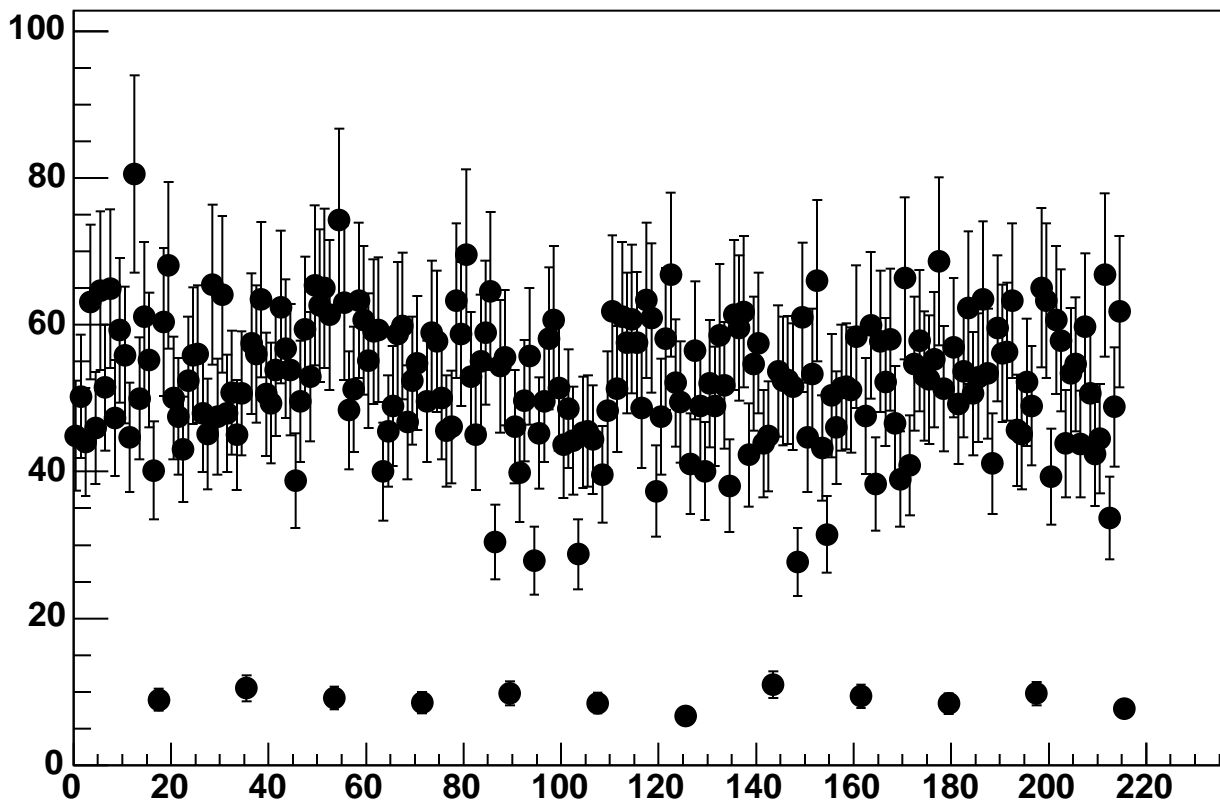
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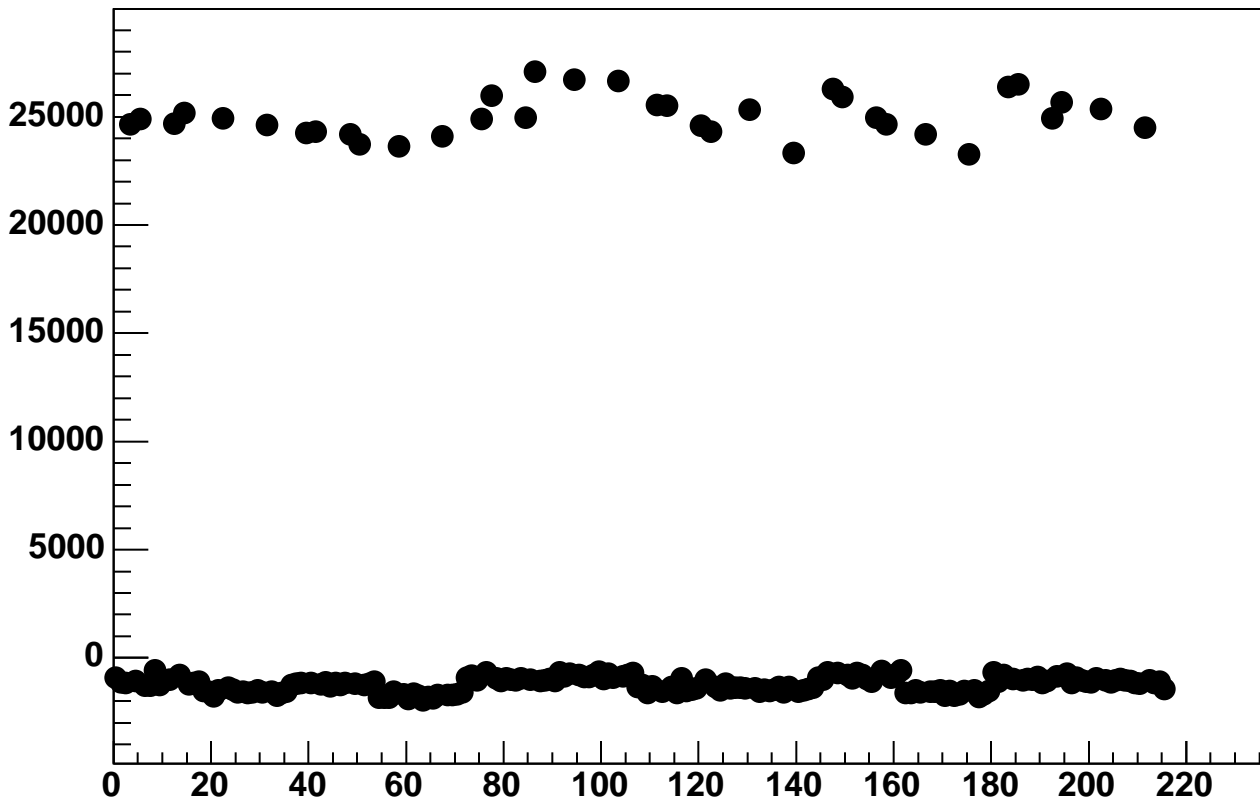
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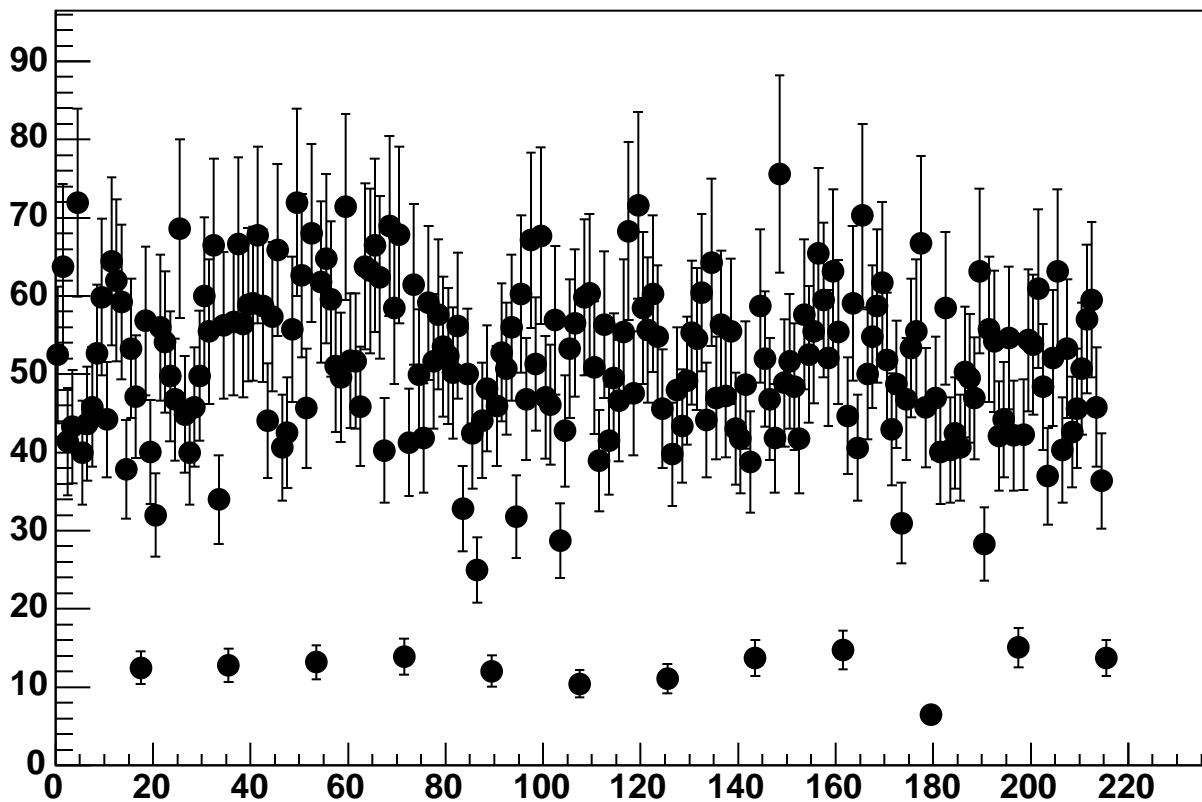
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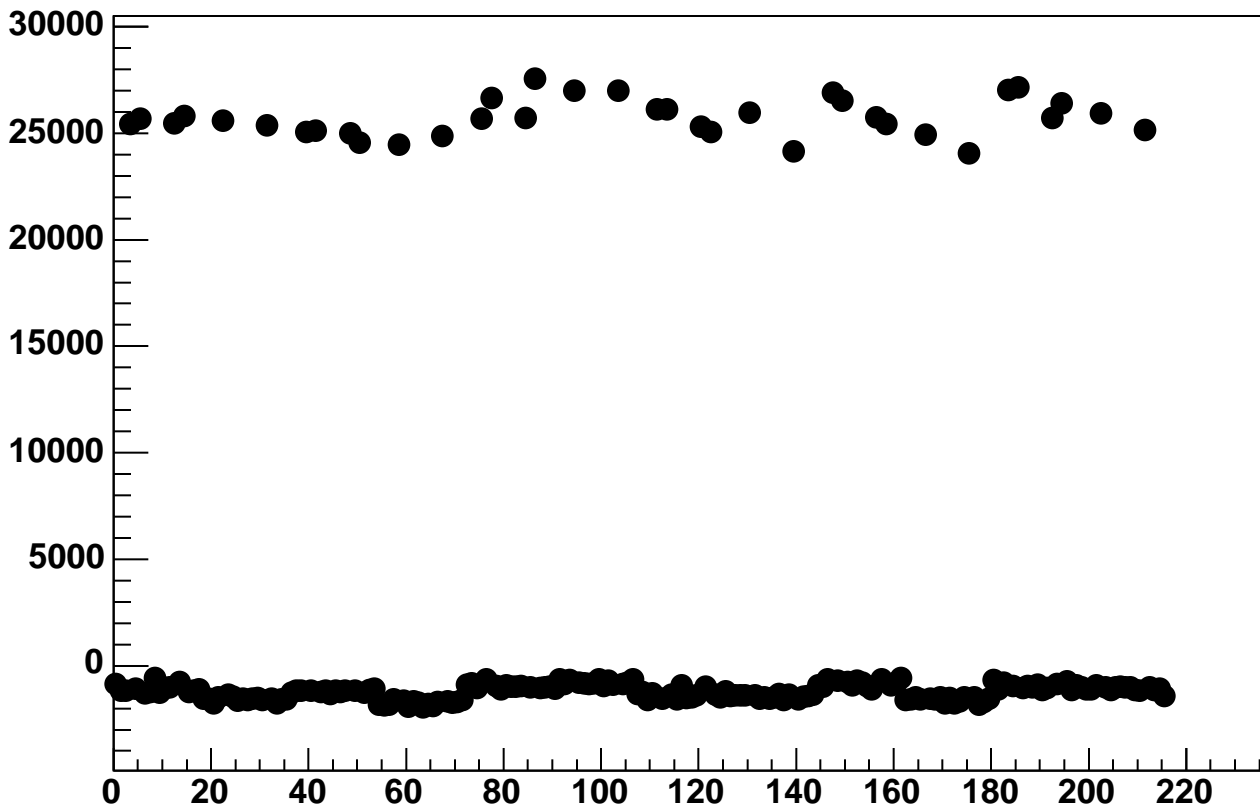
Enable 1, Hold=30, DAC=1400, ADC Mean vs 18\*Chip+Chan



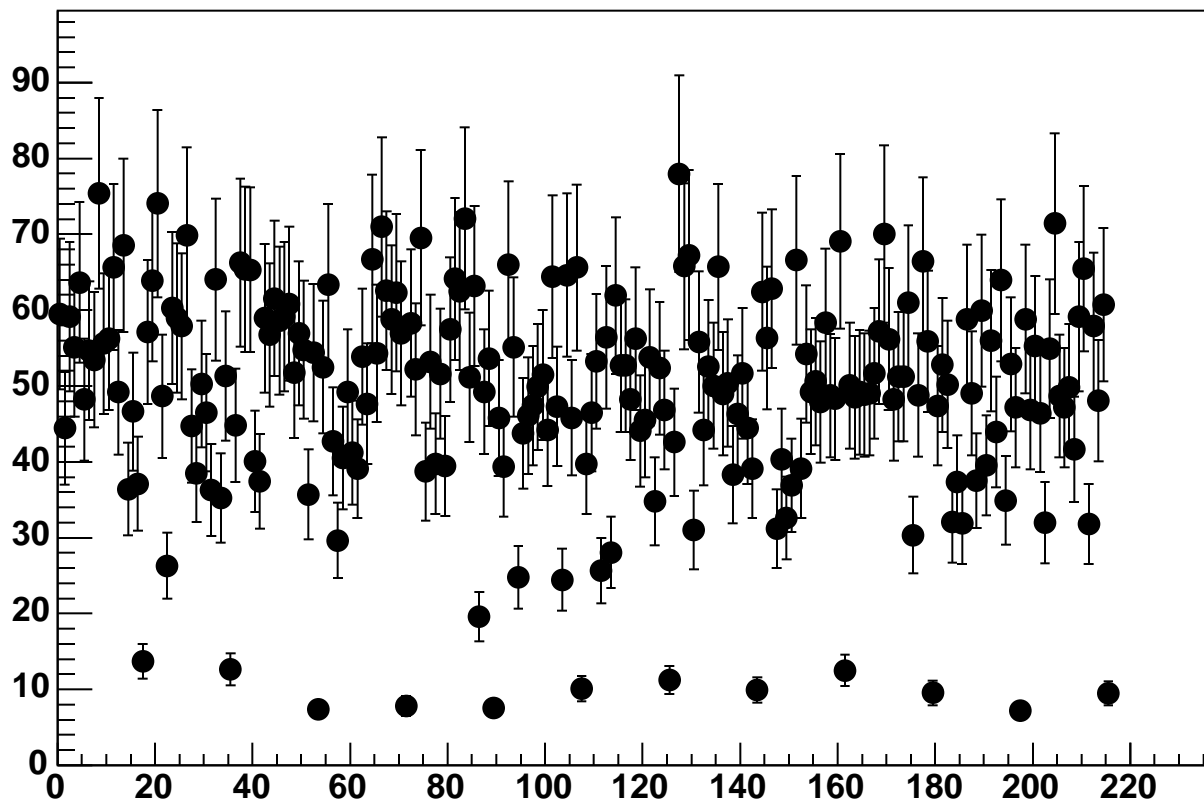
Enable 1, Hold=30, DAC=1400, ADC Noise vs 18\*Chip+Chan



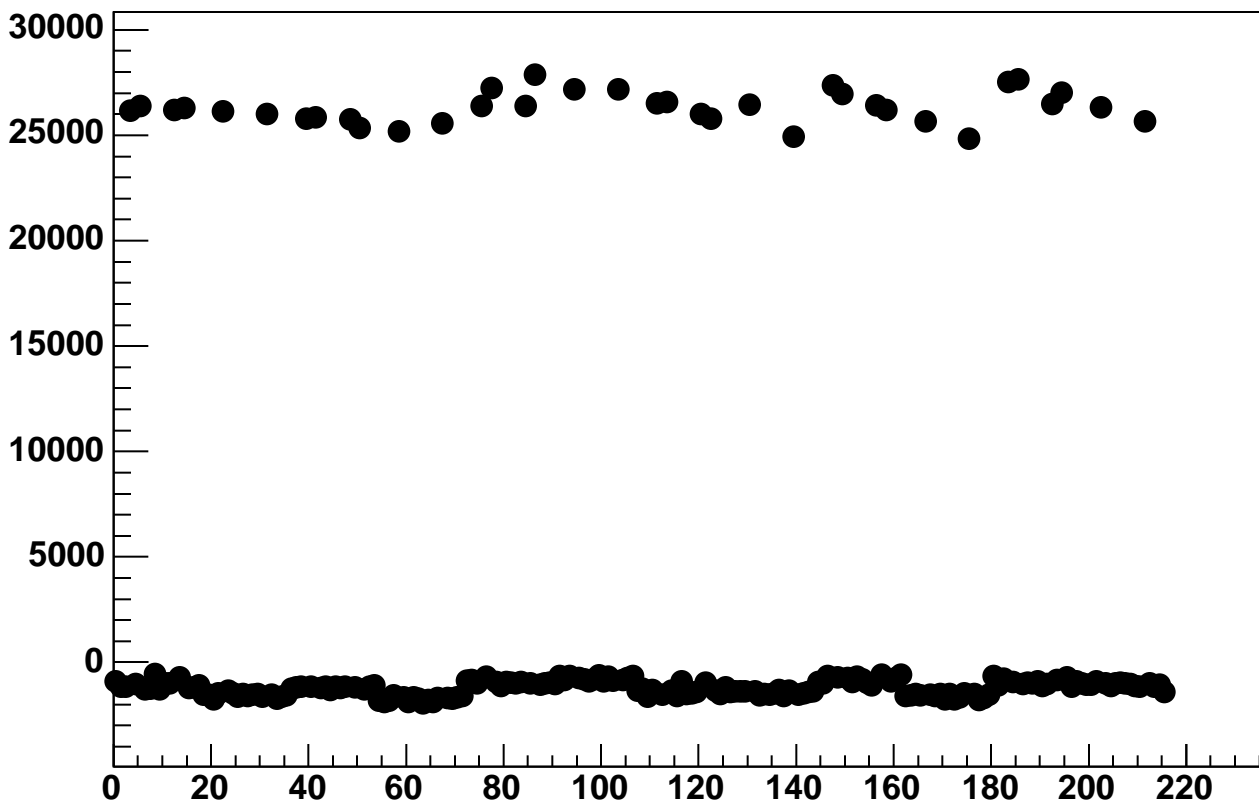
Enable 1, Hold=30, DAC=1450, ADC Mean vs 18\*Chip+Chan



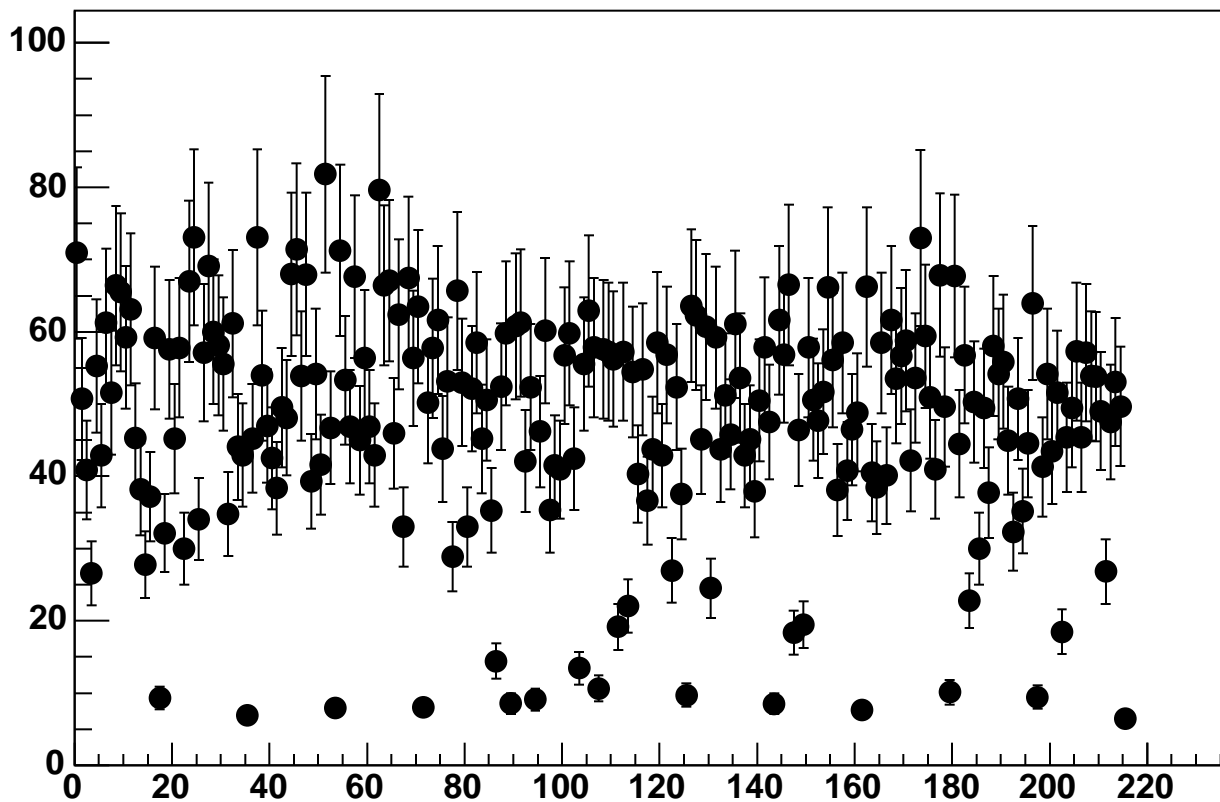
Enable 1, Hold=30, DAC=1450, ADC Noise vs 18\*Chip+Chan



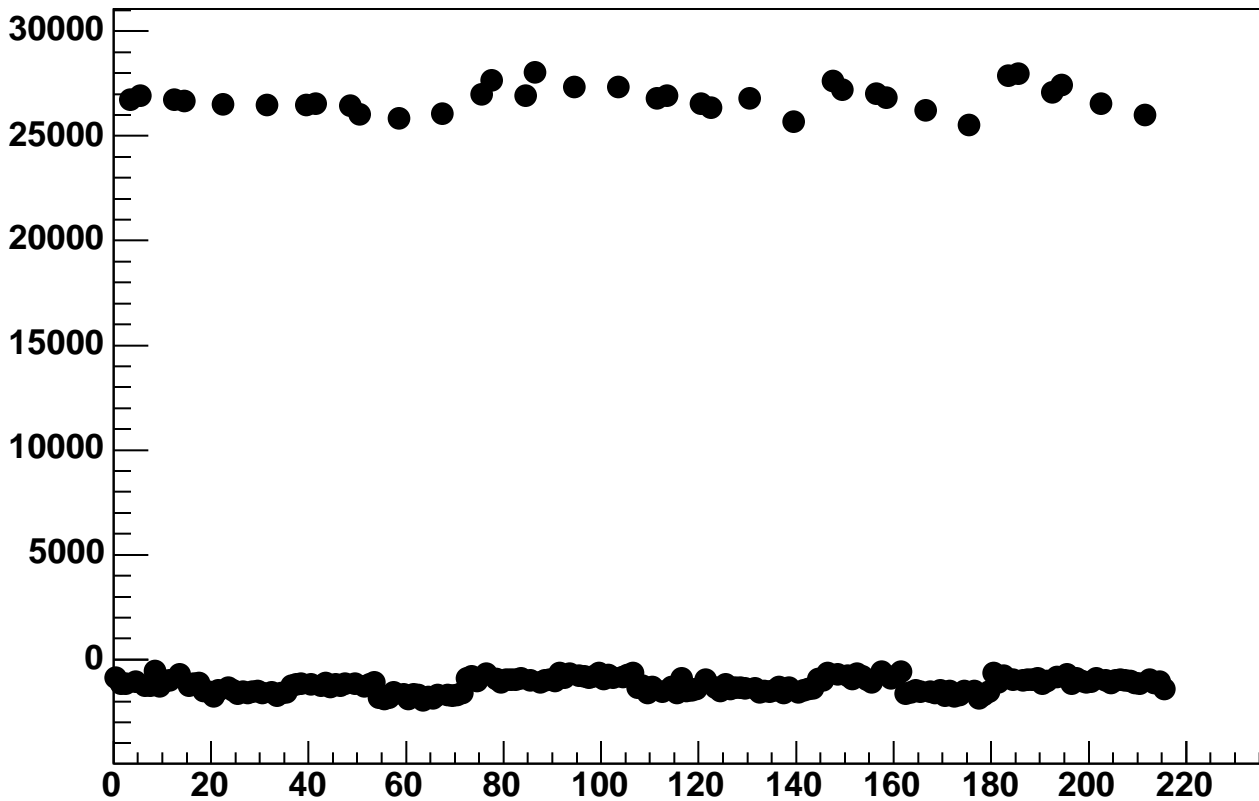
Enable 1, Hold=30, DAC=1500, ADC Mean vs 18\*Chip+Chan



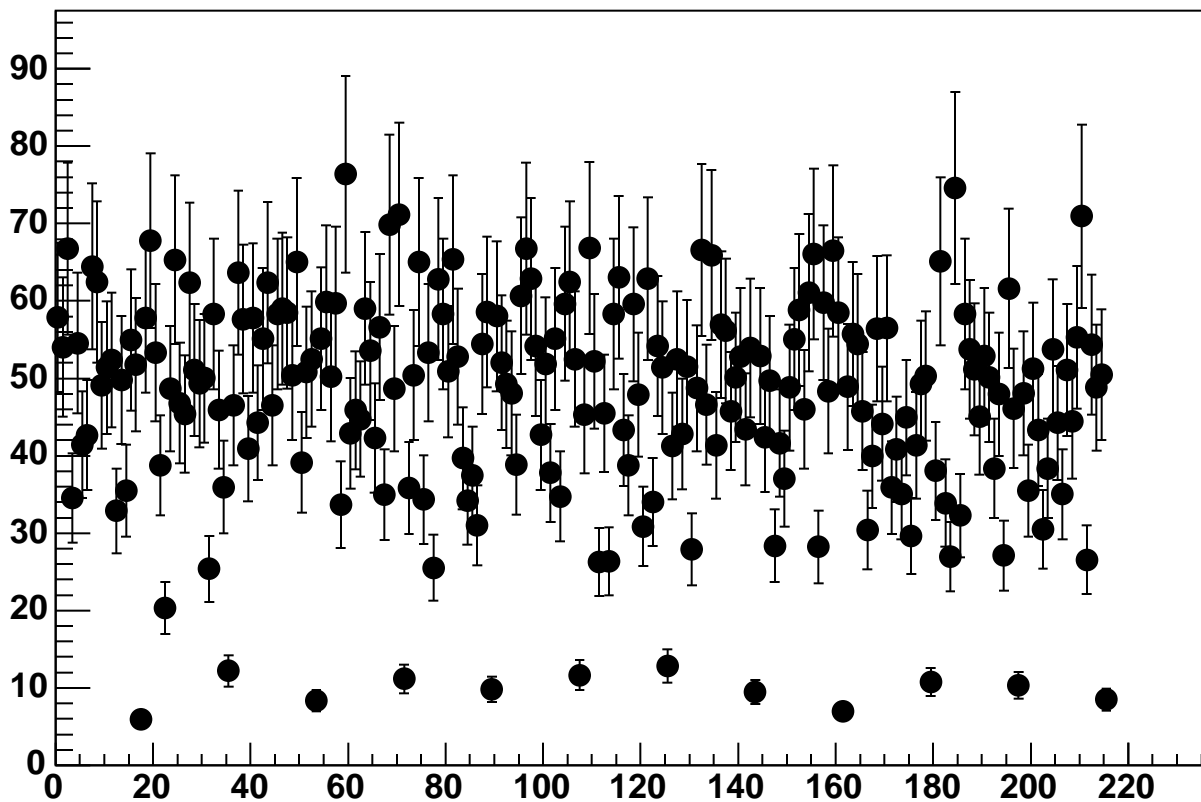
Enable 1, Hold=30, DAC=1500, ADC Noise vs 18\*Chip+Chan



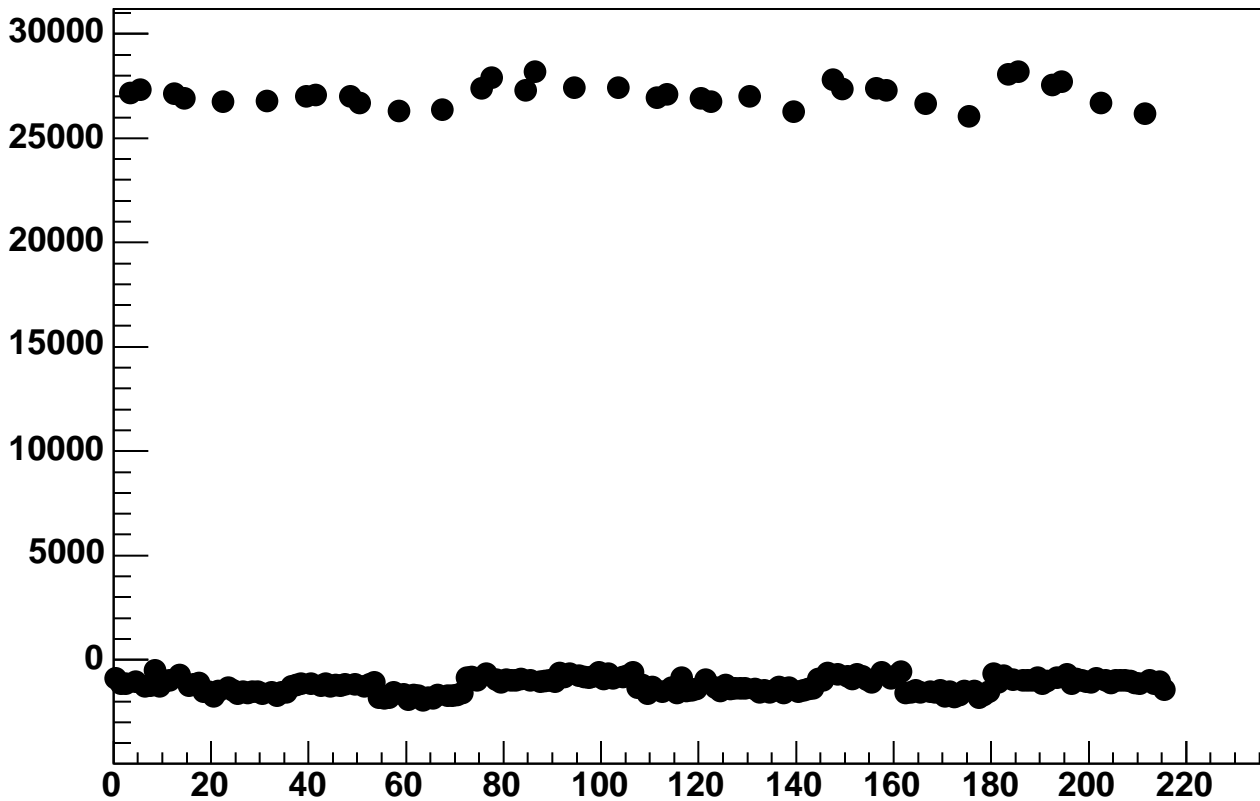
Enable 1, Hold=30, DAC=1550, ADC Mean vs 18\*Chip+Chan



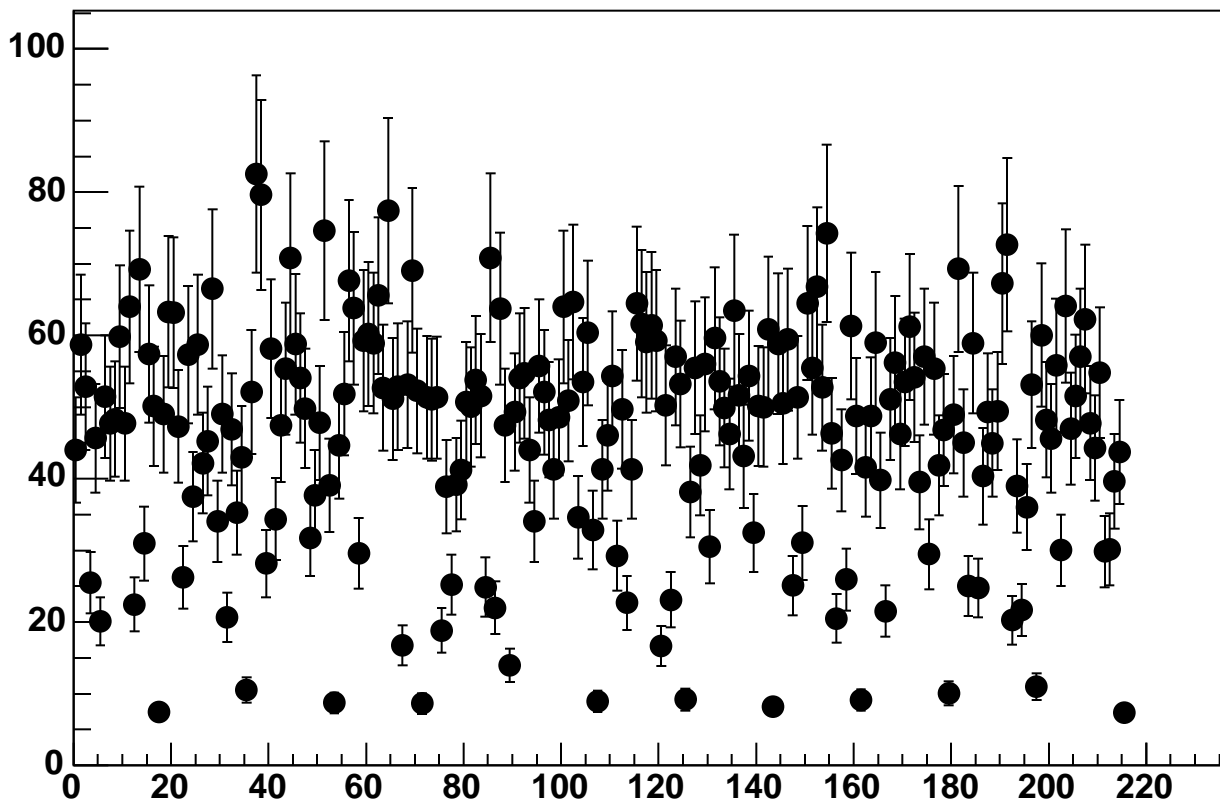
Enable 1, Hold=30, DAC=1550, ADC Noise vs 18\*Chip+Chan



Enable 1, Hold=30, DAC=1600, ADC Mean vs 18\*Chip+Chan

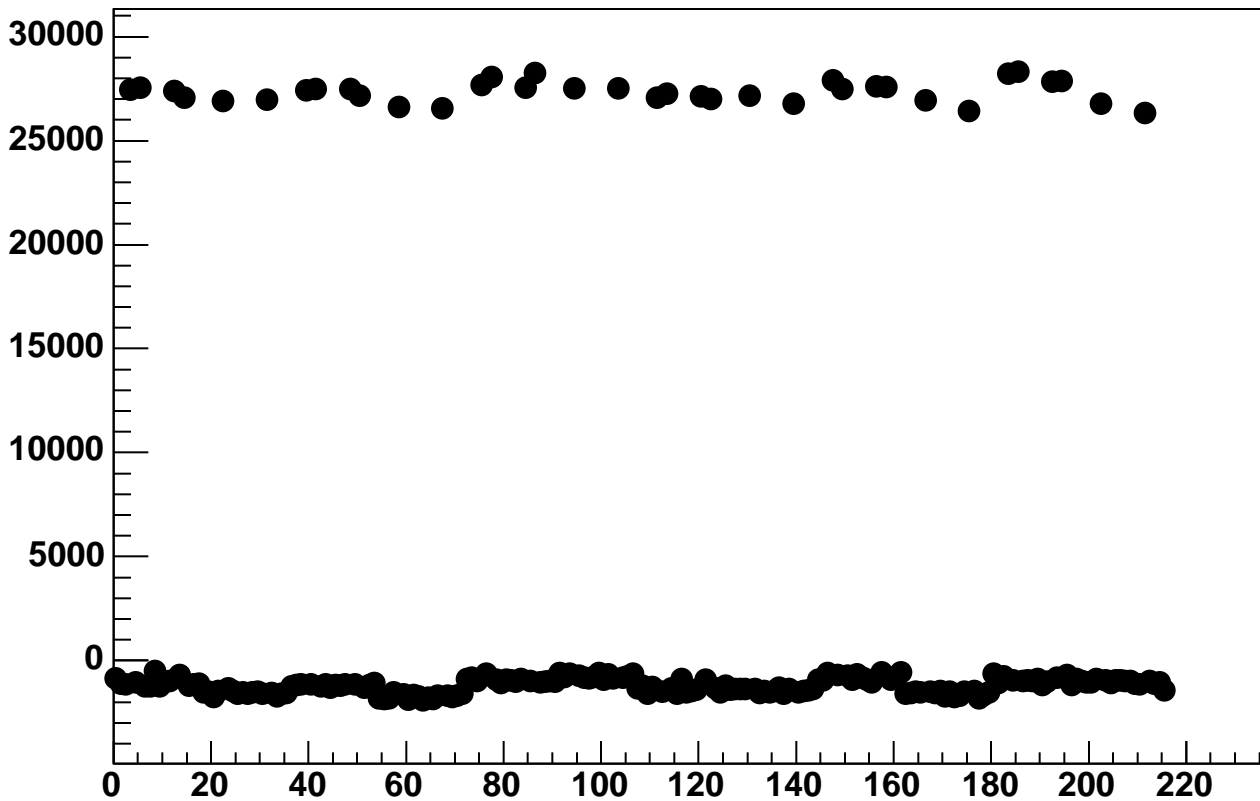


Enable 1, Hold=30, DAC=1600, ADC Noise vs 18\*Chip+Chan

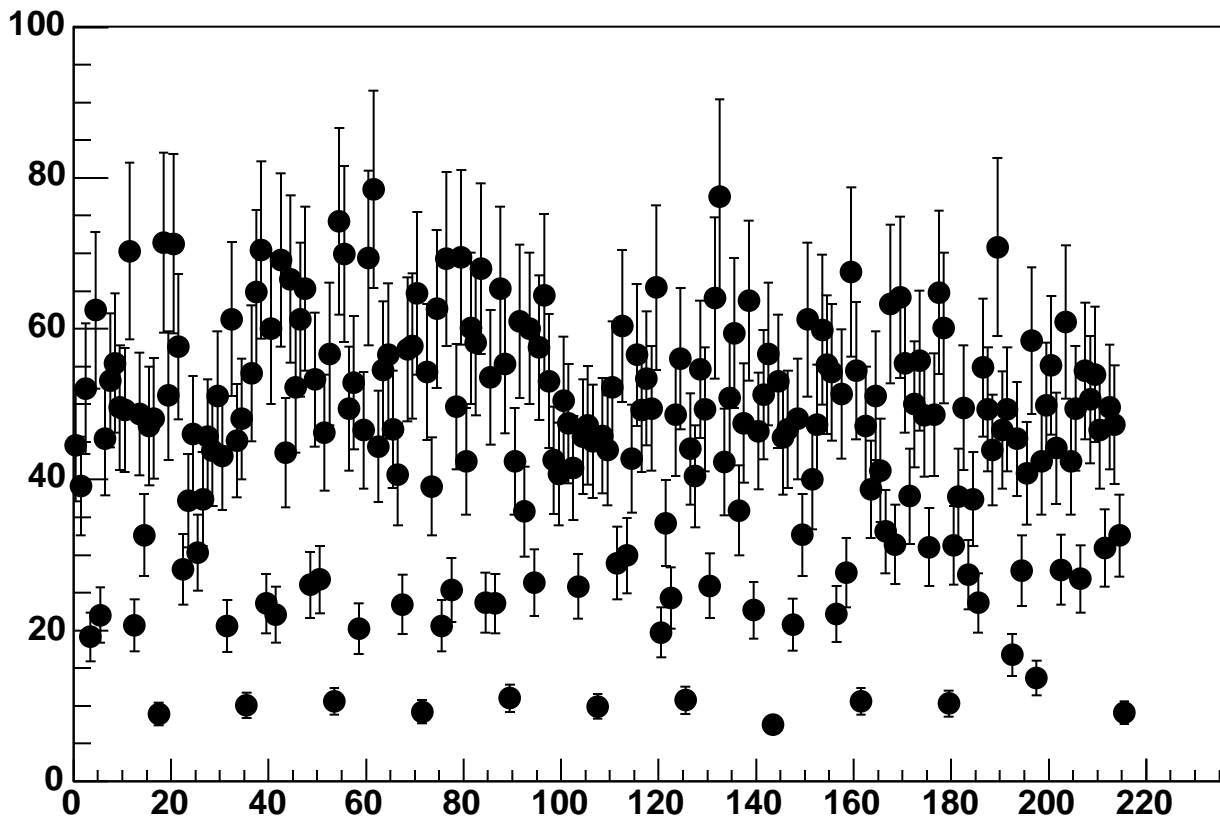




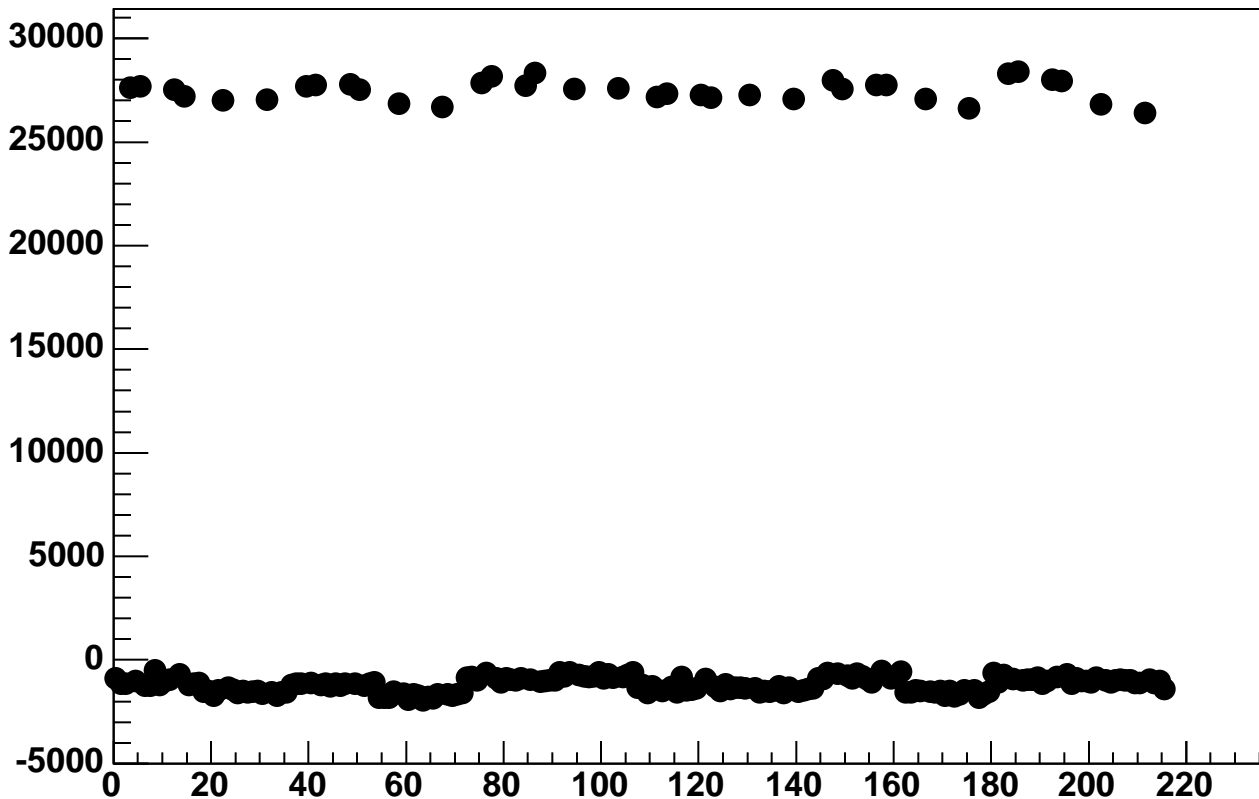
Enable 1, Hold=30, DAC=1650, ADC Mean vs 18\*Chip+Chan



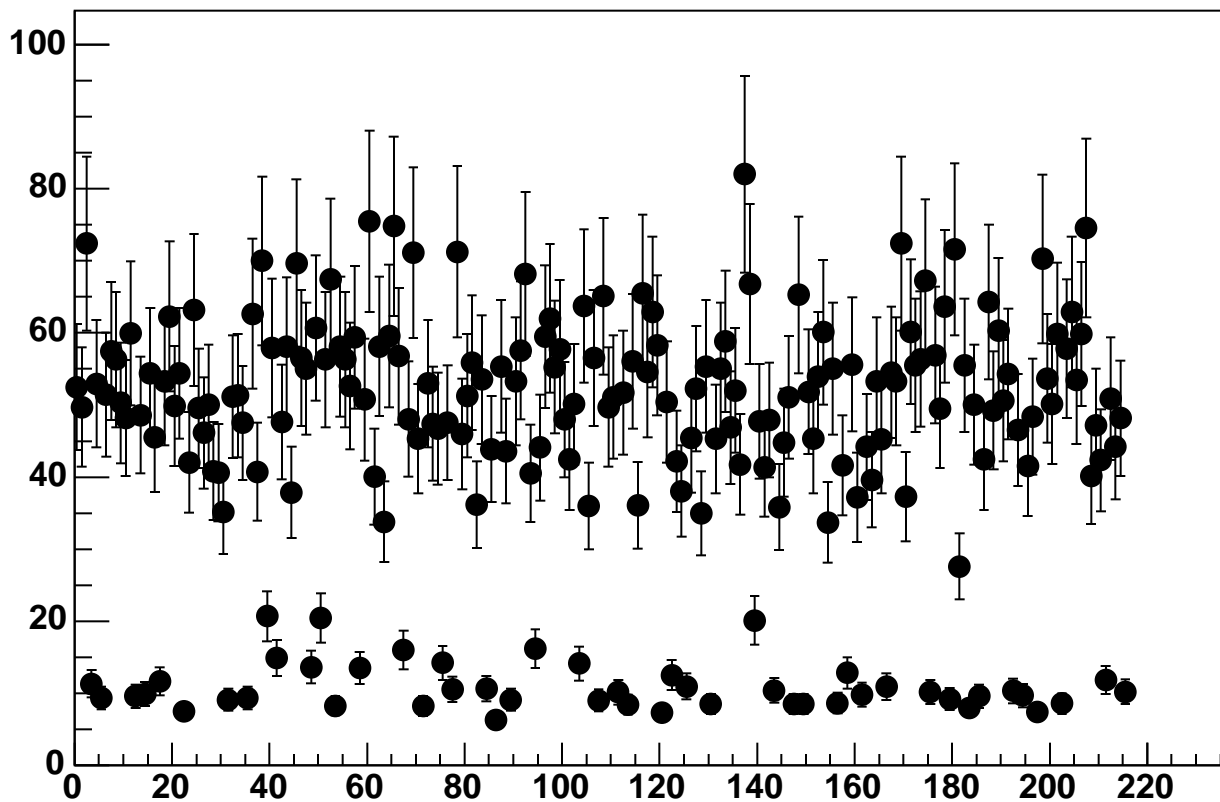
Enable 1, Hold=30, DAC=1650, ADC Noise vs 18\*Chip+Chan



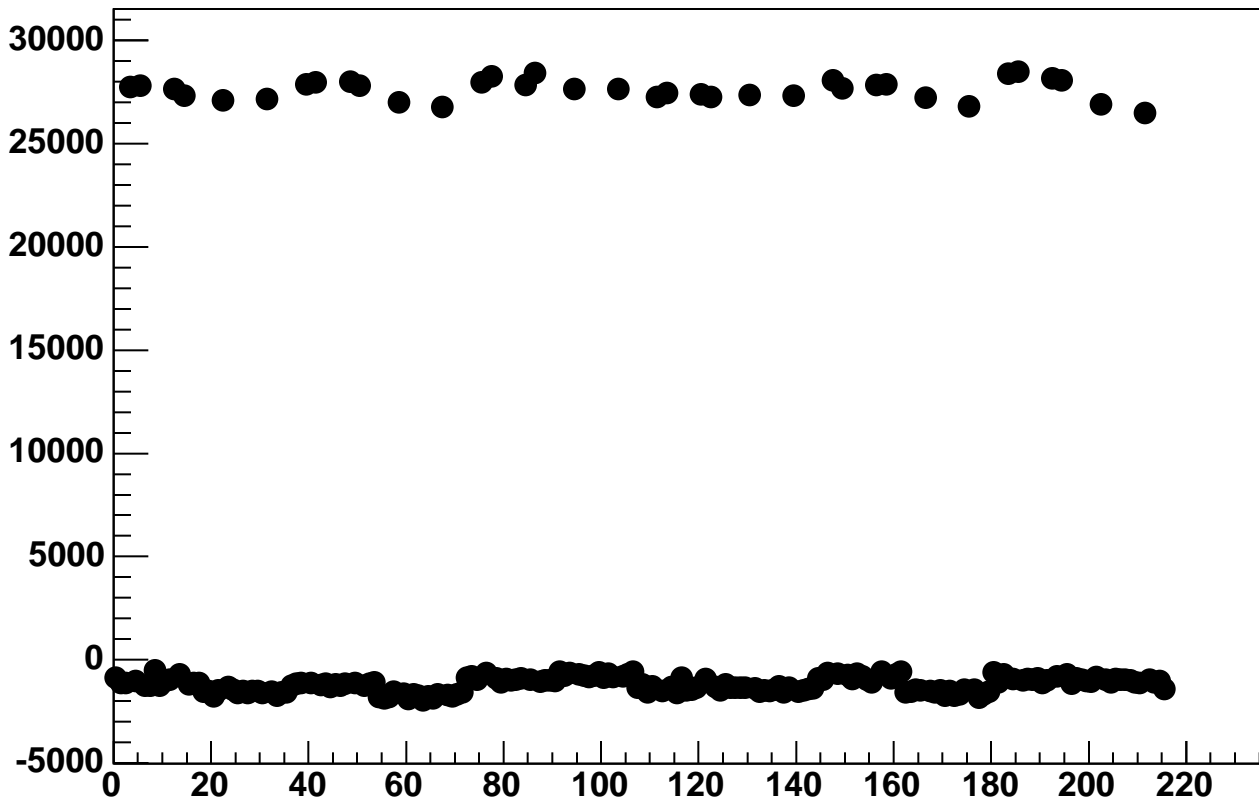
Enable 1, Hold=30, DAC=1700, ADC Mean vs 18\*Chip+Chan



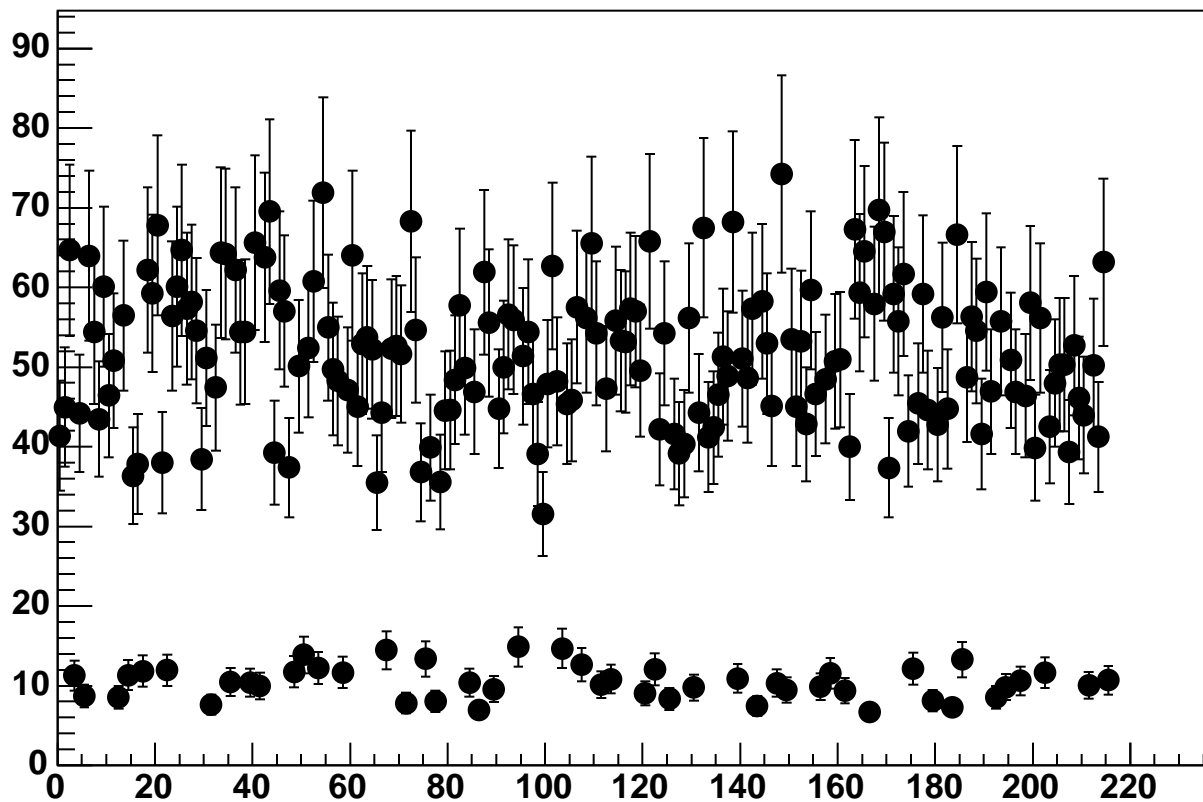
Enable 1, Hold=30, DAC=1700, ADC Noise vs 18\*Chip+Chan



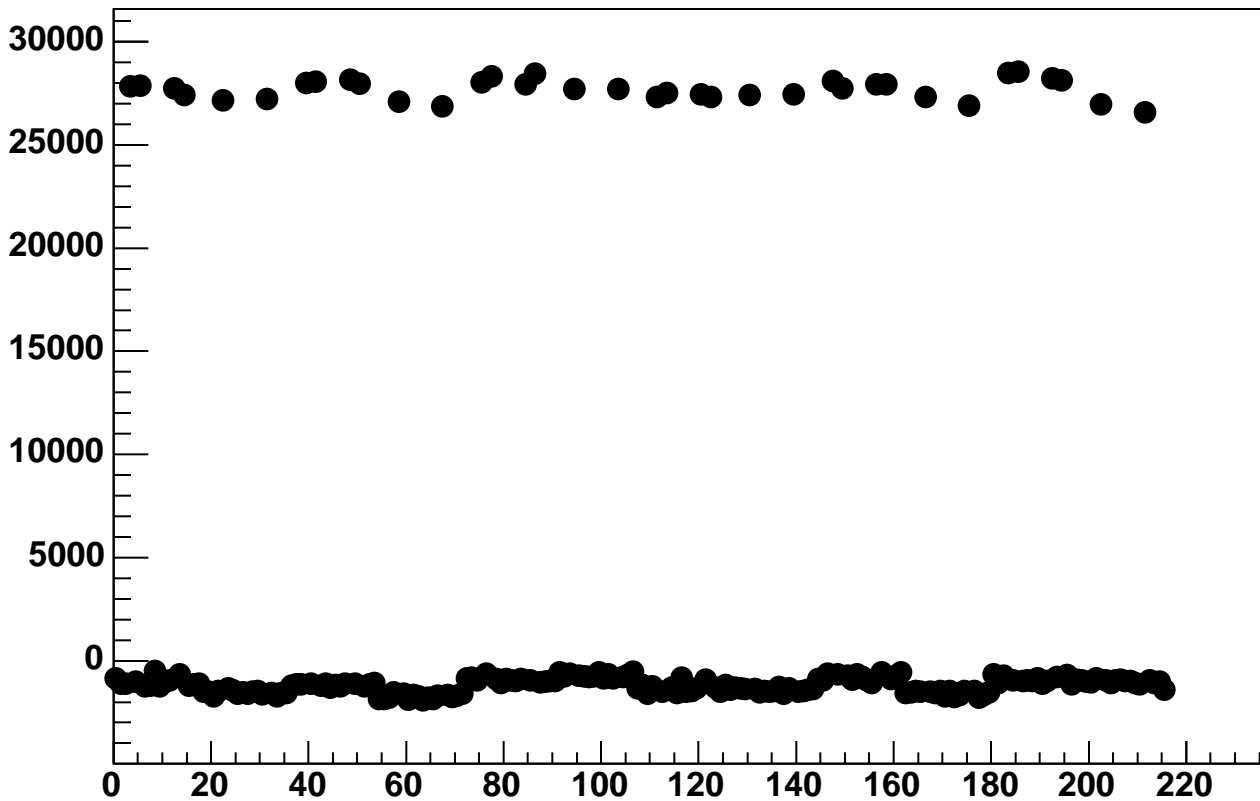
Enable 1, Hold=30, DAC=1750, ADC Mean vs 18\*Chip+Chan



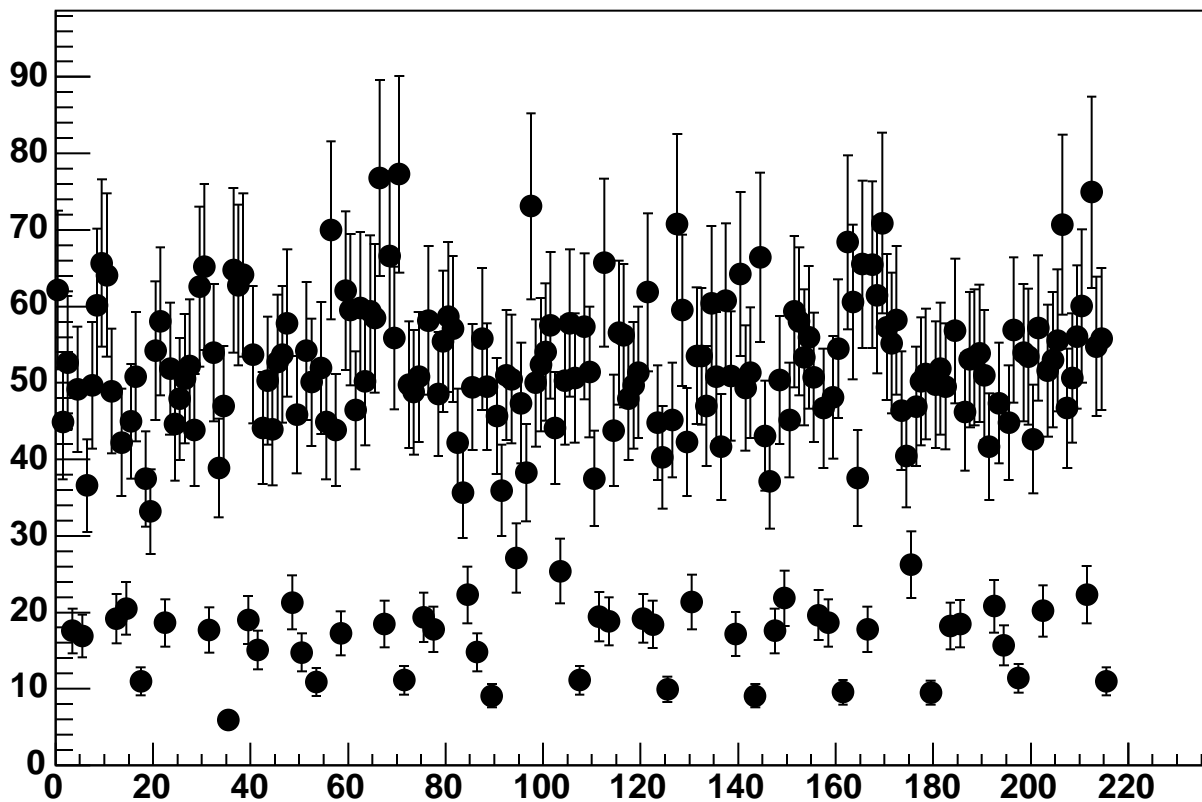
Enable 1, Hold=30, DAC=1750, ADC Noise vs 18\*Chip+Chan



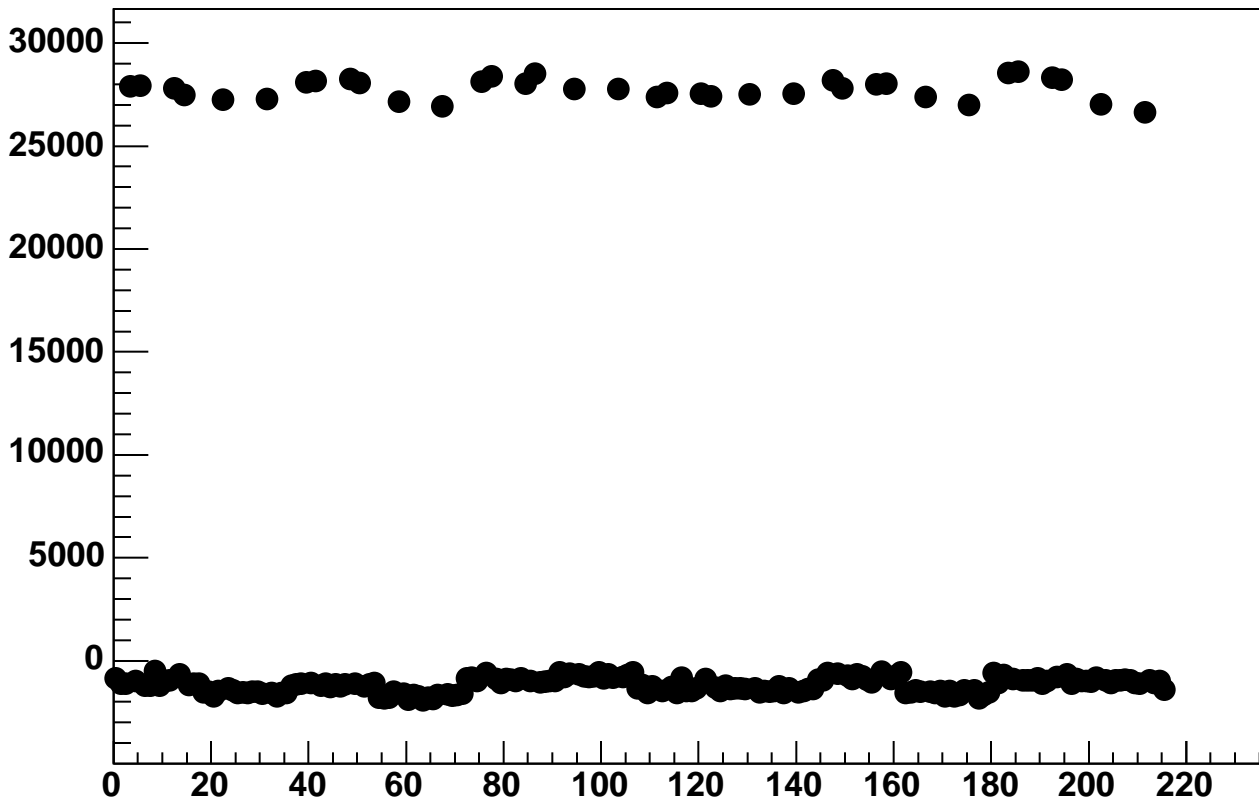
Enable 1, Hold=30, DAC=1800, ADC Mean vs 18\*Chip+Chan



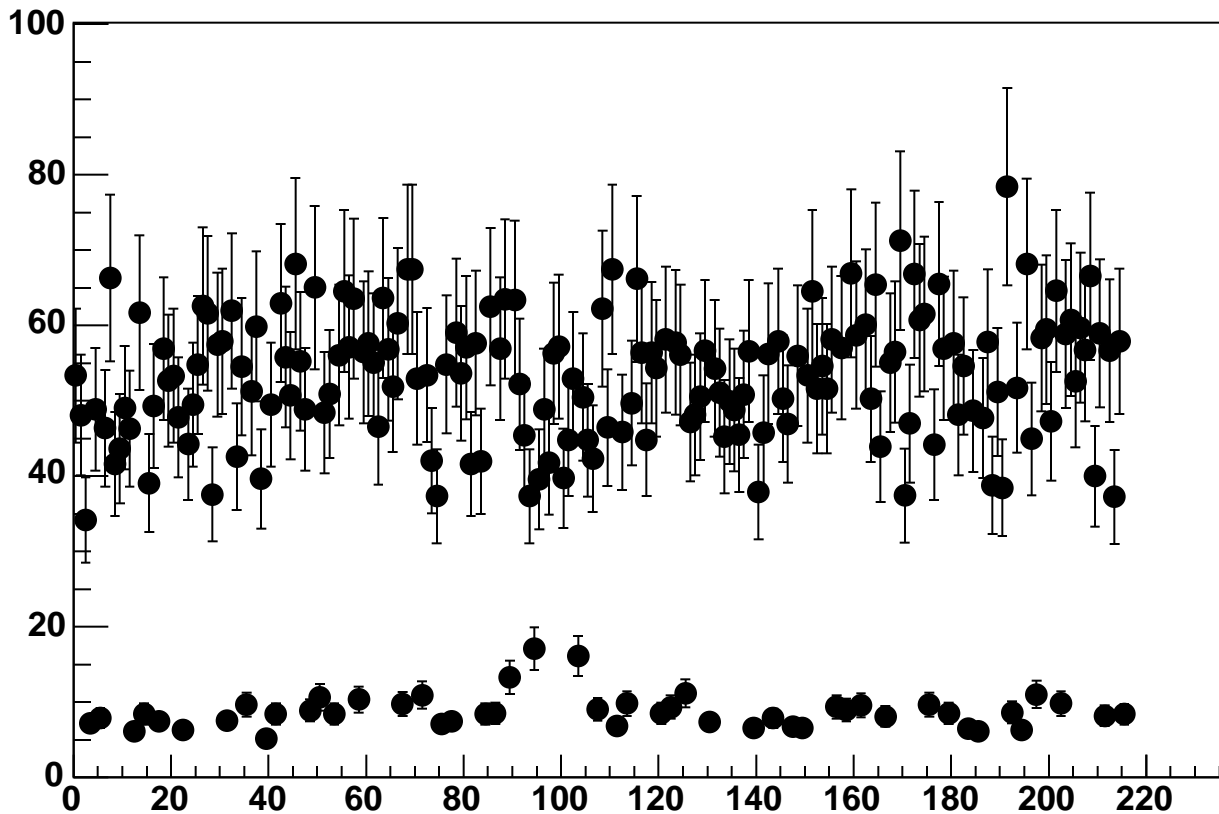
Enable 1, Hold=30, DAC=1800, ADC Noise vs 18\*Chip+Chan



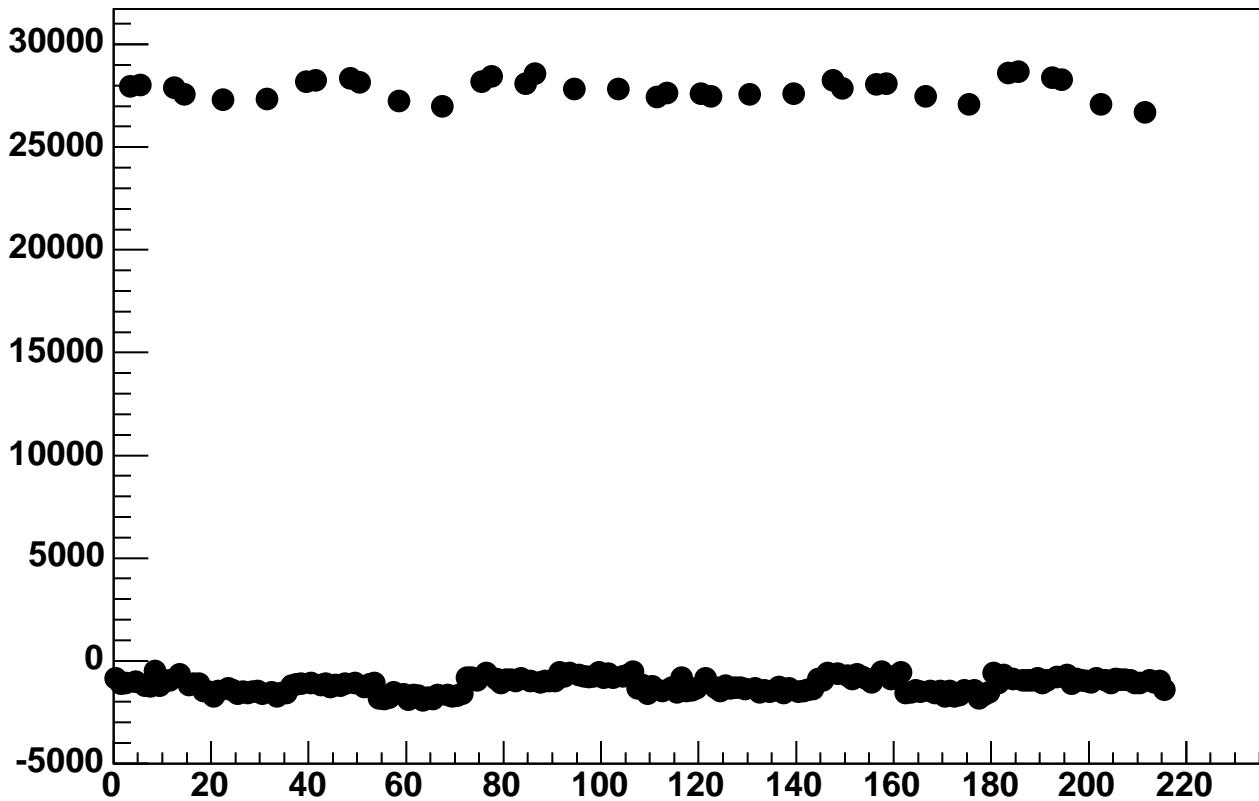
Enable 1, Hold=30, DAC=1850, ADC Mean vs 18\*Chip+Chan



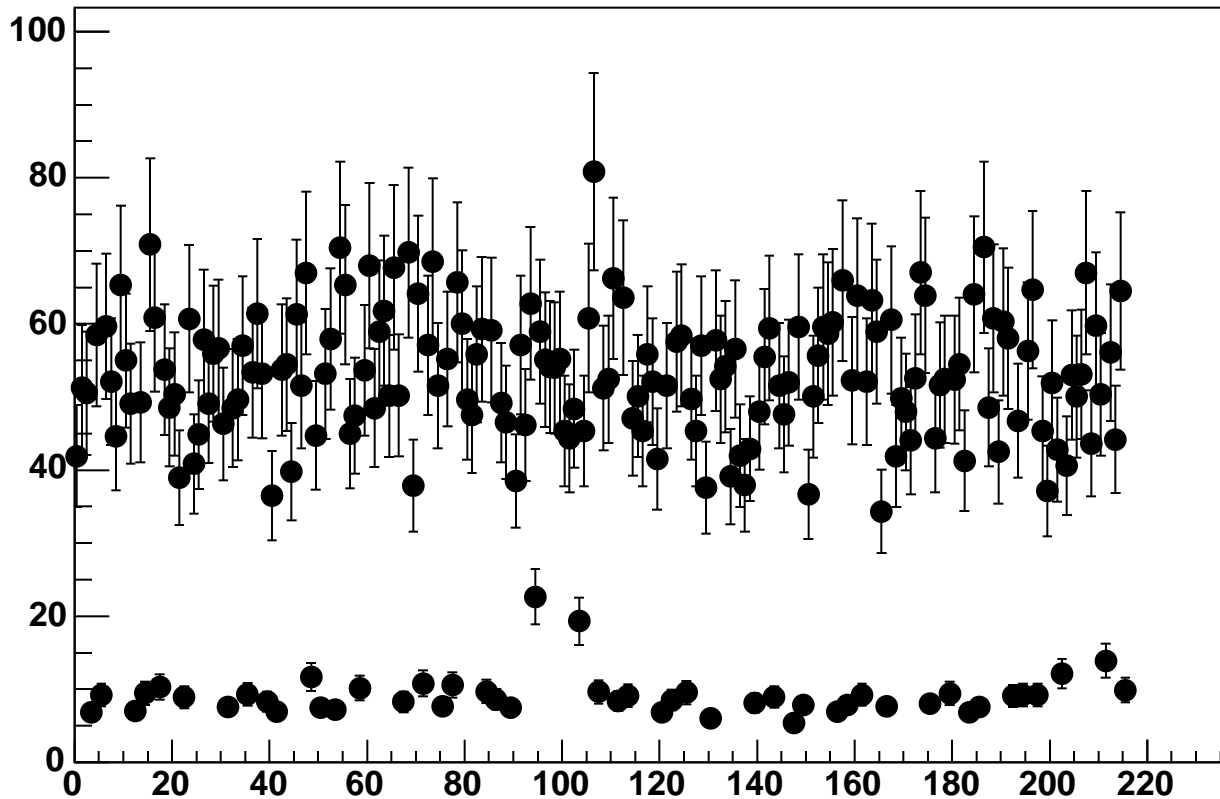
Enable 1, Hold=30, DAC=1850, ADC Noise vs 18\*Chip+Chan



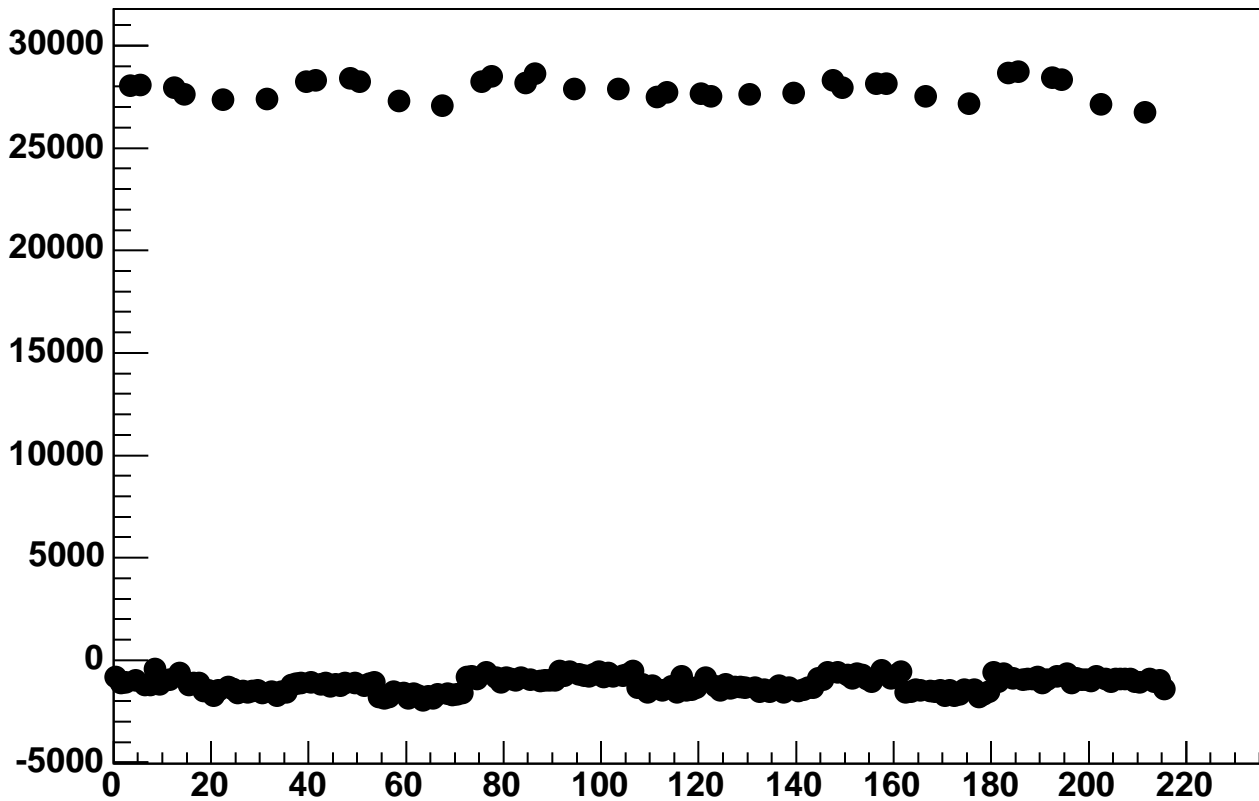
Enable 1, Hold=30, DAC=1900, ADC Mean vs 18\*Chip+Chan



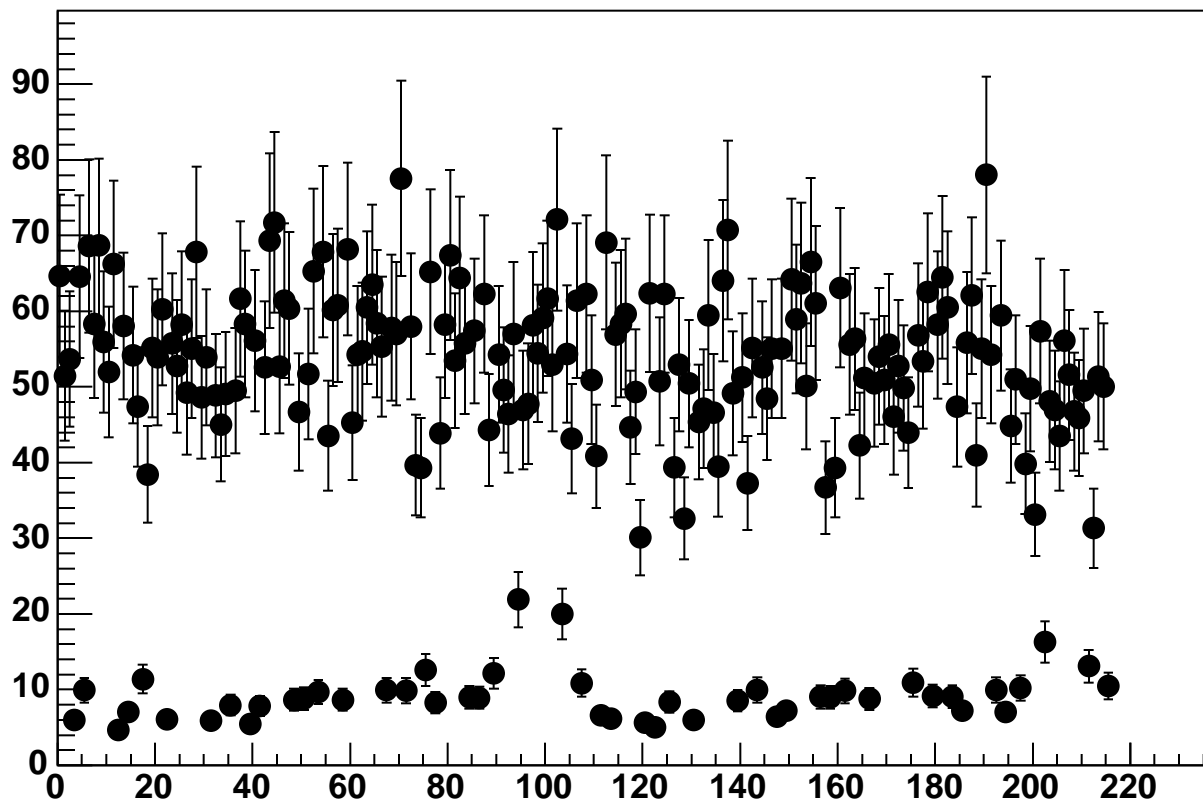
Enable 1, Hold=30, DAC=1900, ADC Noise vs 18\*Chip+Chan



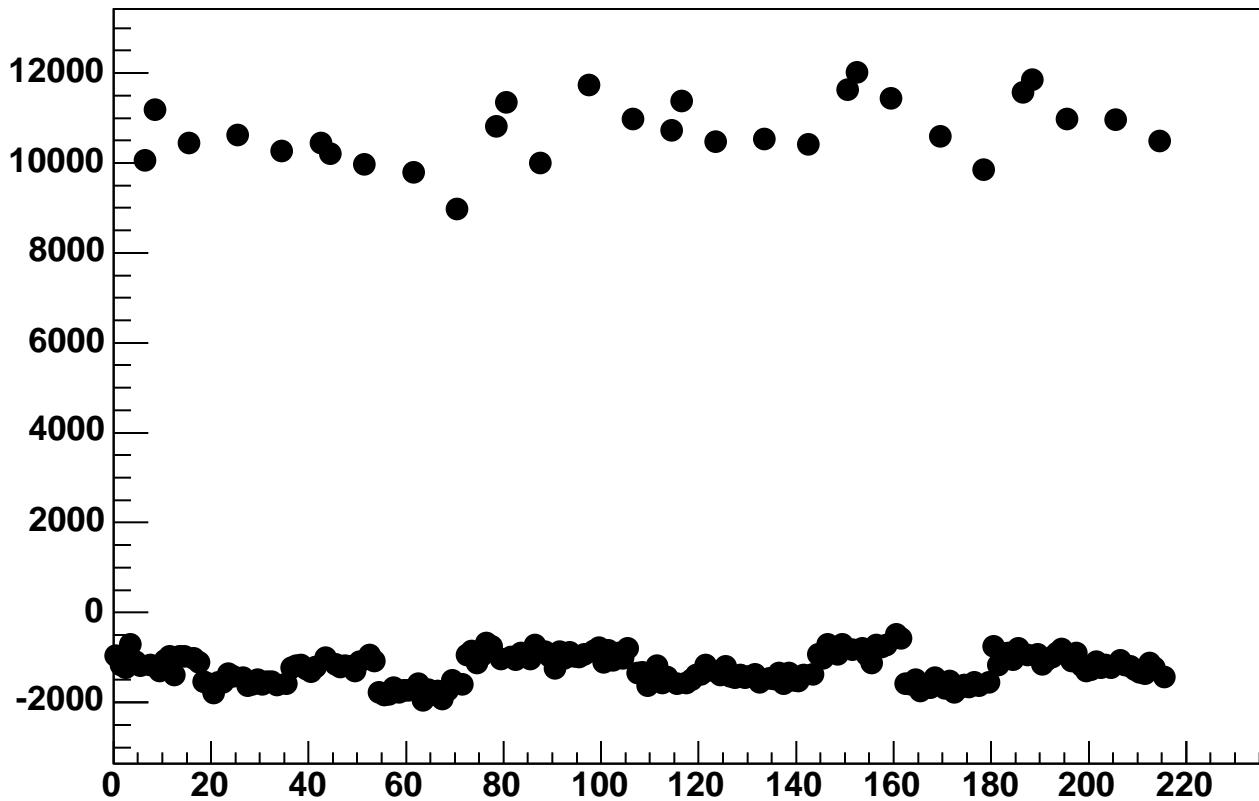
Enable 1, Hold=30, DAC=1950, ADC Mean vs 18\*Chip+Chan



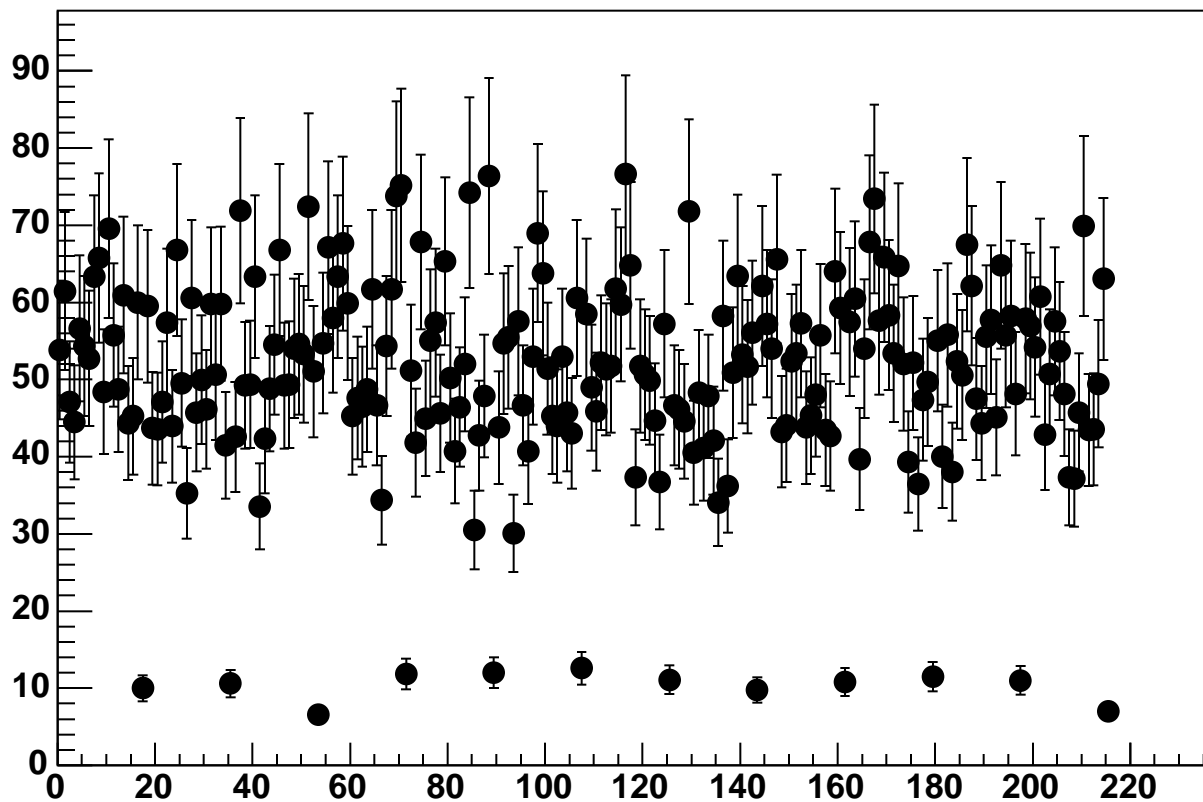
Enable 1, Hold=30, DAC=1950, ADC Noise vs 18\*Chip+Chan



Enable 2, Hold=30, DAC=0, ADC Mean vs 18\*Chip+Chan

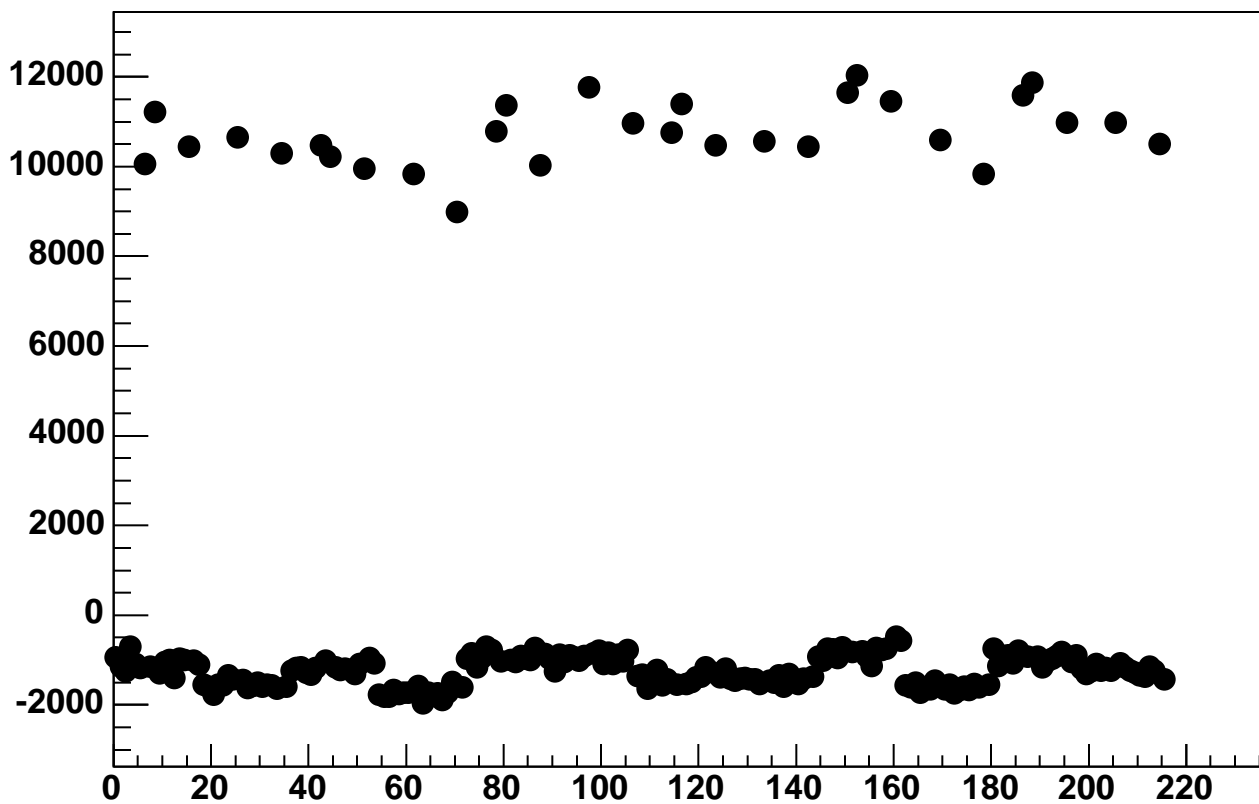


Enable 2, Hold=30, DAC=0, ADC Noise vs 18\*Chip+Chan

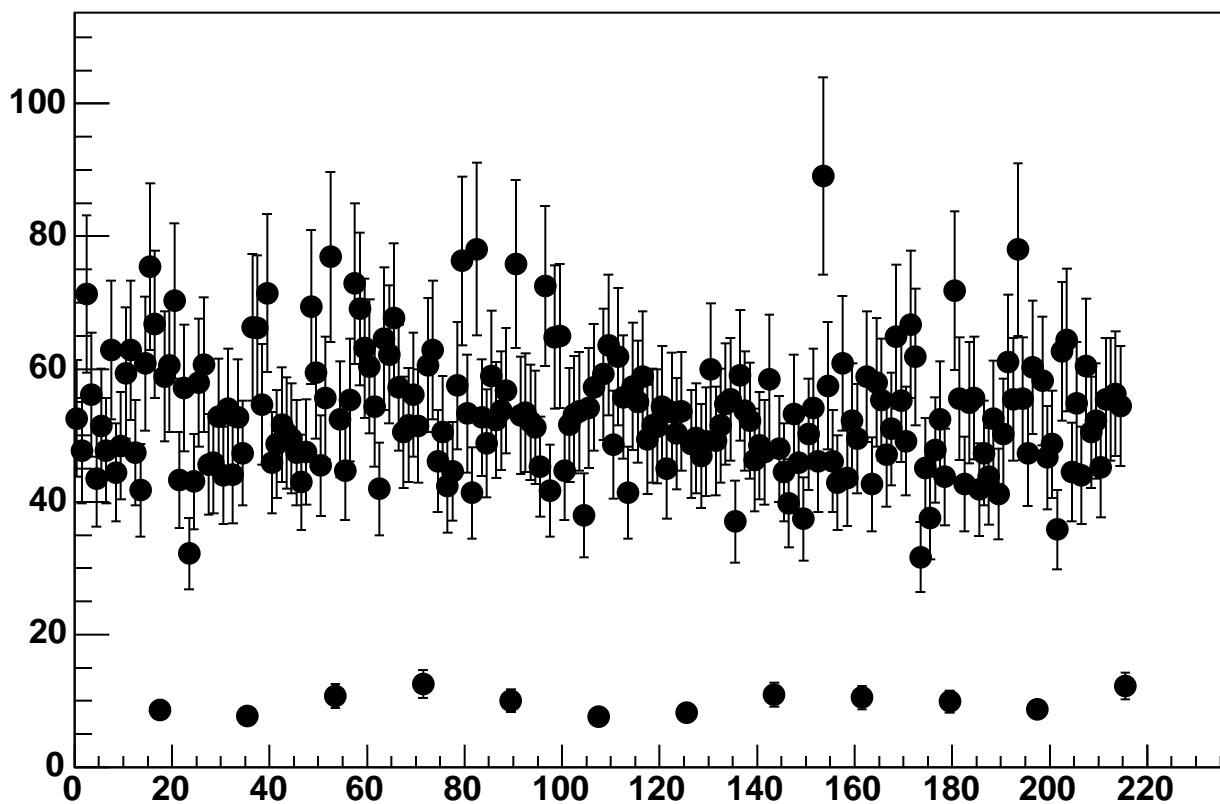




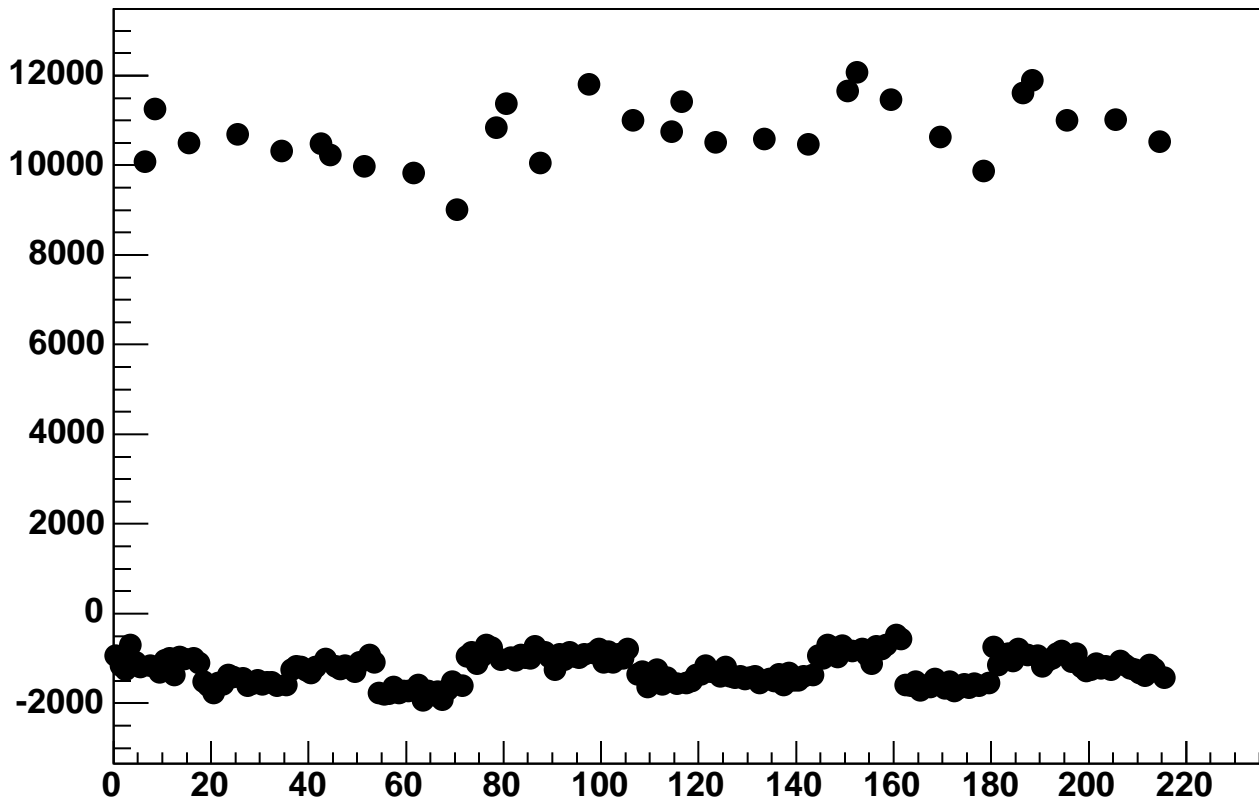
Enable 2, Hold=30, DAC=50, ADC Mean vs 18\*Chip+Chan



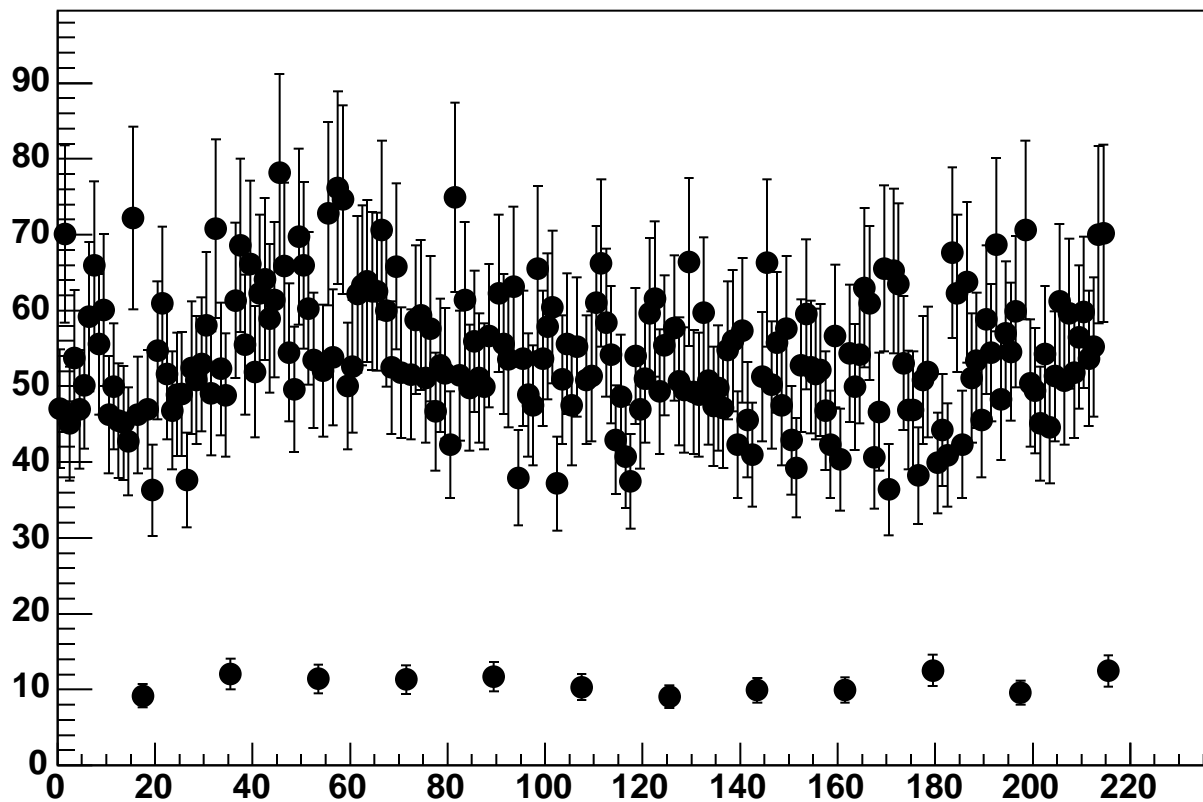
Enable 2, Hold=30, DAC=50, ADC Noise vs 18\*Chip+Chan



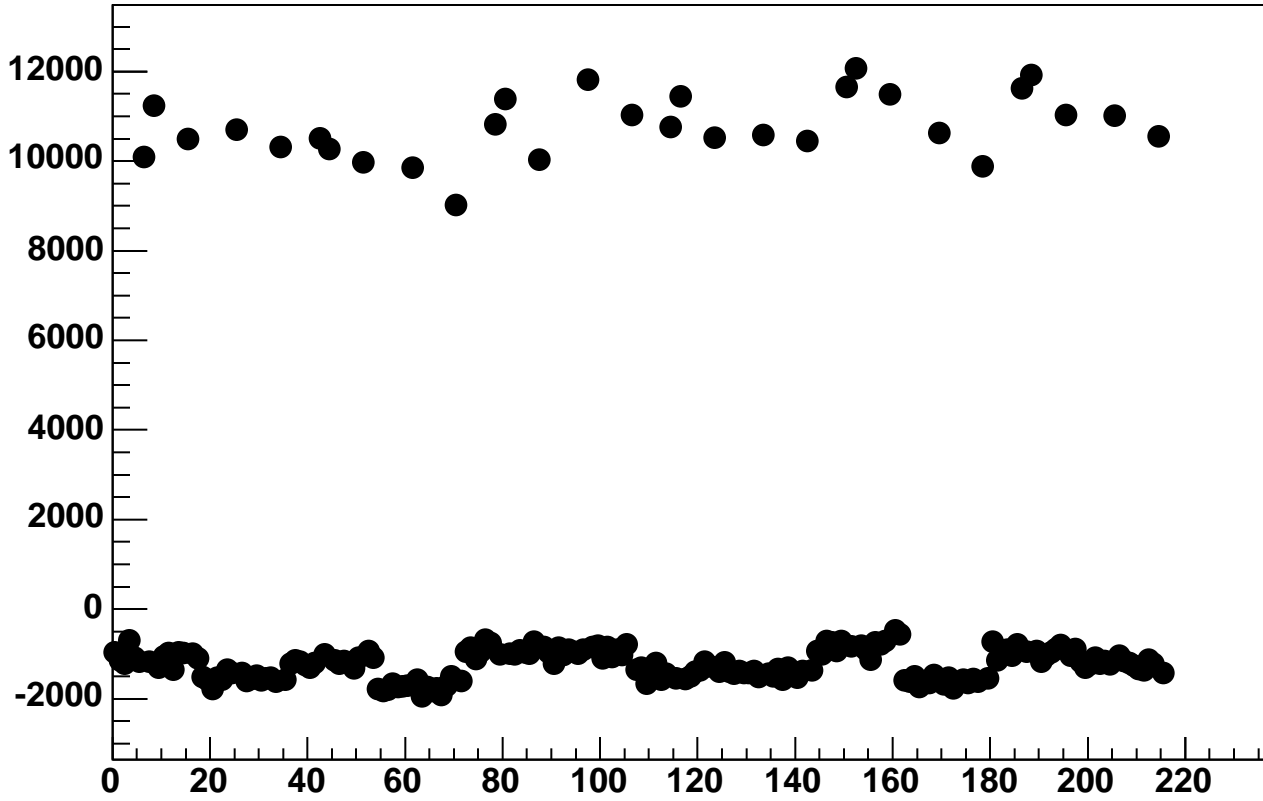
Enable 2, Hold=30, DAC=100, ADC Mean vs 18\*Chip+Chan



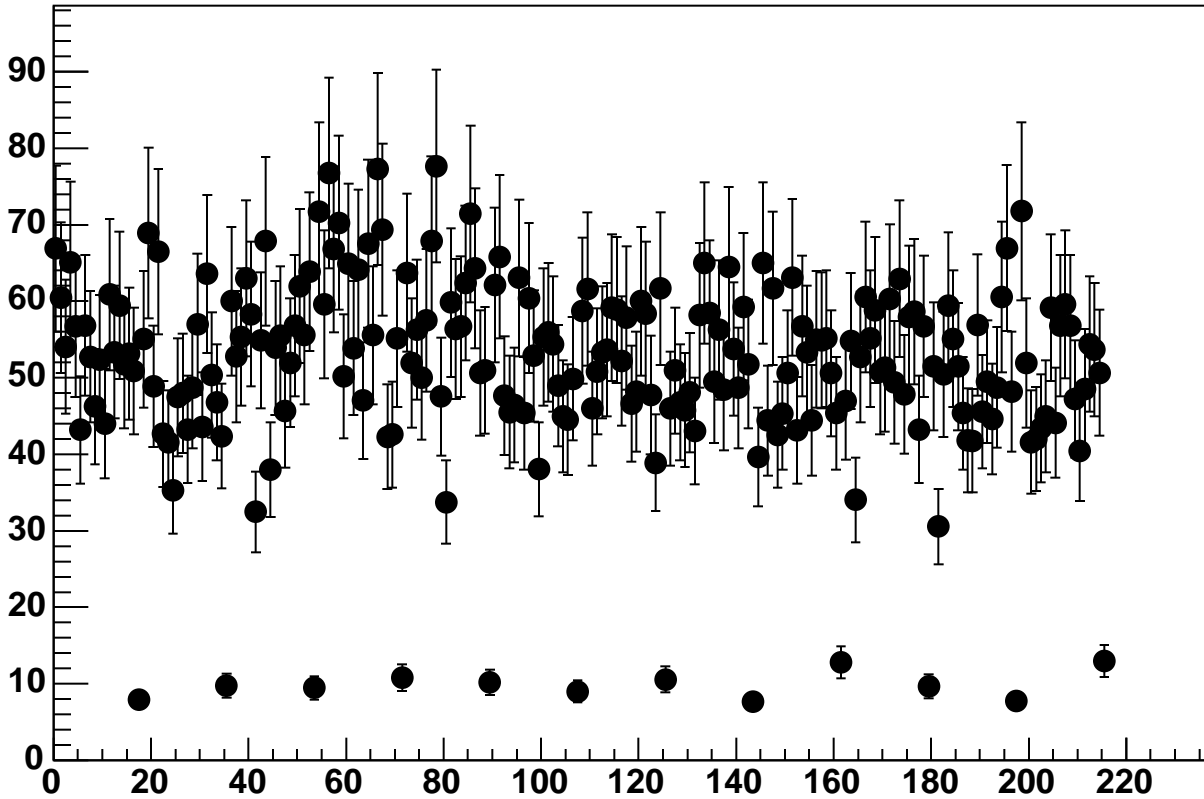
Enable 2, Hold=30, DAC=100, ADC Noise vs 18\*Chip+Chan



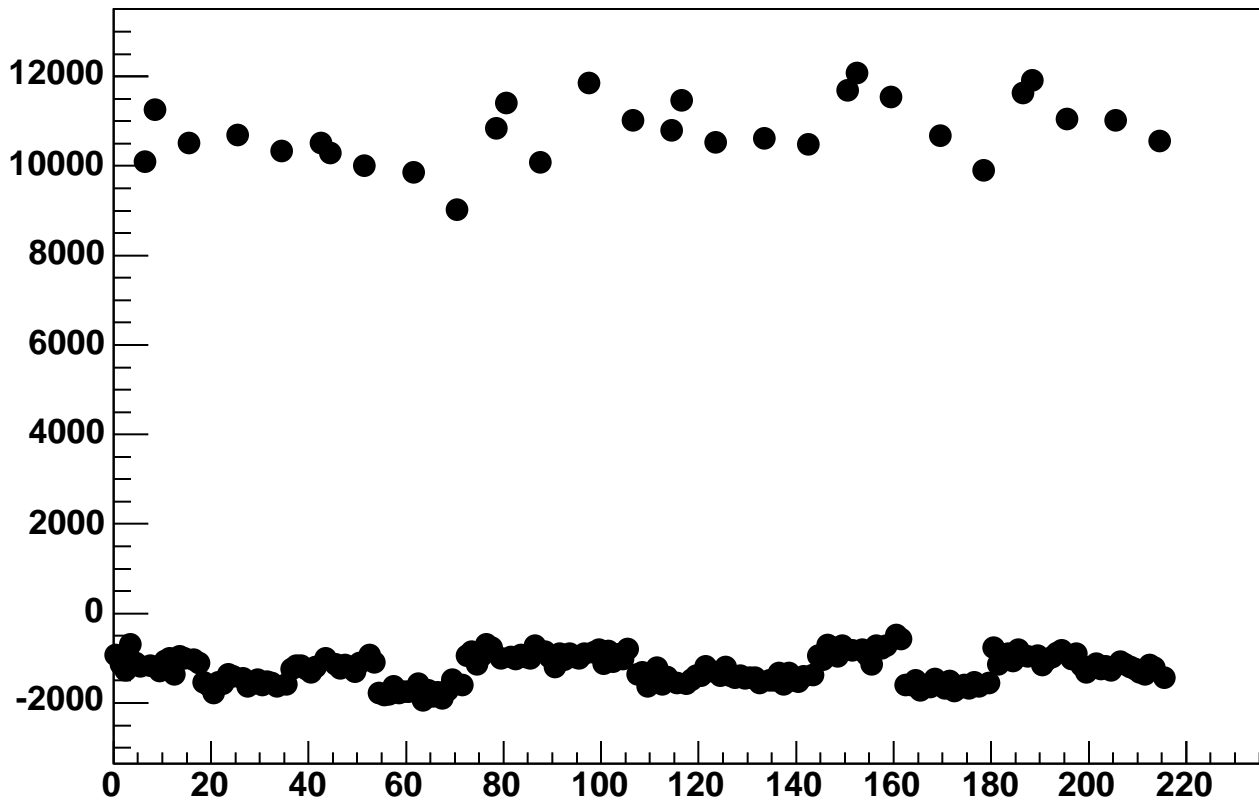
Enable 2, Hold=30, DAC=150, ADC Mean vs 18\*Chip+Chan



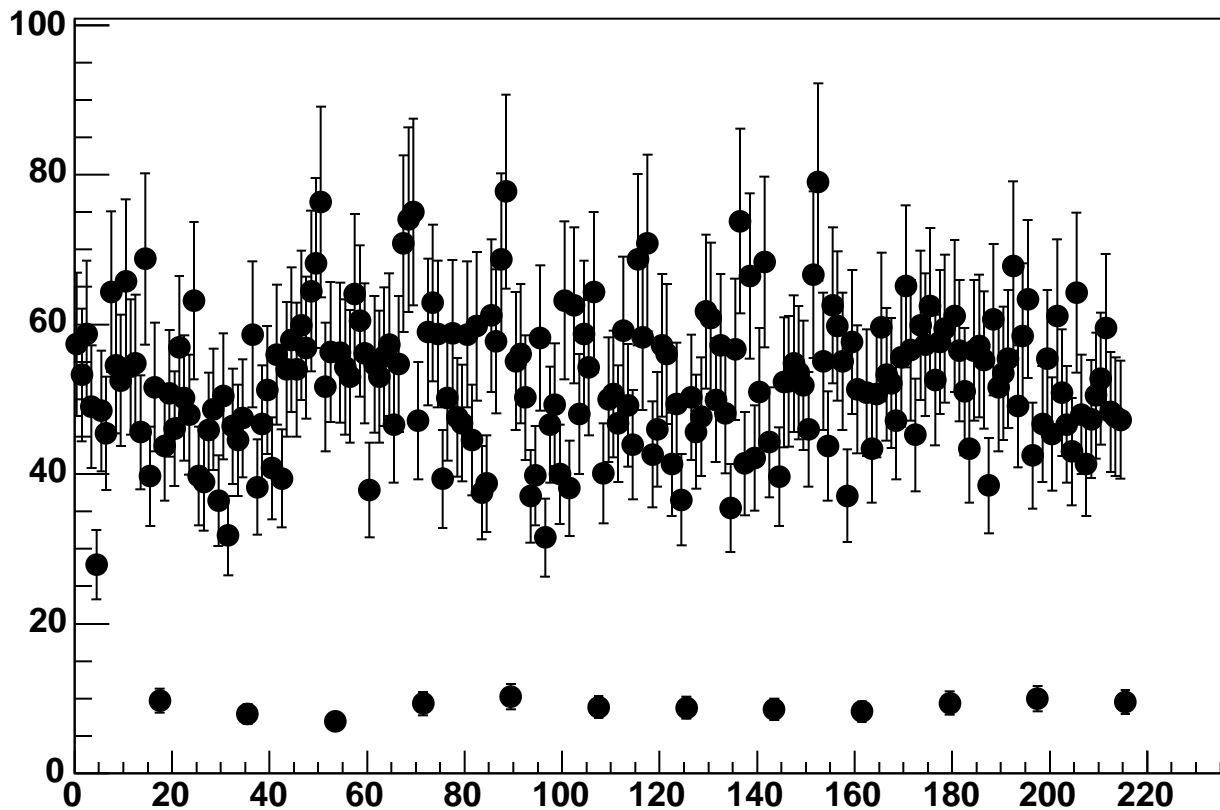
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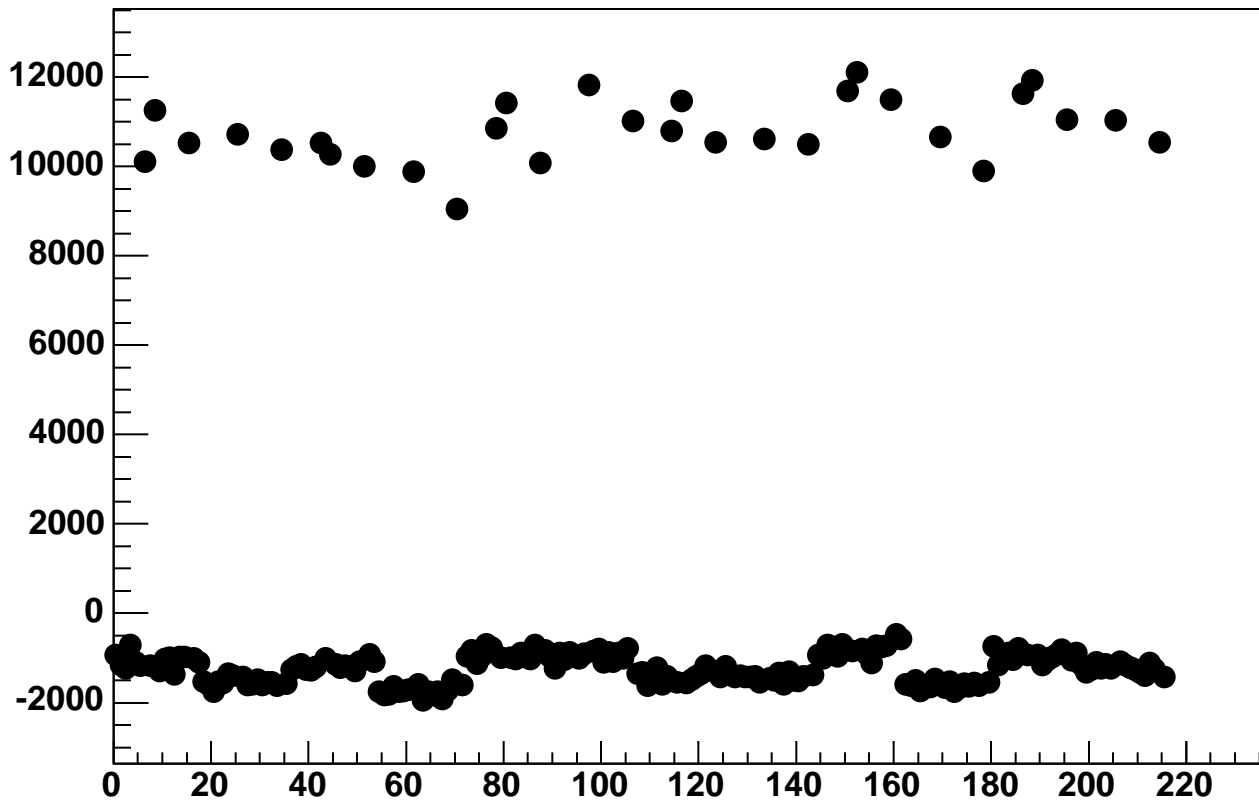
Enable 2, Hold=30, DAC=200, ADC Mean vs 18\*Chip+Chan



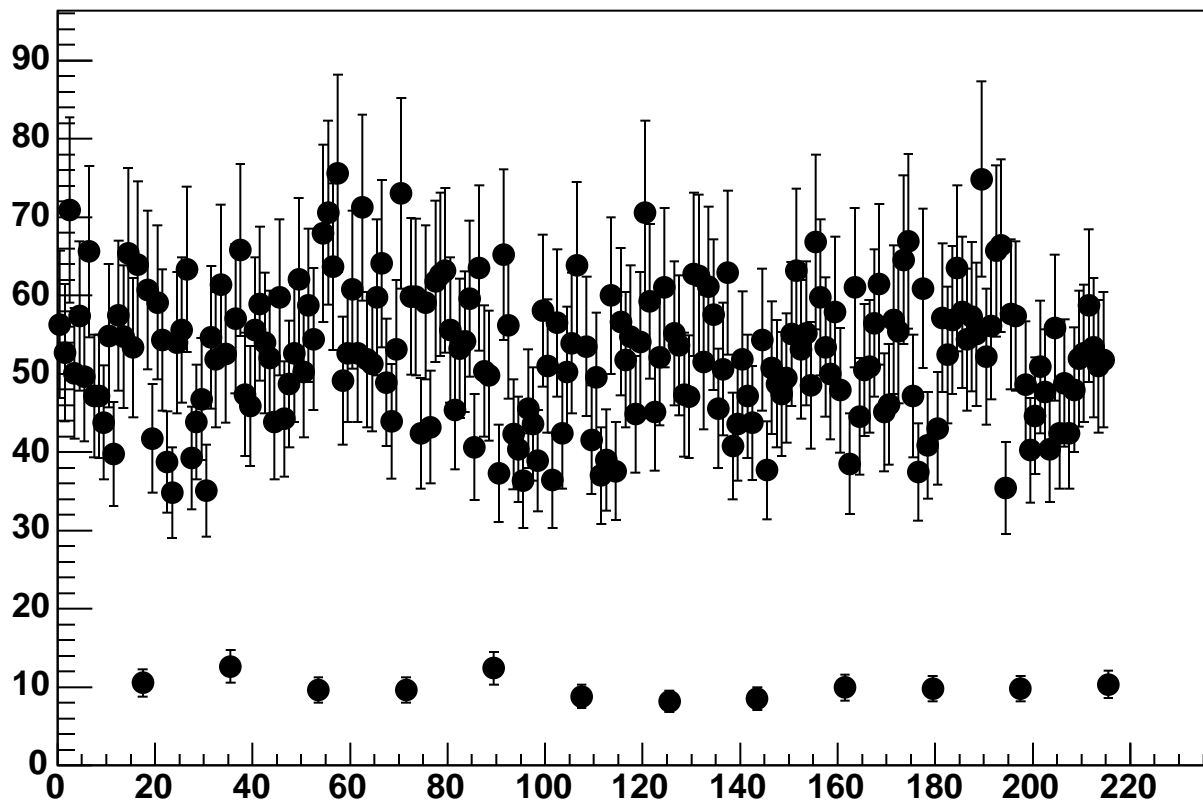
Enable 2, Hold=30, DAC=200, ADC Noise vs 18\*Chip+Chan



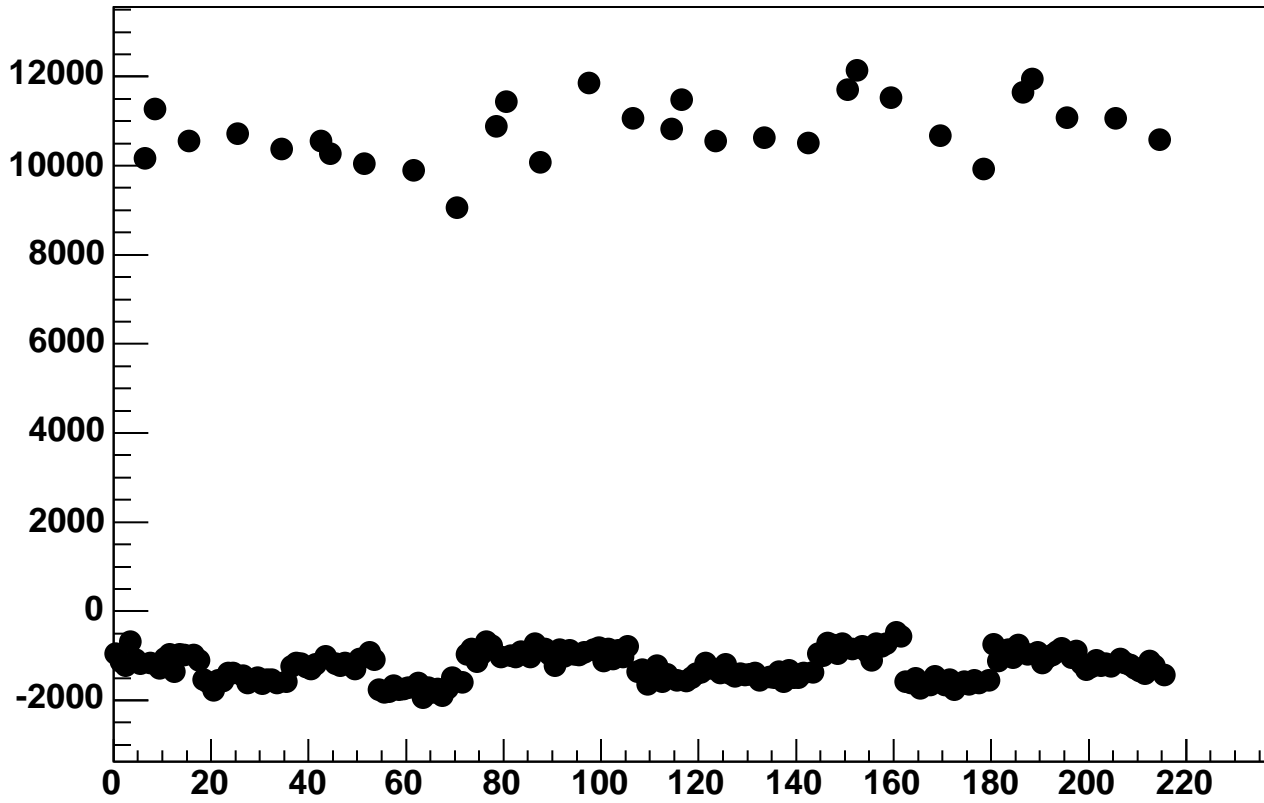
Enable 2, Hold=30, DAC=250, ADC Mean vs 18\*Chip+Chan



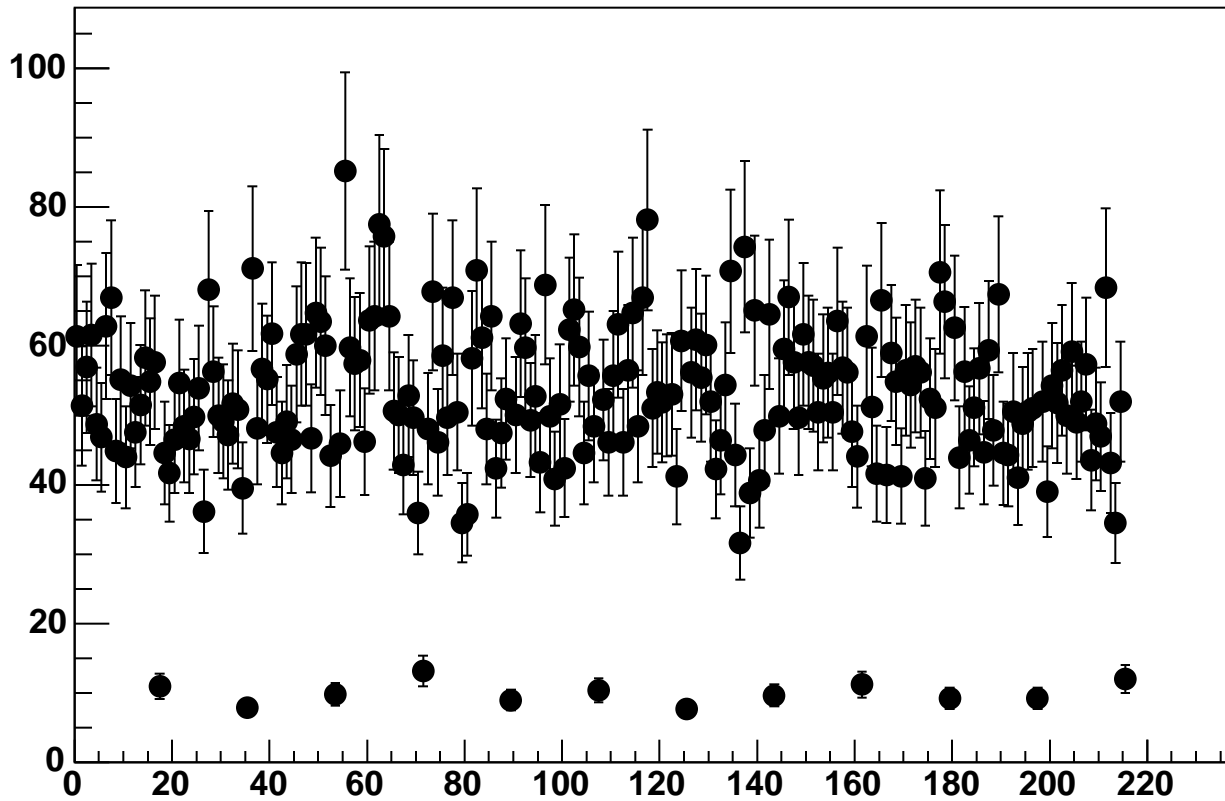
Enable 2, Hold=30, DAC=250, ADC Noise vs 18\*Chip+Chan



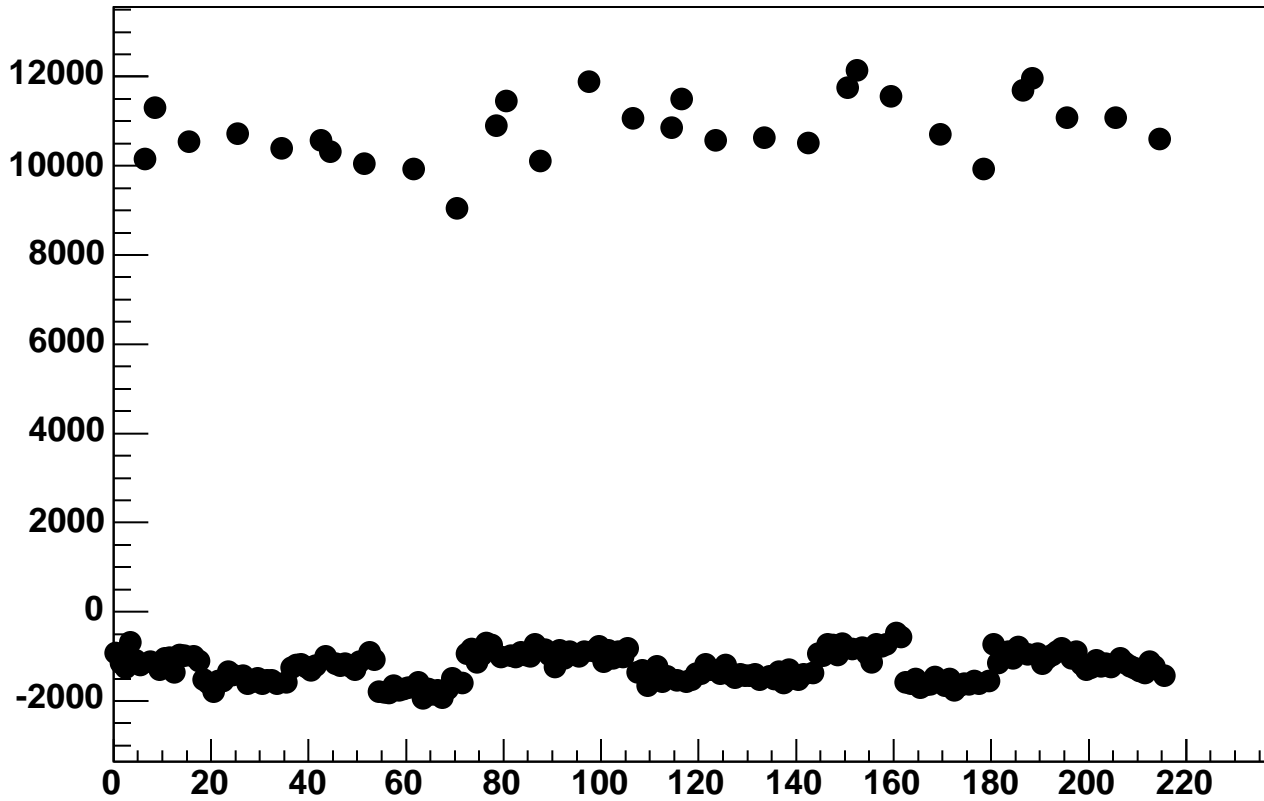
Enable 2, Hold=30, DAC=300, ADC Mean vs 18\*Chip+Chan



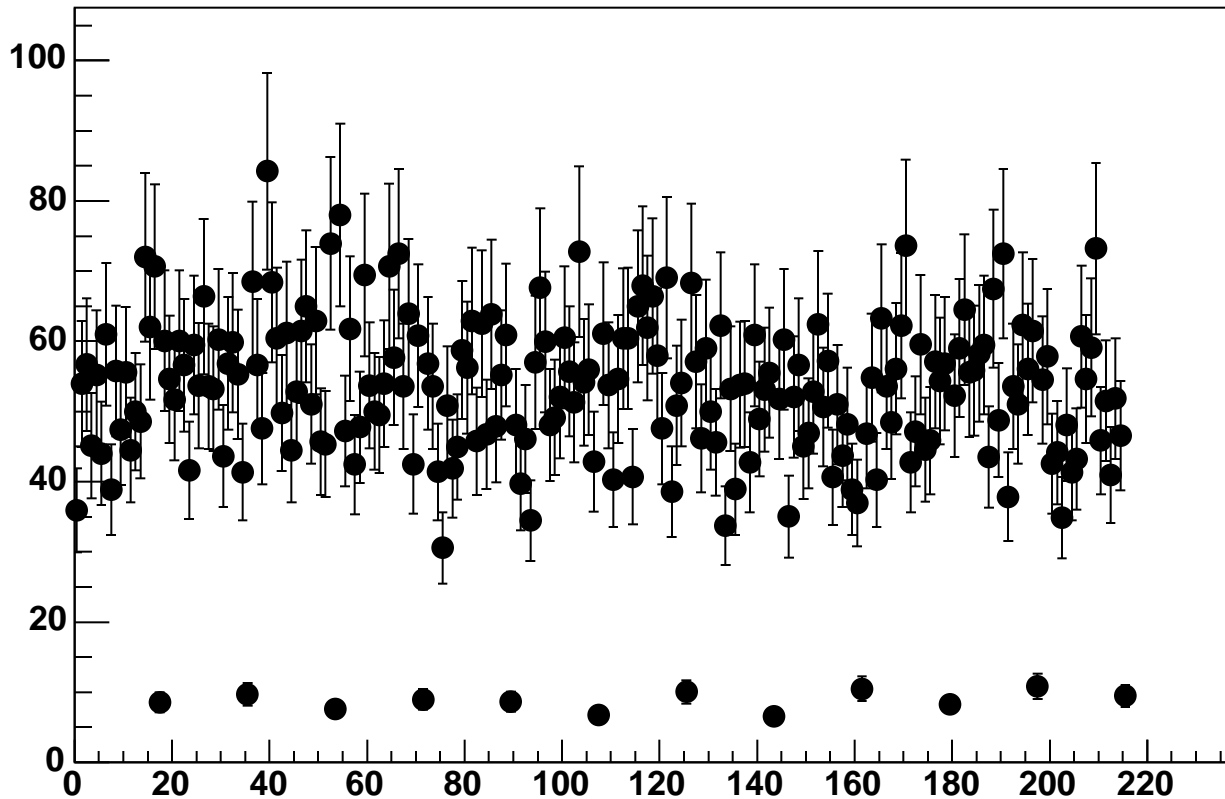
Enable 2, Hold=30, DAC=300, ADC Noise vs 18\*Chip+Chan



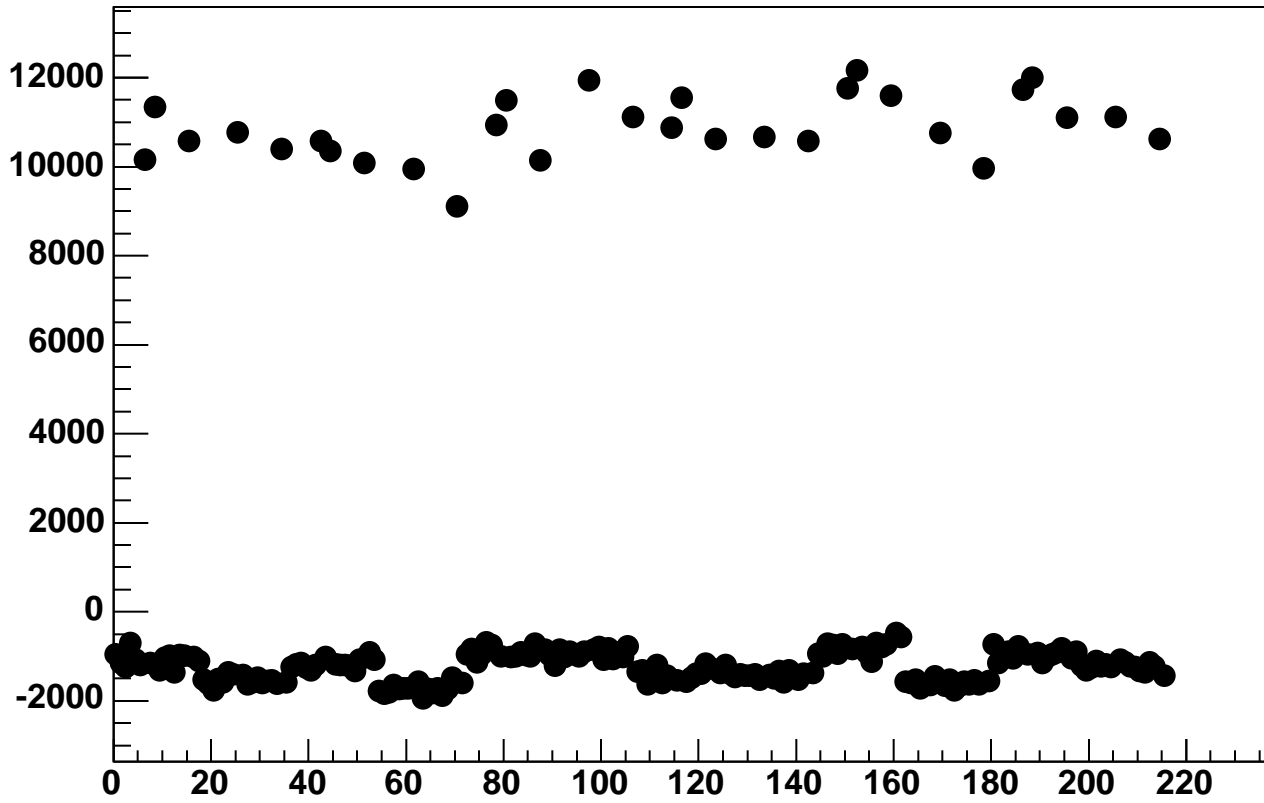
Enable 2, Hold=30, DAC=350, ADC Mean vs 18\*Chip+Chan



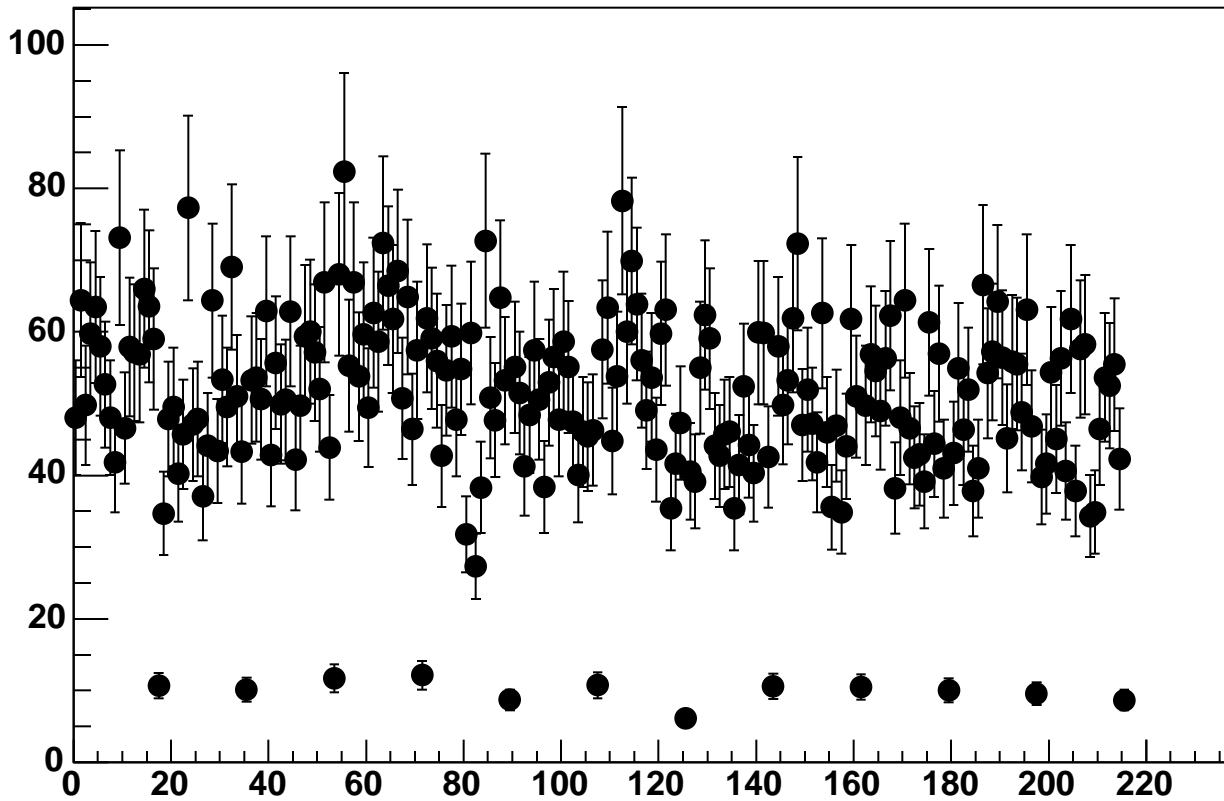
Enable 2, Hold=30, DAC=350, ADC Noise vs 18\*Chip+Chan



Enable 2, Hold=30, DAC=400, ADC Mean vs 18\*Chip+Chan

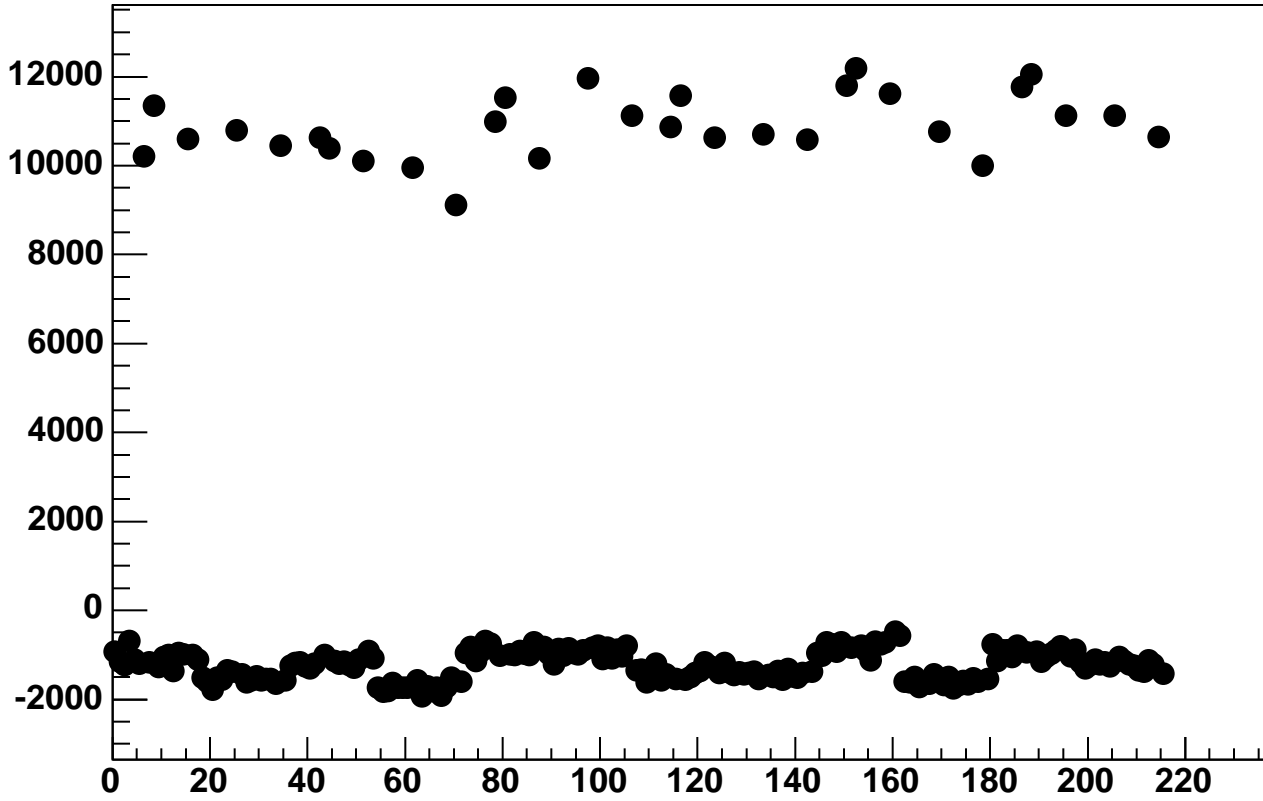


Enable 2, Hold=30, DAC=400, ADC Noise vs 18\*Chip+Chan

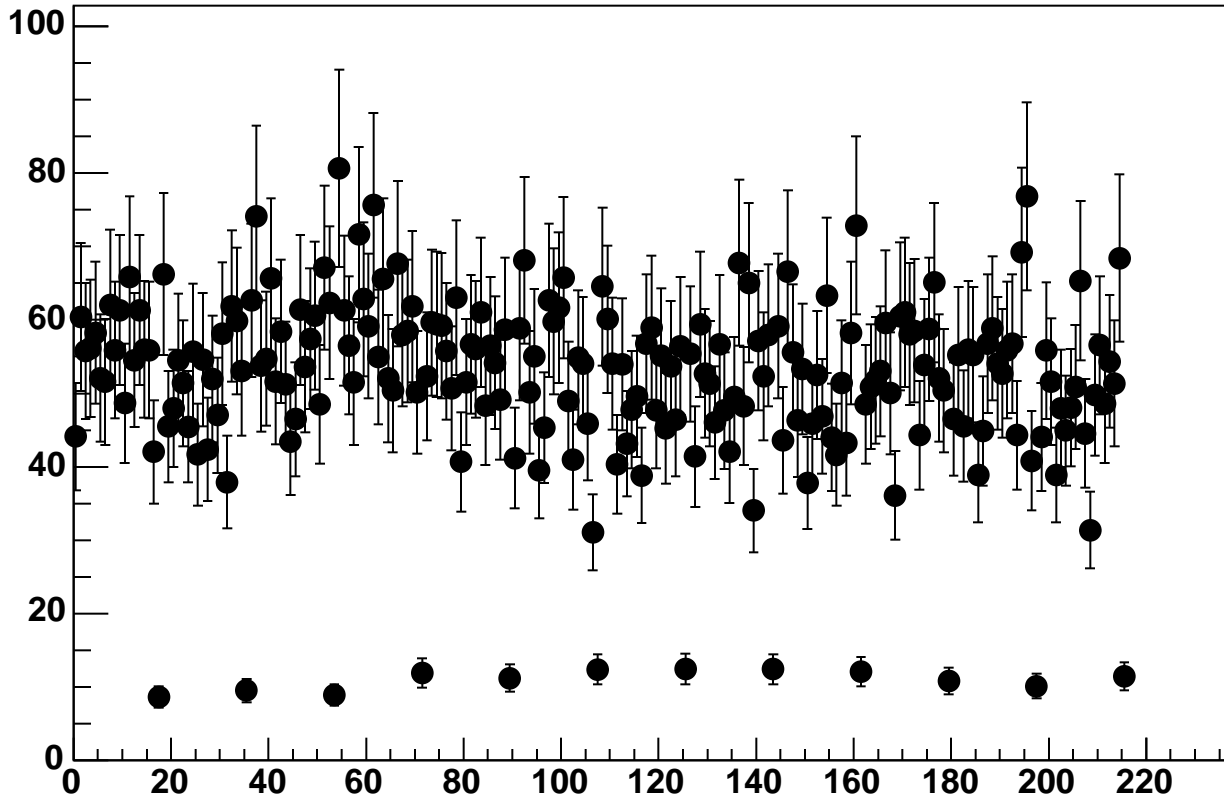




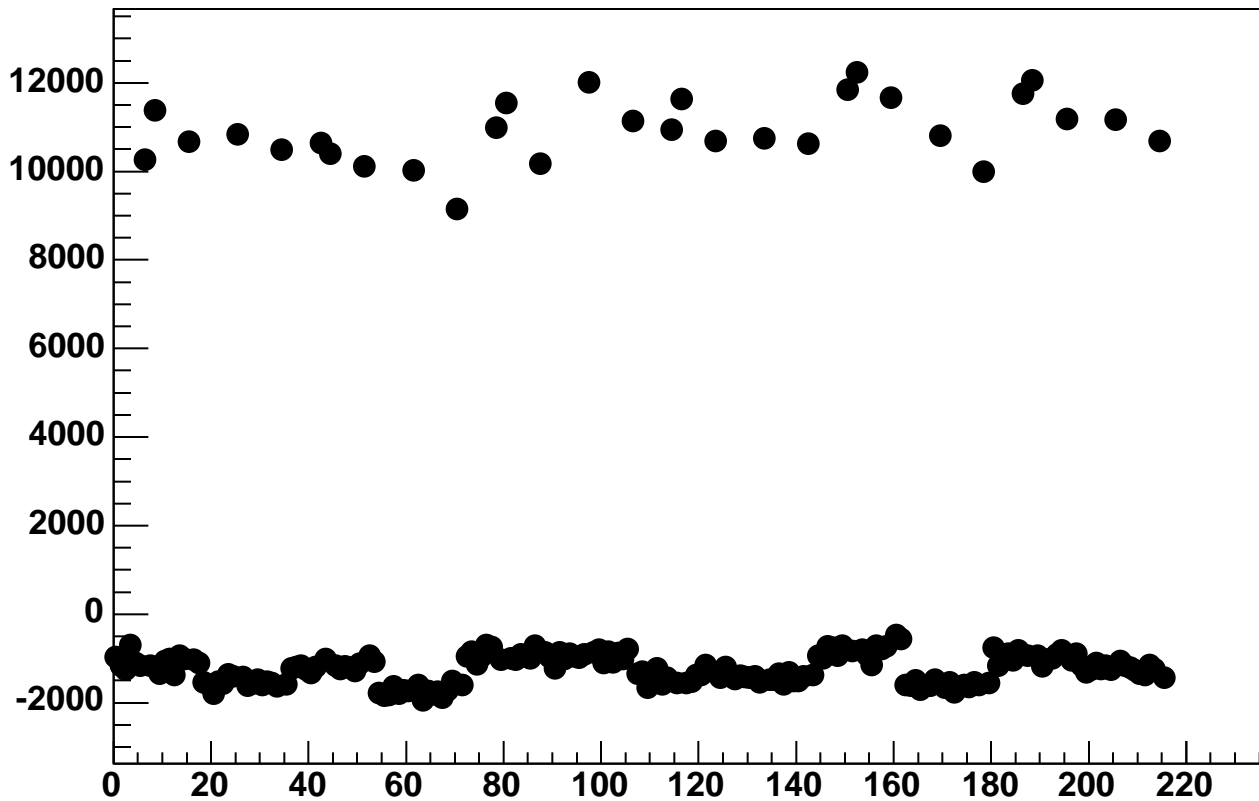
Enable 2, Hold=30, DAC=450, ADC Mean vs 18\*Chip+Chan



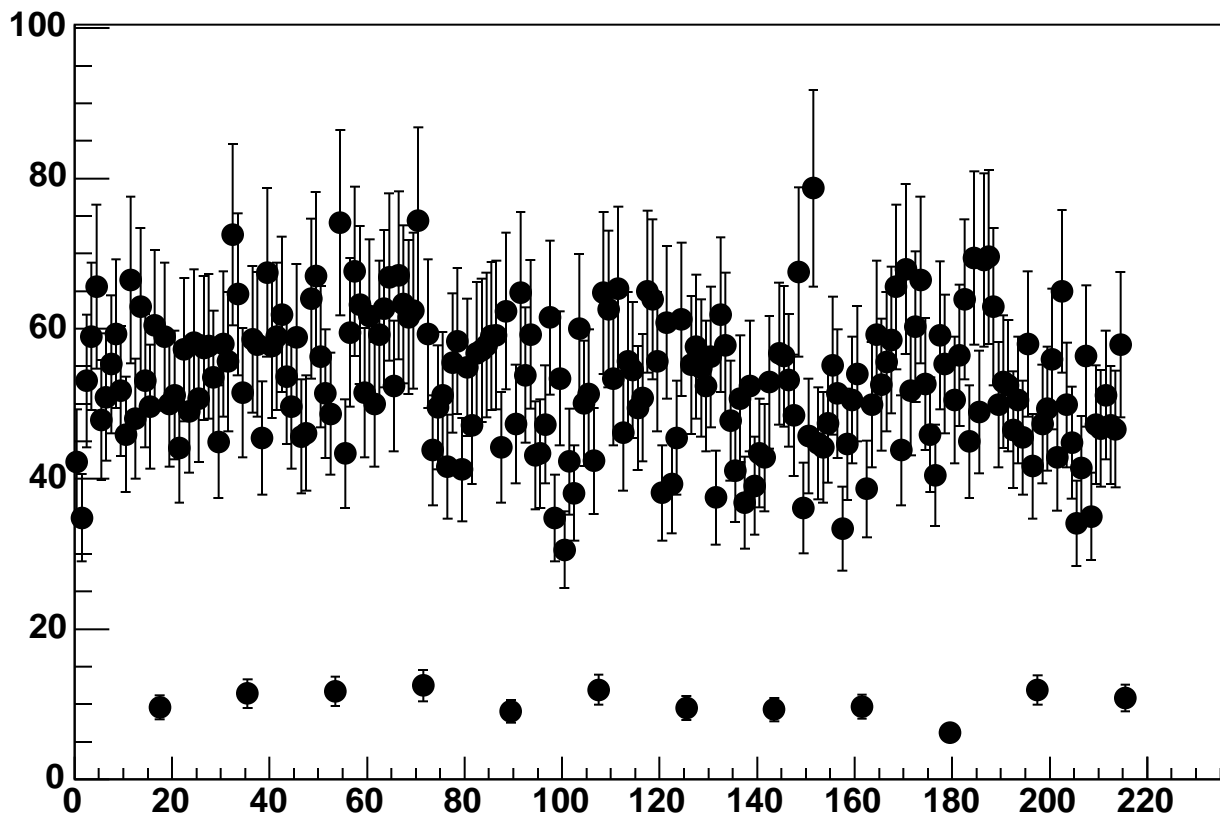
Enable 2, Hold=30, DAC=450, ADC Noise vs 18\*Chip+Chan



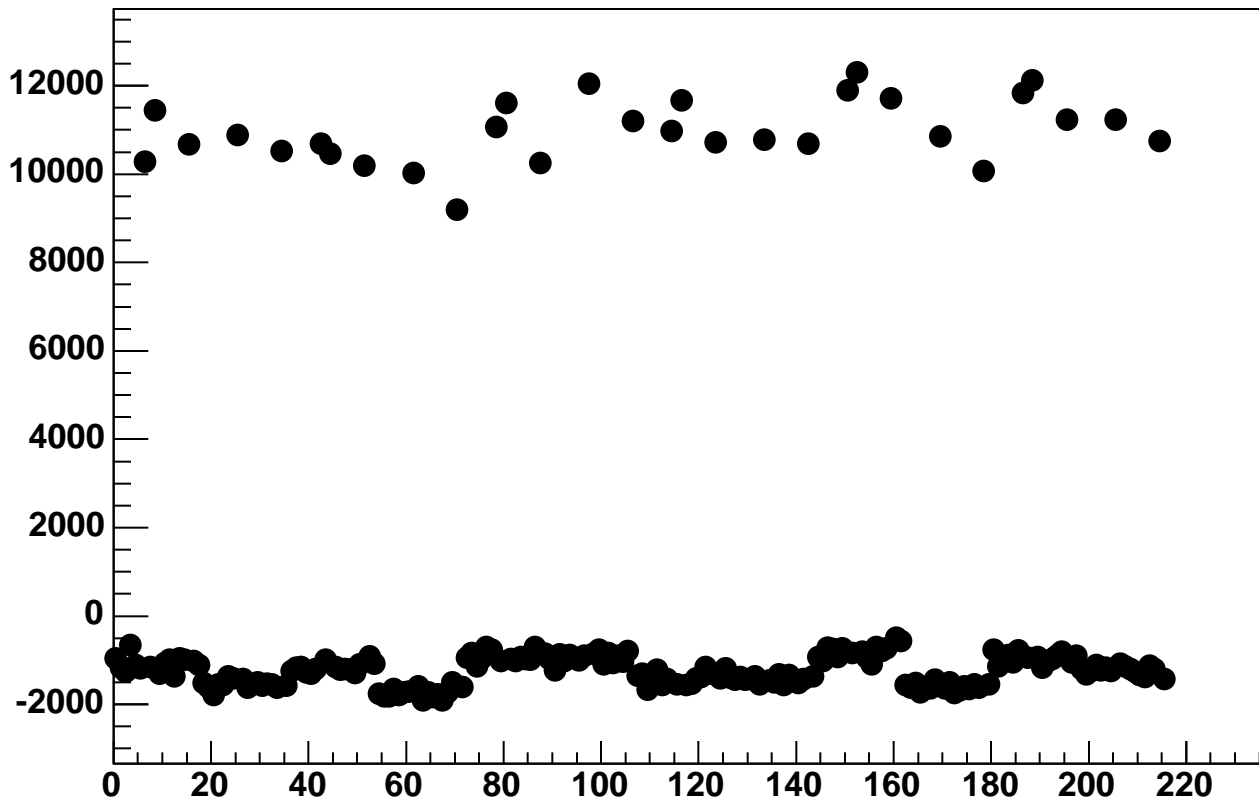
Enable 2, Hold=30, DAC=500, ADC Mean vs 18\*Chip+Chan



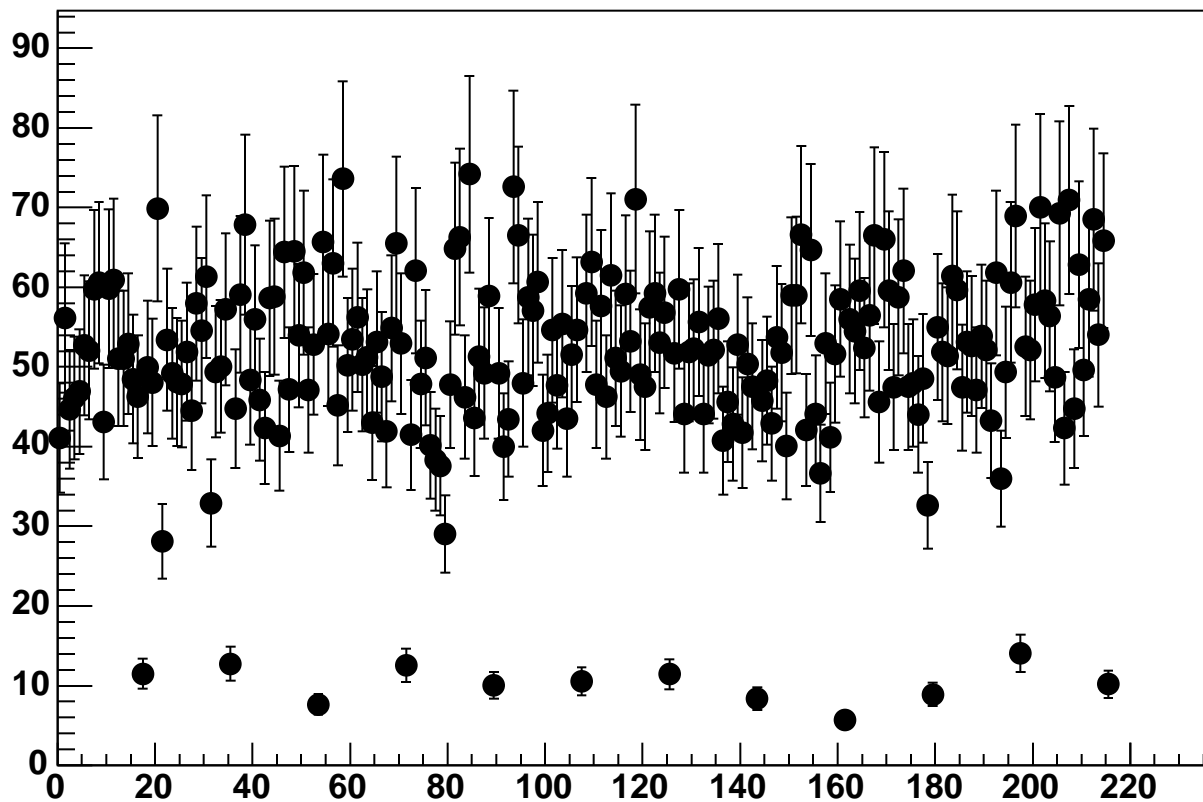
Enable 2, Hold=30, DAC=500, ADC Noise vs 18\*Chip+Chan



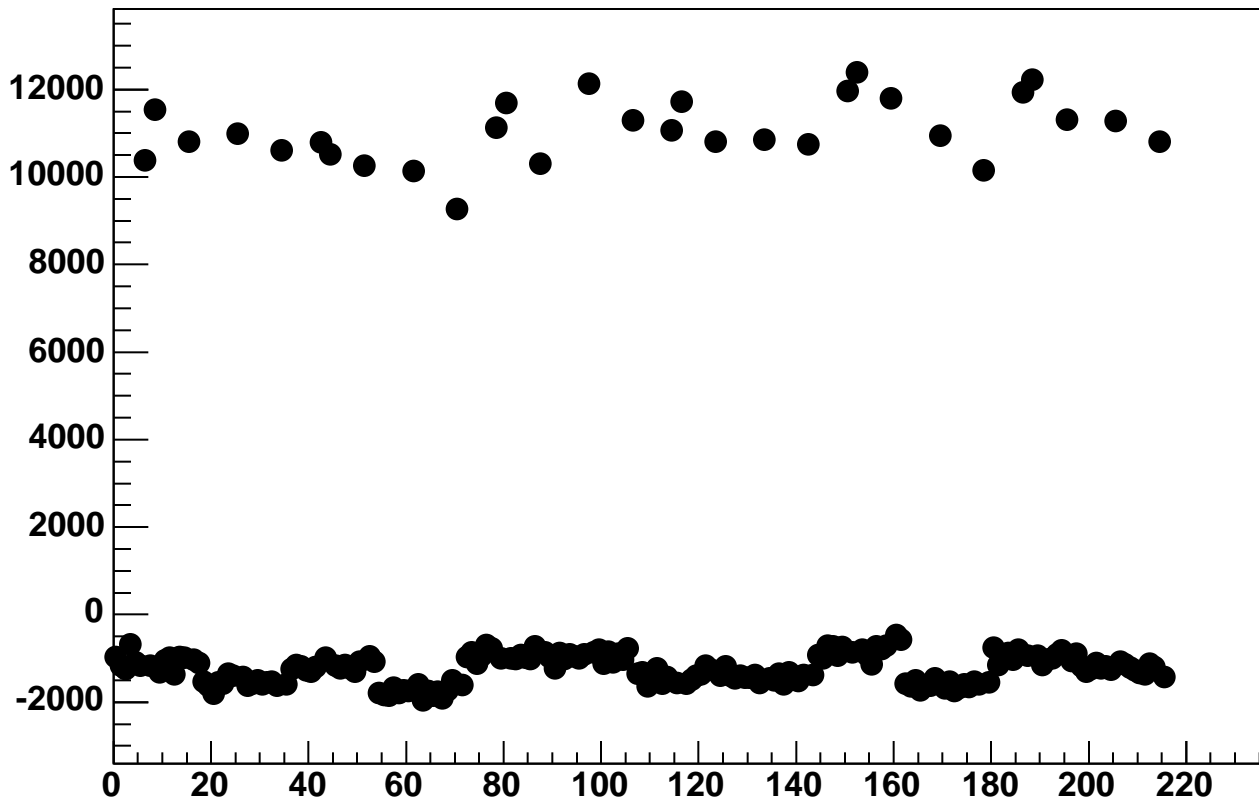
Enable 2, Hold=30, DAC=550, ADC Mean vs 18\*Chip+Chan



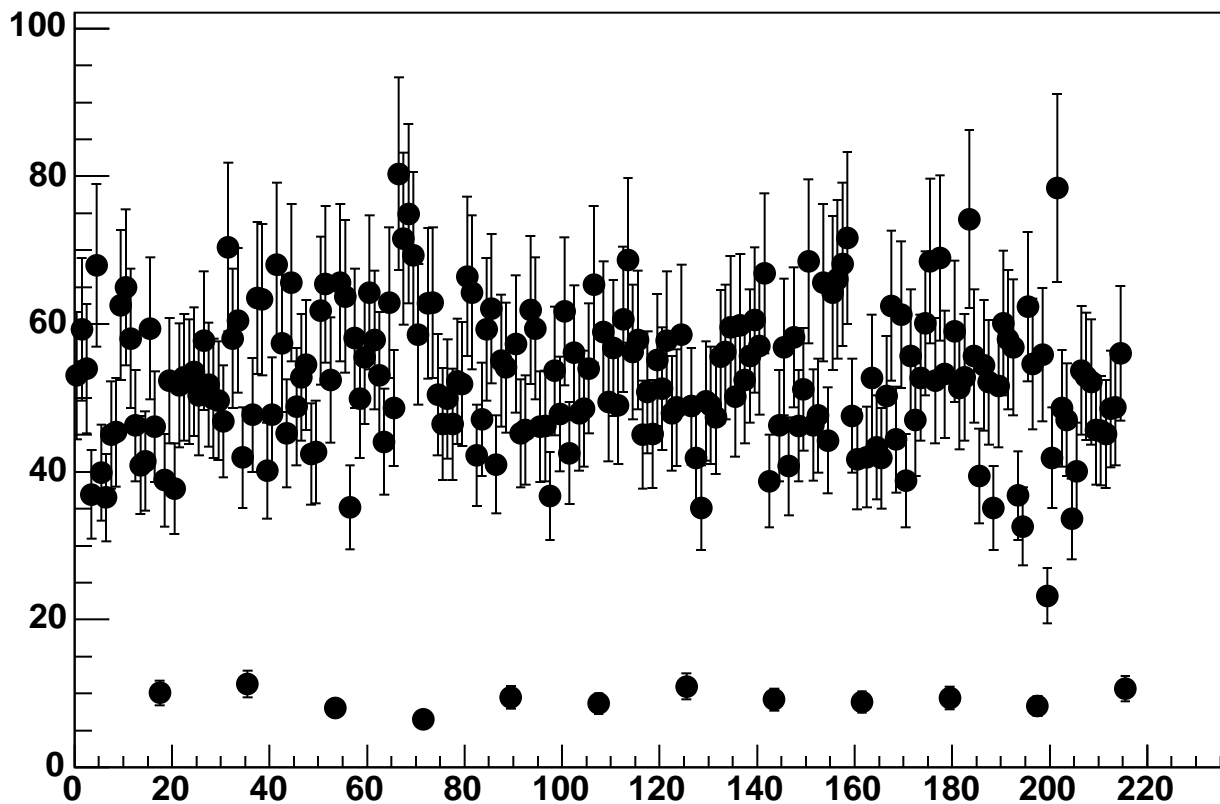
Enable 2, Hold=30, DAC=550, ADC Noise vs 18\*Chip+Chan



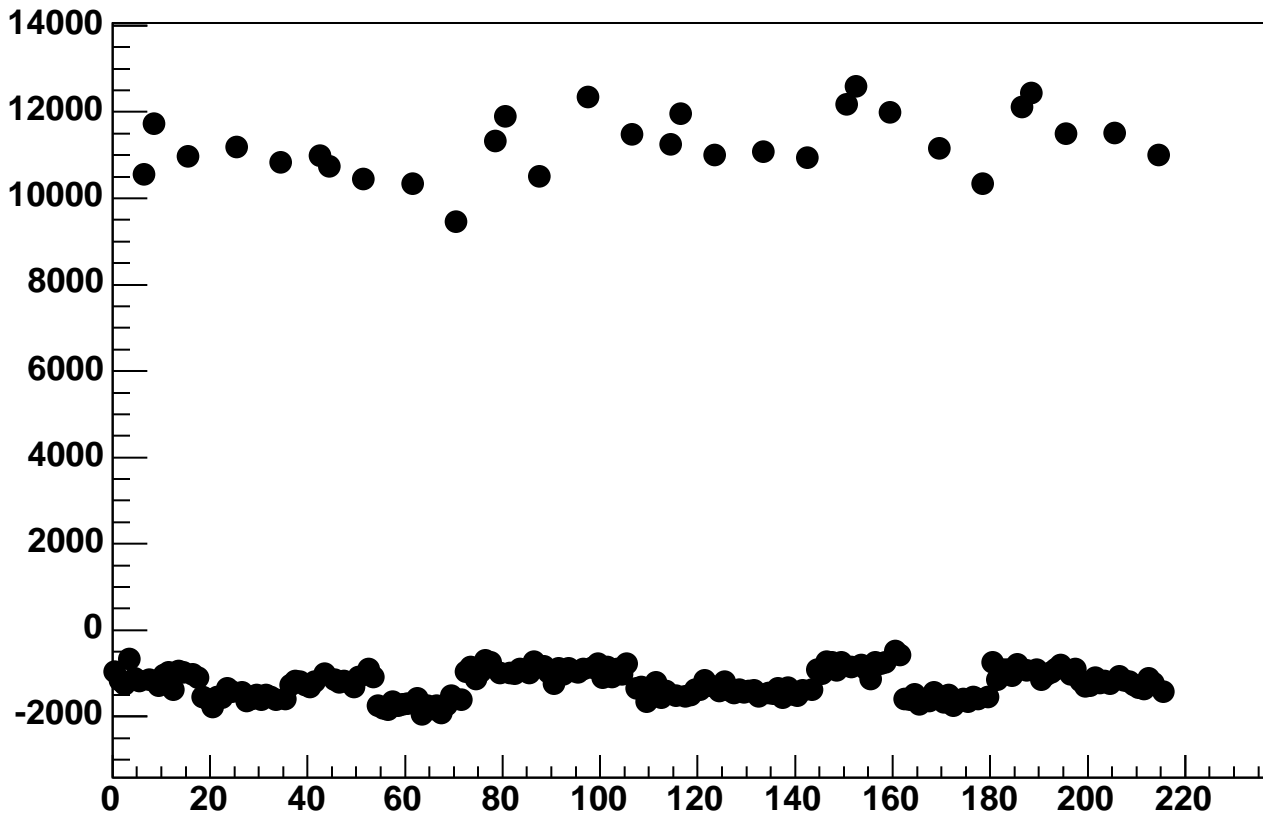
Enable 2, Hold=30, DAC=600, ADC Mean vs 18\*Chip+Chan



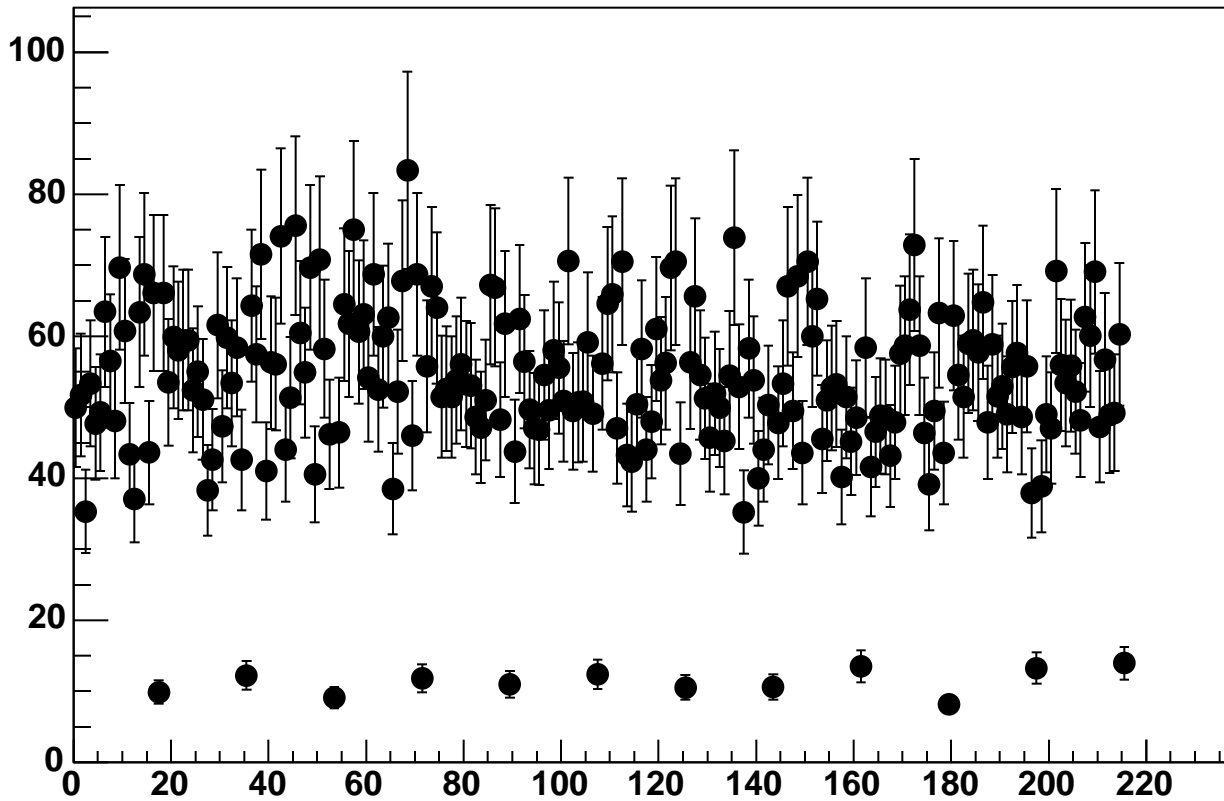
Enable 2, Hold=30, DAC=600, ADC Noise vs 18\*Chip+Chan



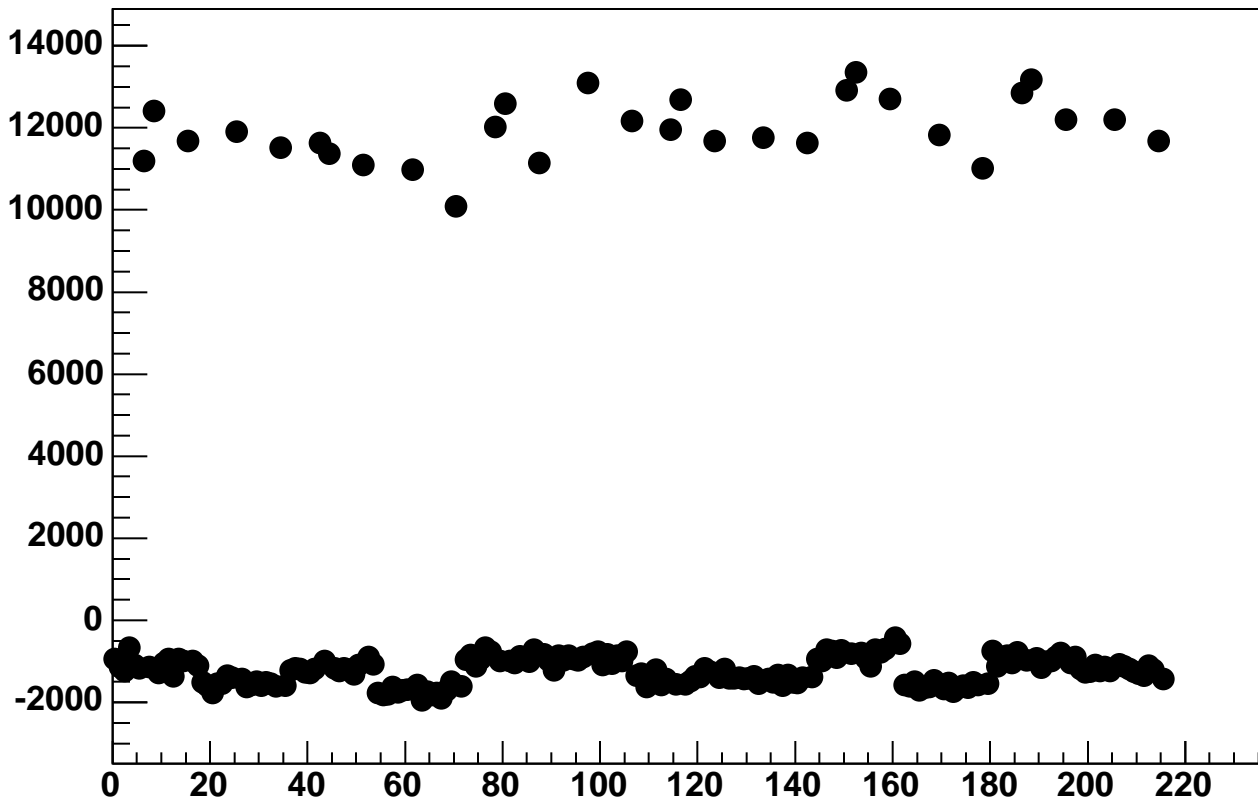
Enable 2, Hold=30, DAC=650, ADC Mean vs 18\*Chip+Chan



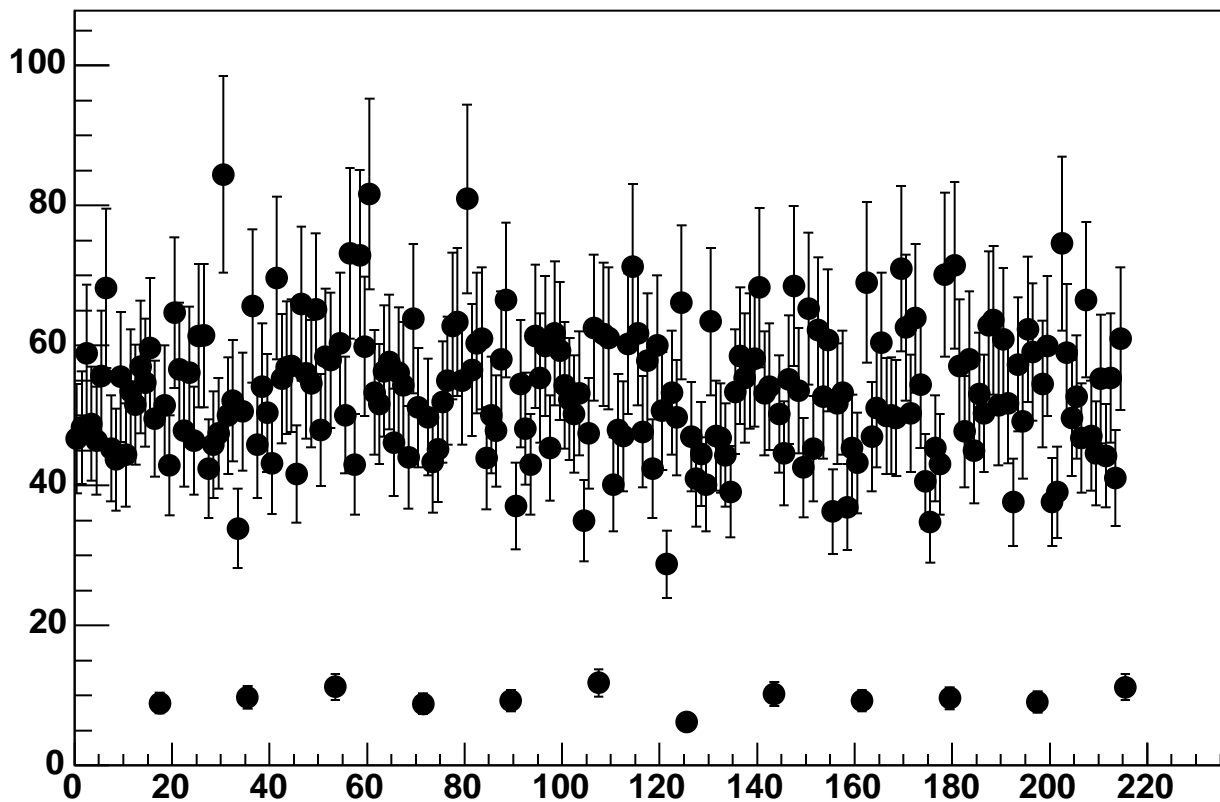
Enable 2, Hold=30, DAC=650, ADC Noise vs 18\*Chip+Chan



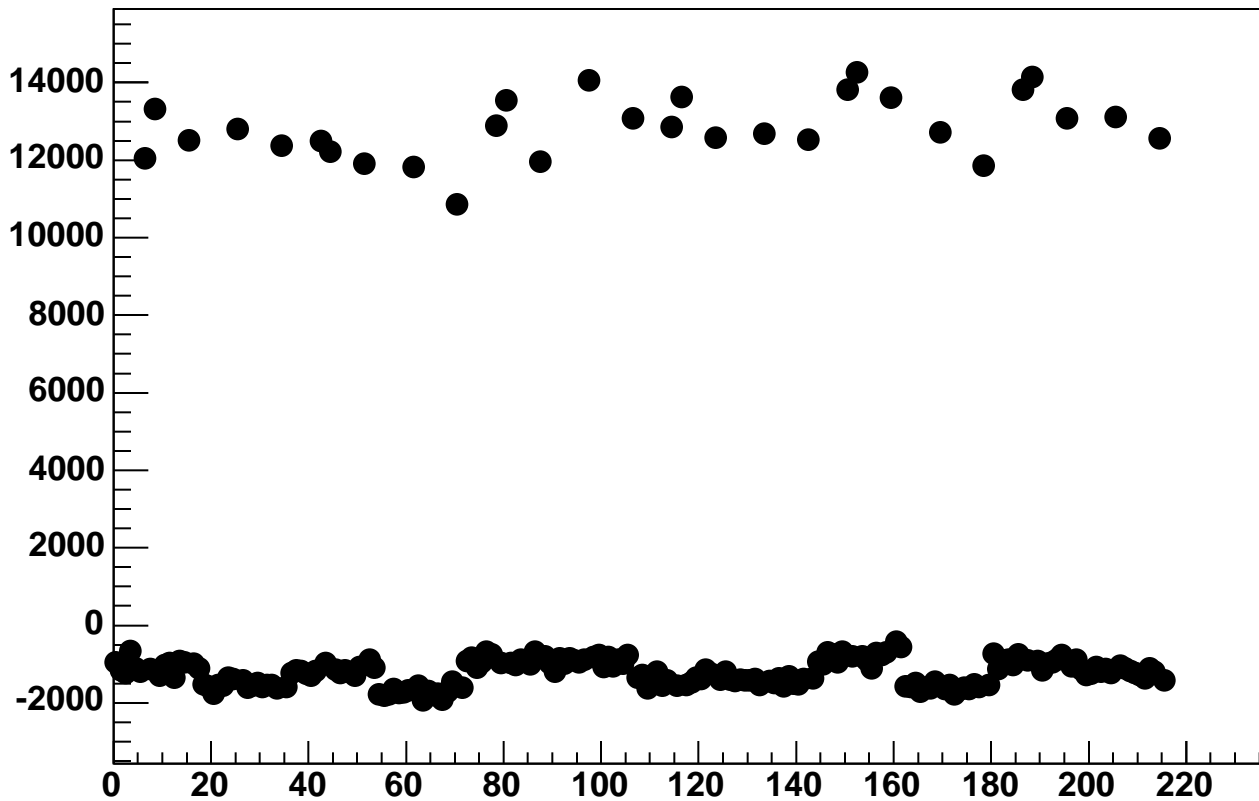
Enable 2, Hold=30, DAC=700, ADC Mean vs 18\*Chip+Chan



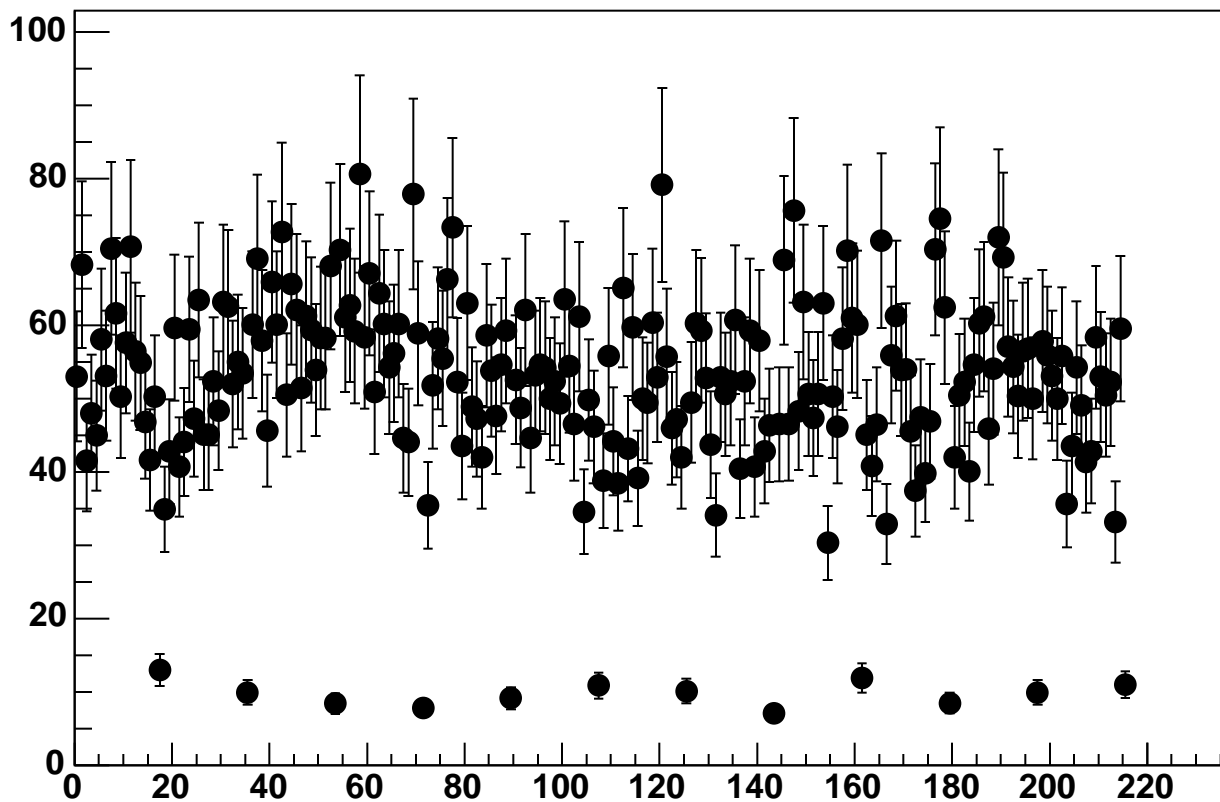
Enable 2, Hold=30, DAC=700, ADC Noise vs 18\*Chip+Chan



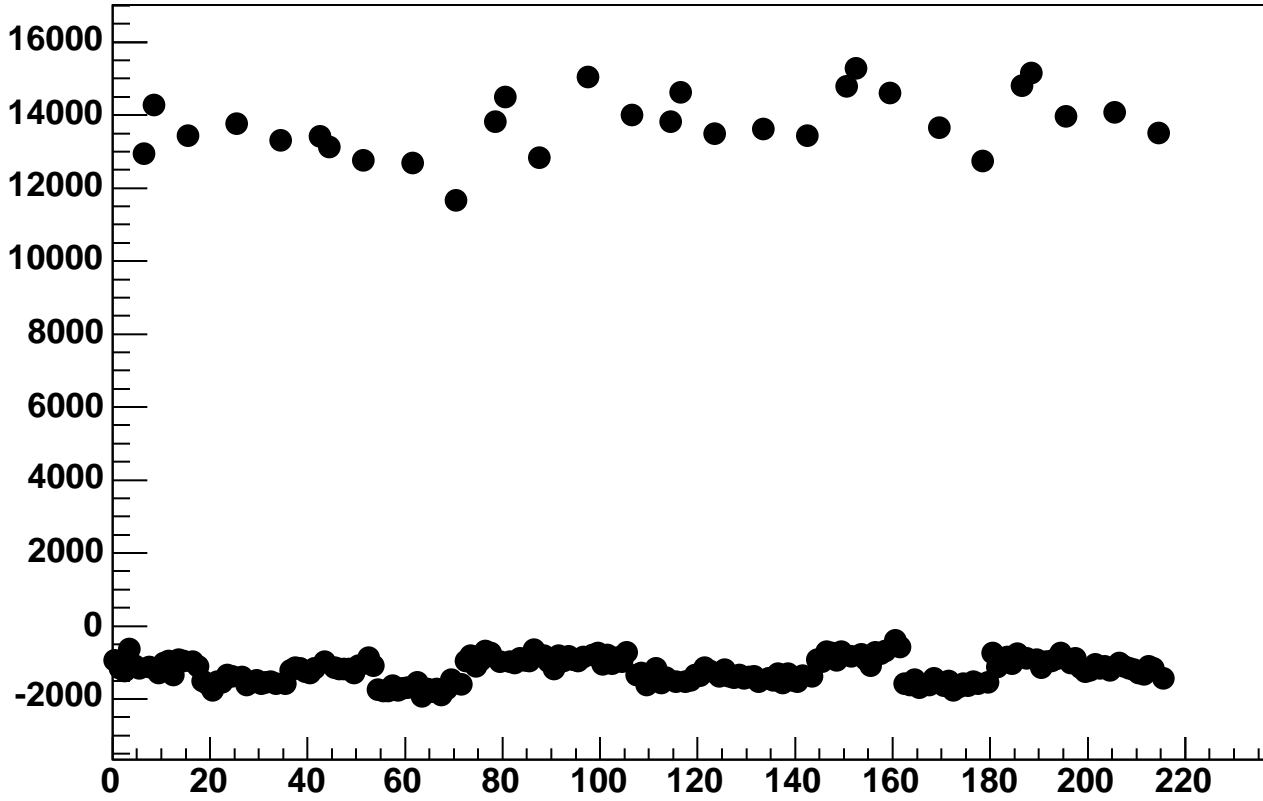
Enable 2, Hold=30, DAC=750, ADC Mean vs 18\*Chip+Chan



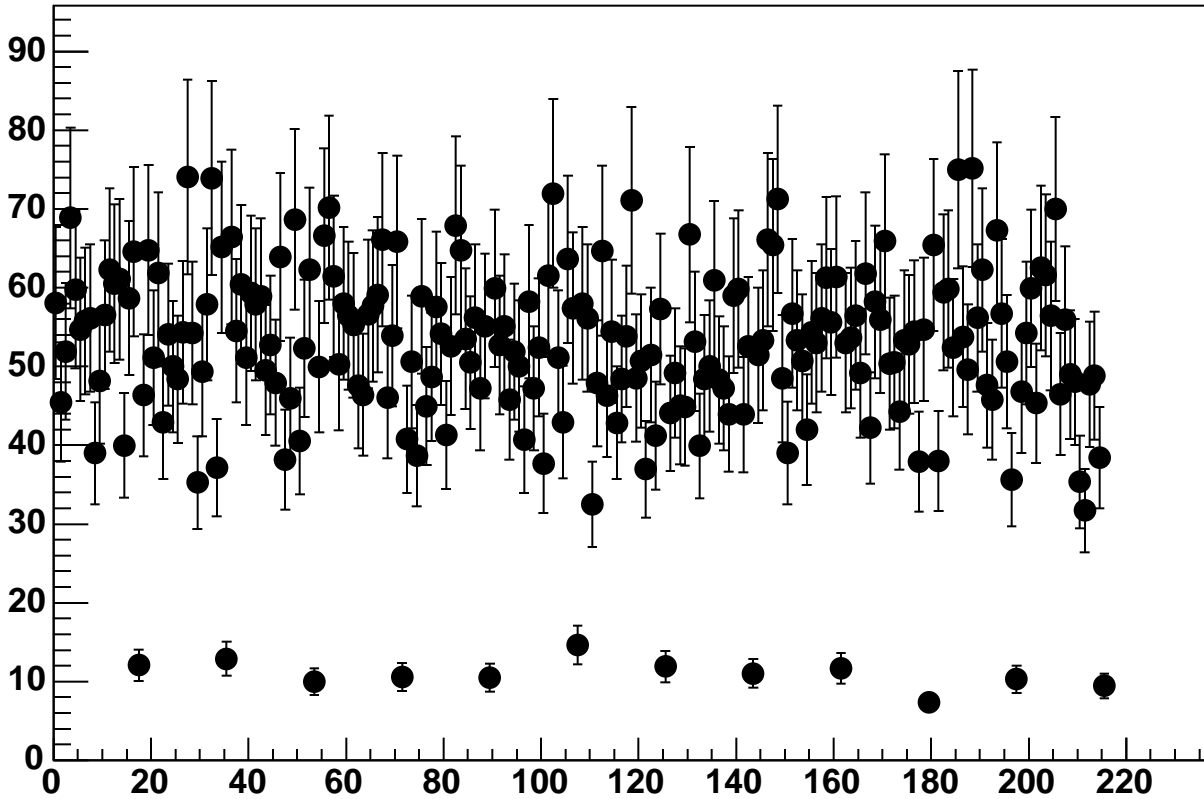
Enable 2, Hold=30, DAC=750, ADC Noise vs 18\*Chip+Chan



Enable 2, Hold=30, DAC=800, ADC Mean vs 18\*Chip+Chan

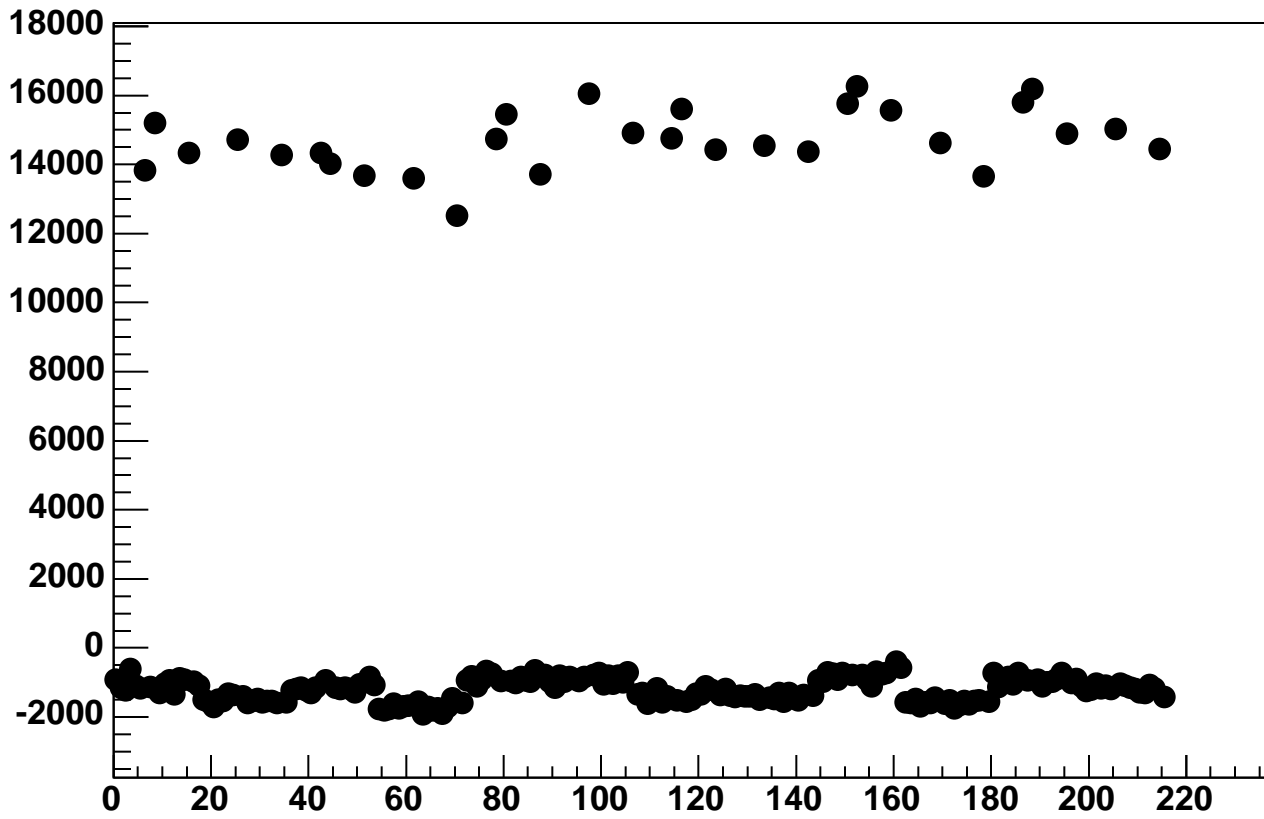


Enable 2, Hold=30, DAC=800, ADC Noise vs 18\*Chip+Chan

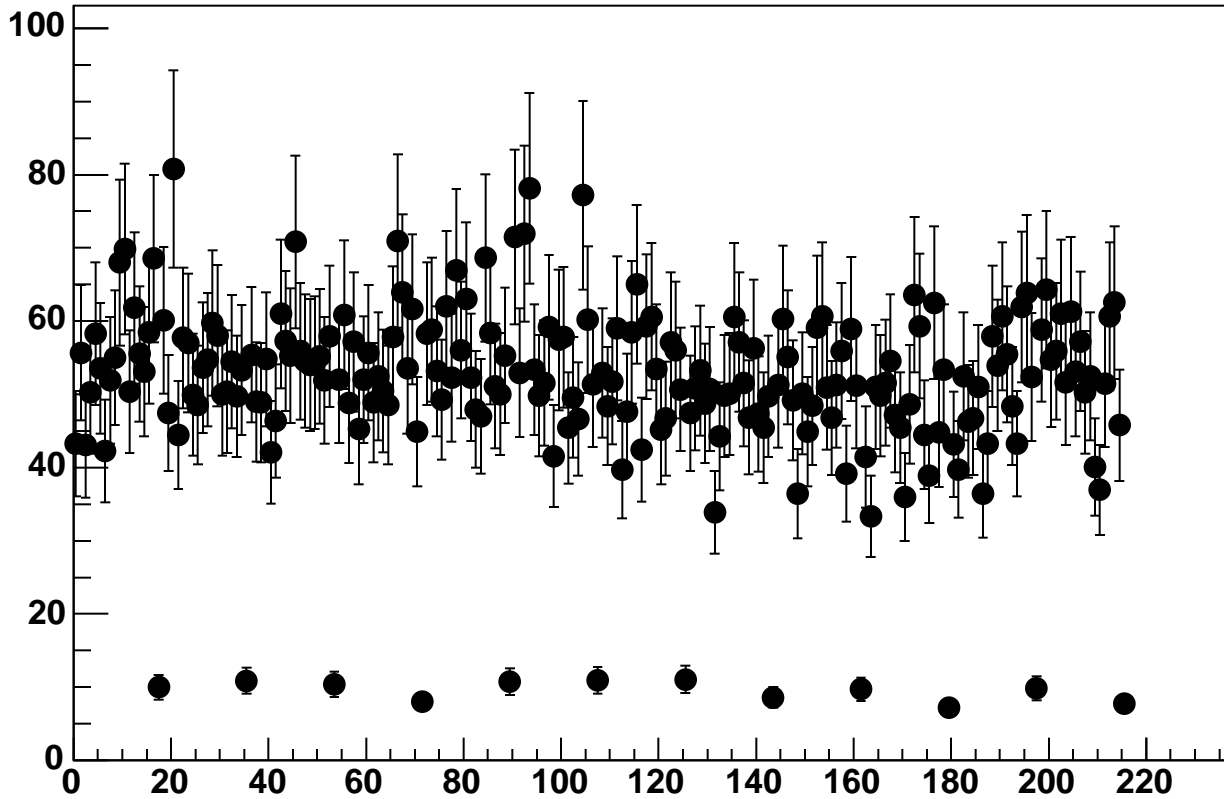




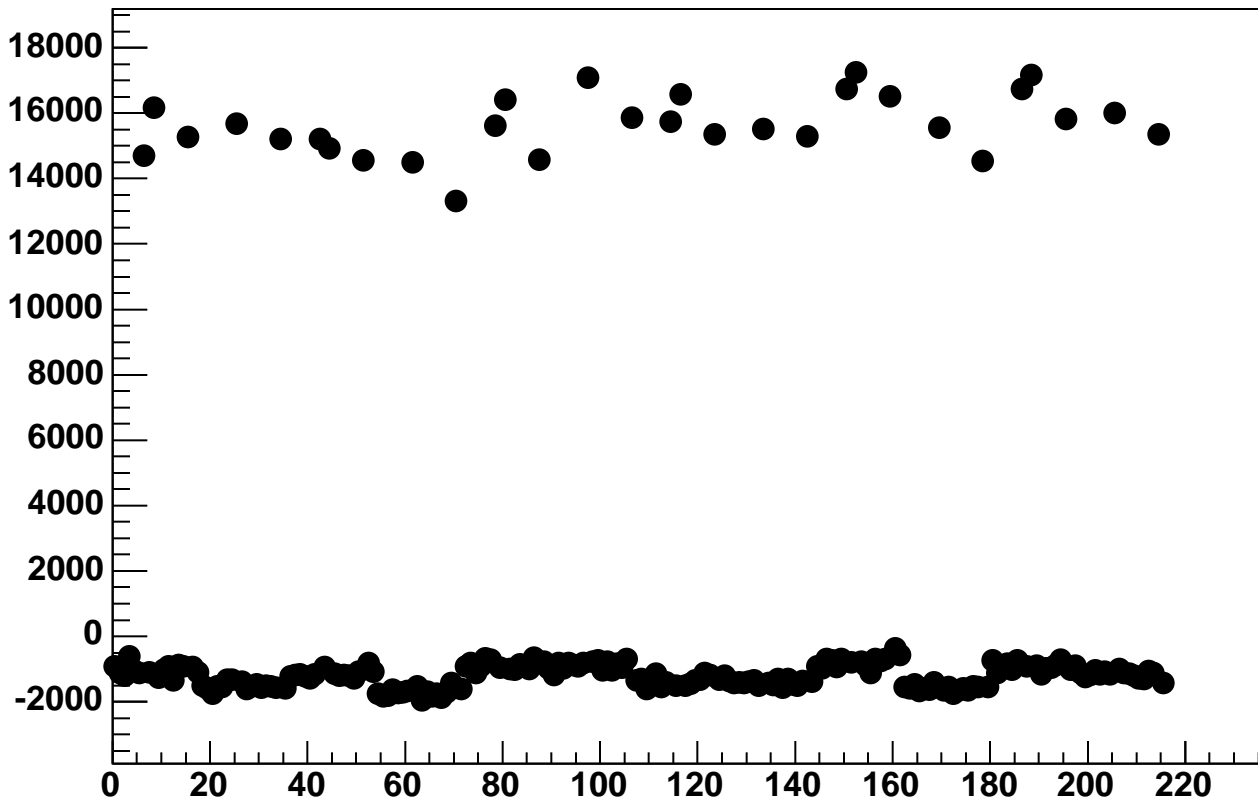
Enable 2, Hold=30, DAC=850, ADC Mean vs 18\*Chip+Chan



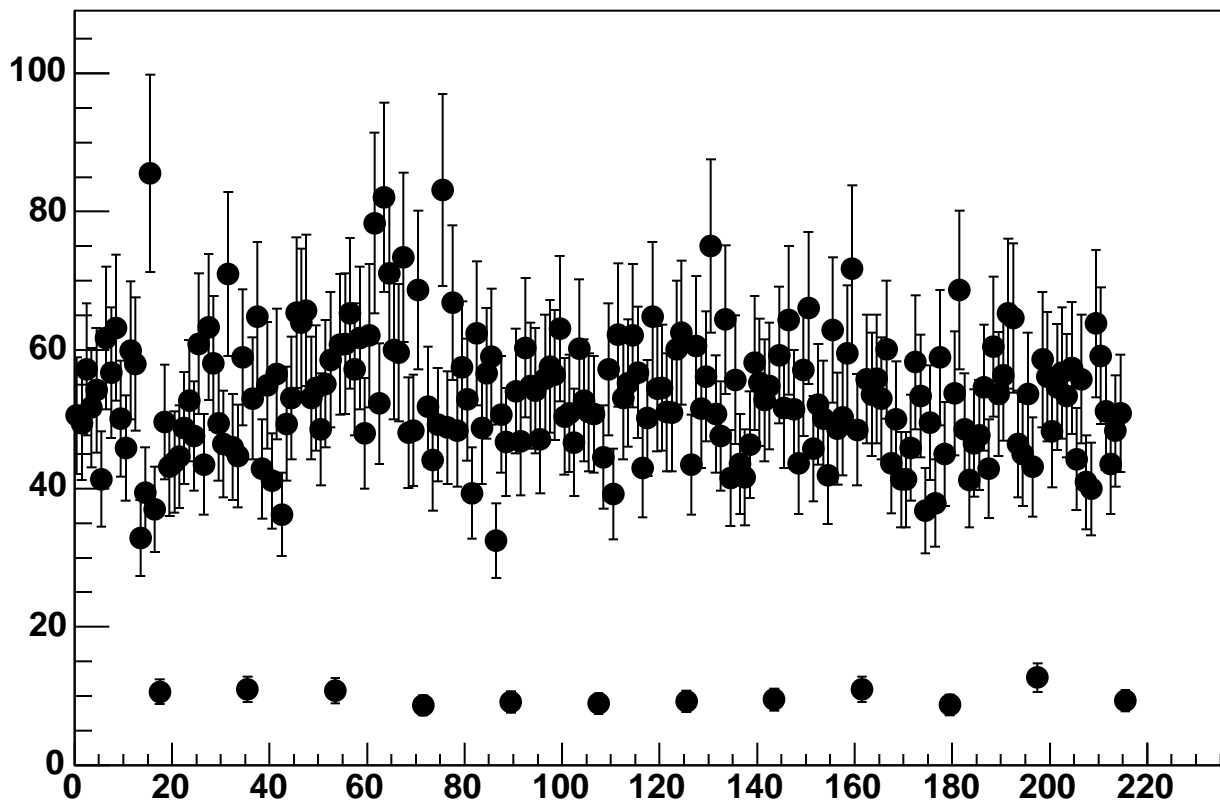
Enable 2, Hold=30, DAC=850, ADC Noise vs 18\*Chip+Chan



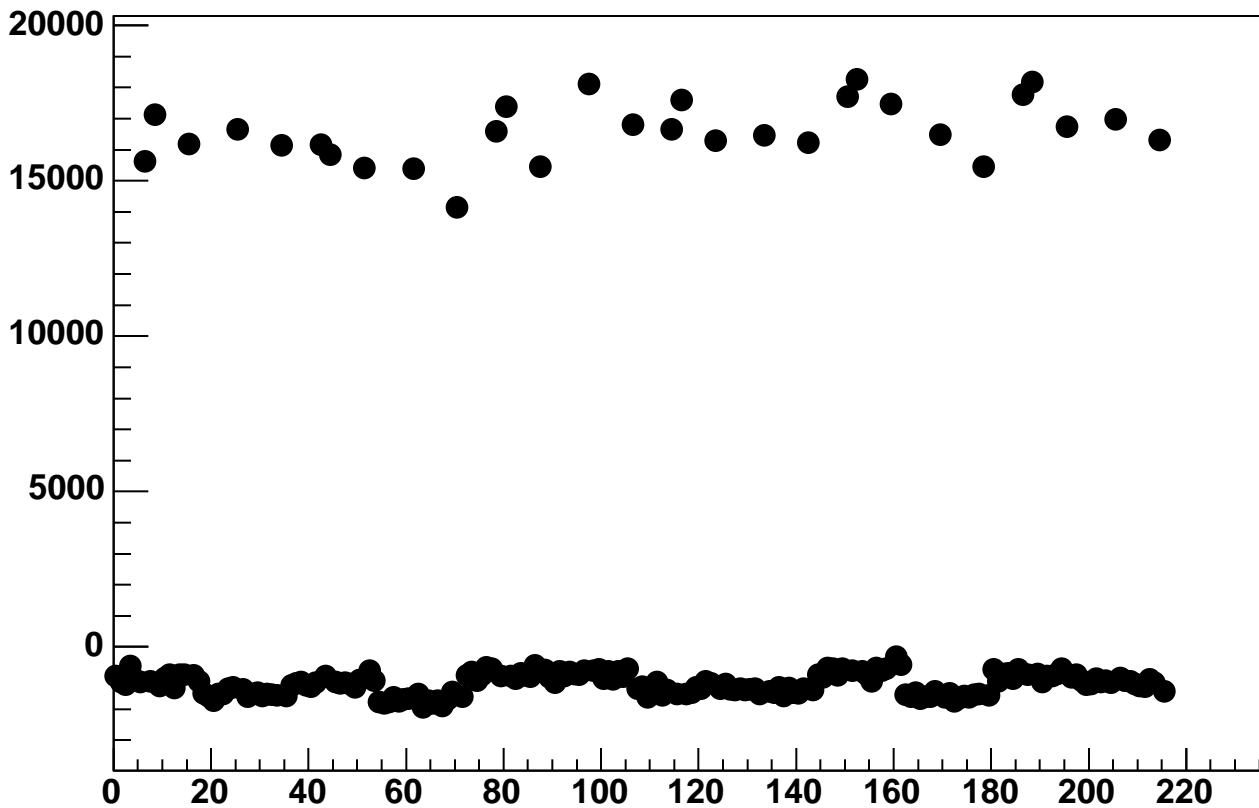
Enable 2, Hold=30, DAC=900, ADC Mean vs 18\*Chip+Chan



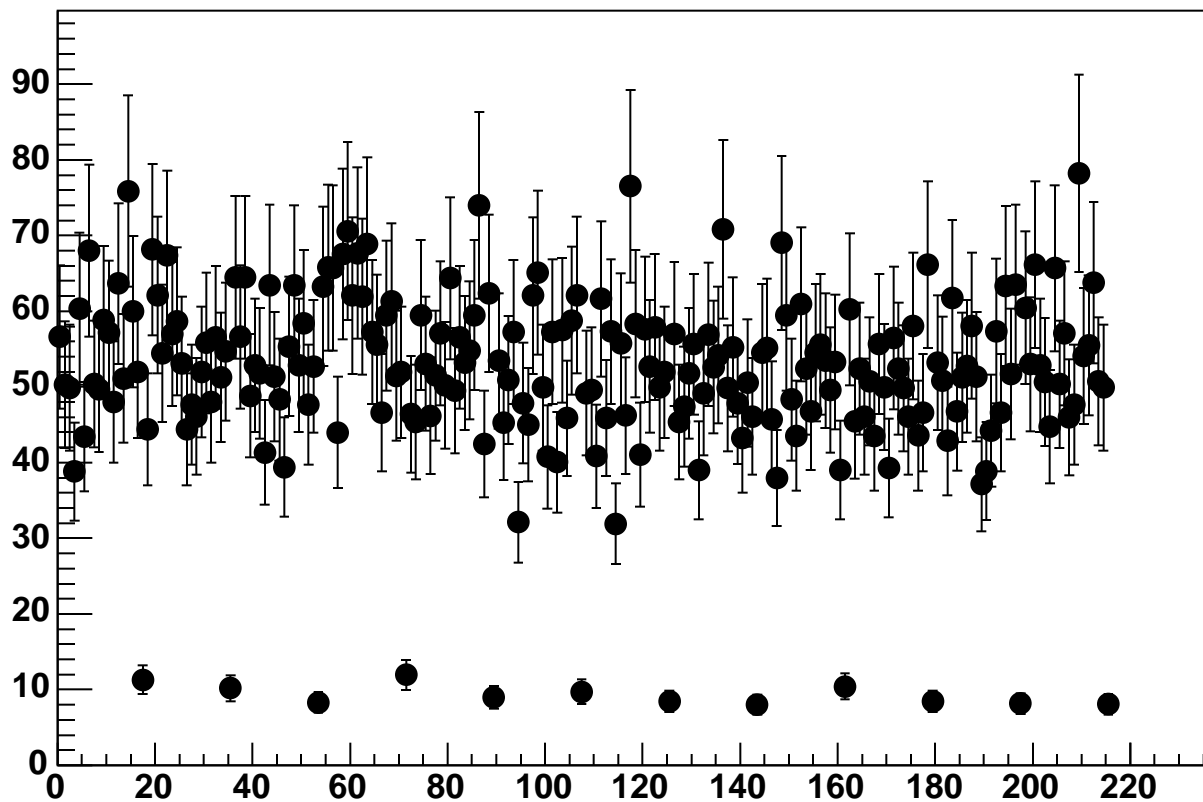
Enable 2, Hold=30, DAC=900, ADC Noise vs 18\*Chip+Chan



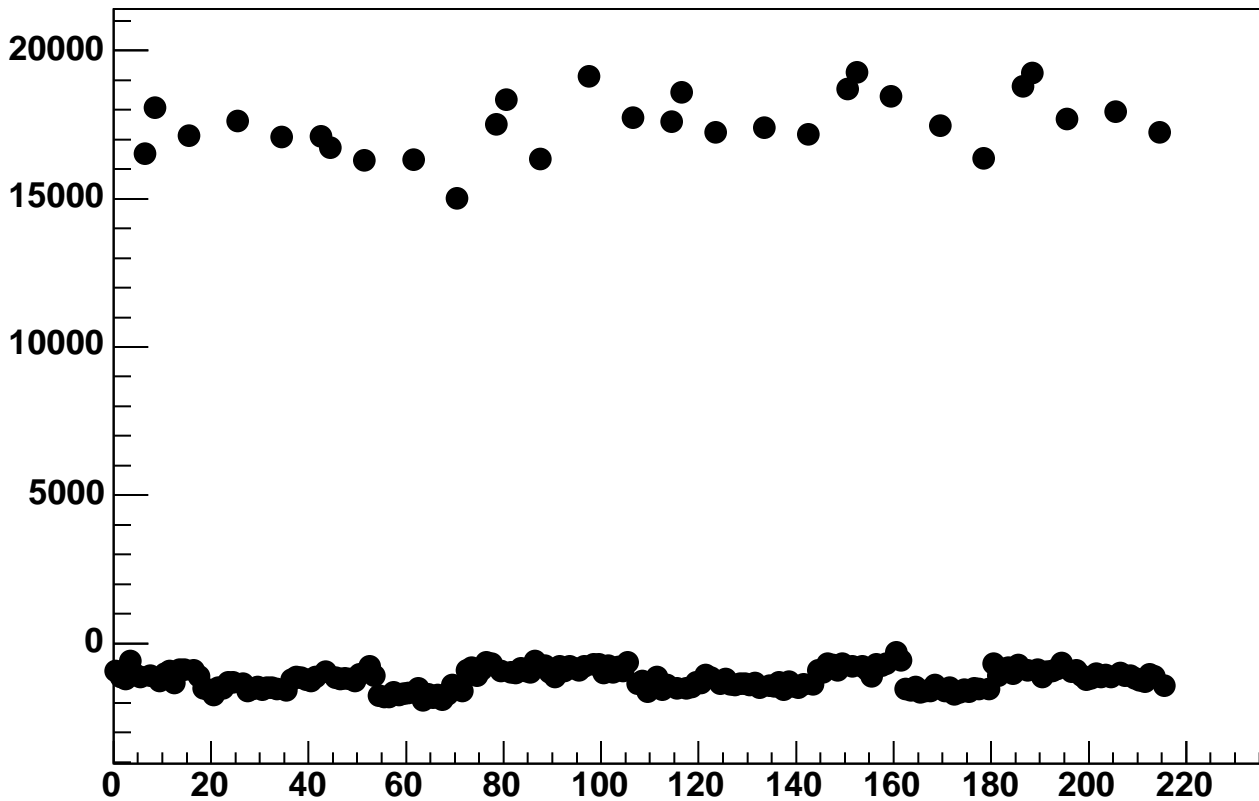
Enable 2, Hold=30, DAC=950, ADC Mean vs 18\*Chip+Chan



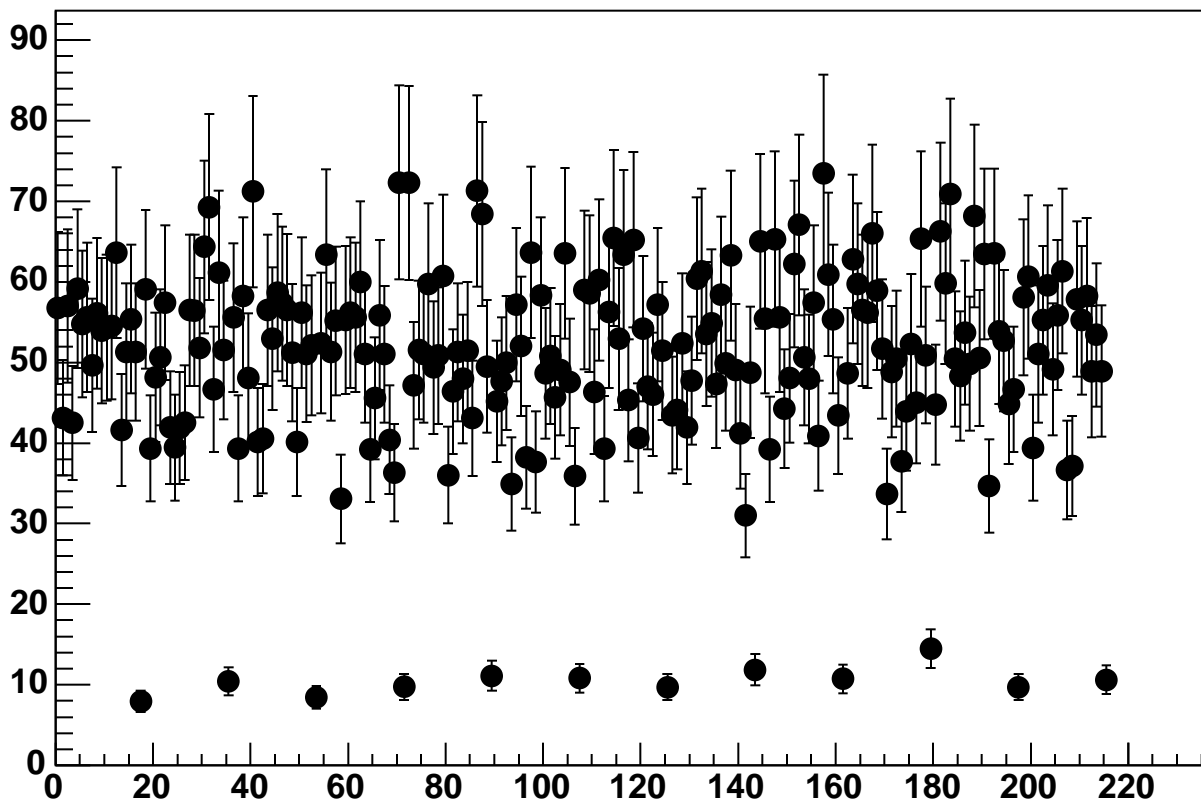
Enable 2, Hold=30, DAC=950, ADC Noise vs 18\*Chip+Chan



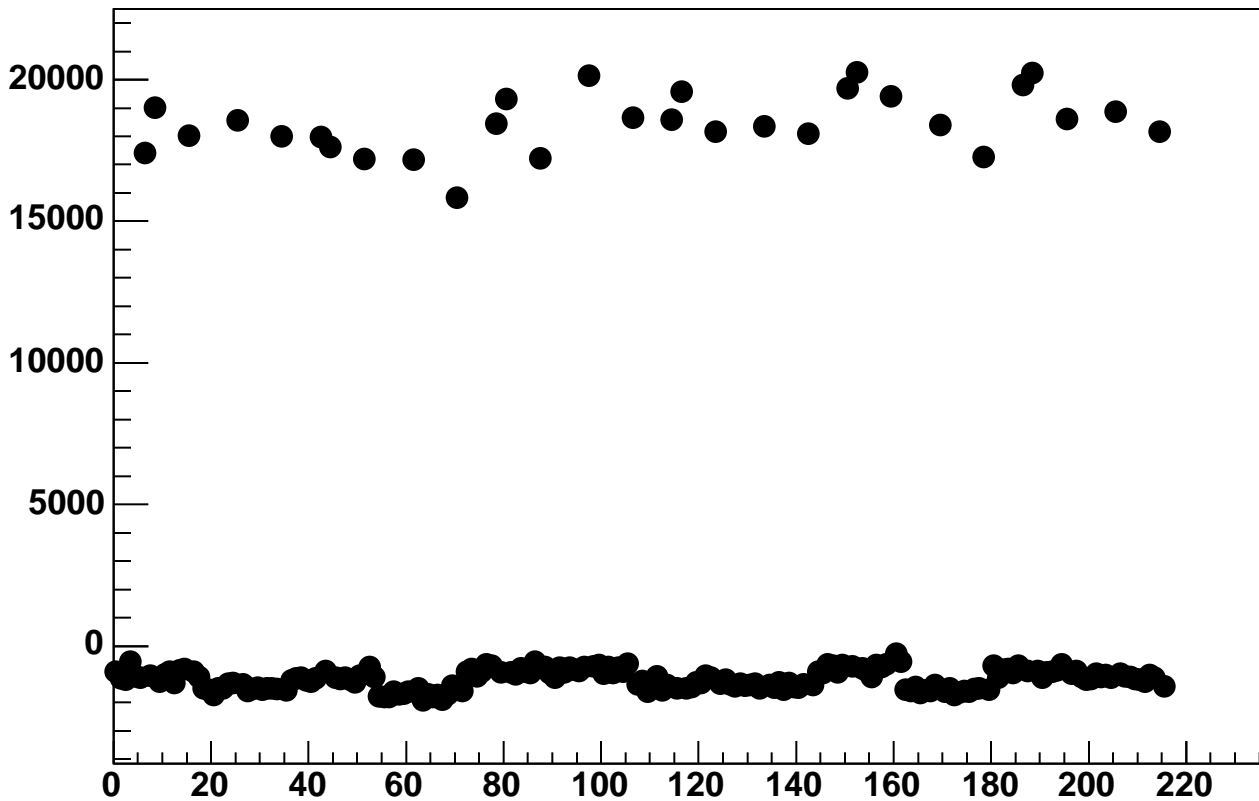
Enable 2, Hold=30, DAC=1000, ADC Mean vs 18\*Chip+Chan



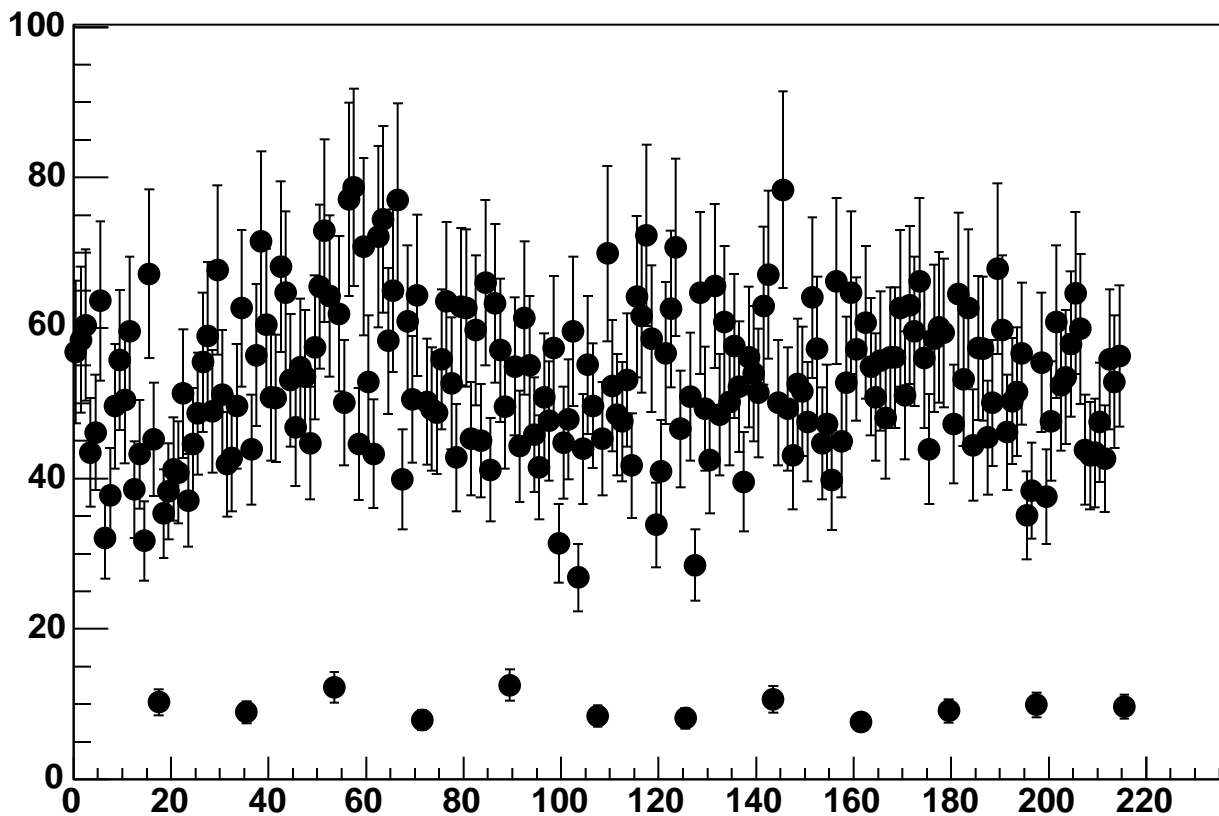
Enable 2, Hold=30, DAC=1000, ADC Noise vs 18\*Chip+Chan



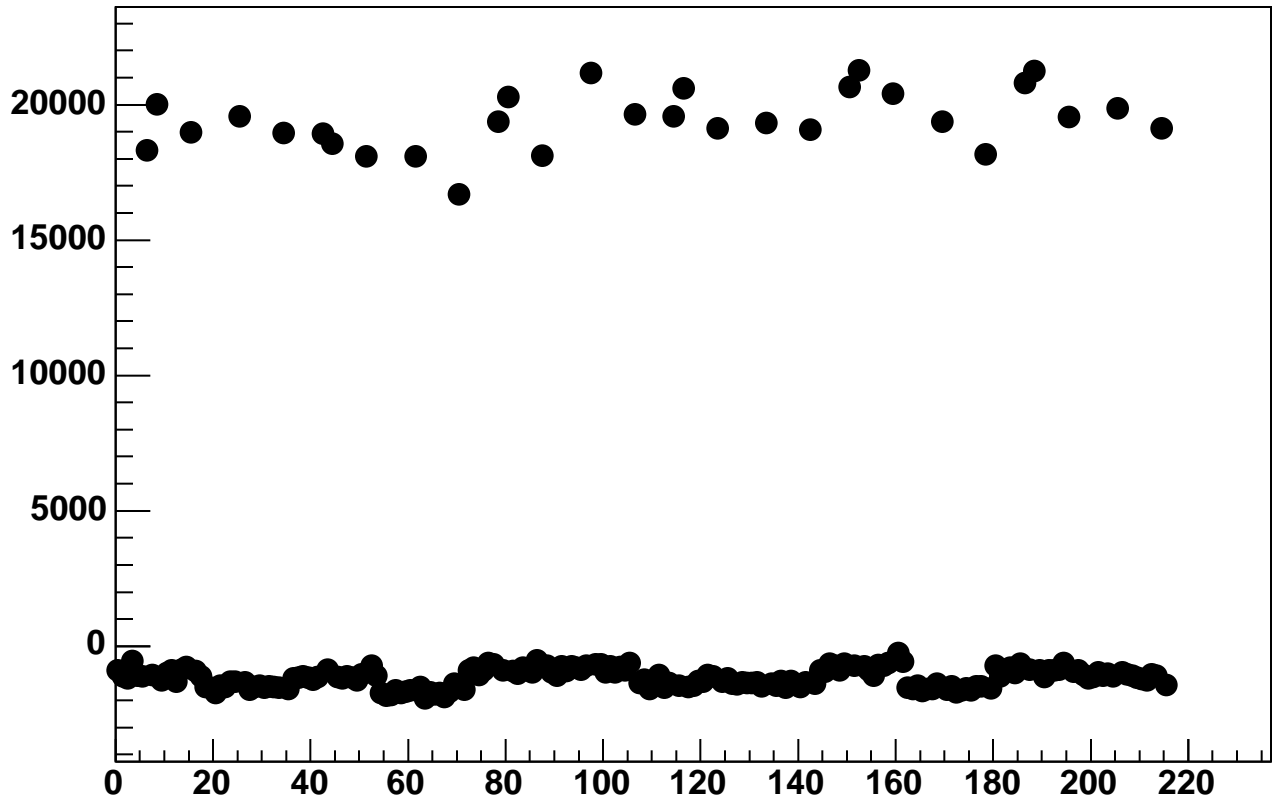
Enable 2, Hold=30, DAC=1050, ADC Mean vs 18\*Chip+Chan



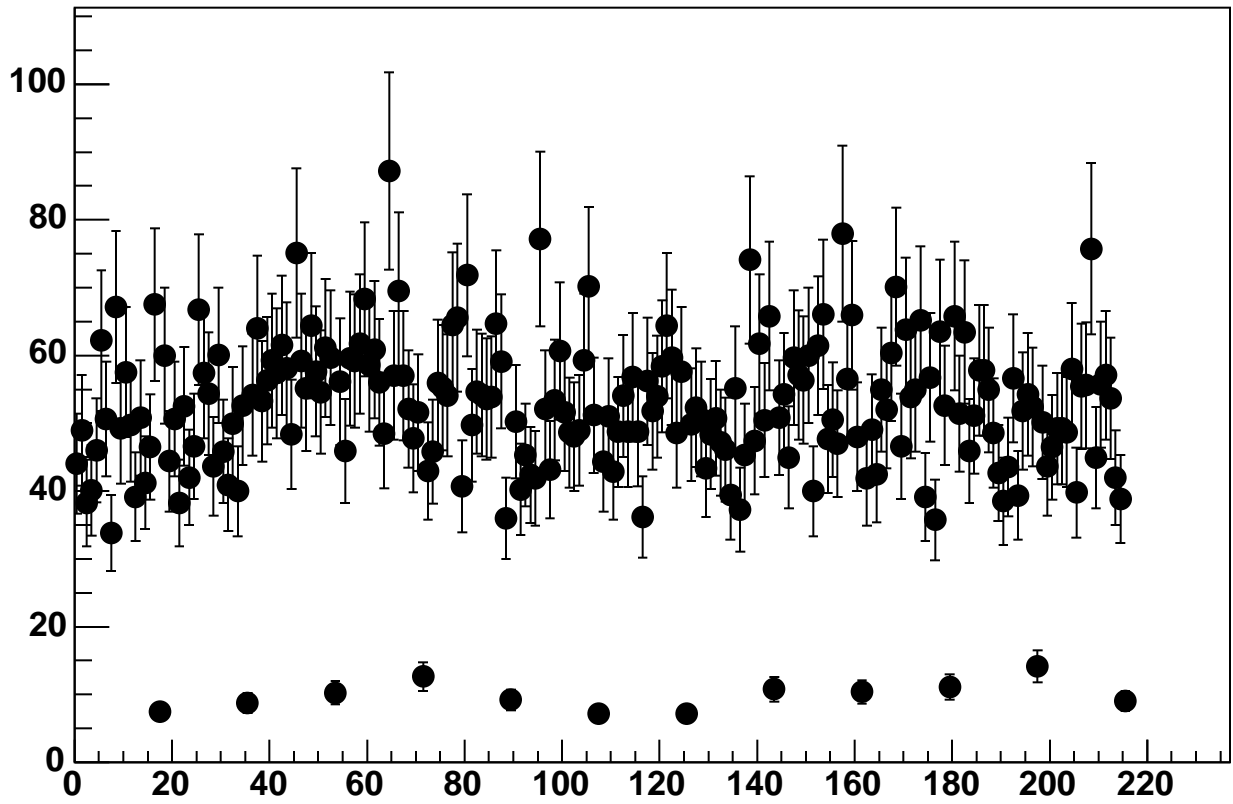
Enable 2, Hold=30, DAC=1050, ADC Noise vs 18\*Chip+Chan



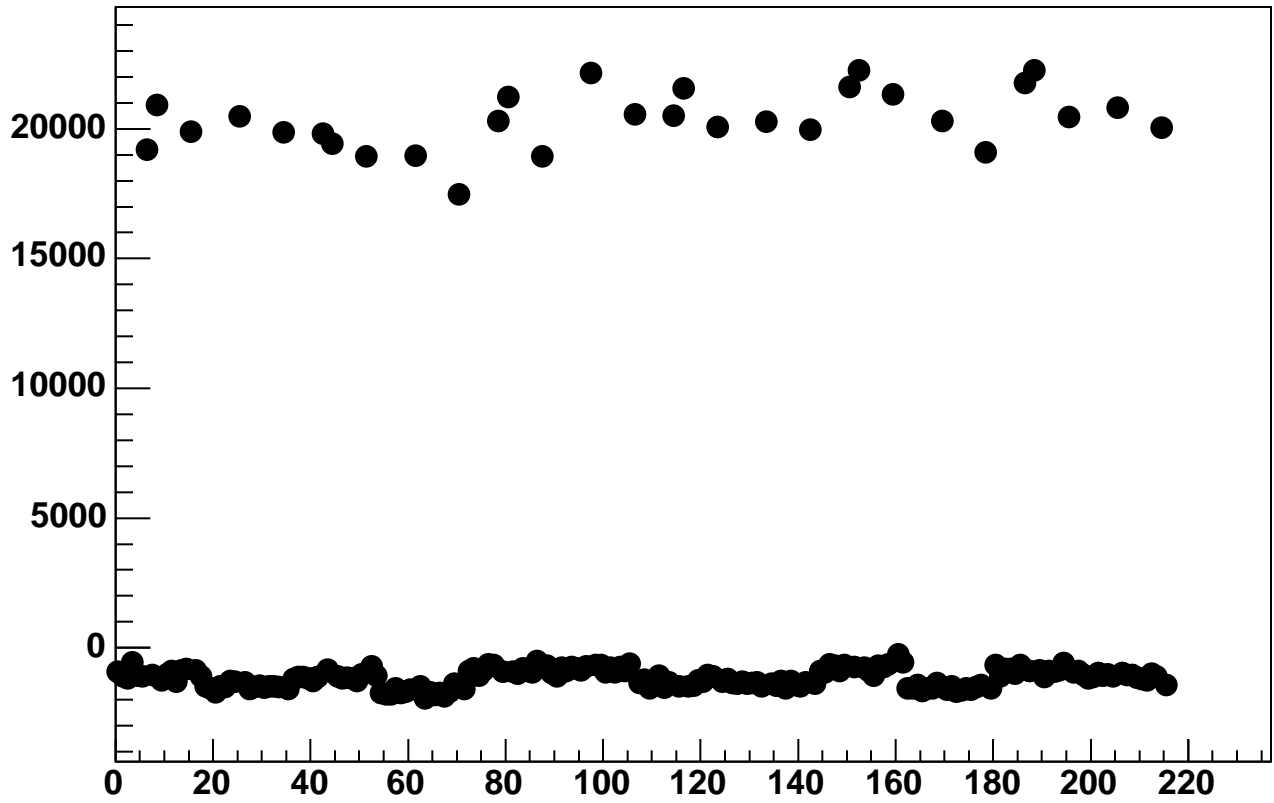
Enable 2, Hold=30, DAC=1100, ADC Mean vs 18\*Chip+Chan



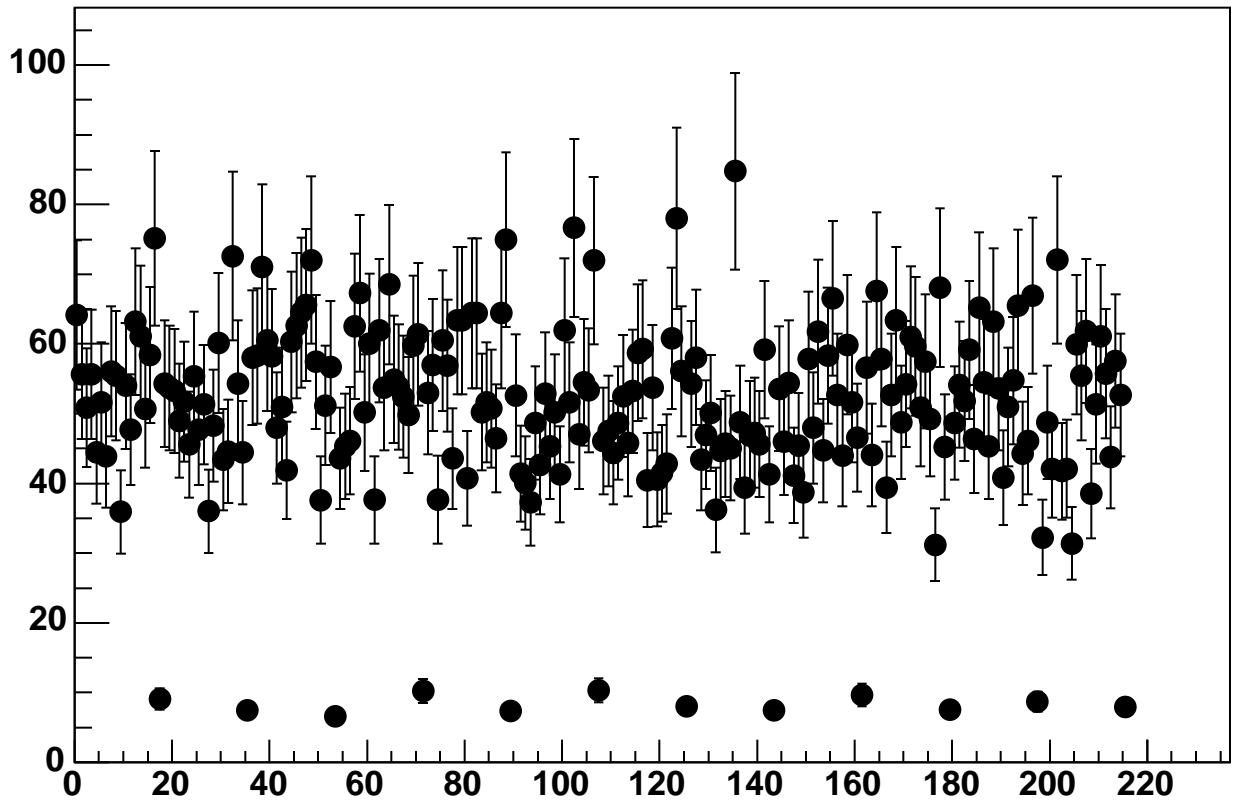
Enable 2, Hold=30, DAC=1100, ADC Noise vs 18\*Chip+Chan



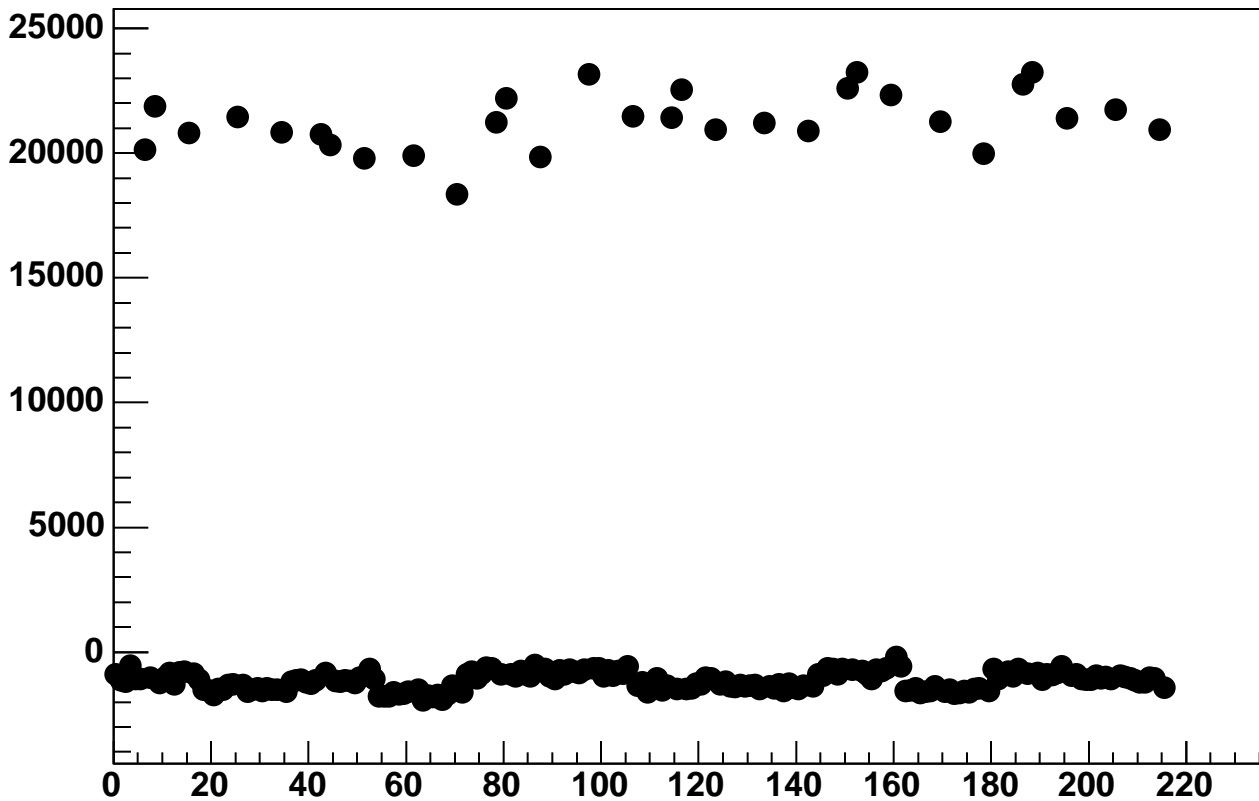
Enable 2, Hold=30, DAC=1150, ADC Mean vs 18\*Chip+Chan



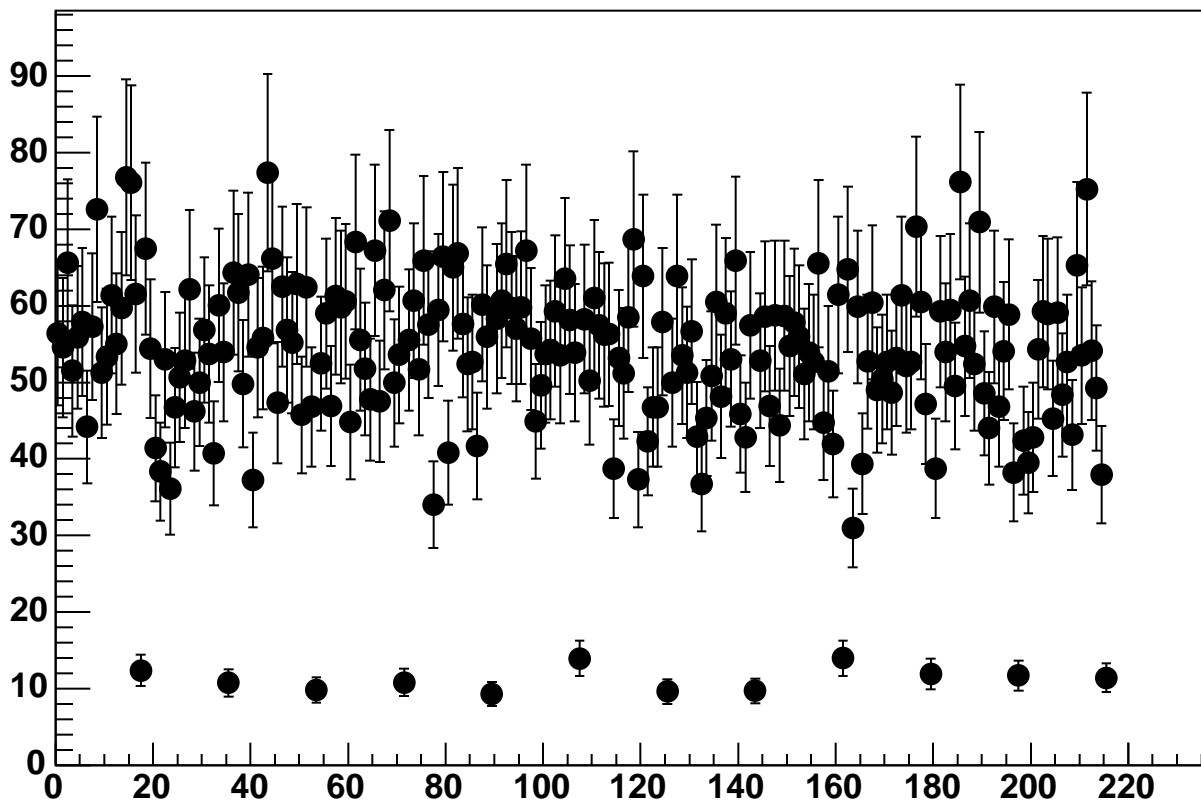
Enable 2, Hold=30, DAC=1150, ADC Noise vs 18\*Chip+Chan



Enable 2, Hold=30, DAC=1200, ADC Mean vs 18\*Chip+Chan

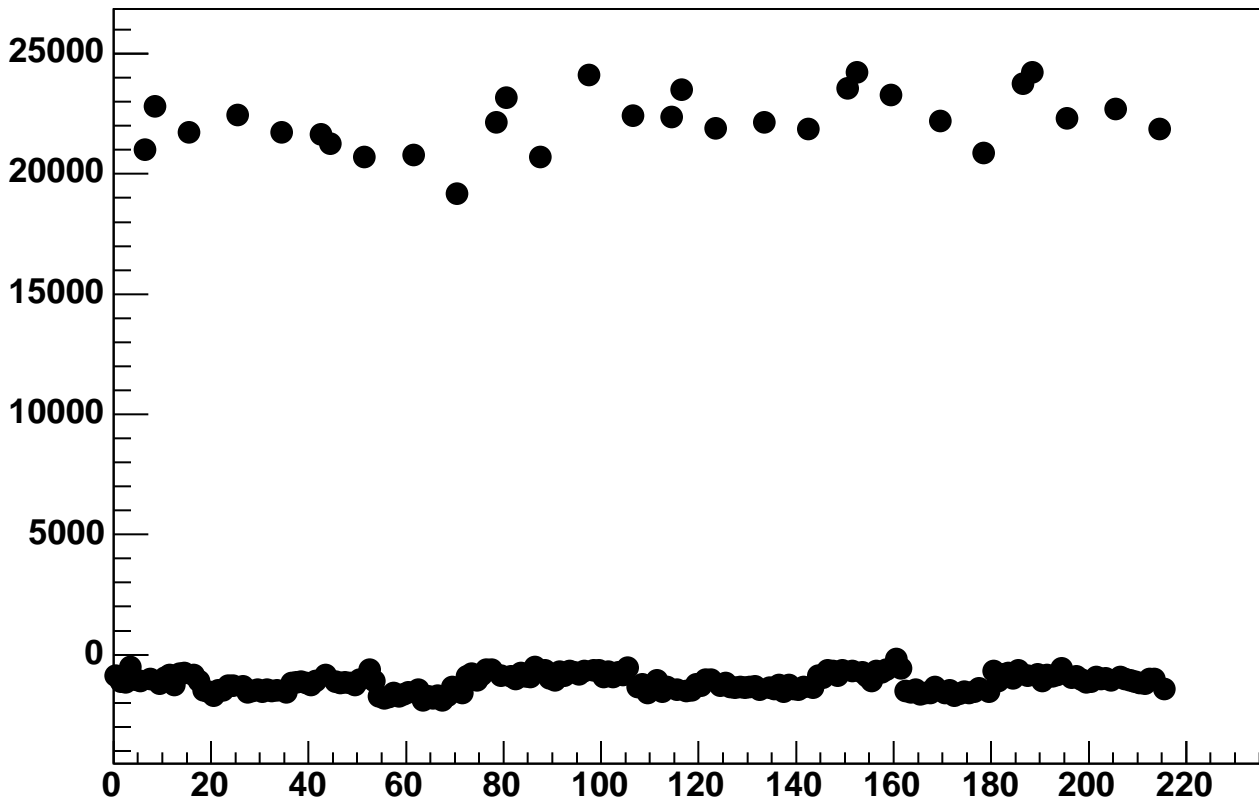


Enable 2, Hold=30, DAC=1200, ADC Noise vs 18\*Chip+Chan

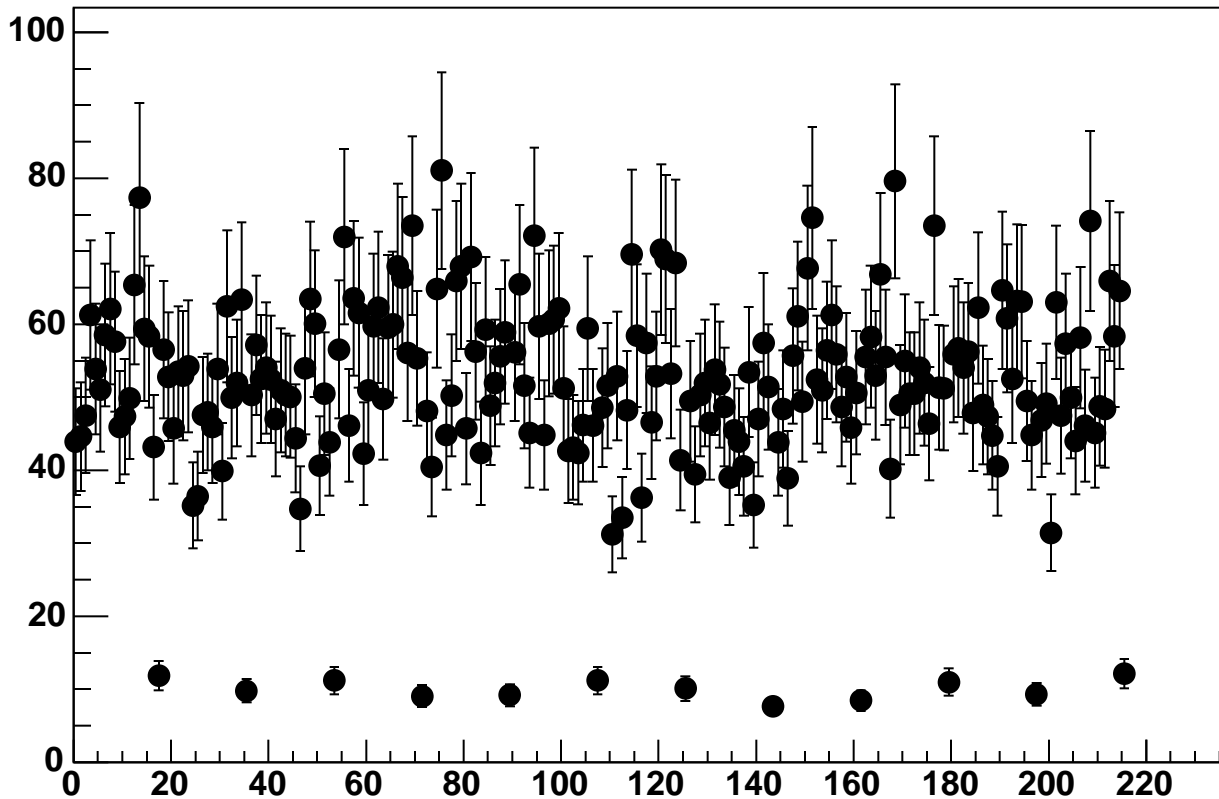




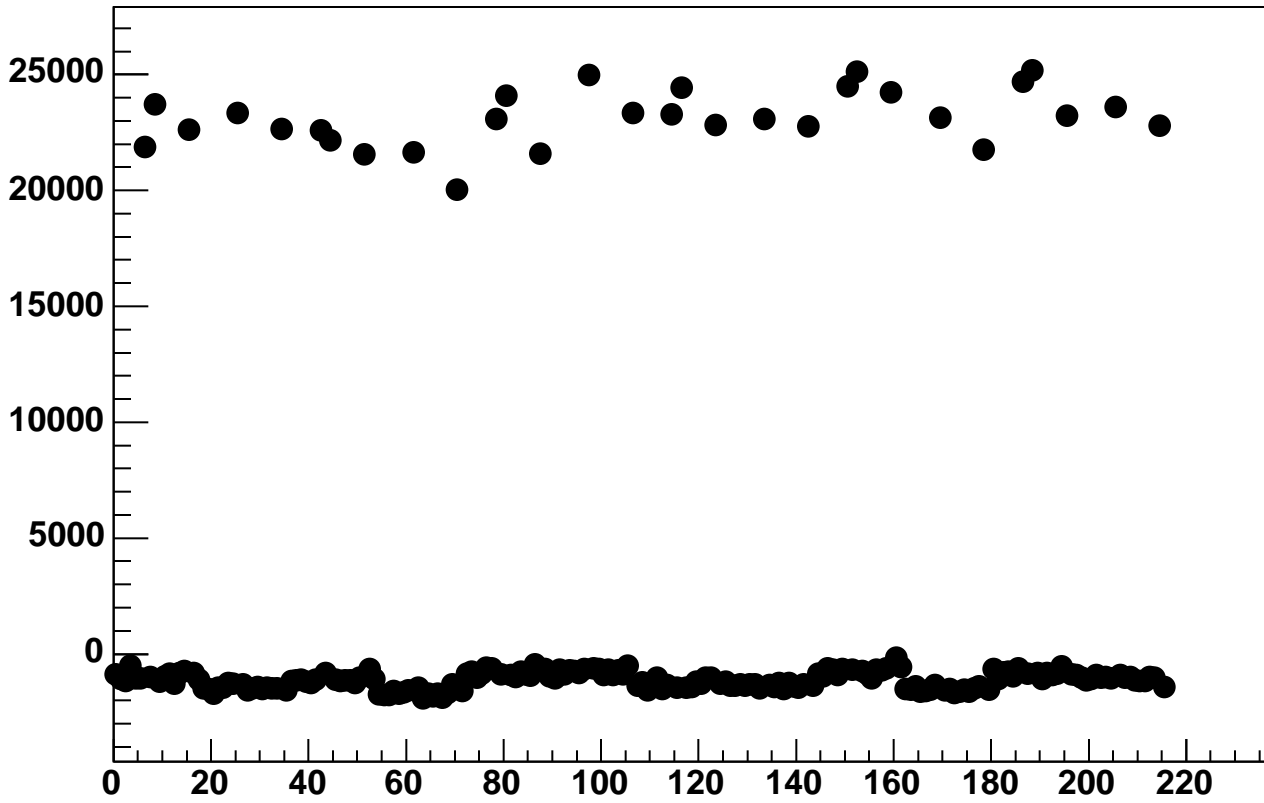
Enable 2, Hold=30, DAC=1250, ADC Mean vs 18\*Chip+Chan



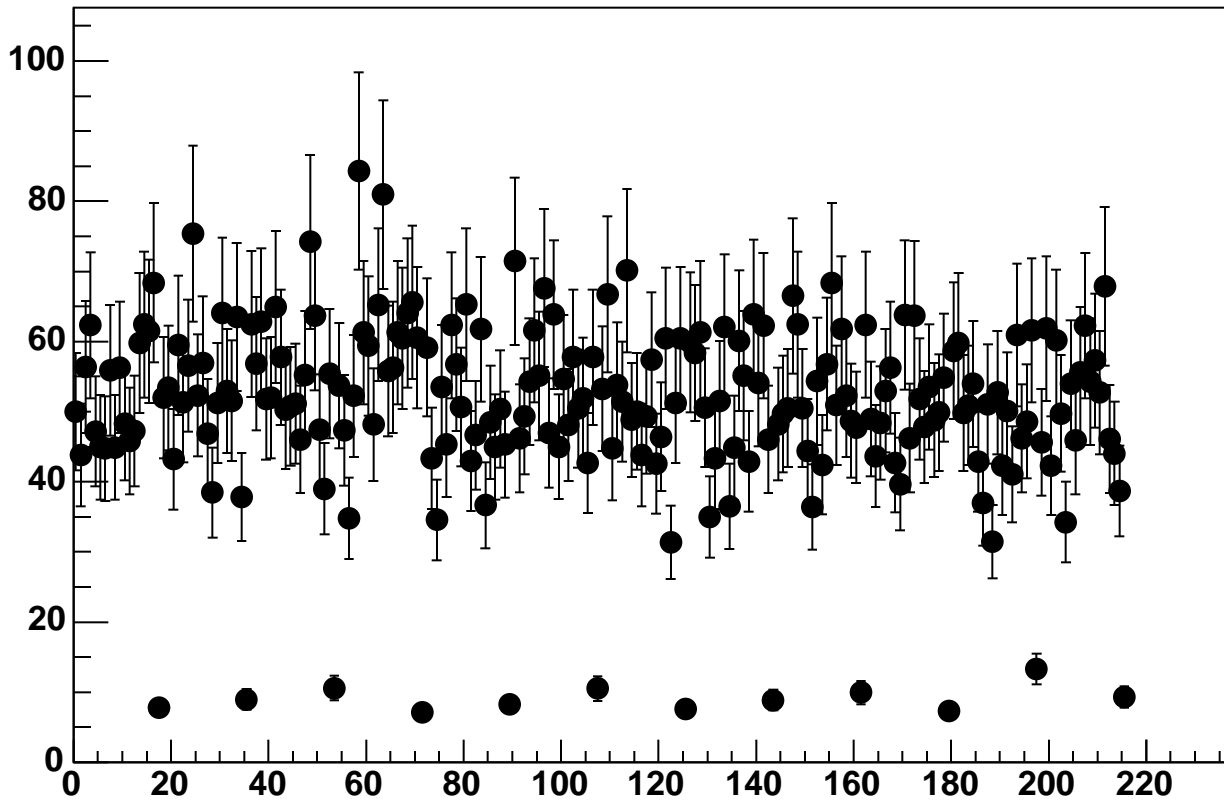
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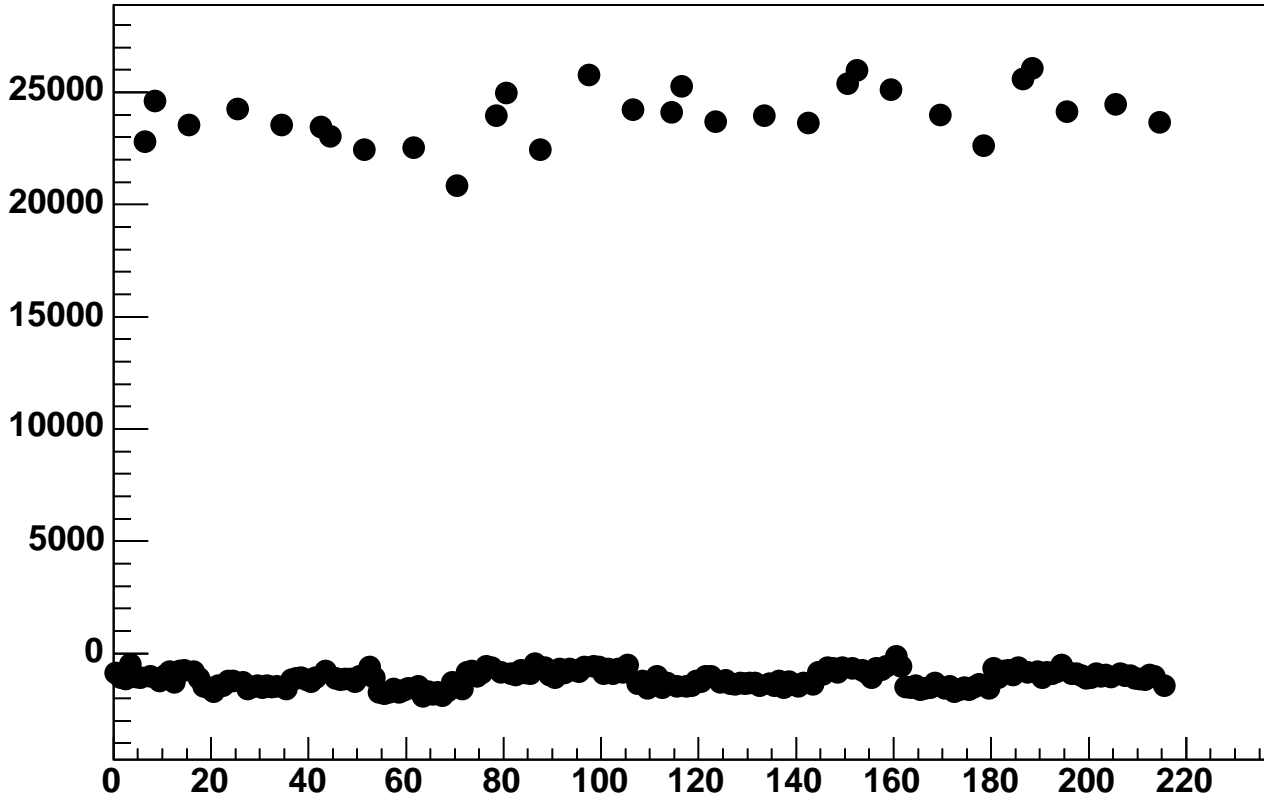
Enable 2, Hold=30, DAC=1300, ADC Mean vs 18\*Chip+Chan



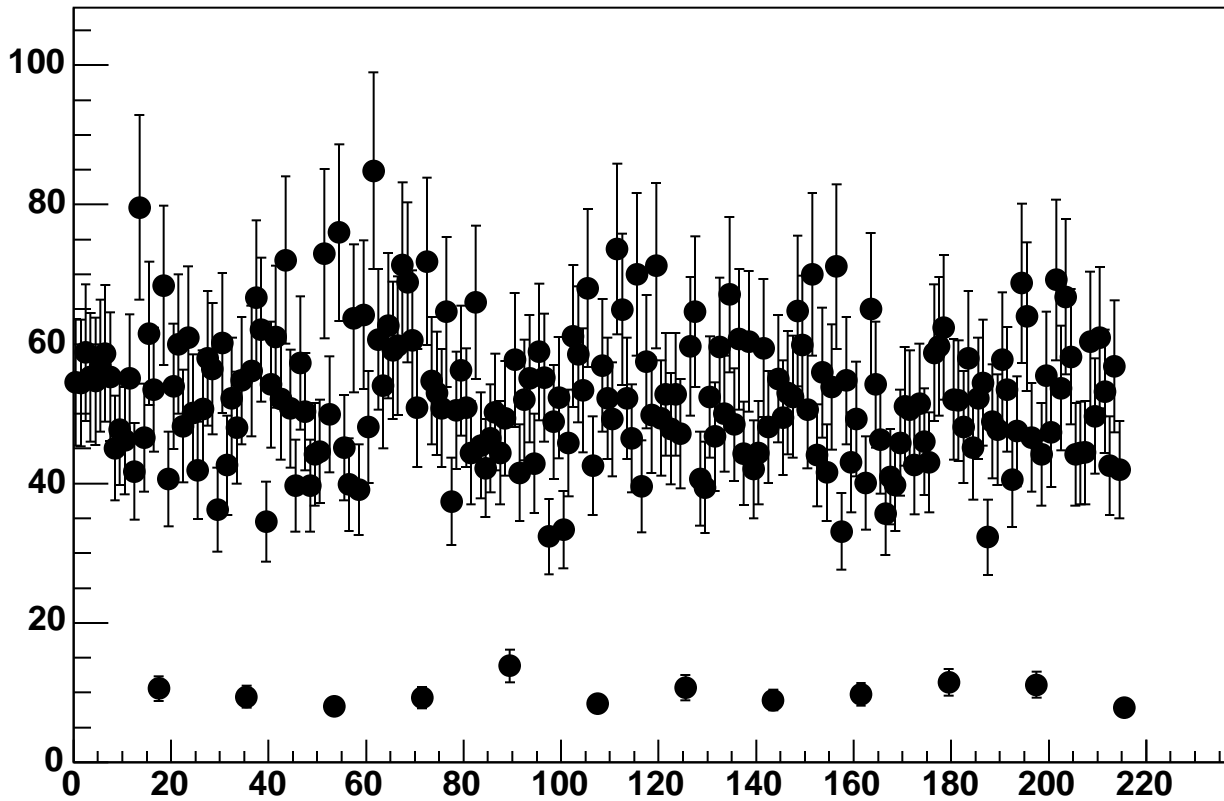
Enable 2, Hold=30, DAC=1300, ADC Noise vs 18\*Chip+Chan



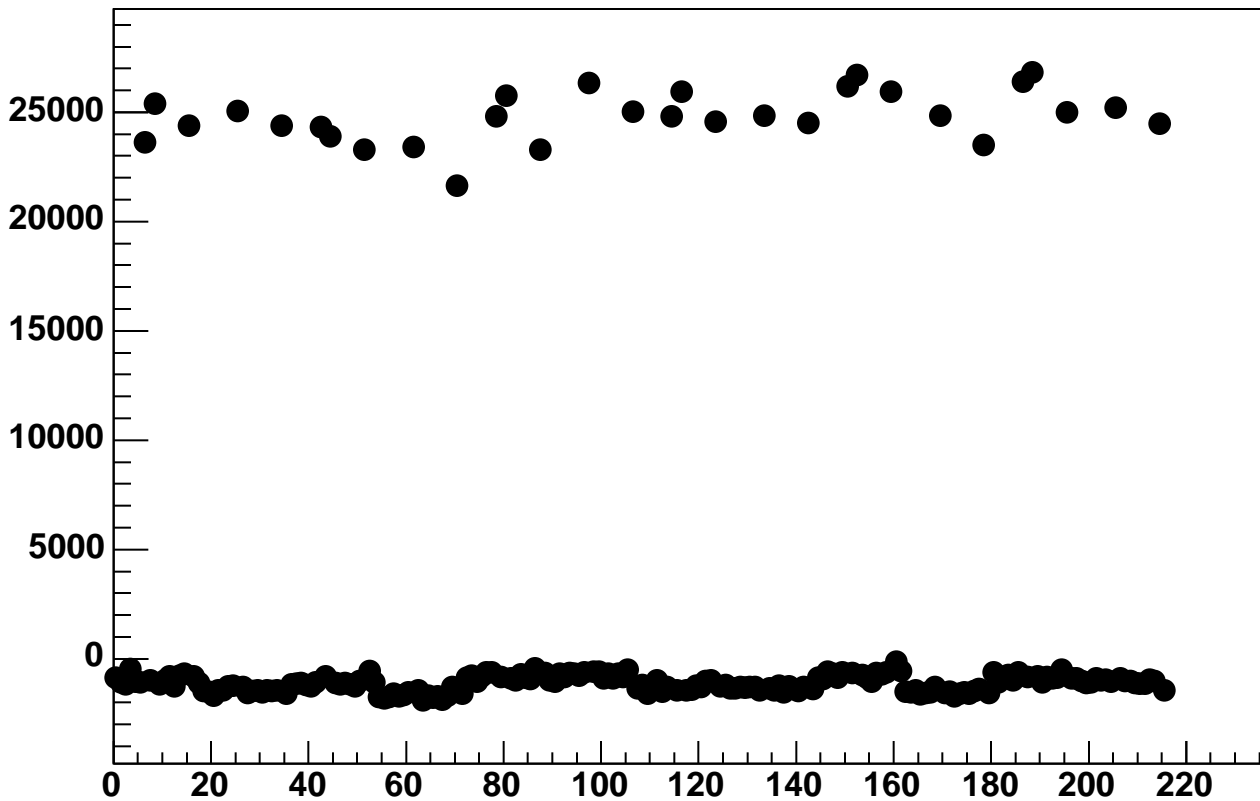
Enable 2, Hold=30, DAC=1350, ADC Mean vs 18\*Chip+Chan



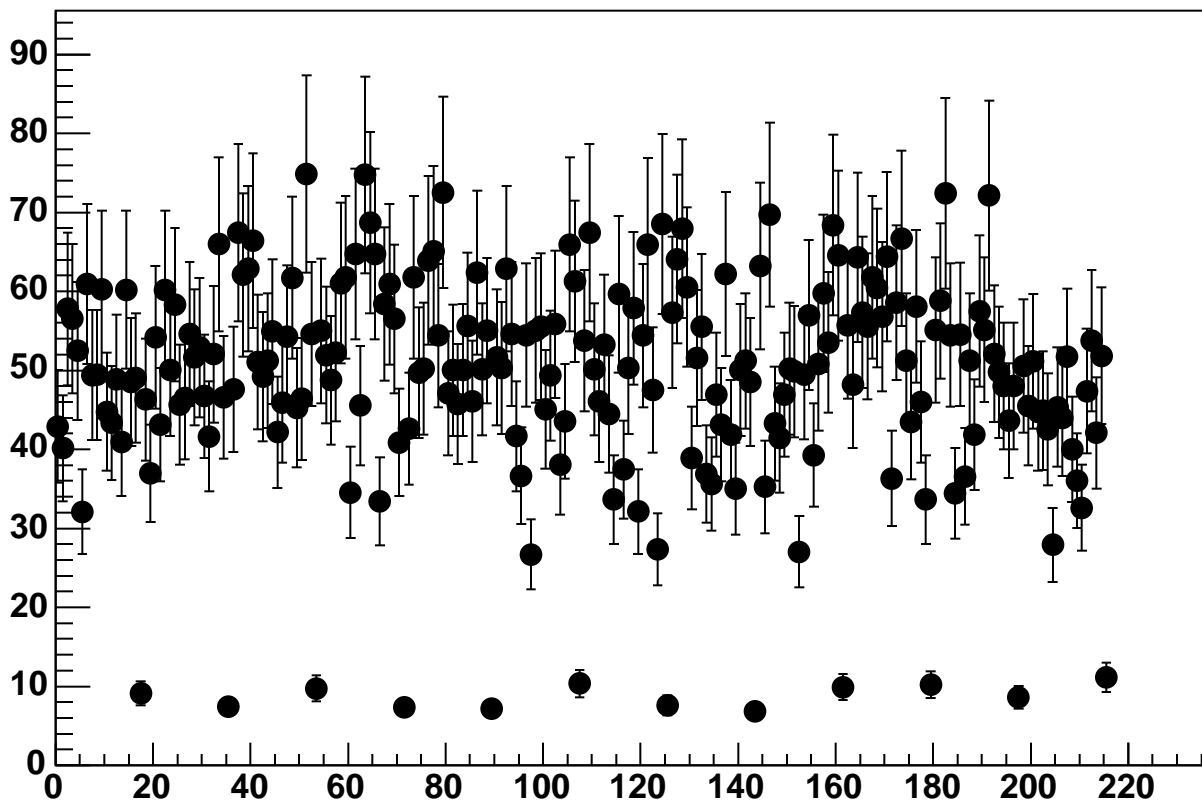
Enable 2, Hold=30, DAC=1350, ADC Noise vs 18\*Chip+Chan



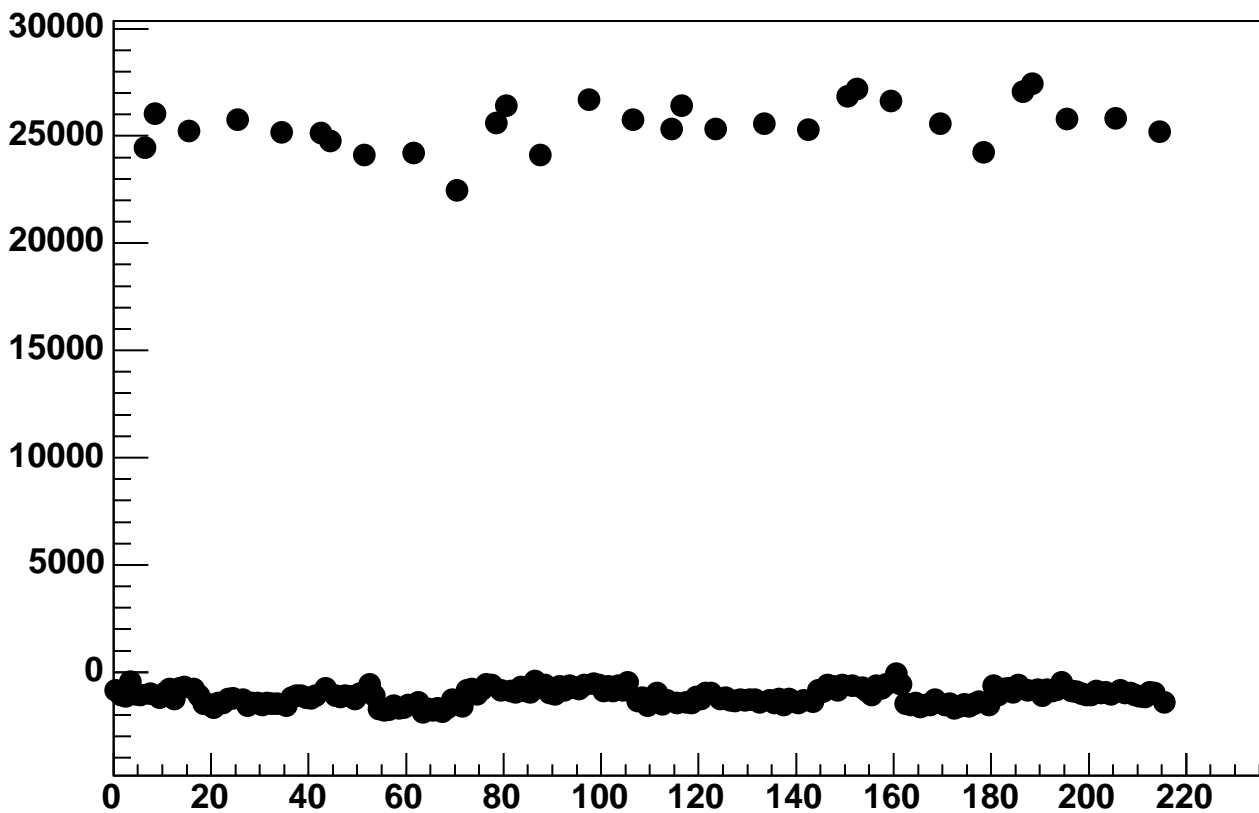
Enable 2, Hold=30, DAC=1400, ADC Mean vs 18\*Chip+Chan



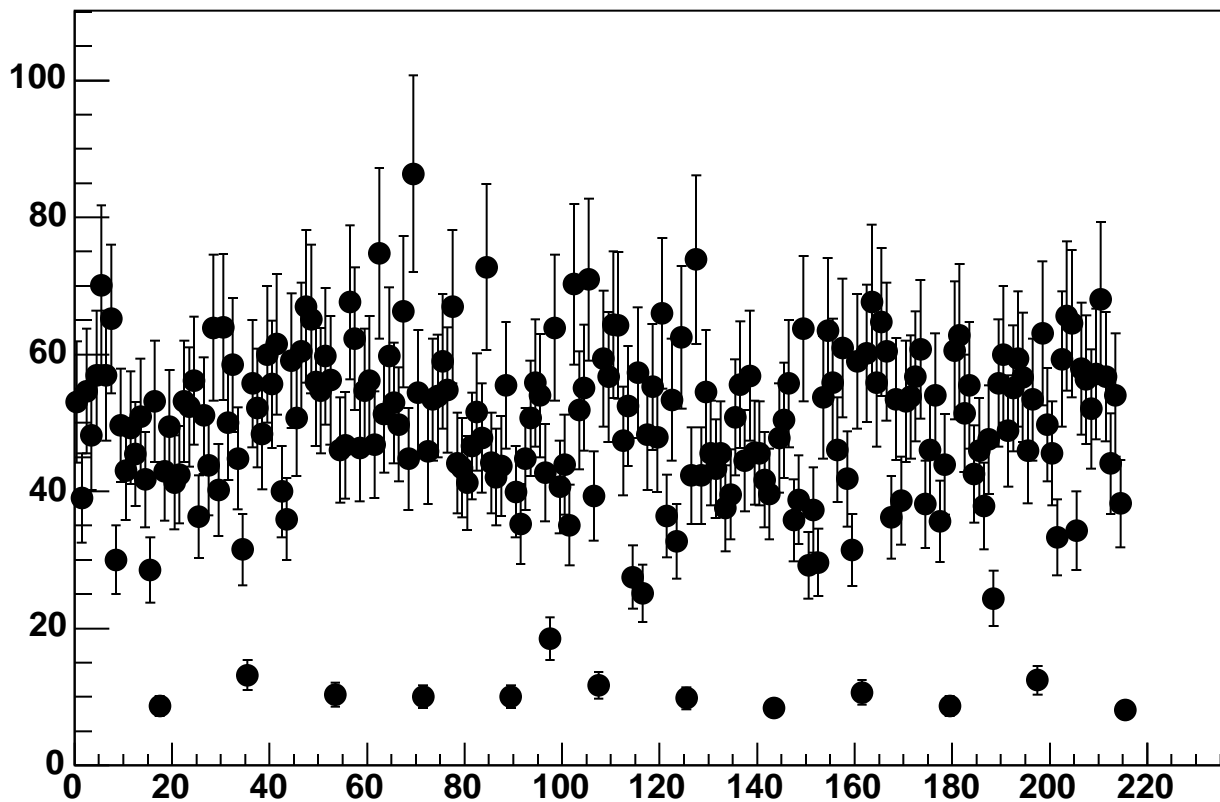
Enable 2, Hold=30, DAC=1400, ADC Noise vs 18\*Chip+Chan



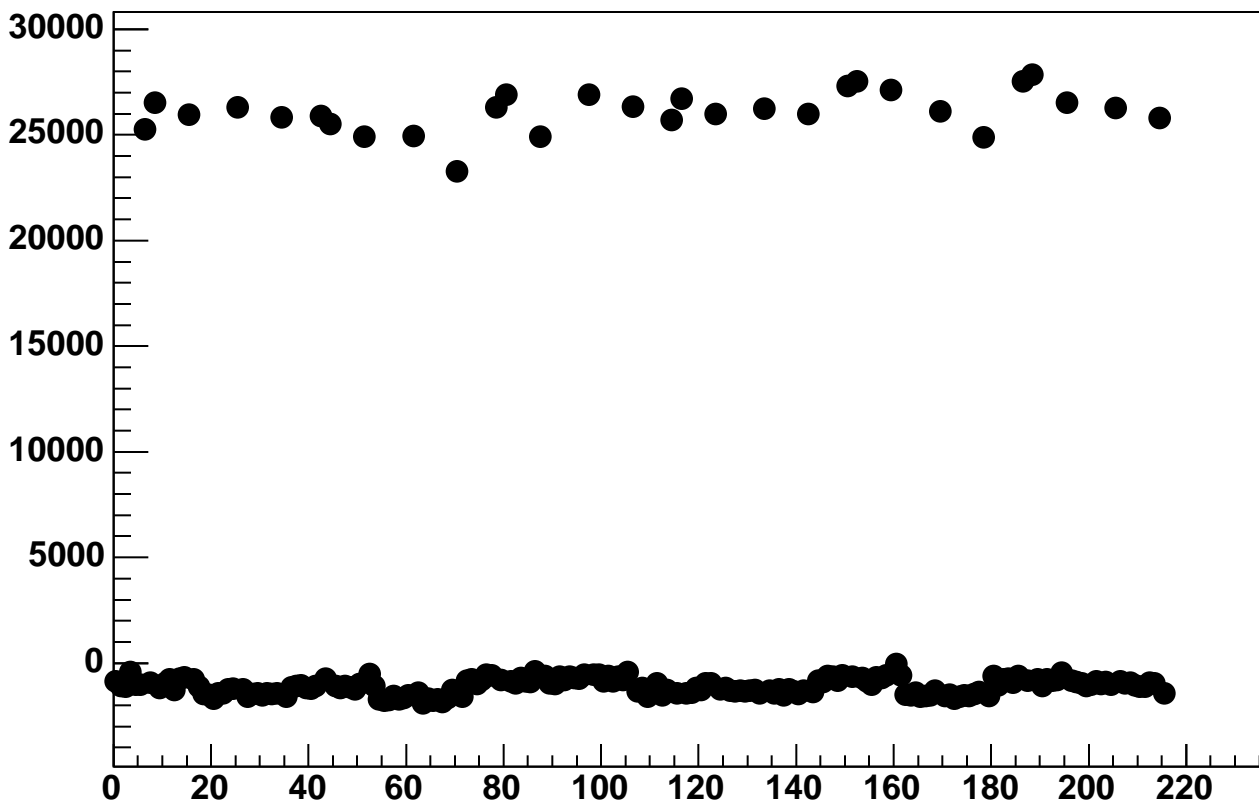
Enable 2, Hold=30, DAC=1450, ADC Mean vs 18\*Chip+Chan



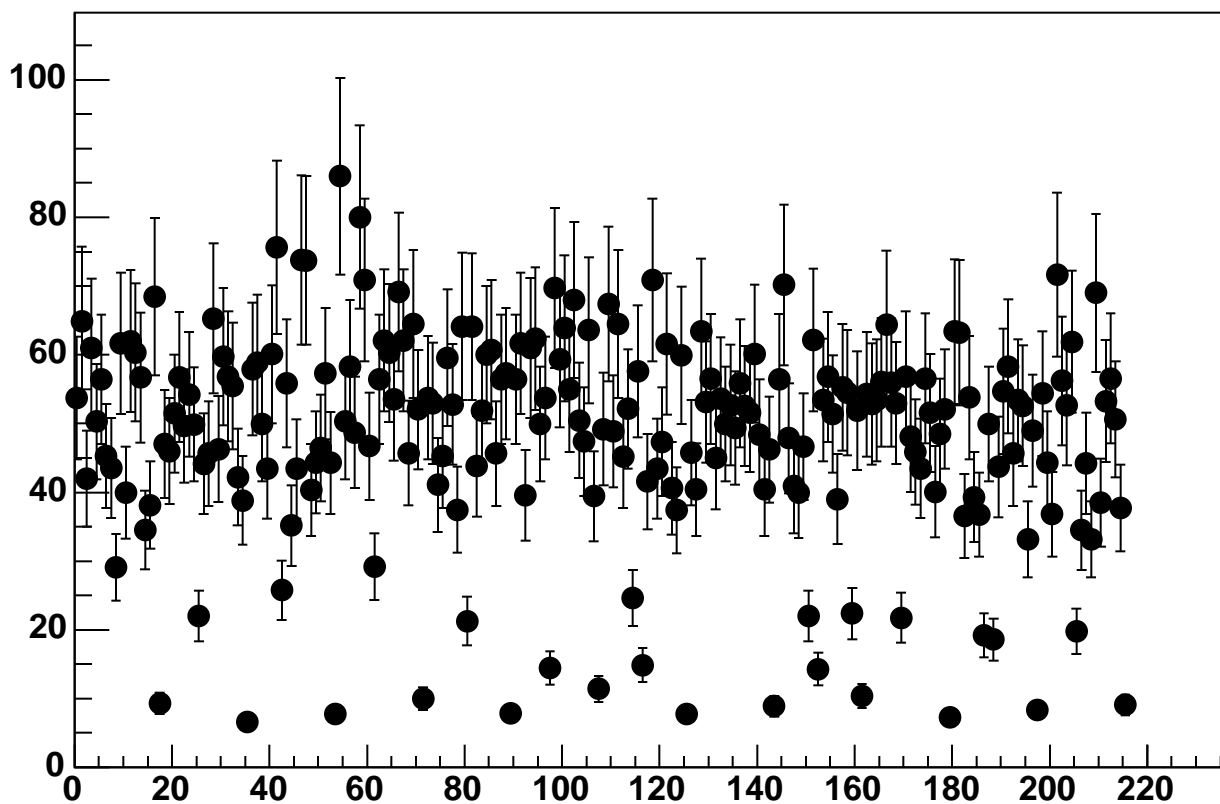
Enable 2, Hold=30, DAC=1450, ADC Noise vs 18\*Chip+Chan



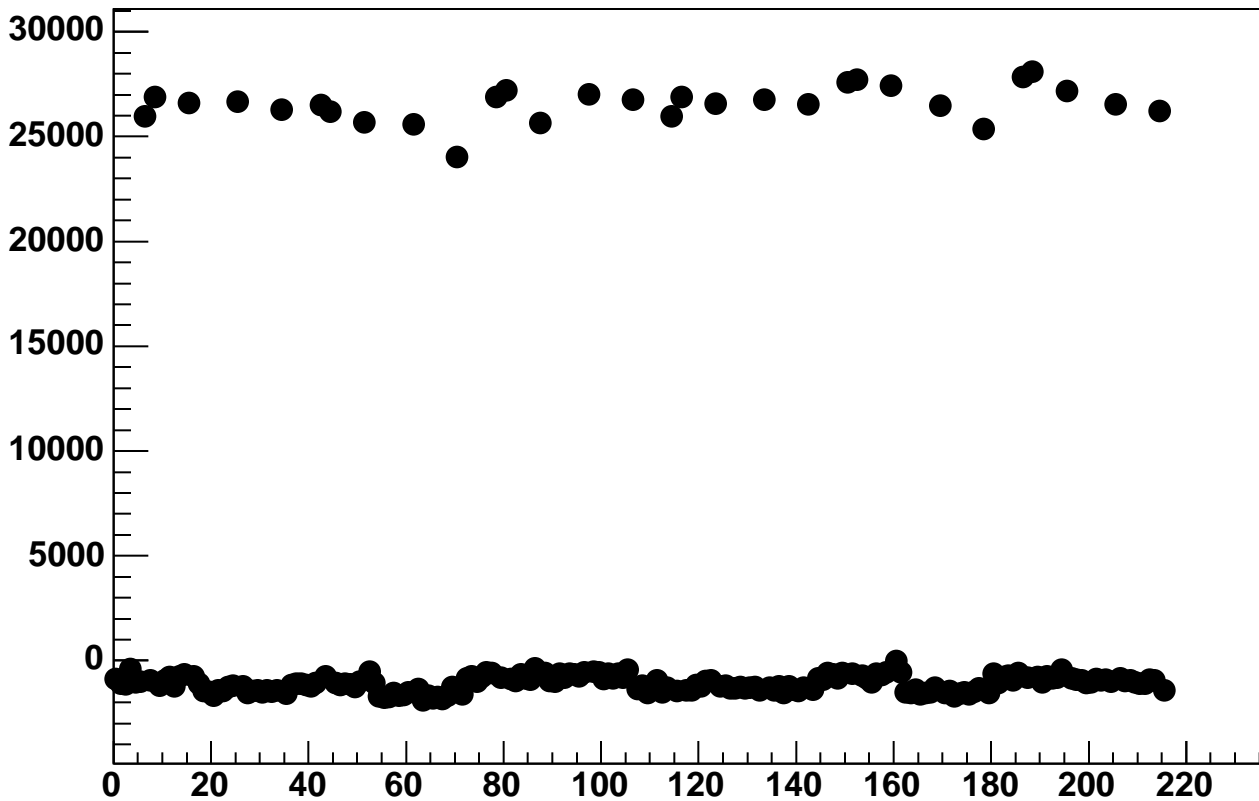
Enable 2, Hold=30, DAC=1500, ADC Mean vs 18\*Chip+Chan



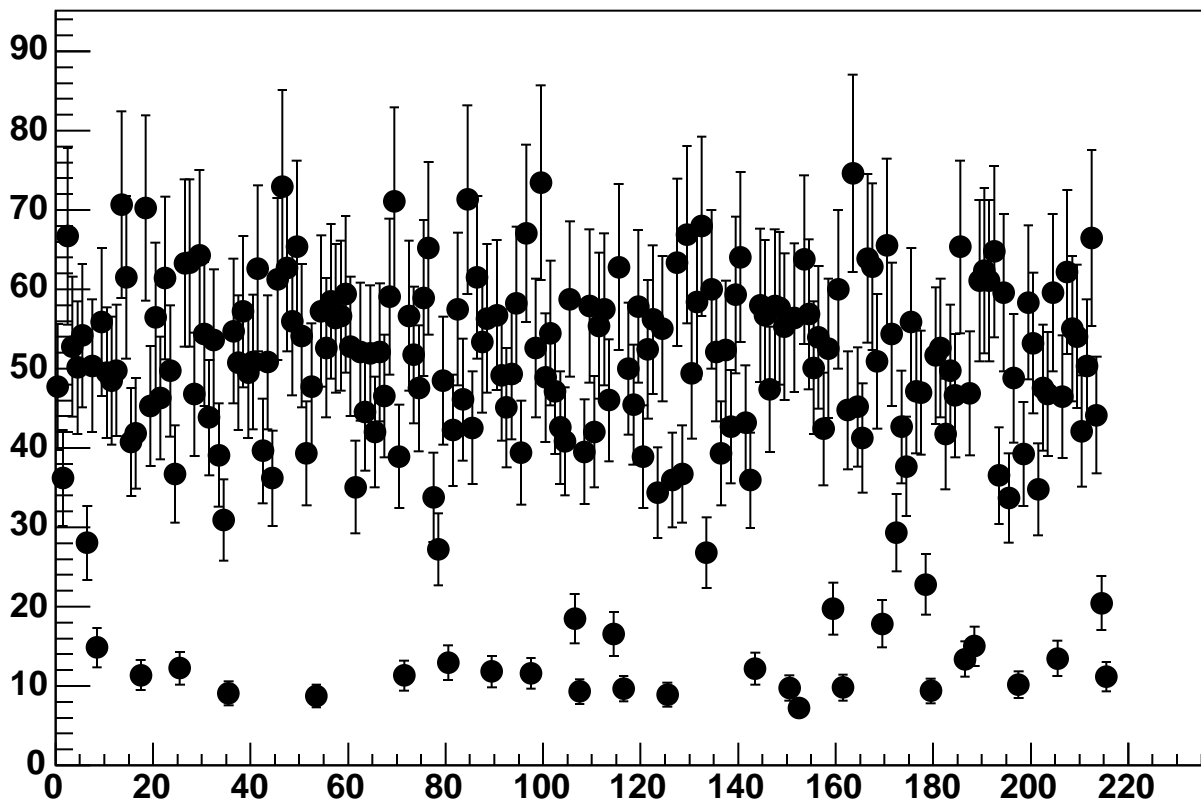
Enable 2, Hold=30, DAC=1500, ADC Noise vs 18\*Chip+Chan



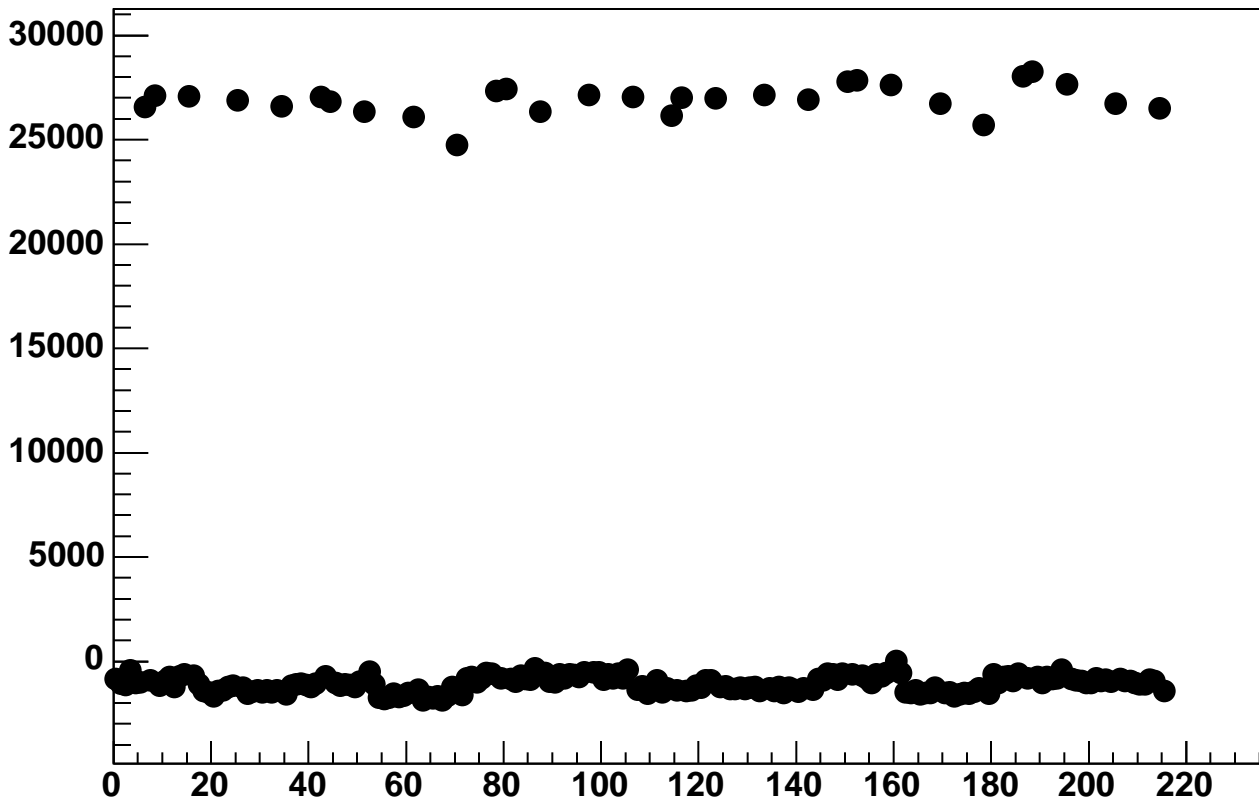
Enable 2, Hold=30, DAC=1550, ADC Mean vs 18\*Chip+Chan



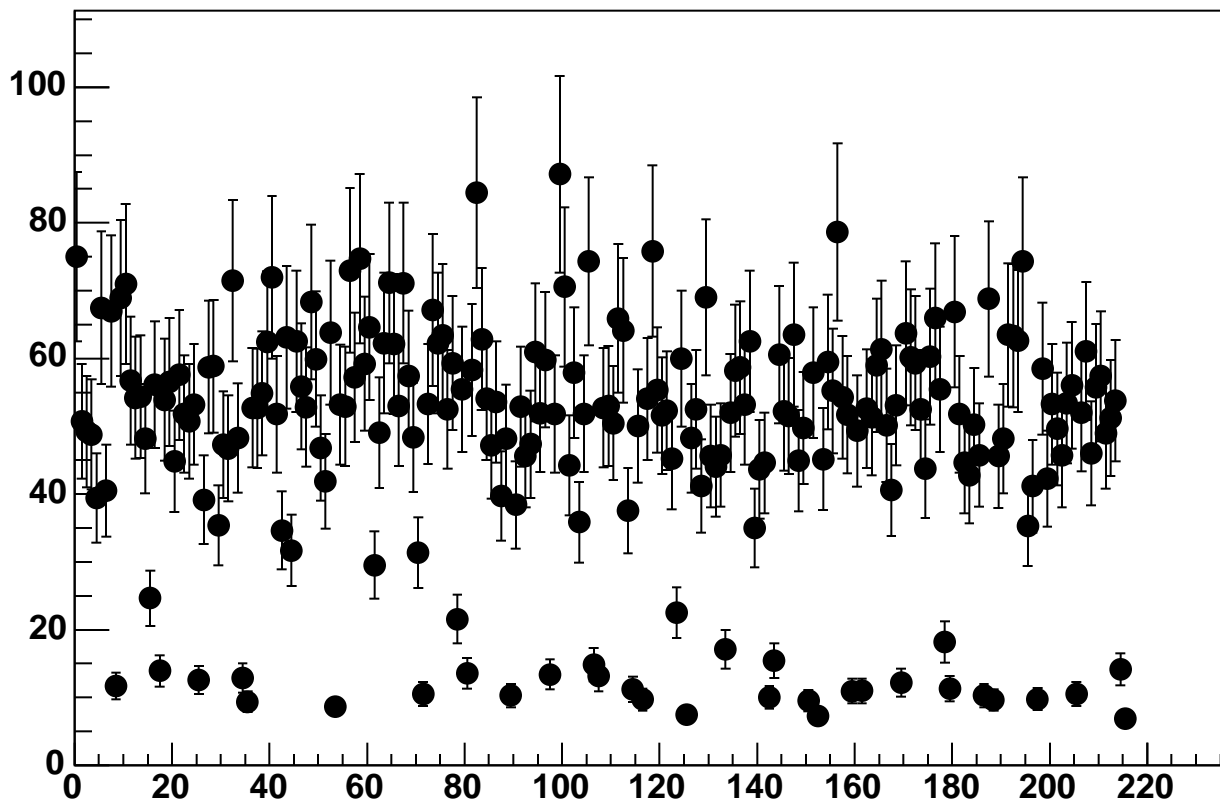
Enable 2, Hold=30, DAC=1550, ADC Noise vs 18\*Chip+Chan



Enable 2, Hold=30, DAC=1600, ADC Mean vs 18\*Chip+Chan

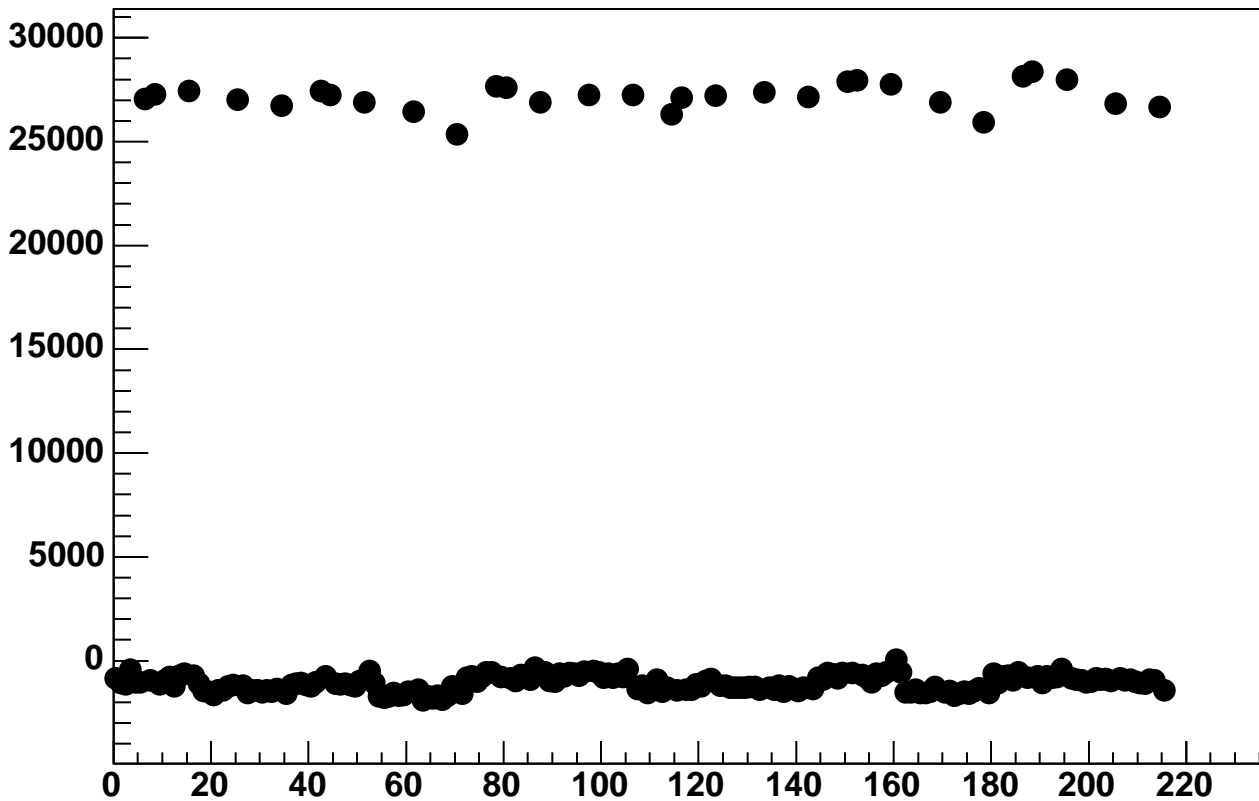


Enable 2, Hold=30, DAC=1600, ADC Noise vs 18\*Chip+Chan

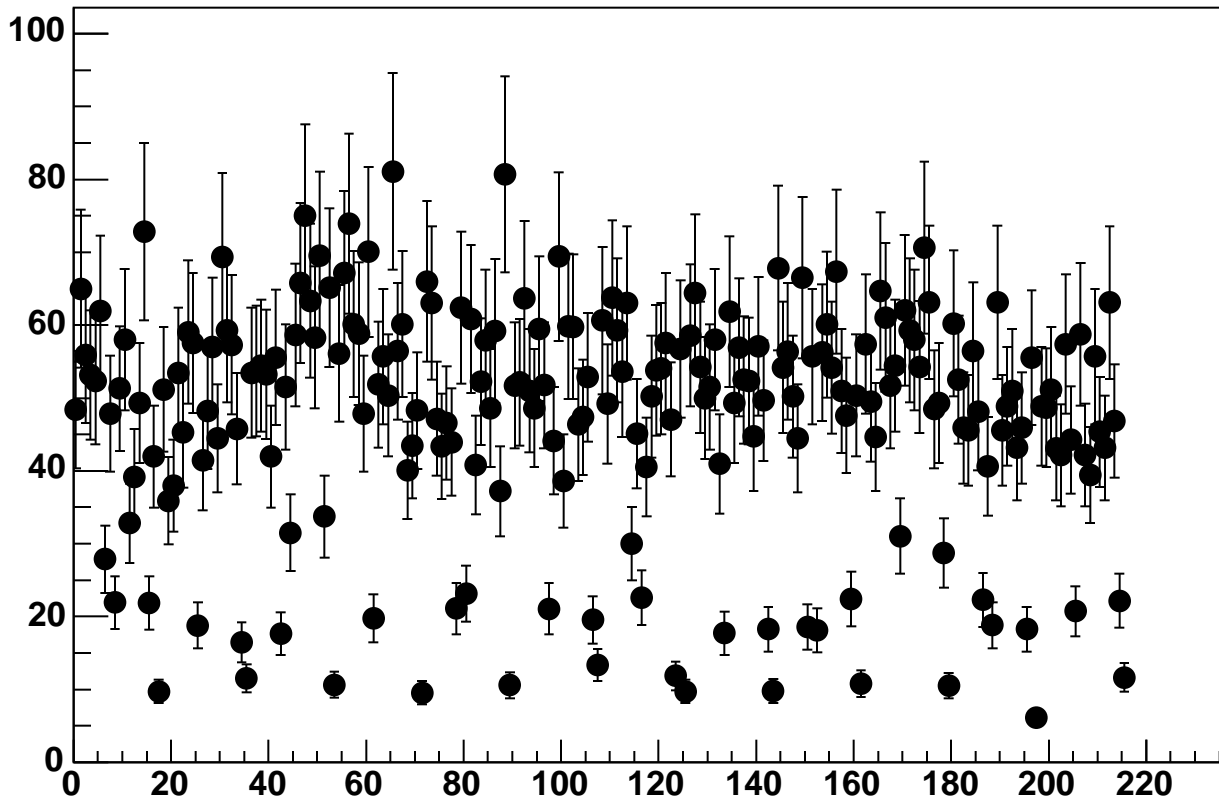




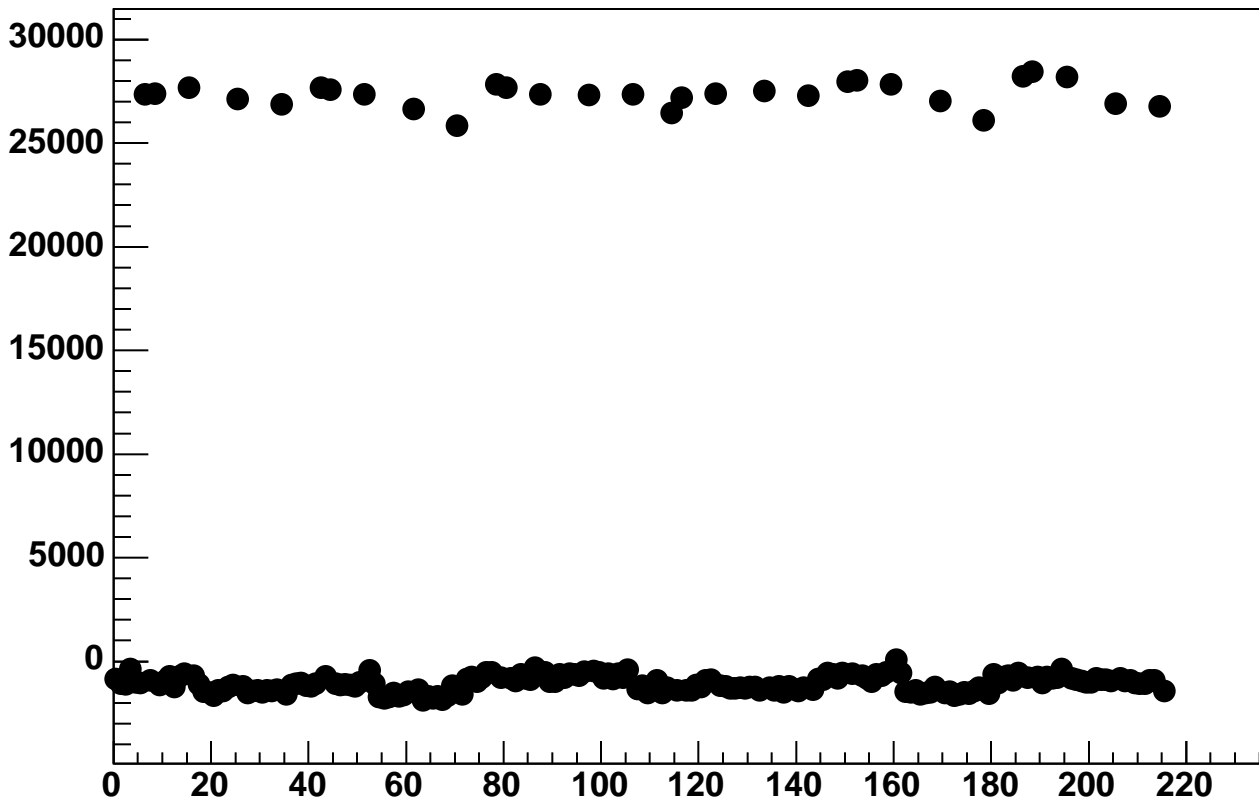
Enable 2, Hold=30, DAC=1650, ADC Mean vs 18\*Chip+Chan



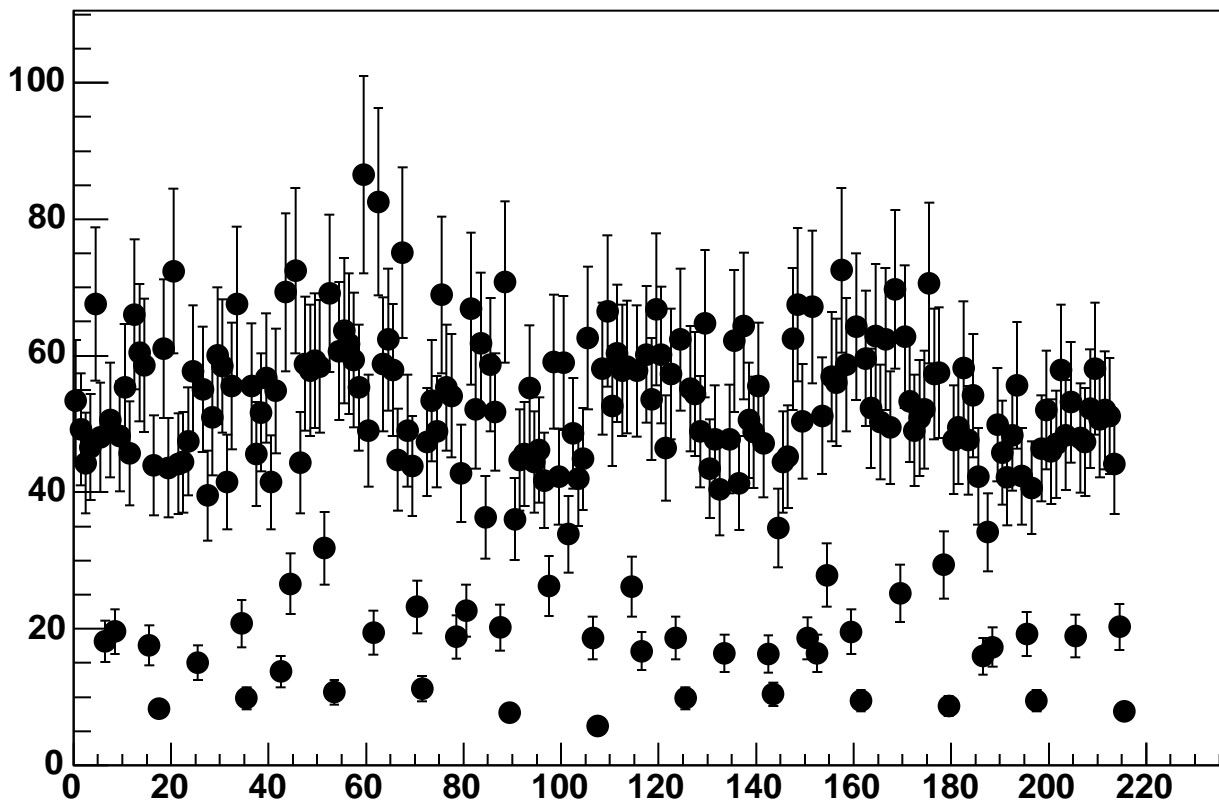
Enable 2, Hold=30, DAC=1650, ADC Noise vs 18\*Chip+Chan



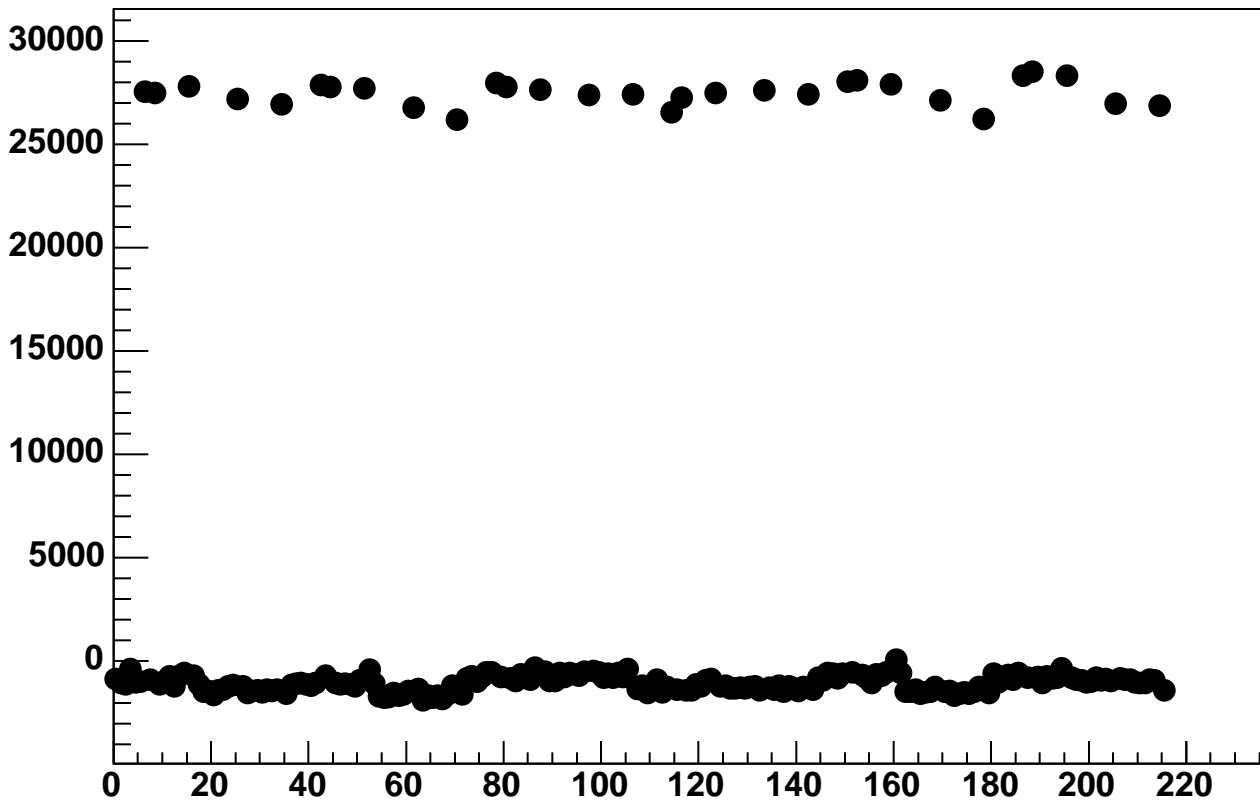
Enable 2, Hold=30, DAC=1700, ADC Mean vs 18\*Chip+Chan



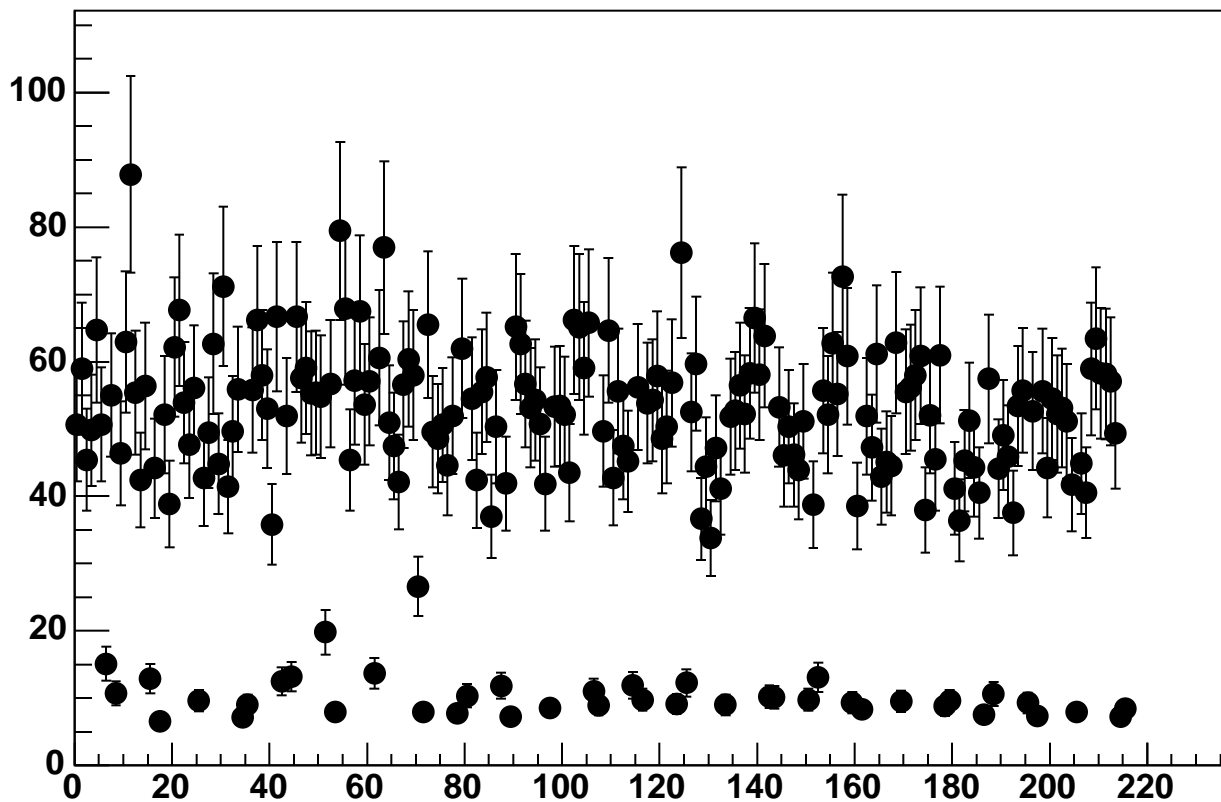
Enable 2, Hold=30, DAC=1700, ADC Noise vs 18\*Chip+Chan



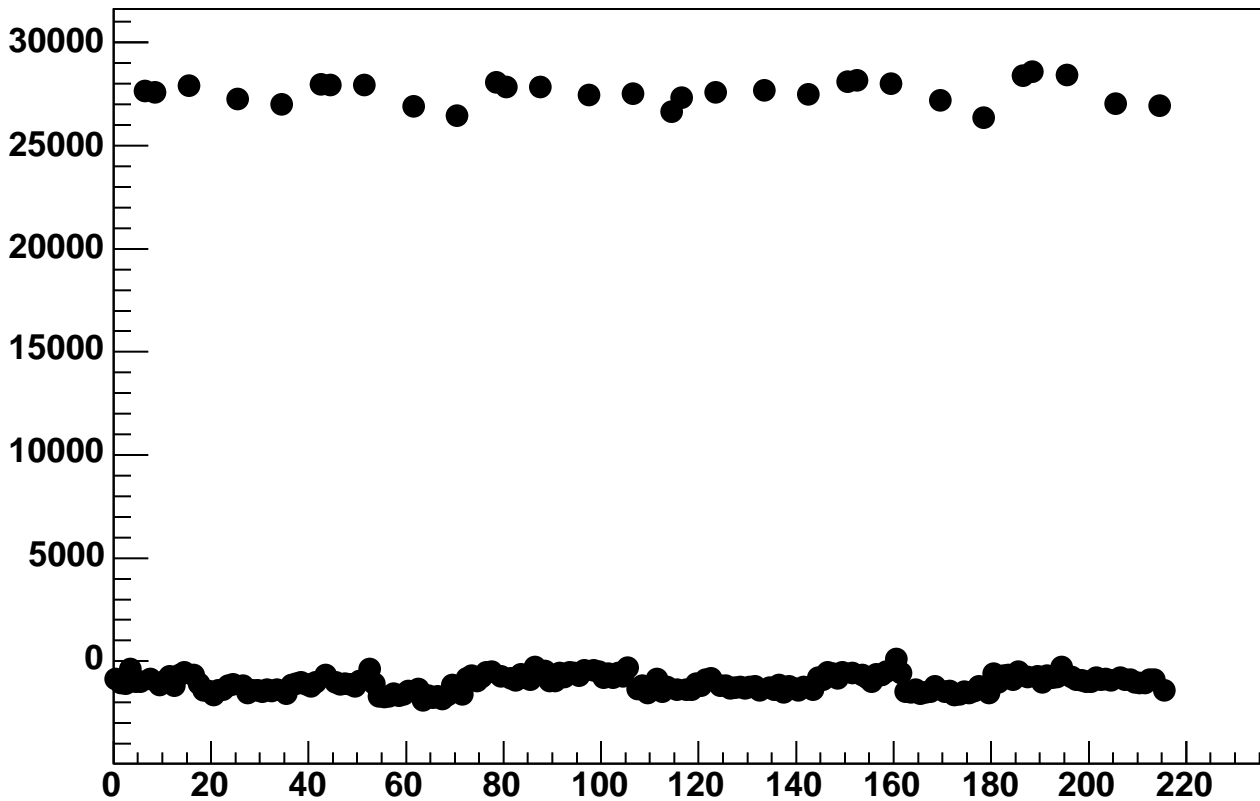
Enable 2, Hold=30, DAC=1750, ADC Mean vs 18\*Chip+Chan



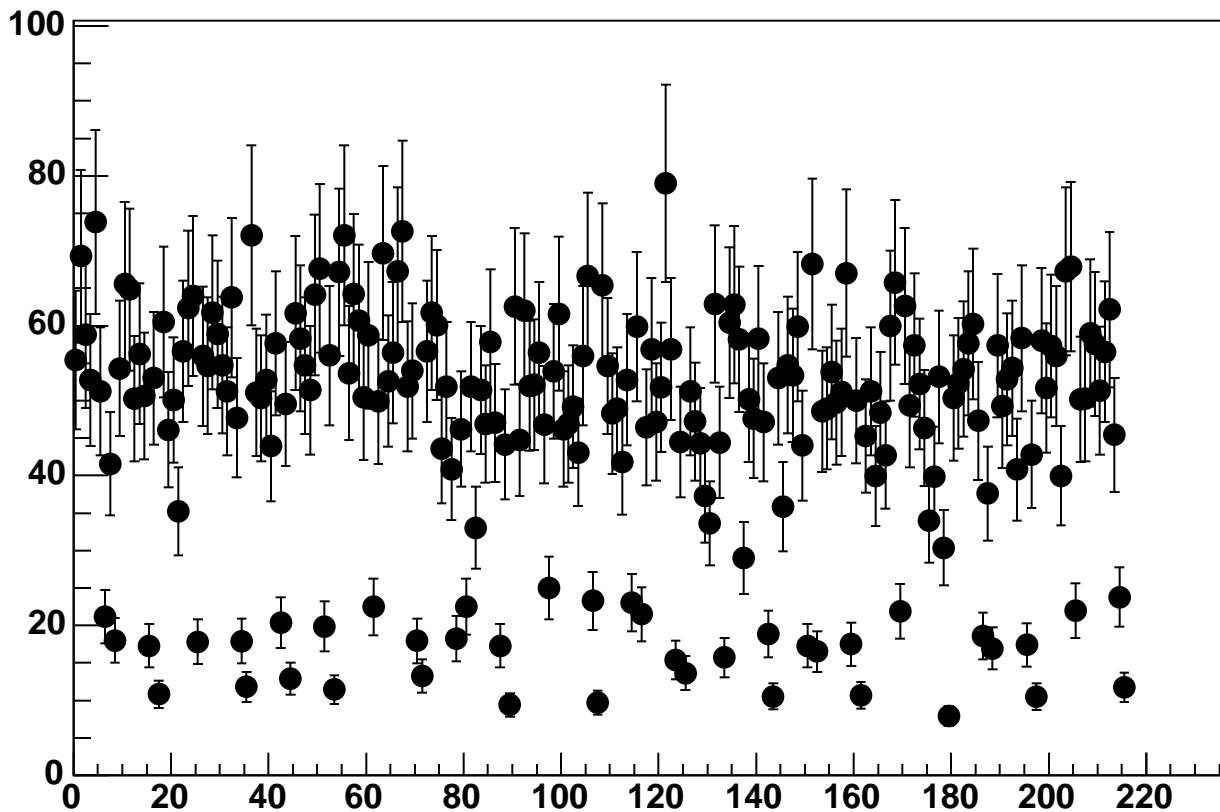
Enable 2, Hold=30, DAC=1750, ADC Noise vs 18\*Chip+Chan



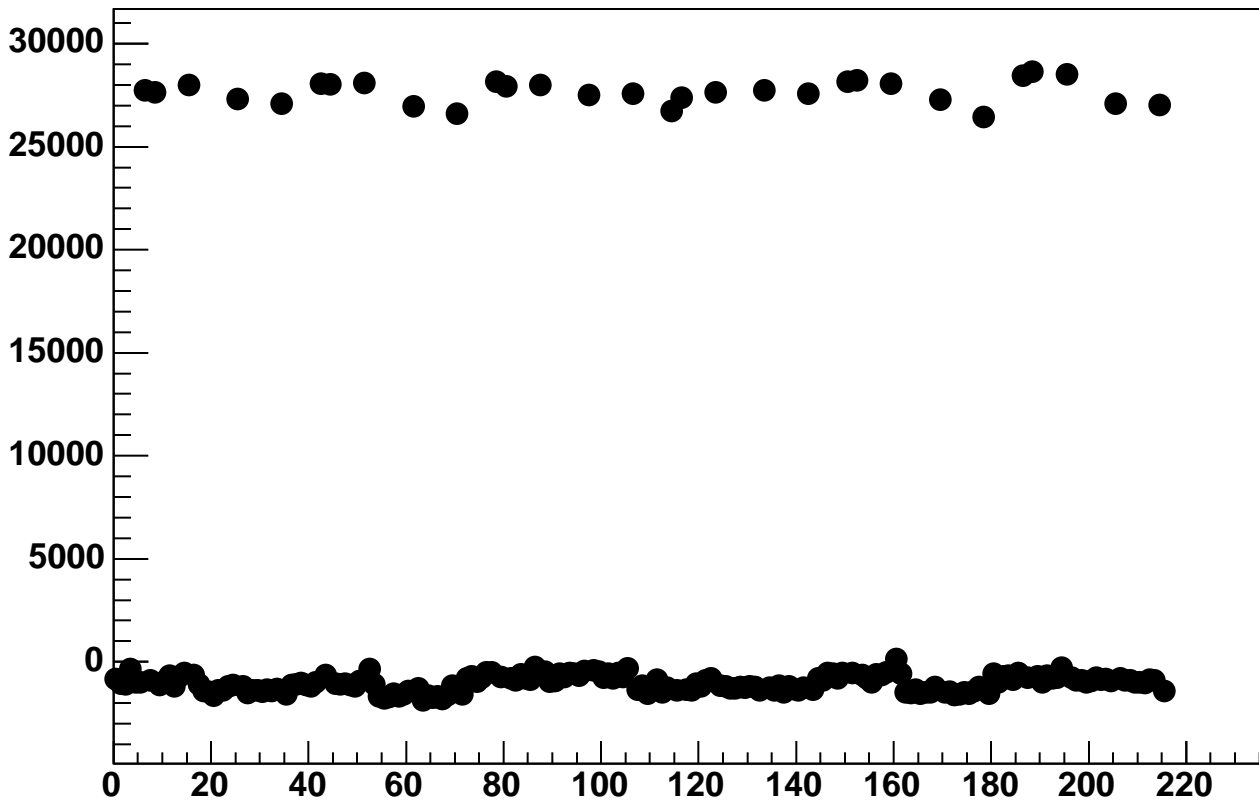
Enable 2, Hold=30, DAC=1800, ADC Mean vs 18\*Chip+Chan



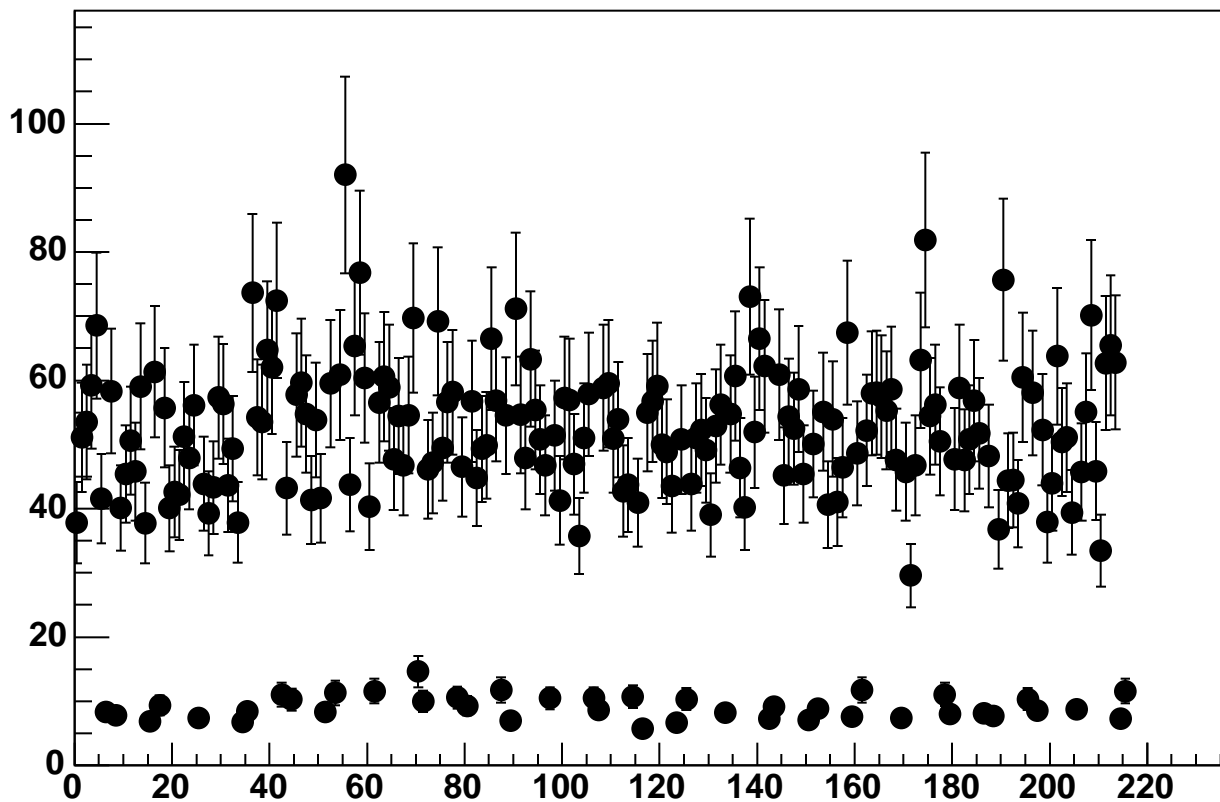
Enable 2, Hold=30, DAC=1800, ADC Noise vs 18\*Chip+Chan



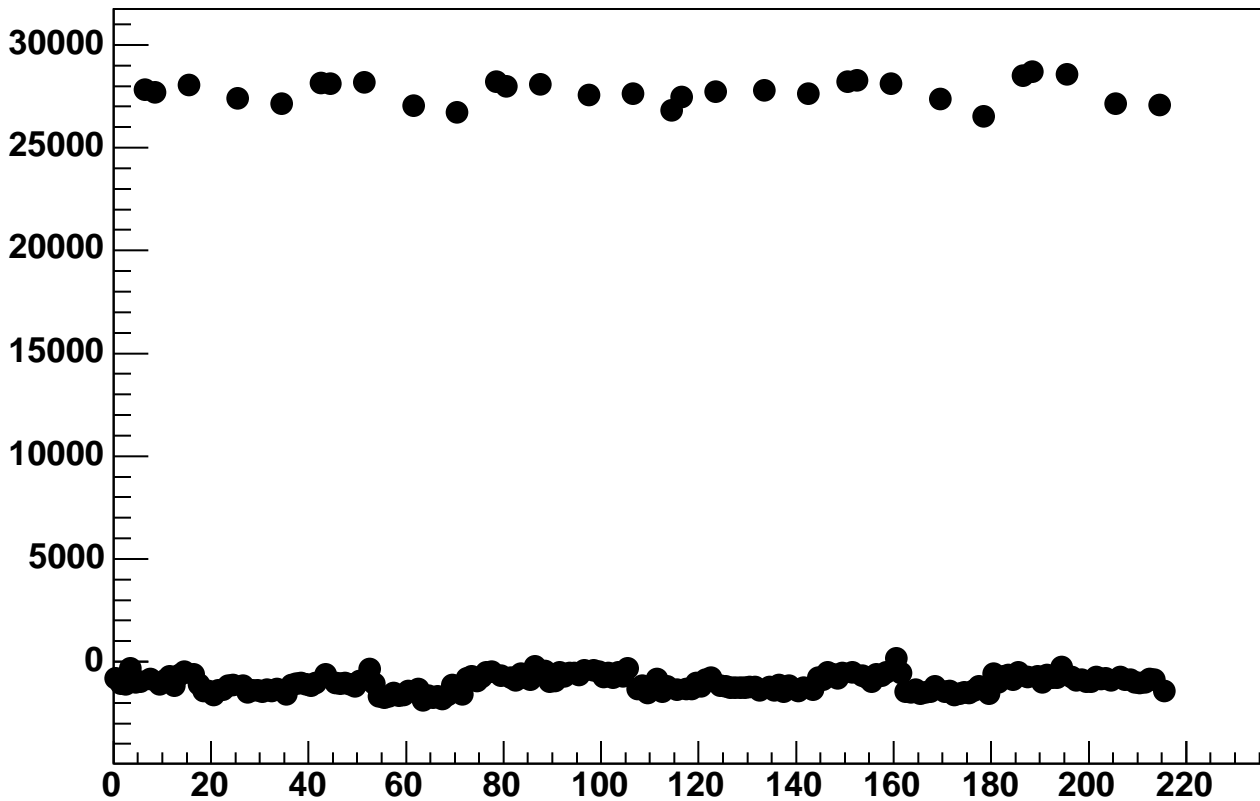
Enable 2, Hold=30, DAC=1850, ADC Mean vs 18\*Chip+Chan



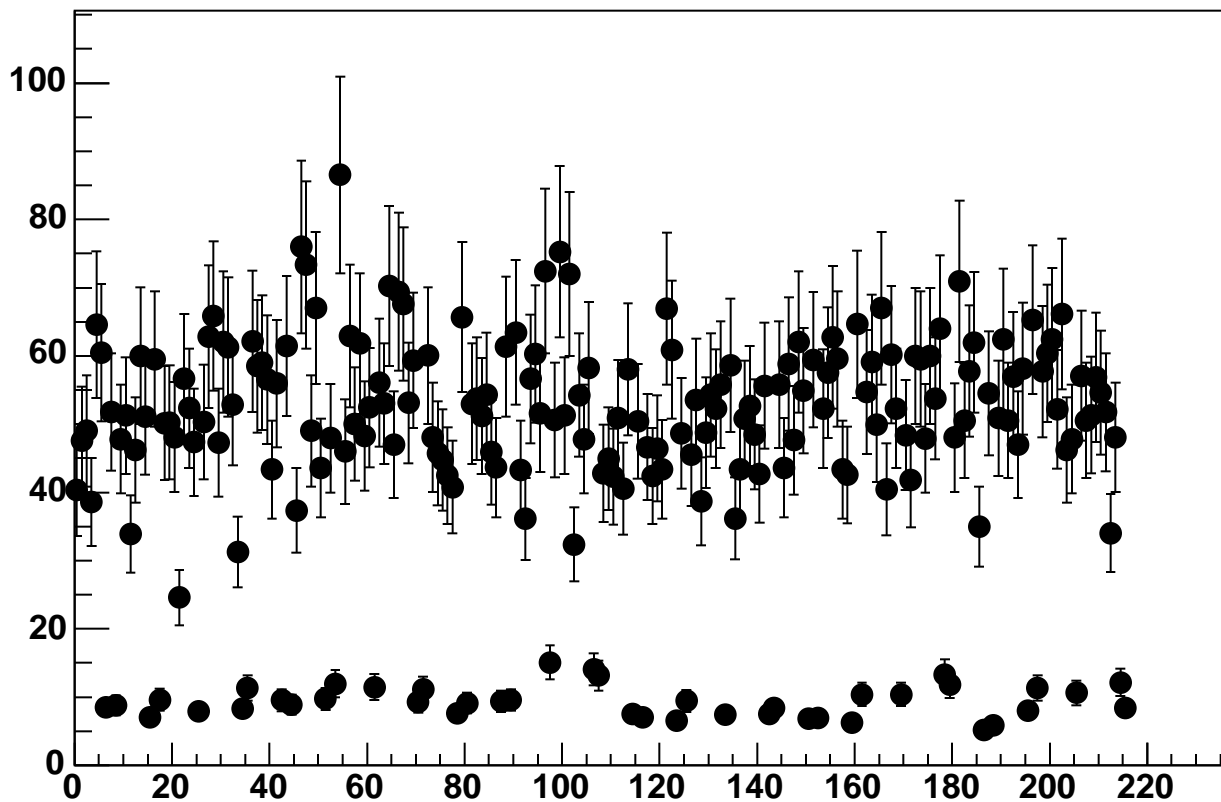
Enable 2, Hold=30, DAC=1850, ADC Noise vs 18\*Chip+Chan



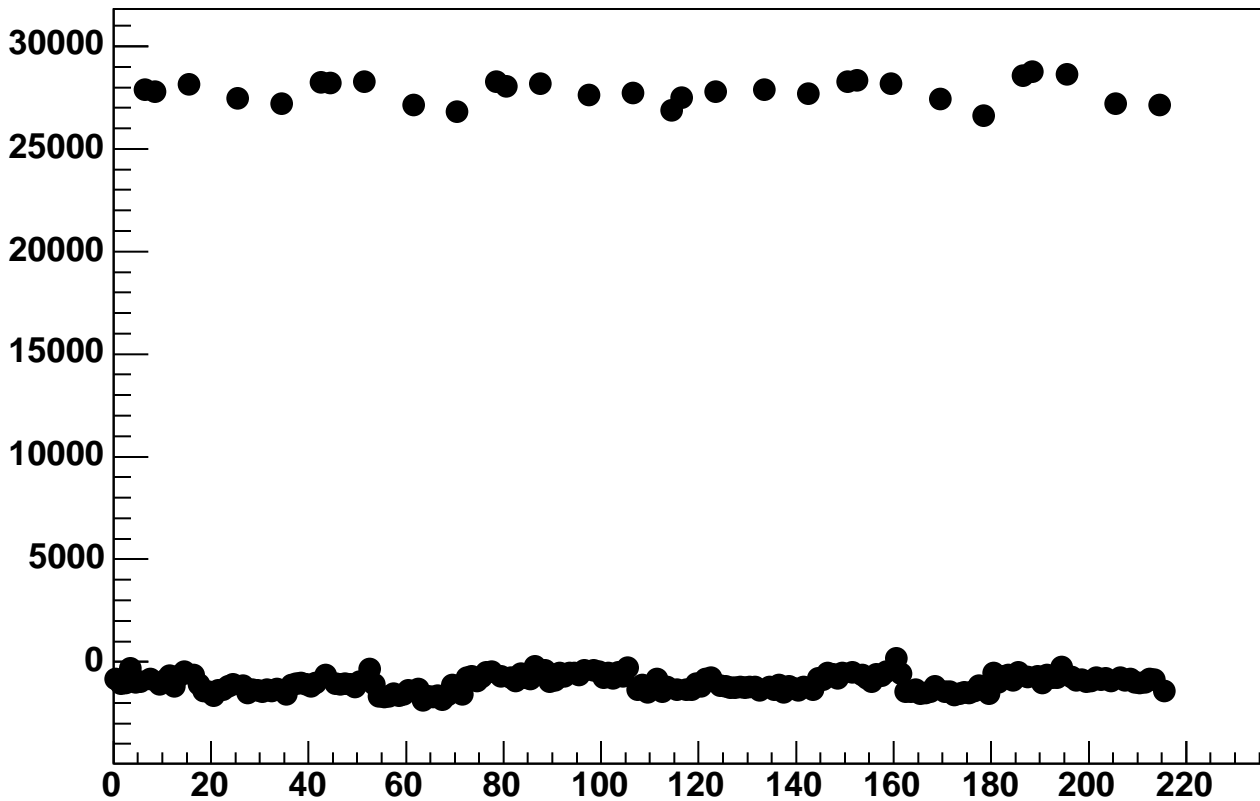
Enable 2, Hold=30, DAC=1900, ADC Mean vs 18\*Chip+Chan



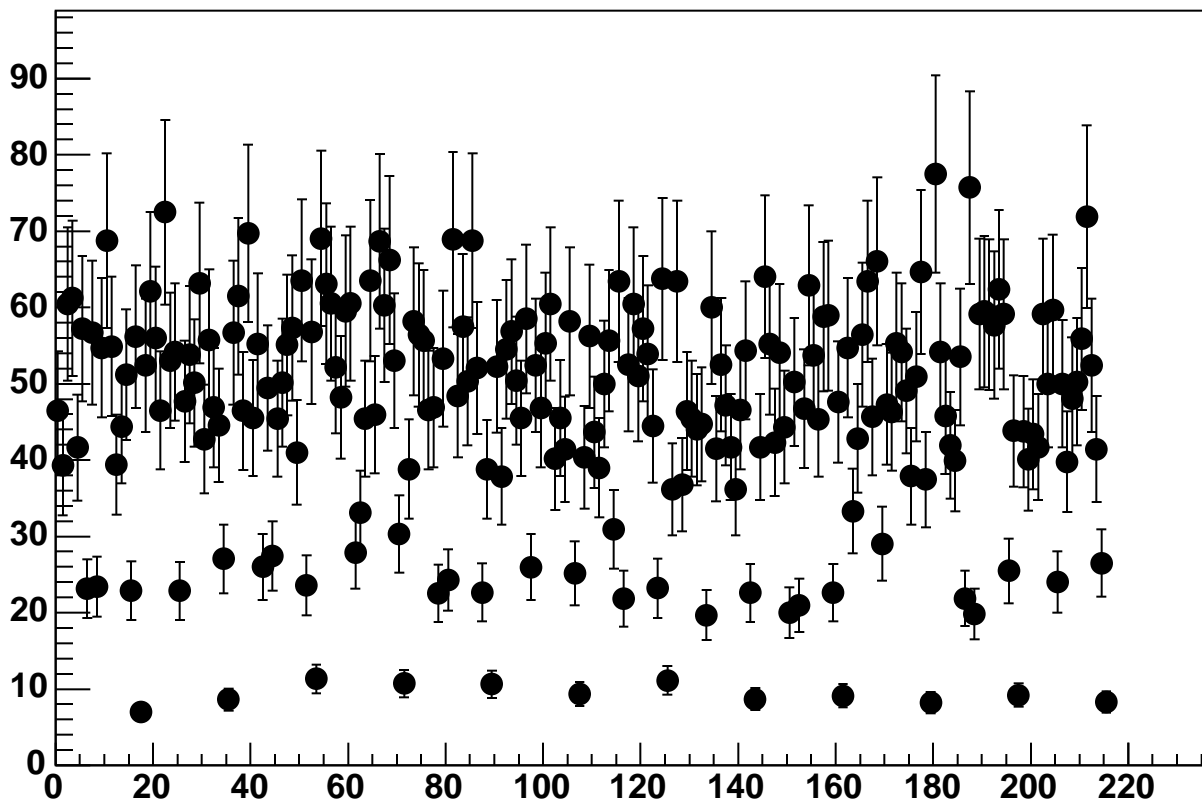
Enable 2, Hold=30, DAC=1900, ADC Noise vs 18\*Chip+Chan



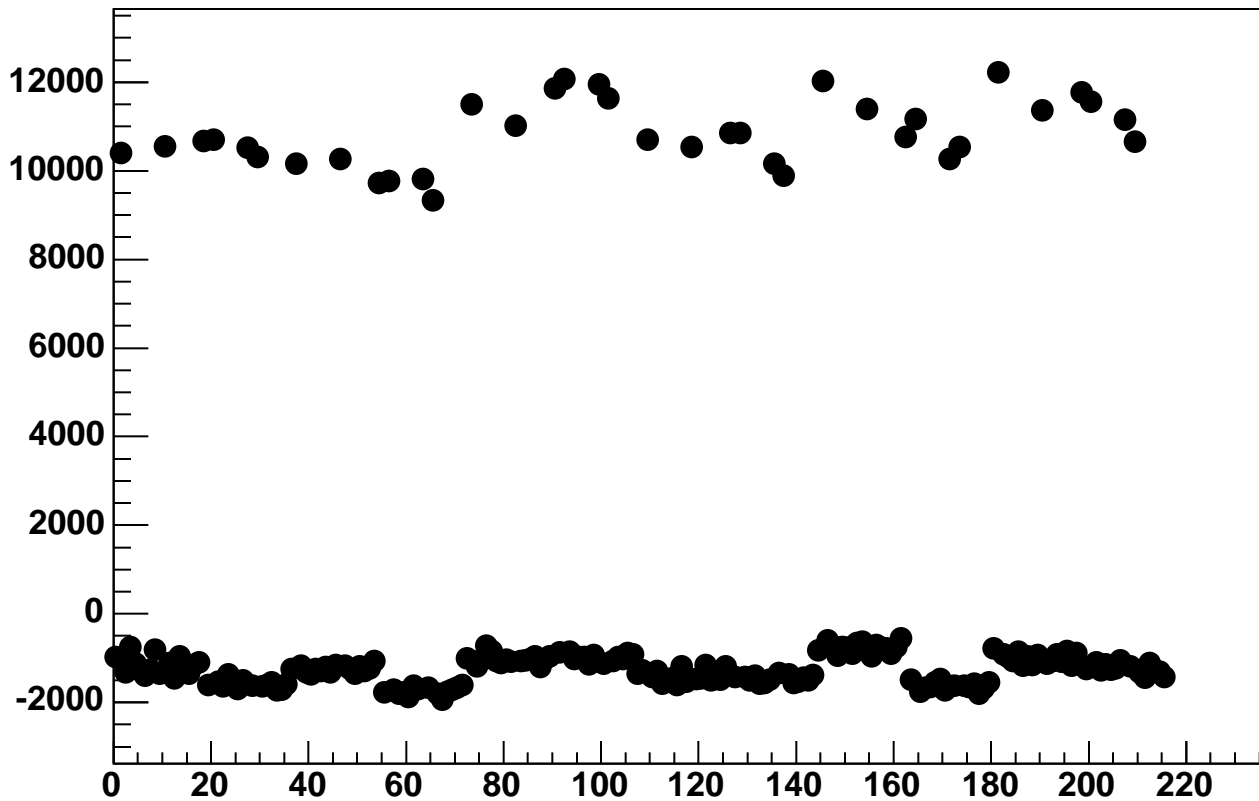
Enable 2, Hold=30, DAC=1950, ADC Mean vs 18\*Chip+Chan



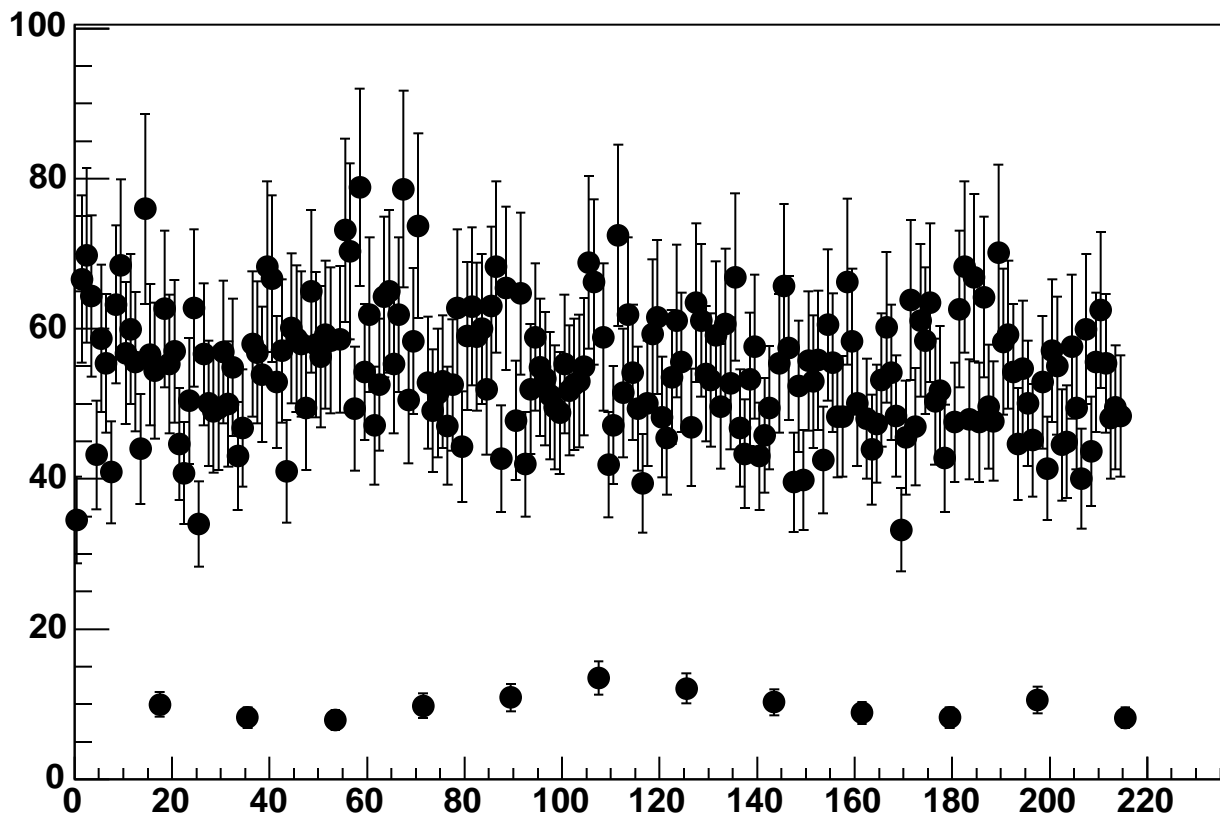
Enable 2, Hold=30, DAC=1950, ADC Noise vs 18\*Chip+Chan



Enable 3, Hold=30, DAC=0, ADC Mean vs 18\*Chip+Chan

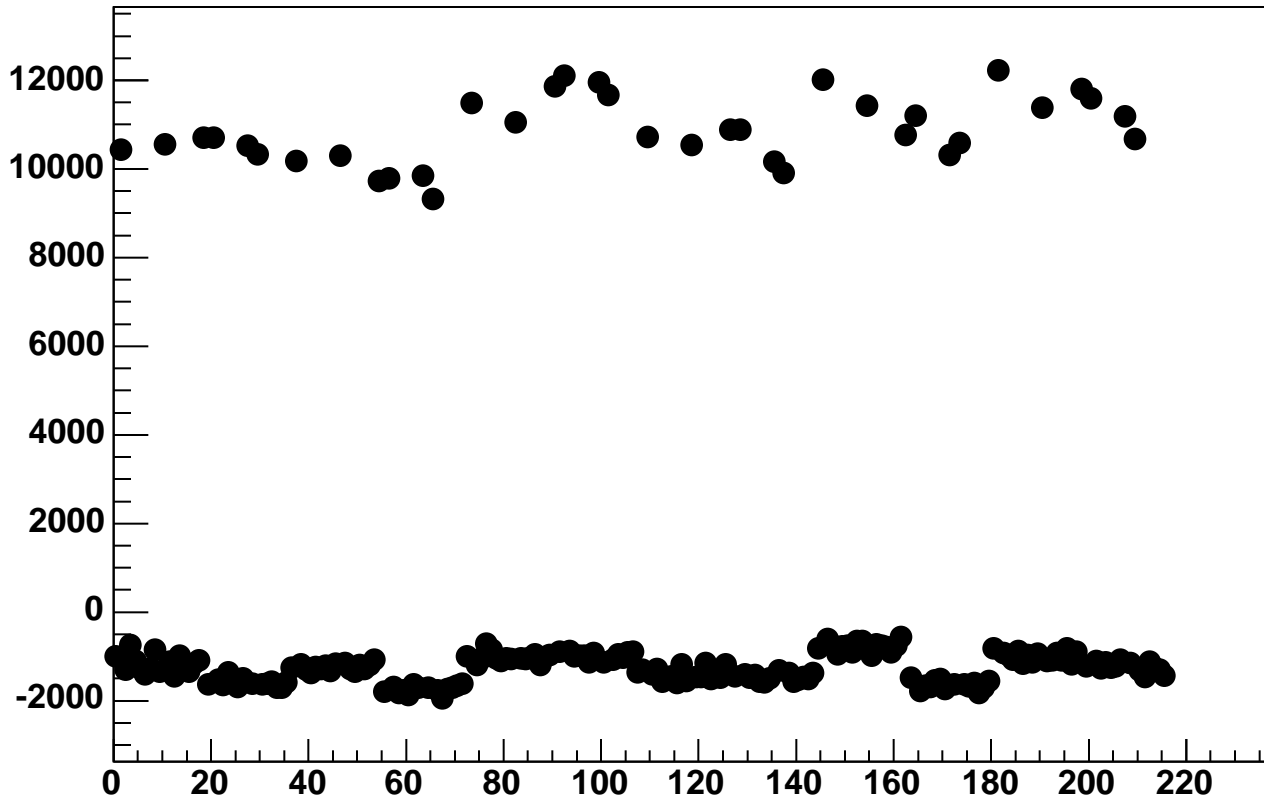


Enable 3, Hold=30, DAC=0, ADC Noise vs 18\*Chip+Chan

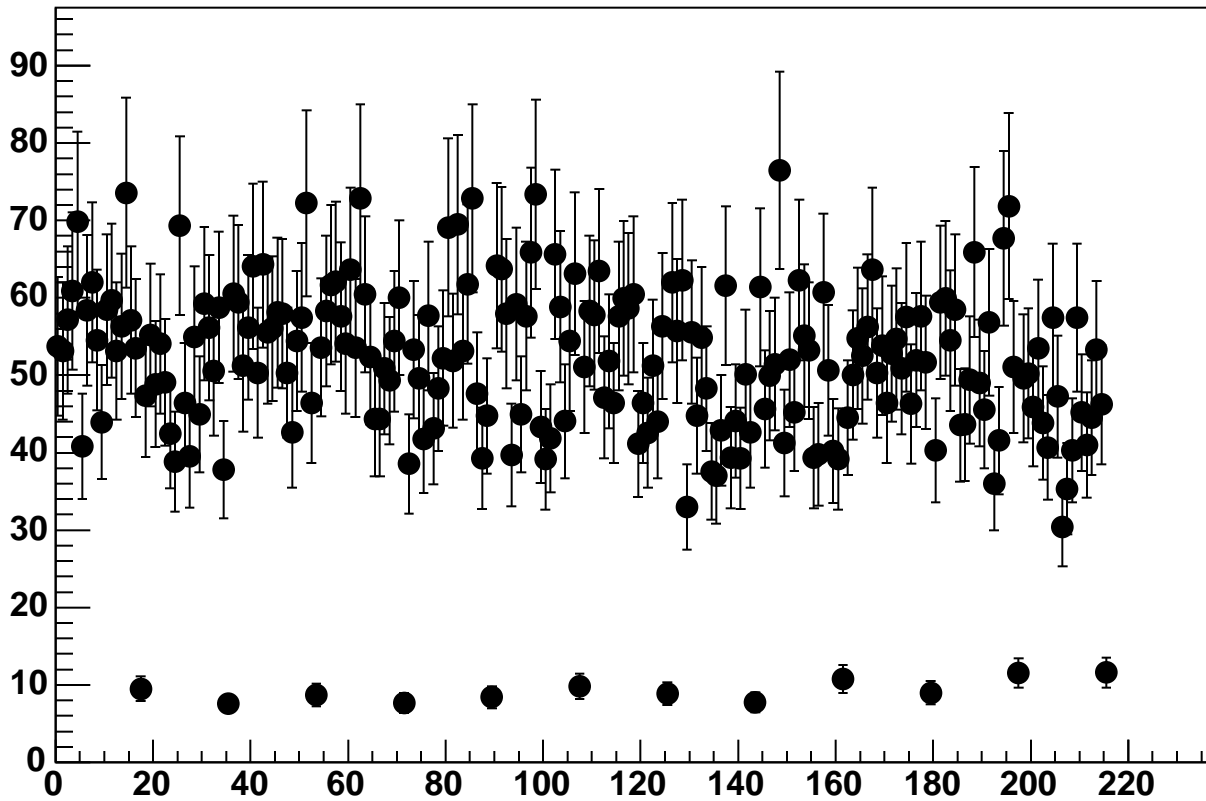




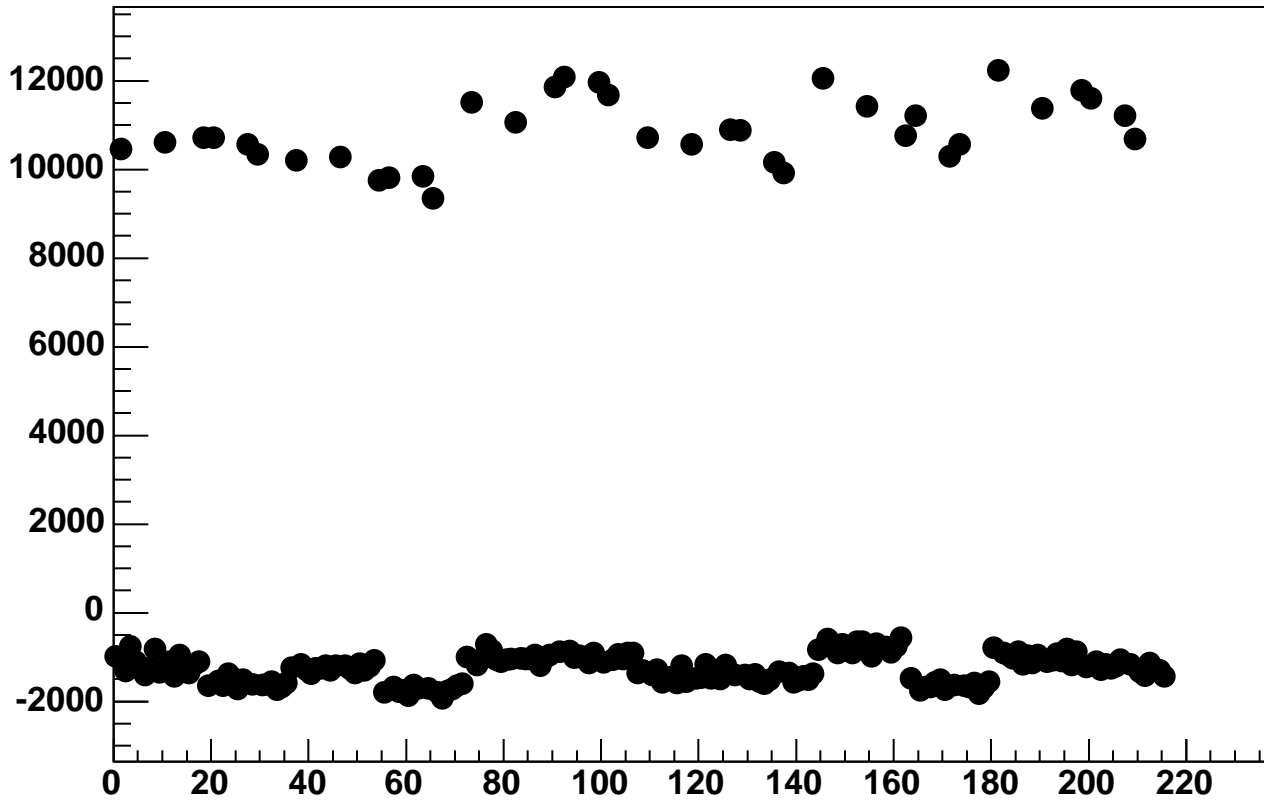
Enable 3, Hold=30, DAC=50, ADC Mean vs 18\*Chip+Chan



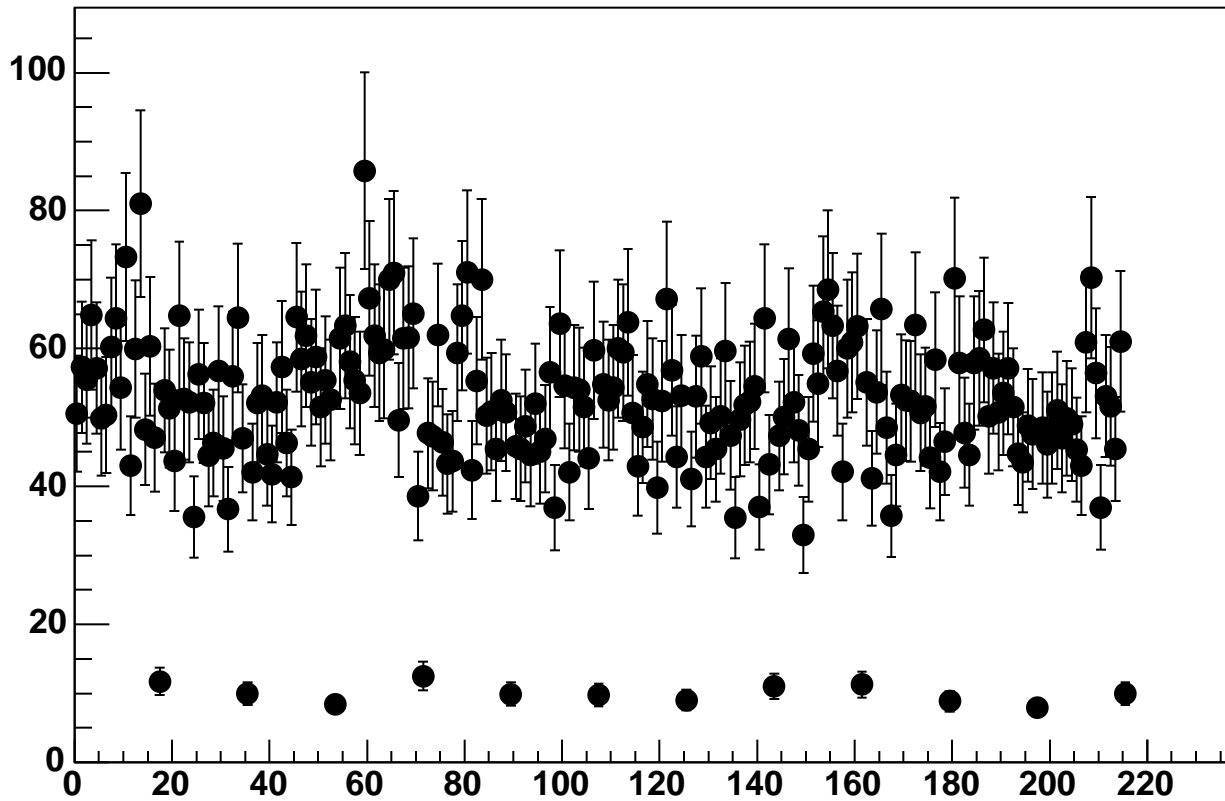
Enable 3, Hold=30, DAC=50, ADC Noise vs 18\*Chip+Chan



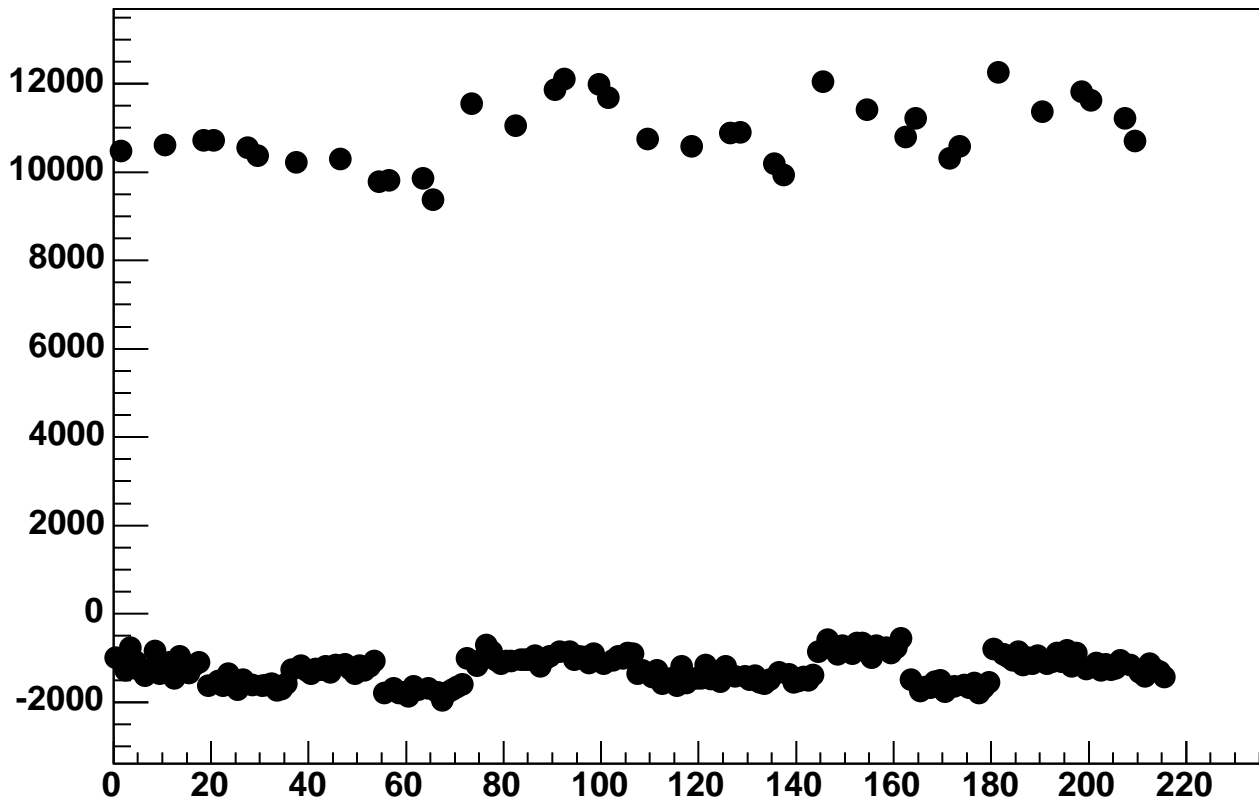
Enable 3, Hold=30, DAC=100, ADC Mean vs 18\*Chip+Chan



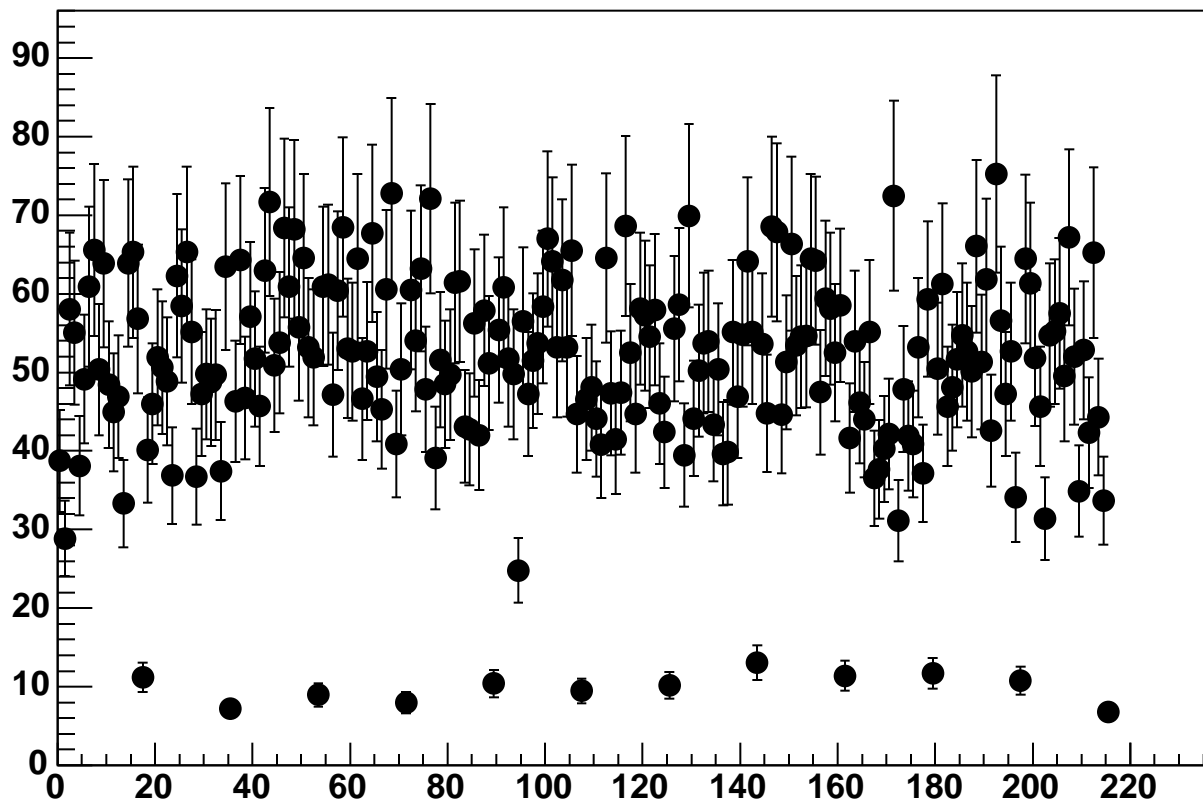
Enable 3, Hold=30, DAC=100, ADC Noise vs 18\*Chip+Chan



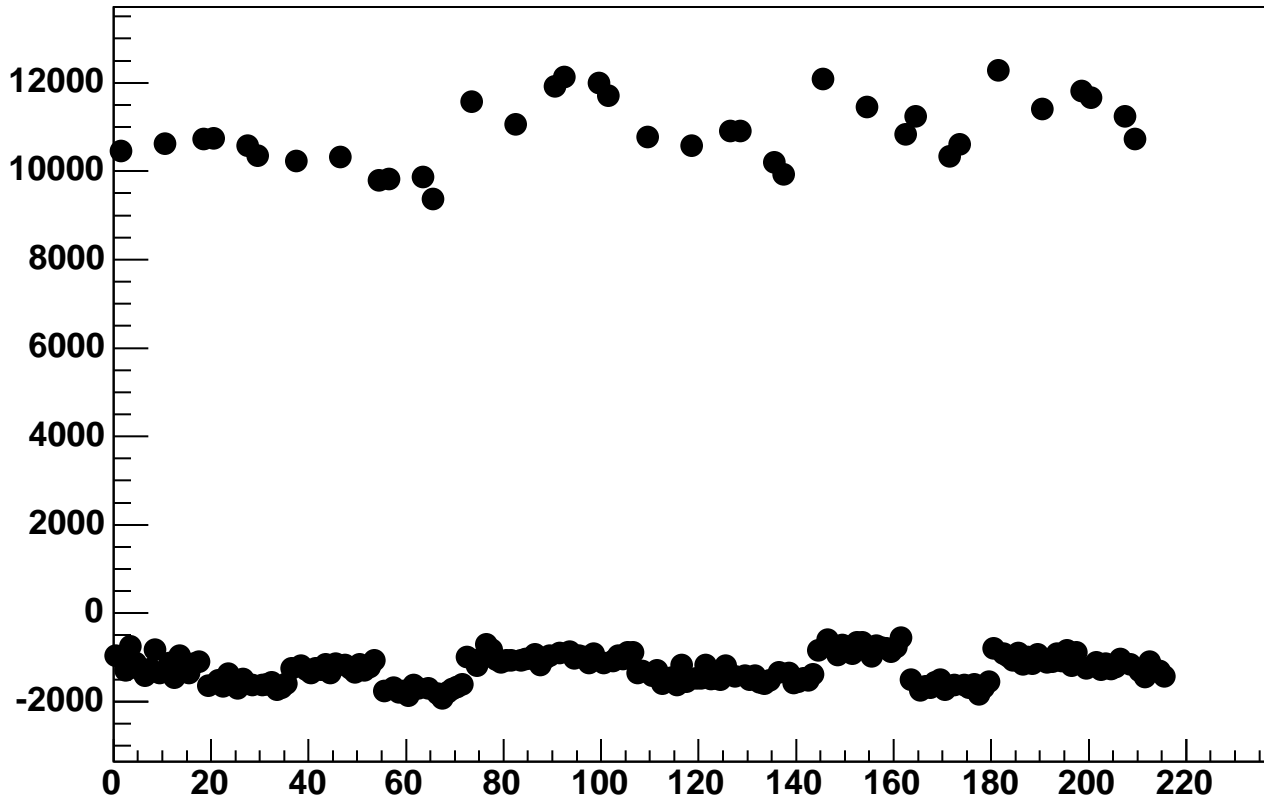
Enable 3, Hold=30, DAC=150, ADC Mean vs 18\*Chip+Chan



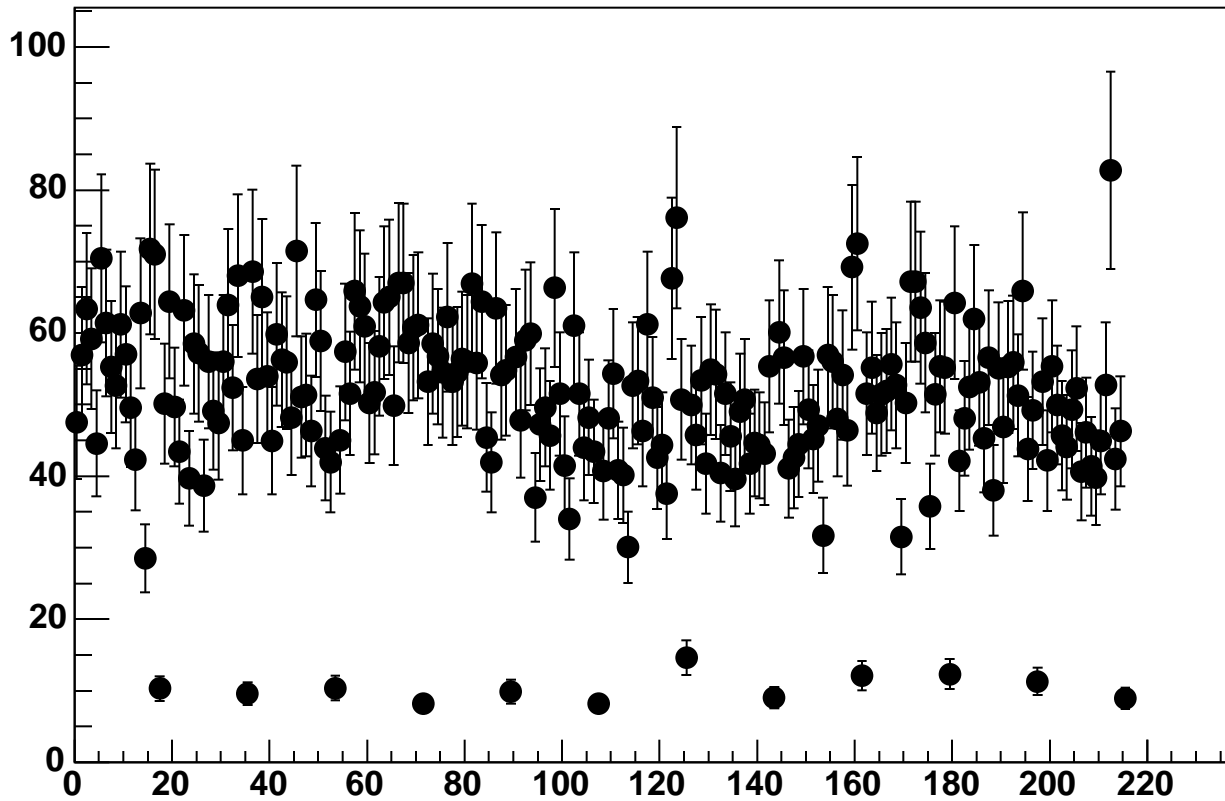
Enable 3, Hold=30, DAC=150, ADC Noise vs 18\*Chip+Chan



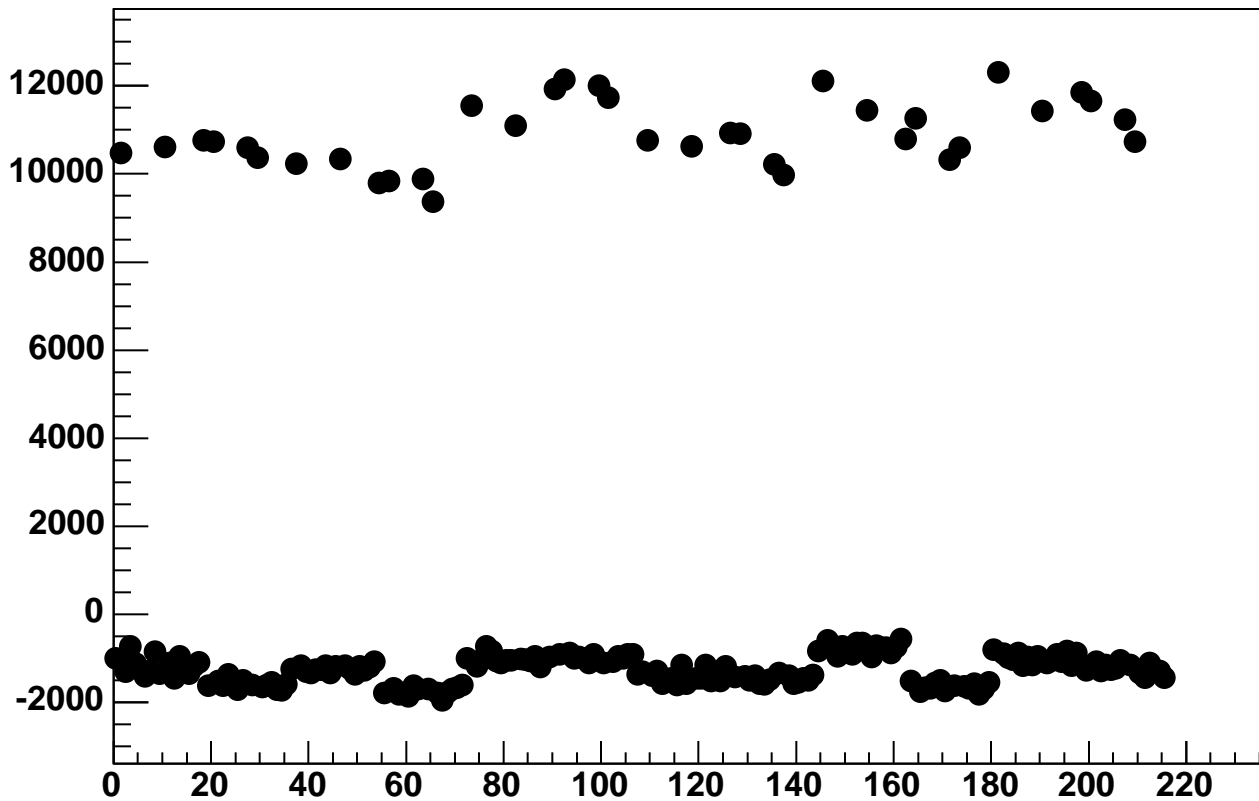
Enable 3, Hold=30, DAC=200, ADC Mean vs 18\*Chip+Chan



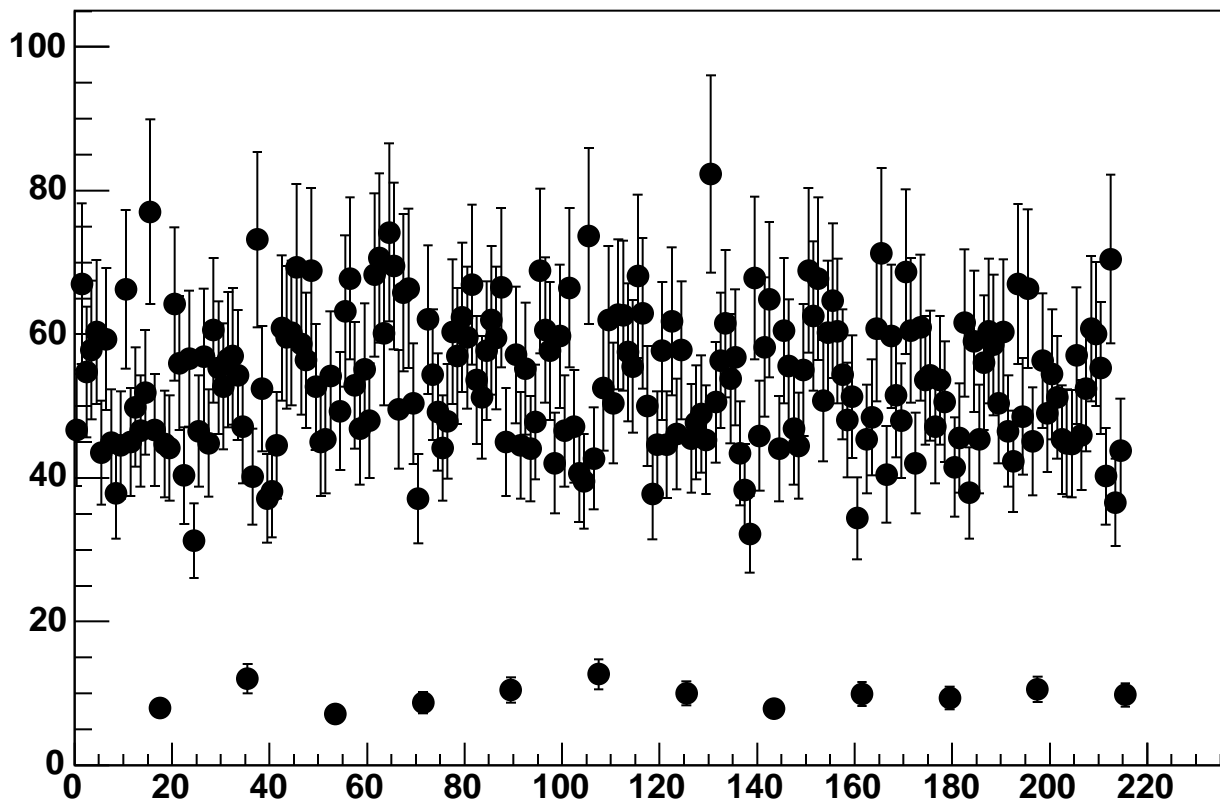
Enable 3, Hold=30, DAC=200, ADC Noise vs 18\*Chip+Chan



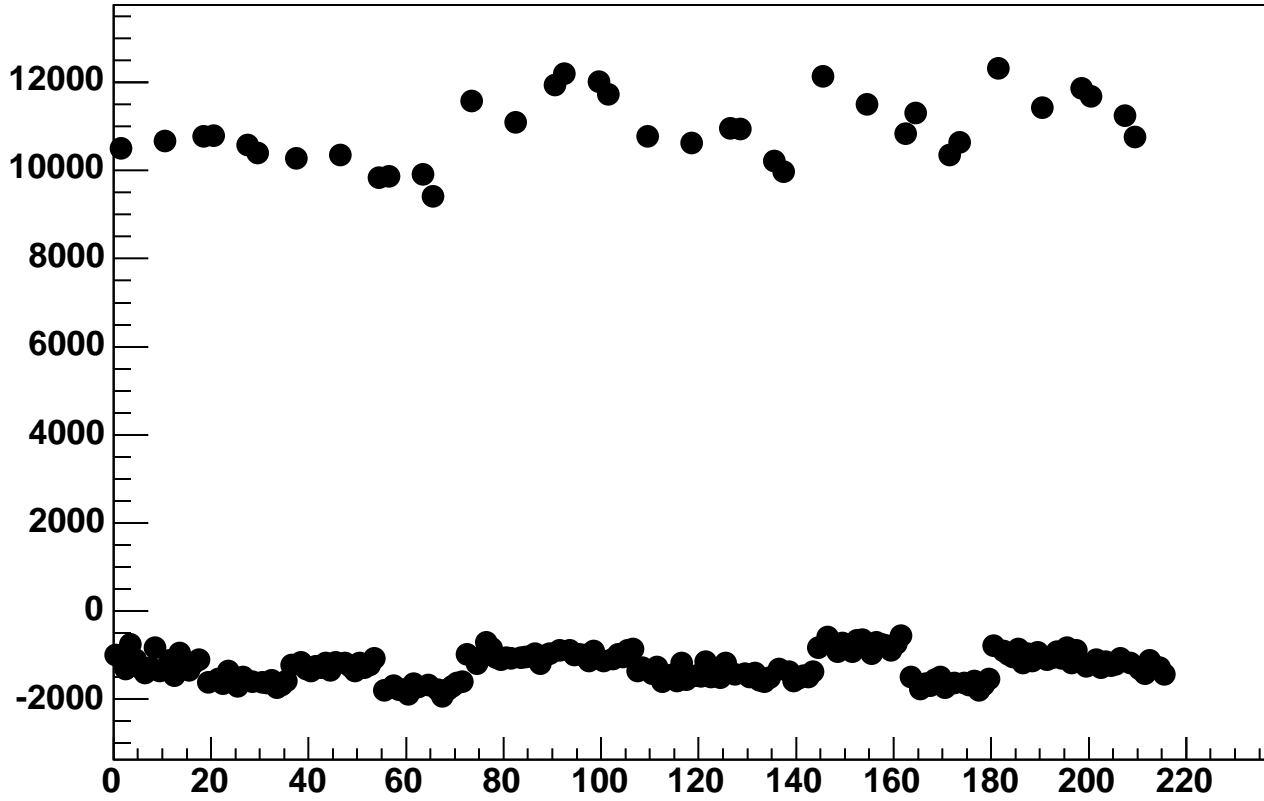
Enable 3, Hold=30, DAC=250, ADC Mean vs 18\*Chip+Chan



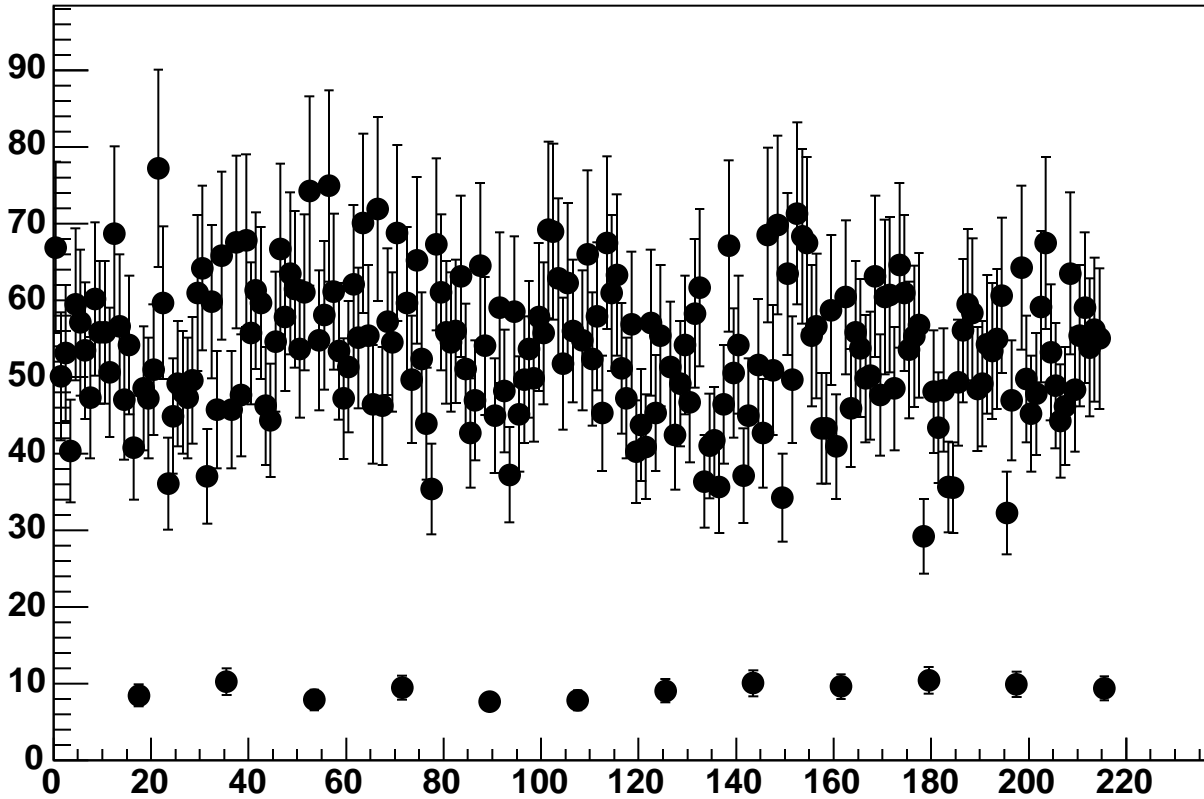
Enable 3, Hold=30, DAC=250, ADC Noise vs 18\*Chip+Chan



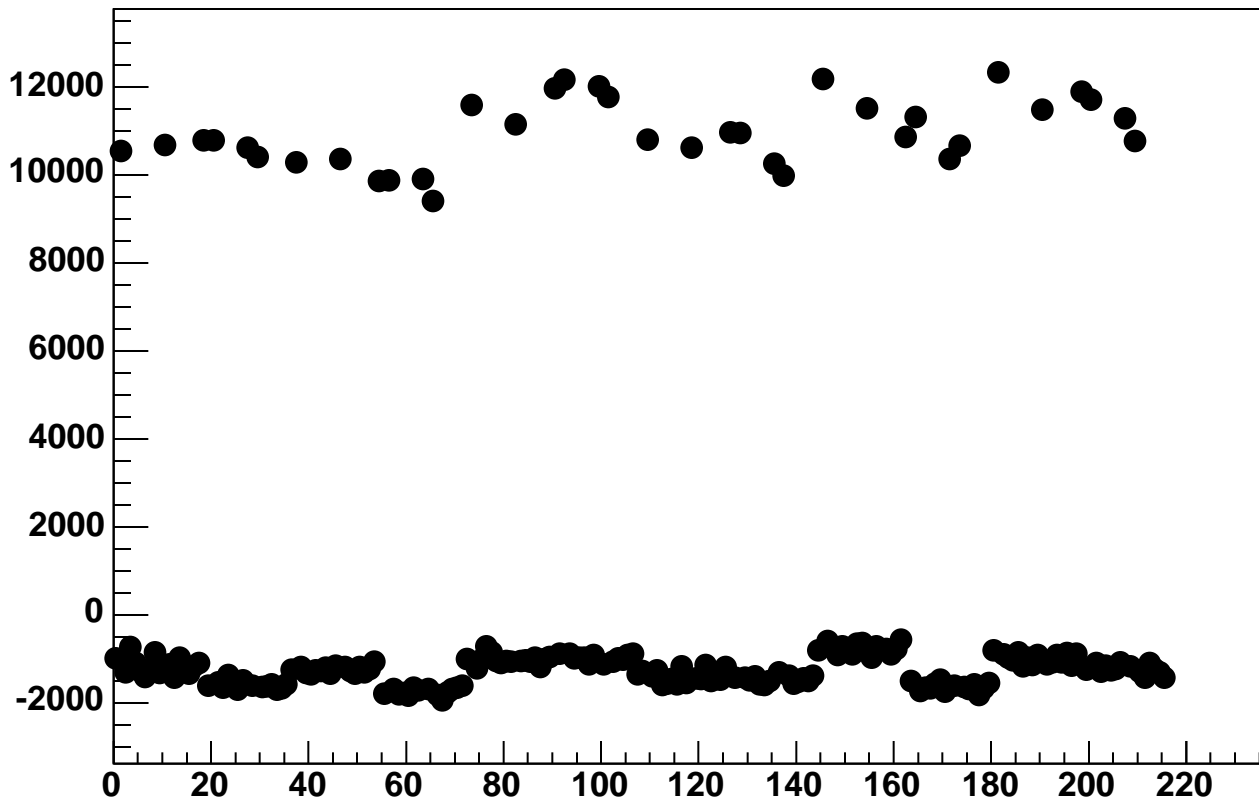
Enable 3, Hold=30, DAC=300, ADC Mean vs 18\*Chip+Chan



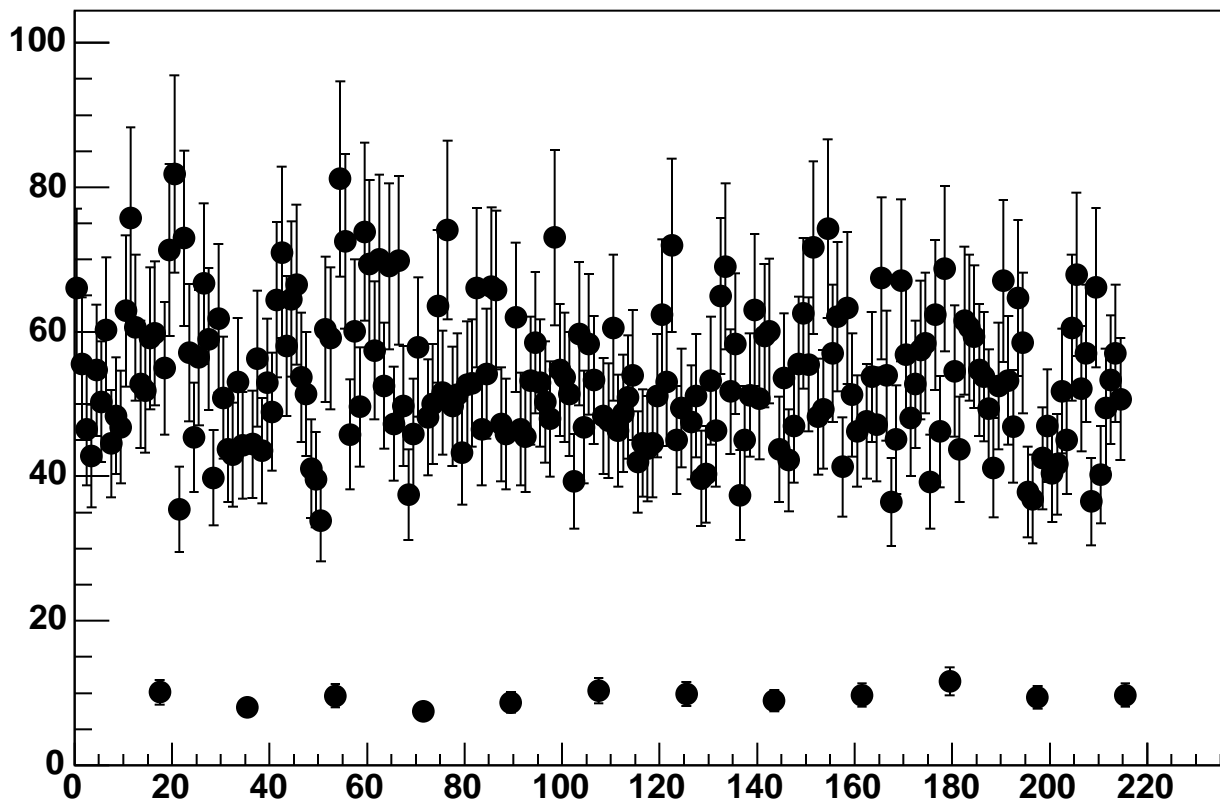
Enable 3, Hold=30, DAC=300, ADC Noise vs 18\*Chip+Chan



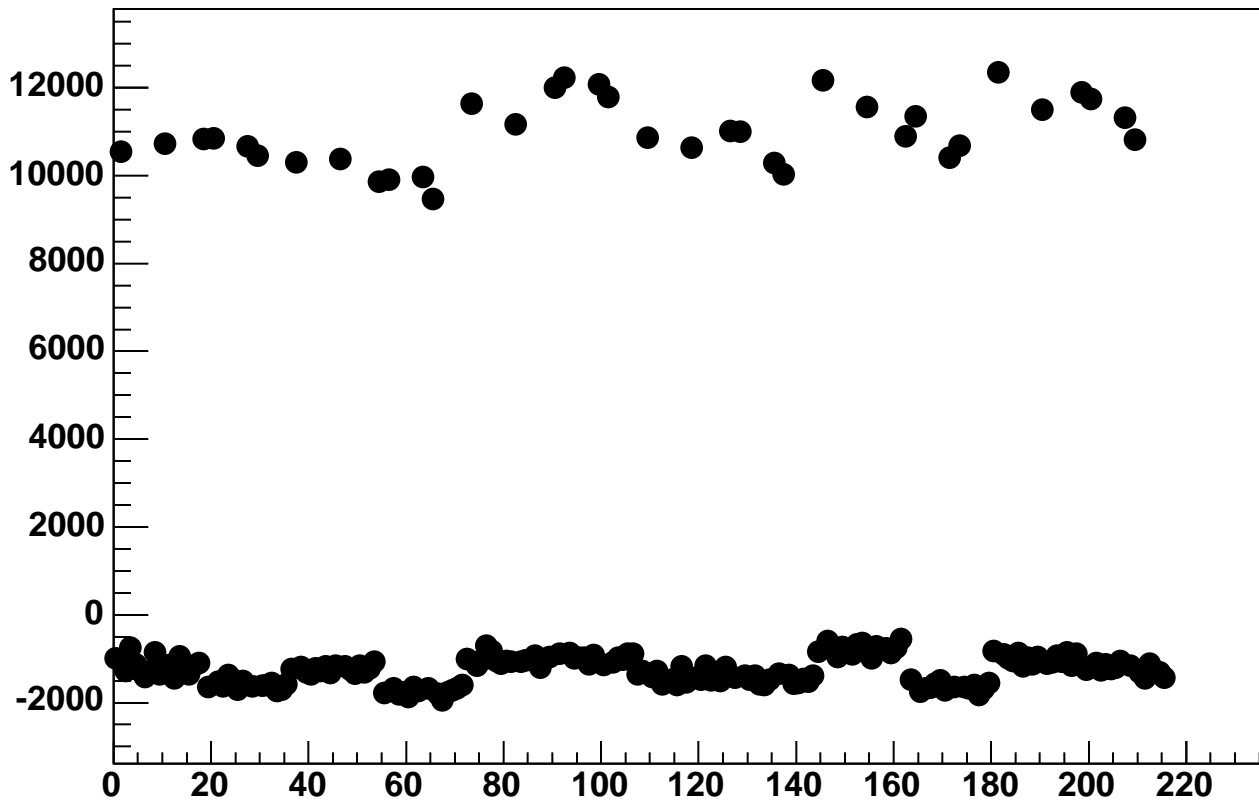
Enable 3, Hold=30, DAC=350, ADC Mean vs 18\*Chip+Chan



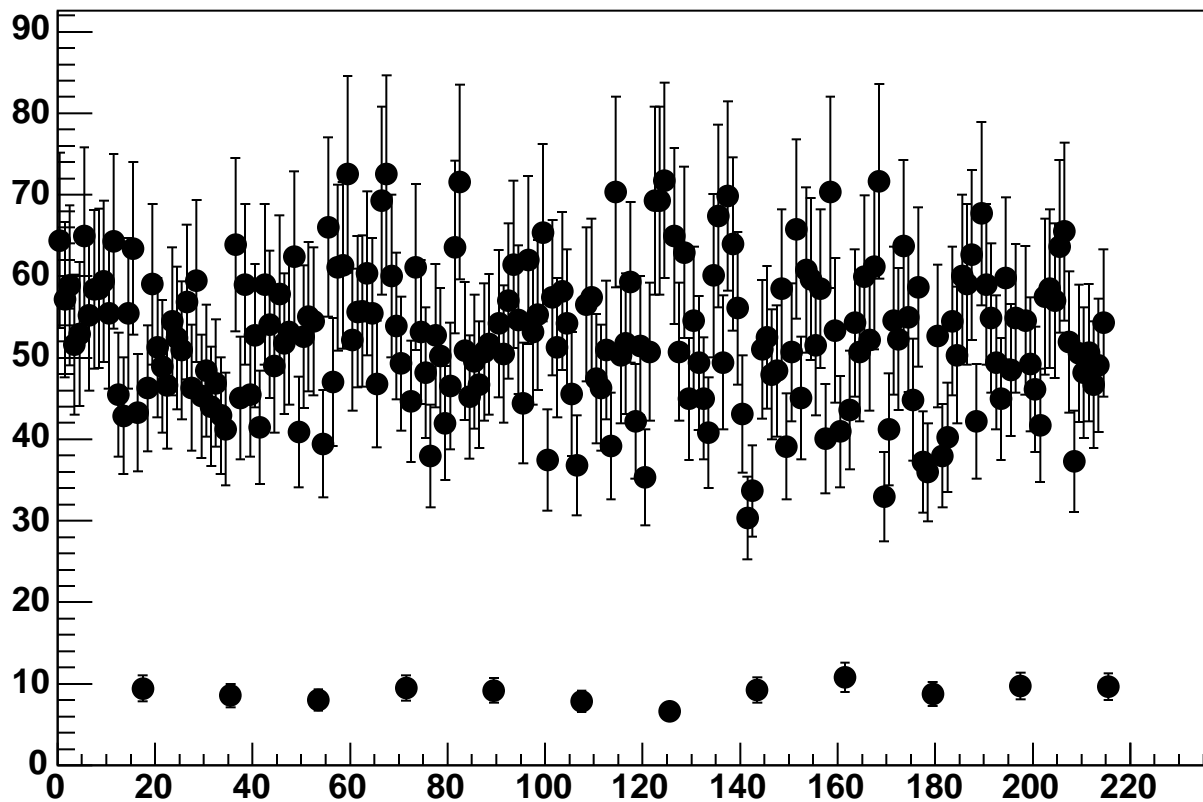
Enable 3, Hold=30, DAC=350, ADC Noise vs 18\*Chip+Chan



Enable 3, Hold=30, DAC=400, ADC Mean vs 18\*Chip+Chan

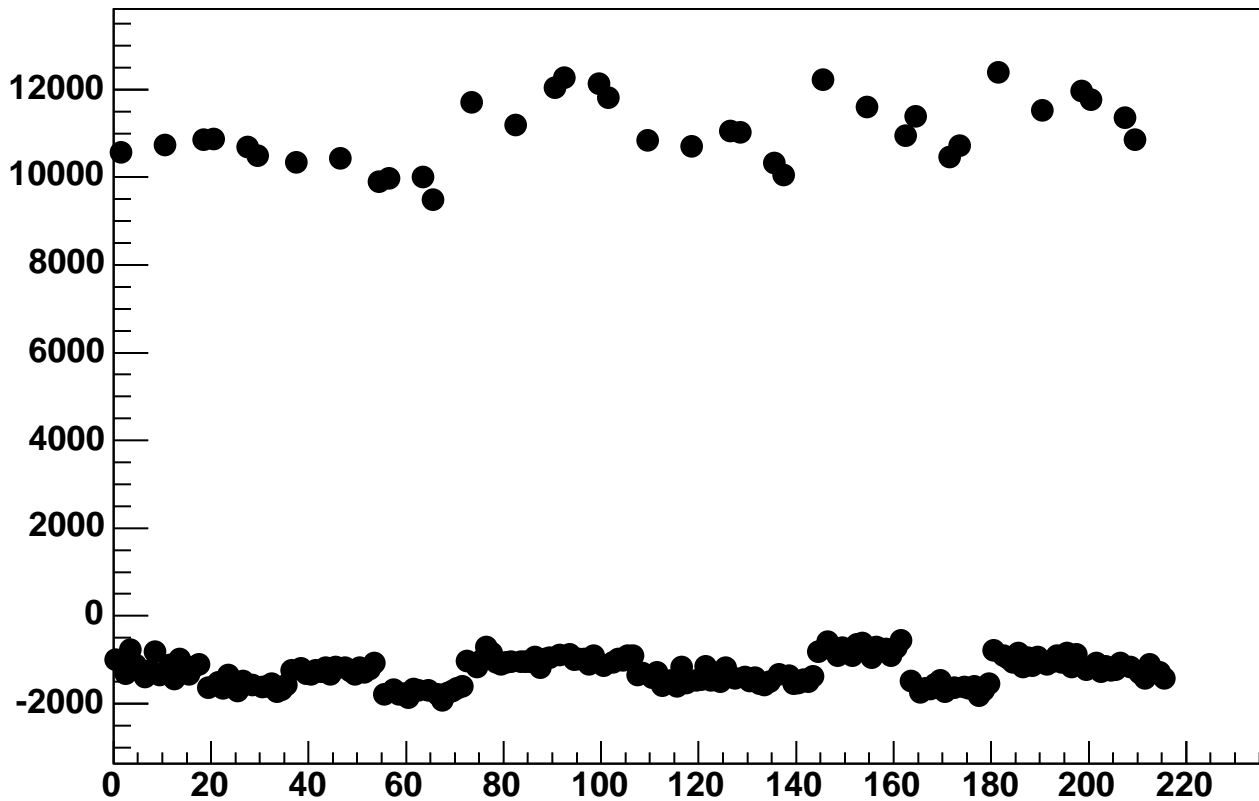


Enable 3, Hold=30, DAC=400, ADC Noise vs 18\*Chip+Chan

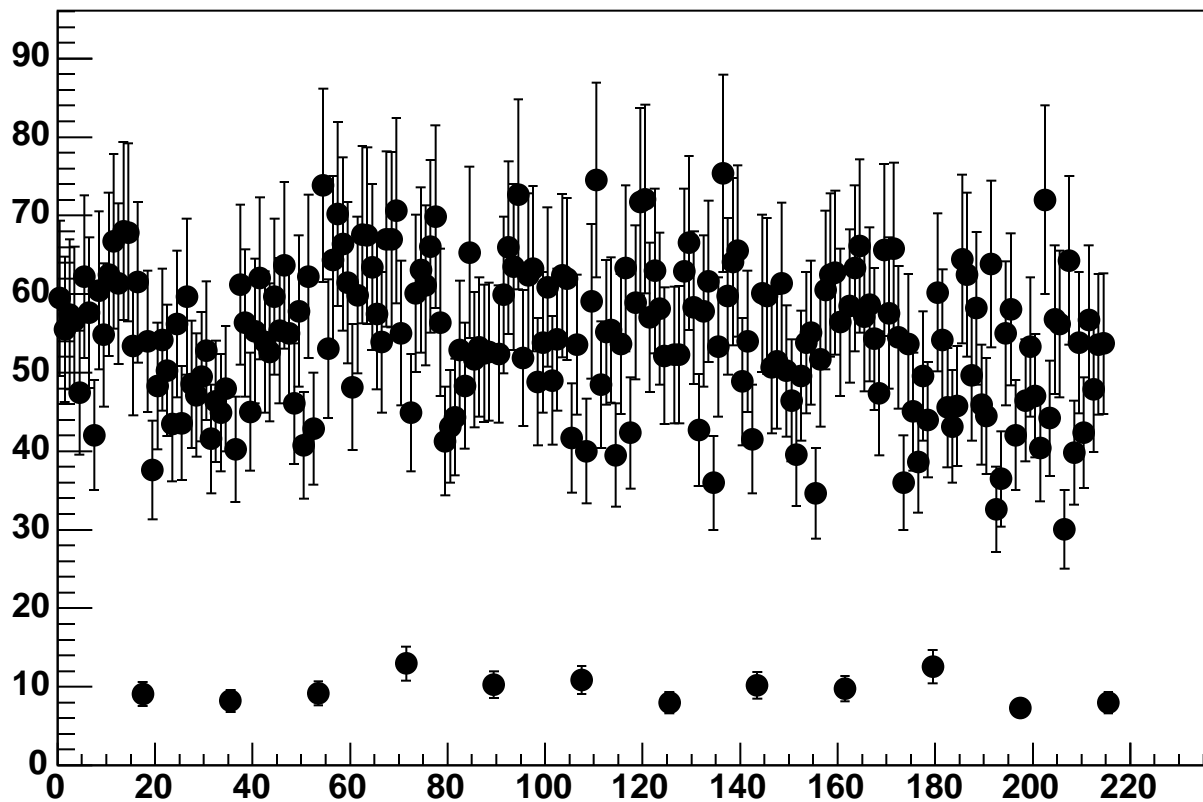




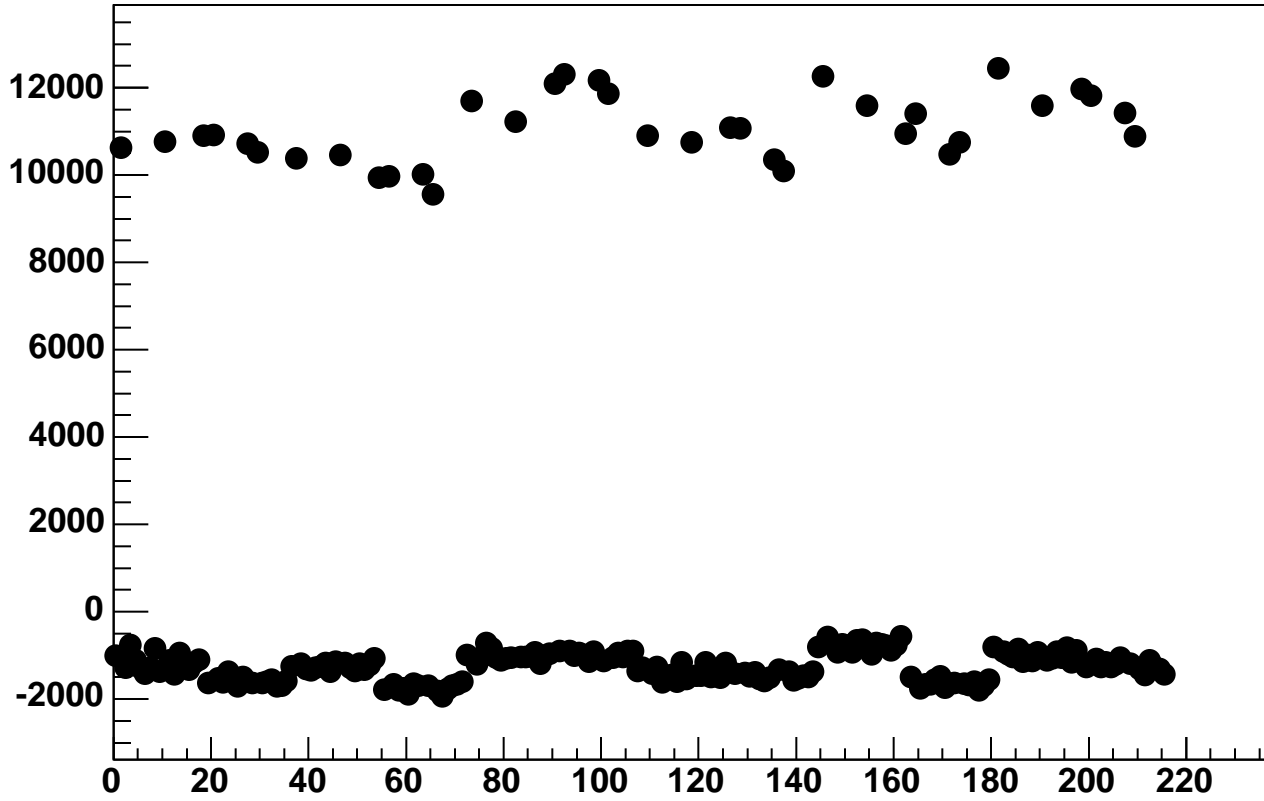
Enable 3, Hold=30, DAC=450, ADC Mean vs 18\*Chip+Chan



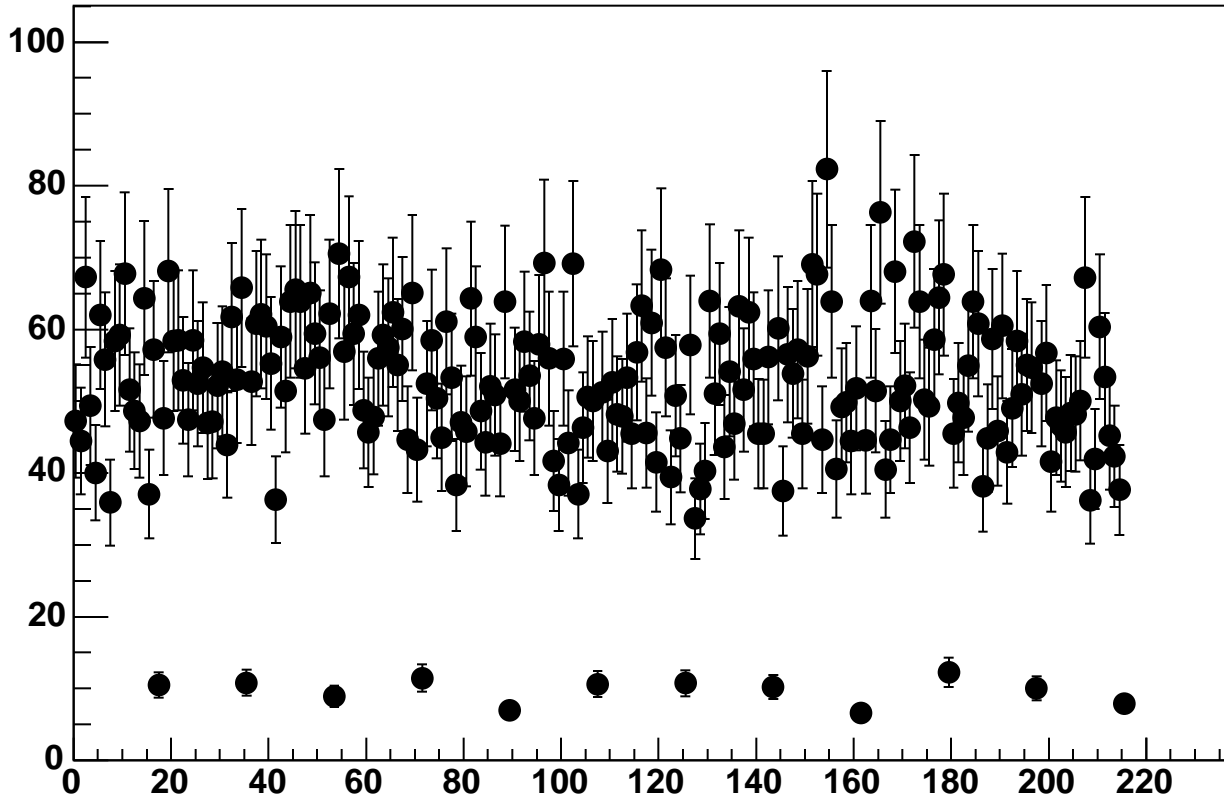
Enable 3, Hold=30, DAC=450, ADC Noise vs 18\*Chip+Chan



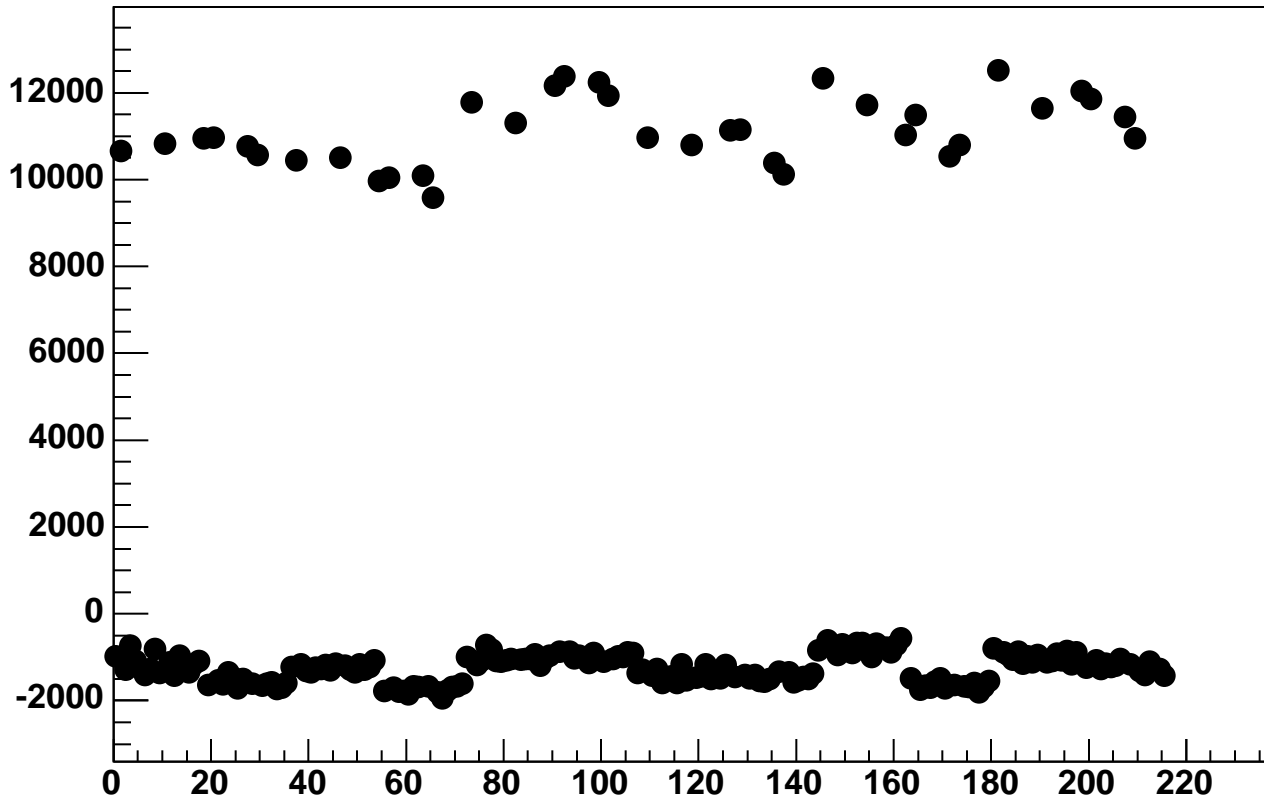
Enable 3, Hold=30, DAC=500, ADC Mean vs 18\*Chip+Chan



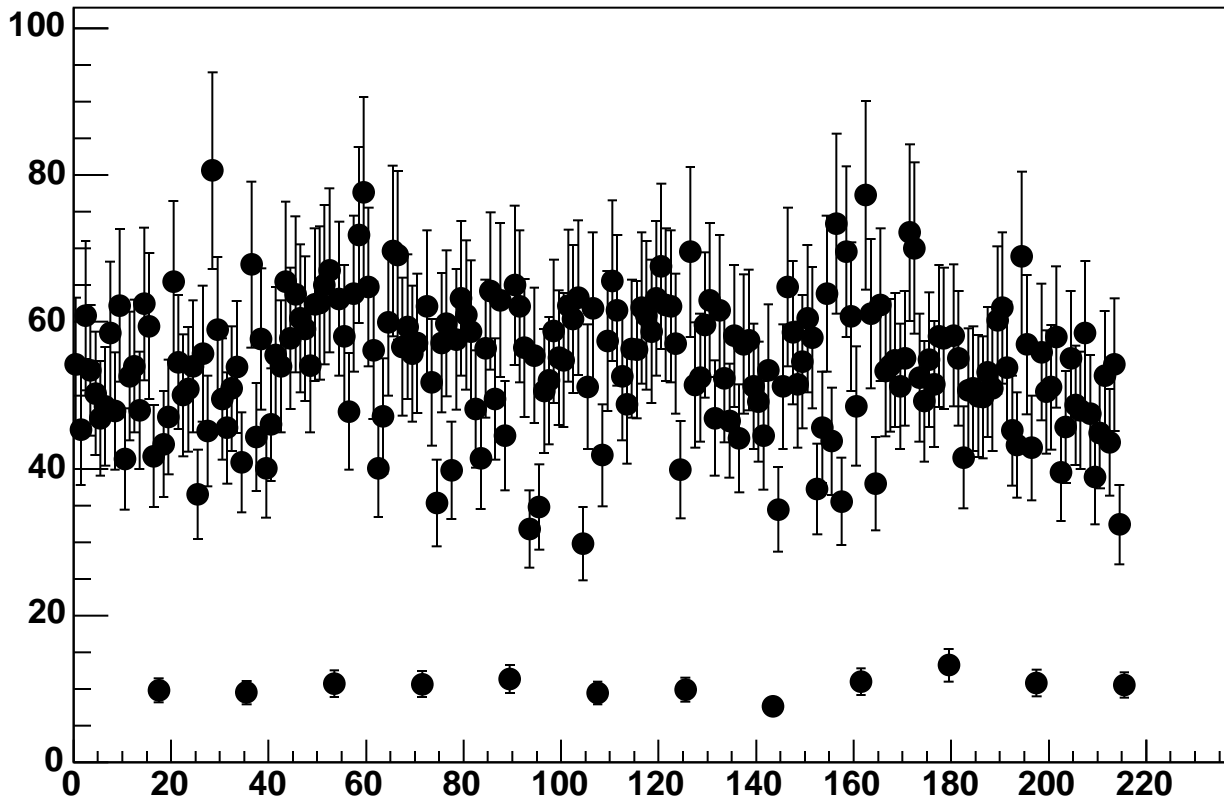
Enable 3, Hold=30, DAC=500, ADC Noise vs 18\*Chip+Chan



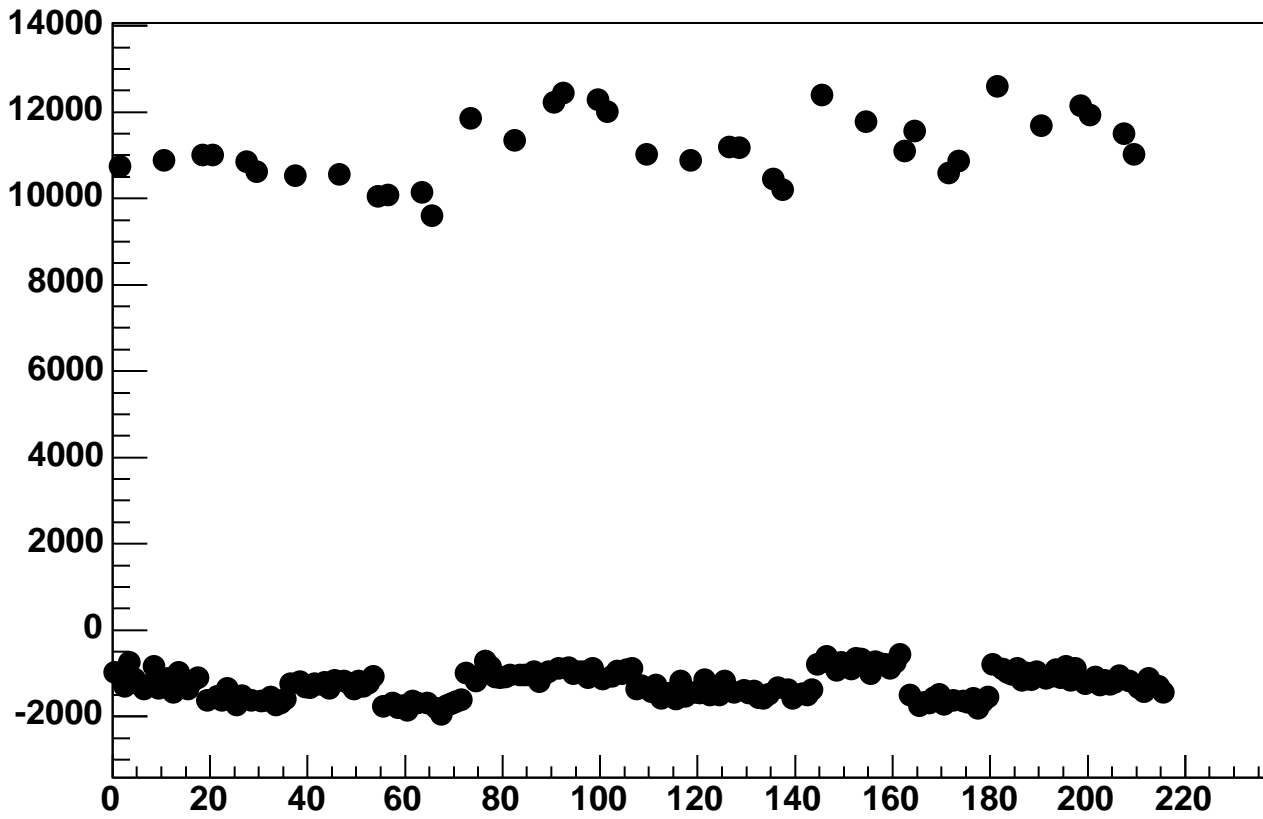
Enable 3, Hold=30, DAC=550, ADC Mean vs 18\*Chip+Chan



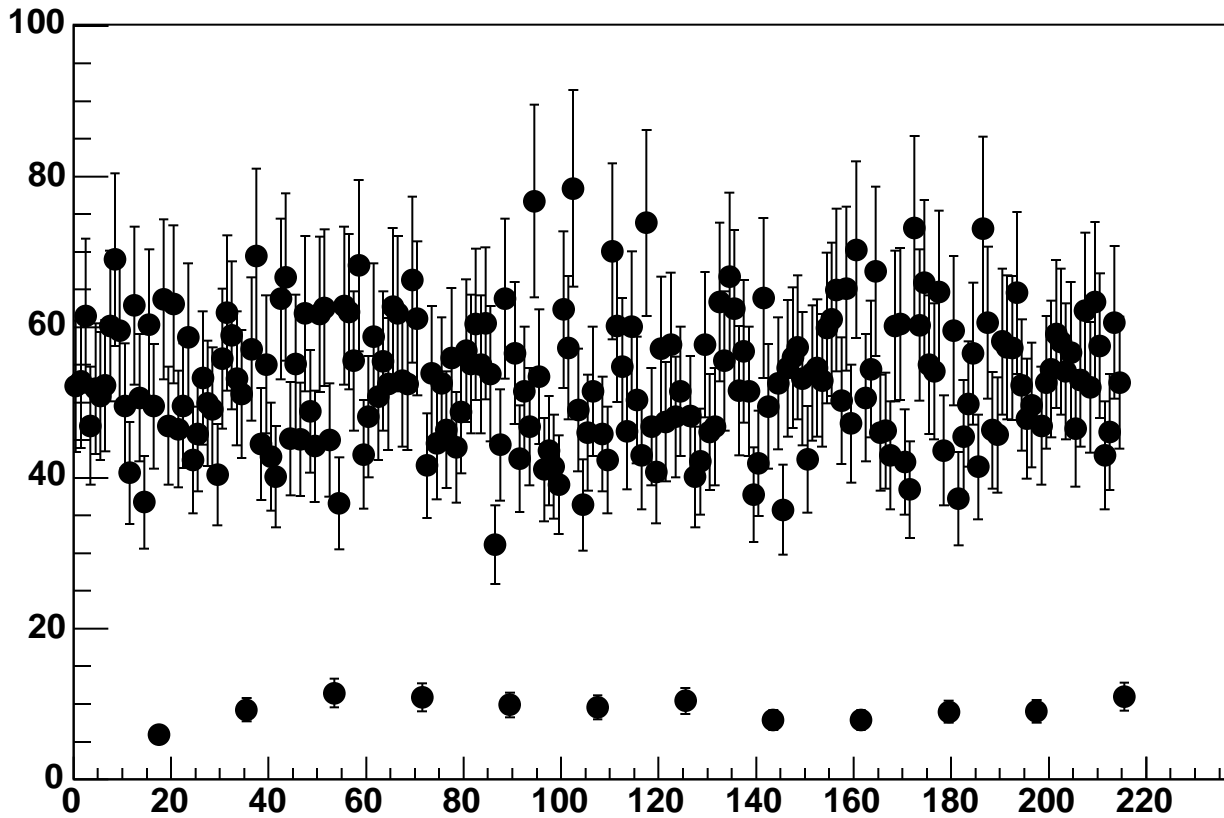
Enable 3, Hold=30, DAC=550, ADC Noise vs 18\*Chip+Chan



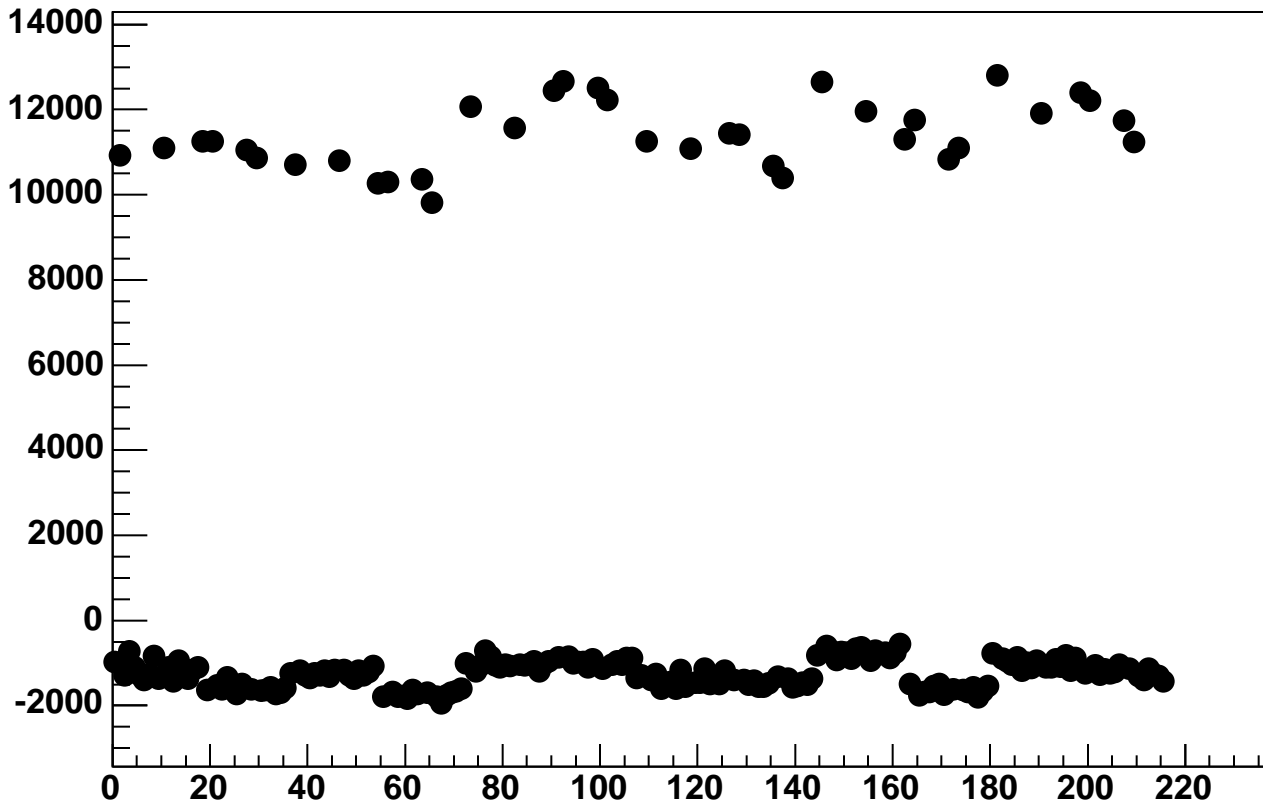
Enable 3, Hold=30, DAC=600, ADC Mean vs 18\*Chip+Chan



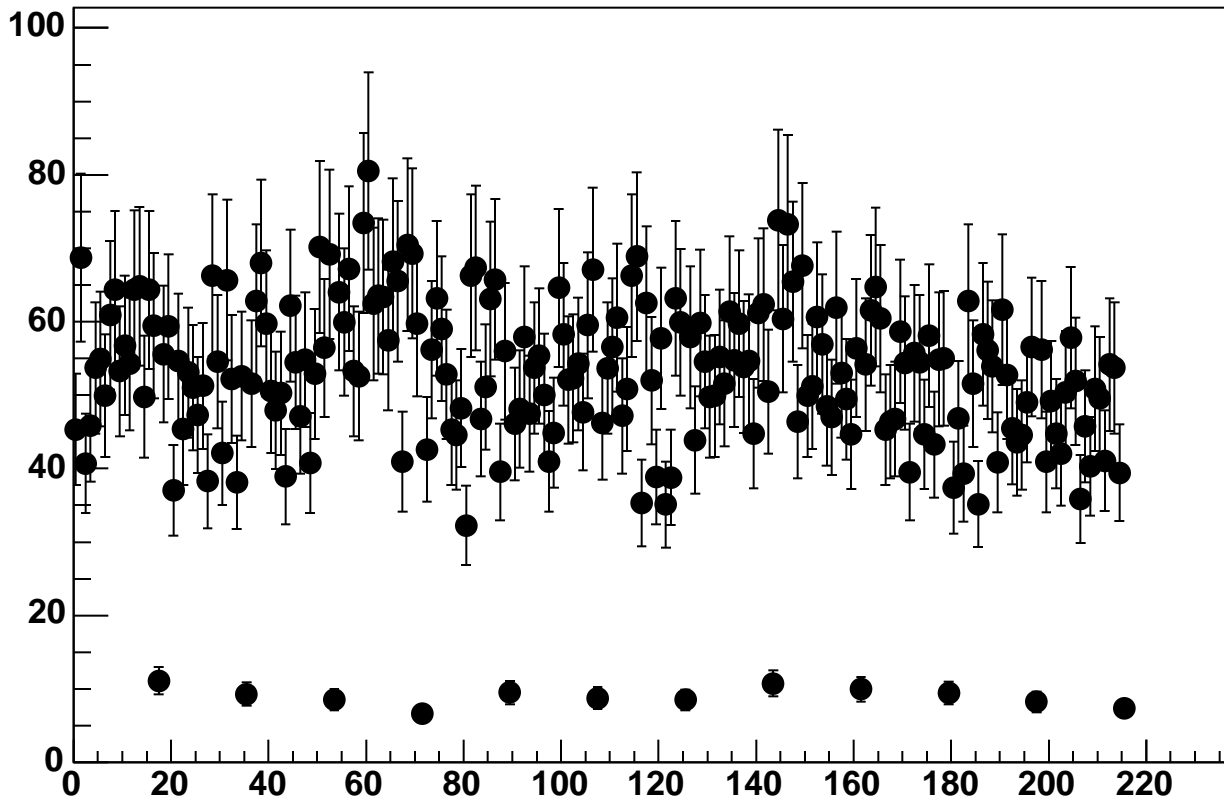
Enable 3, Hold=30, DAC=600, ADC Noise vs 18\*Chip+Chan



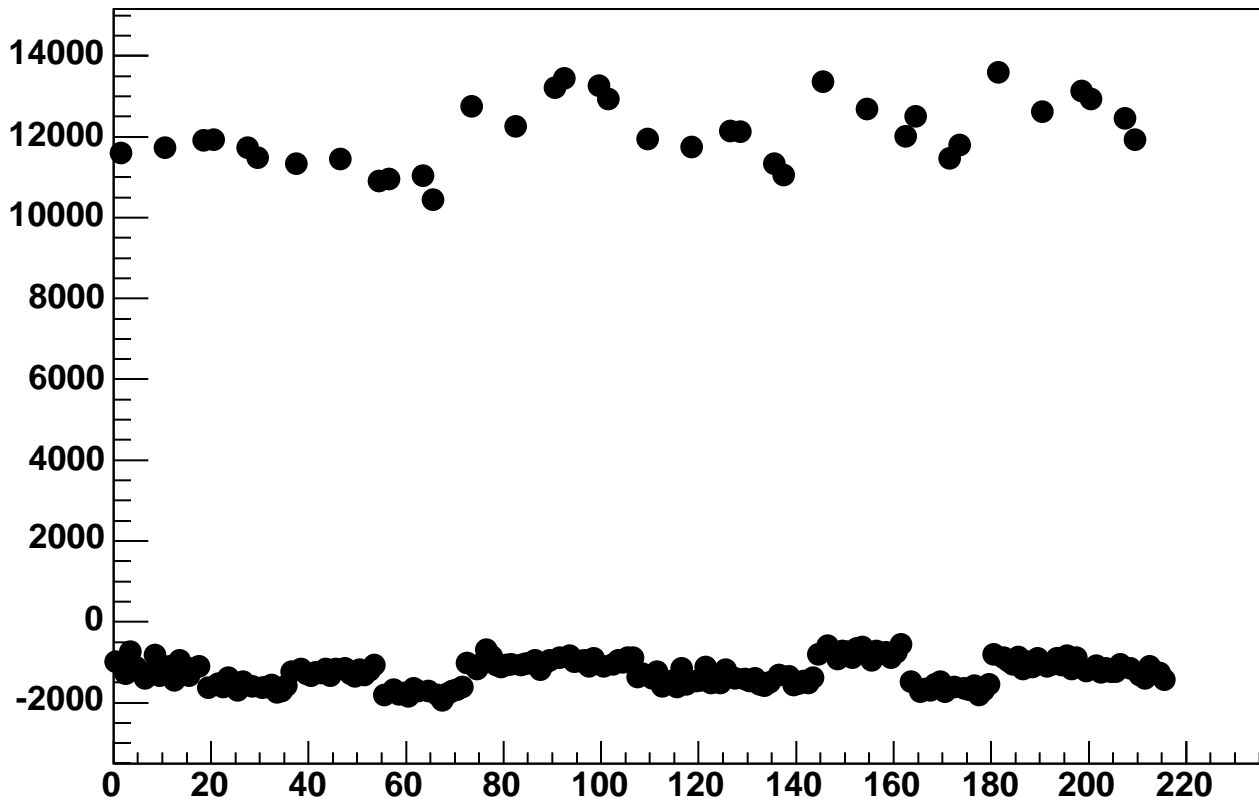
Enable 3, Hold=30, DAC=650, ADC Mean vs 18\*Chip+Chan



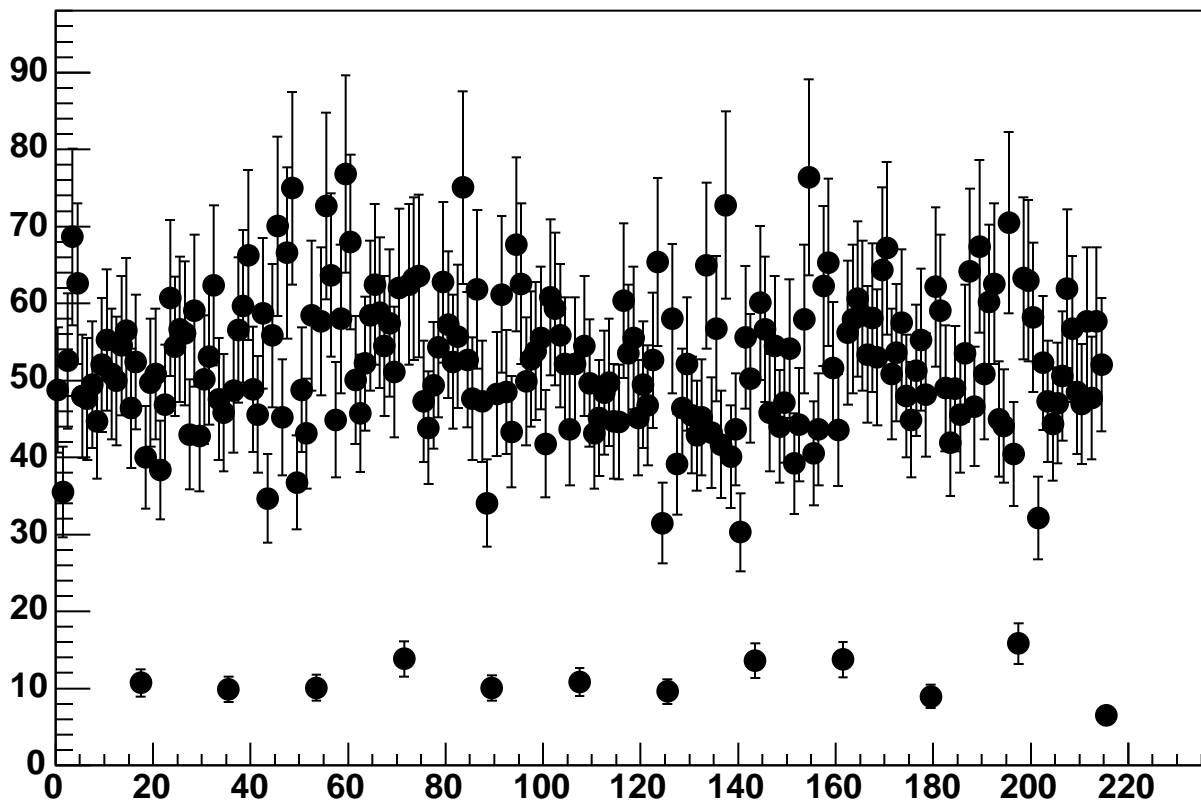
Enable 3, Hold=30, DAC=650, ADC Noise vs 18\*Chip+Chan



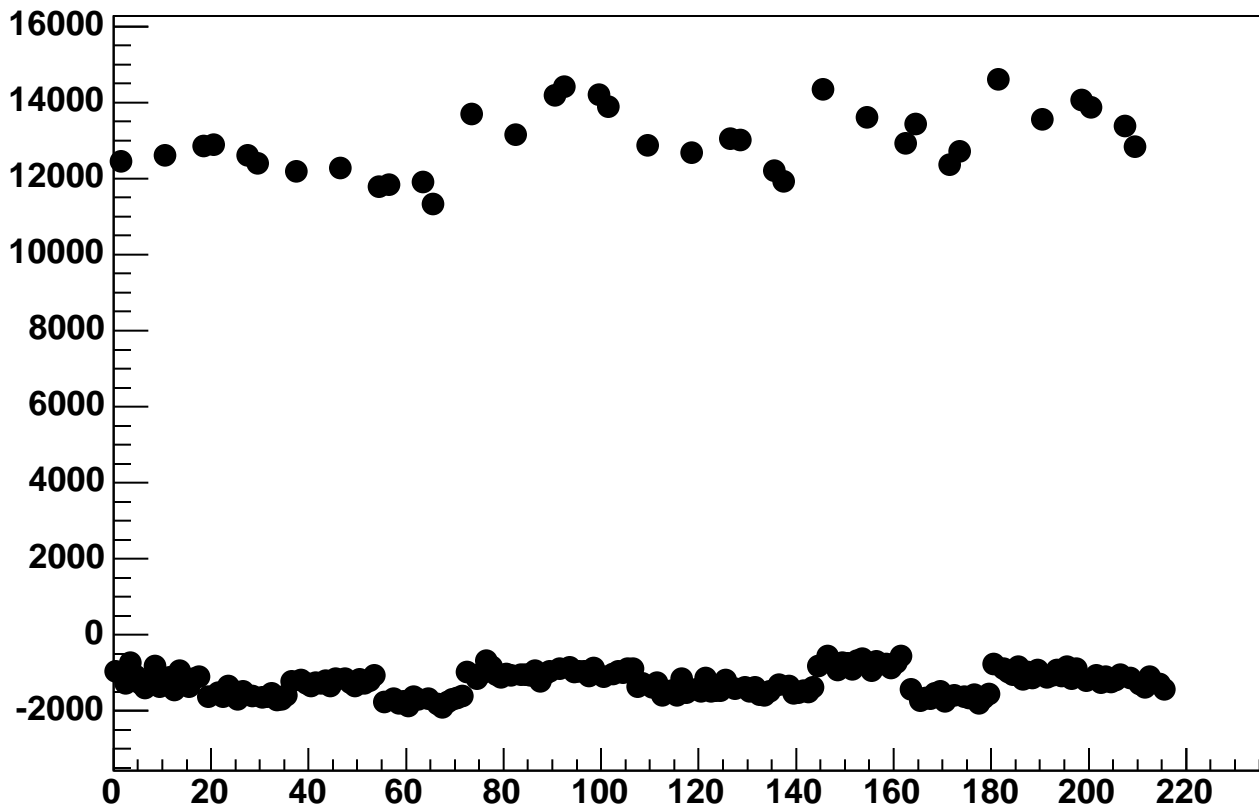
Enable 3, Hold=30, DAC=700, ADC Mean vs 18\*Chip+Chan



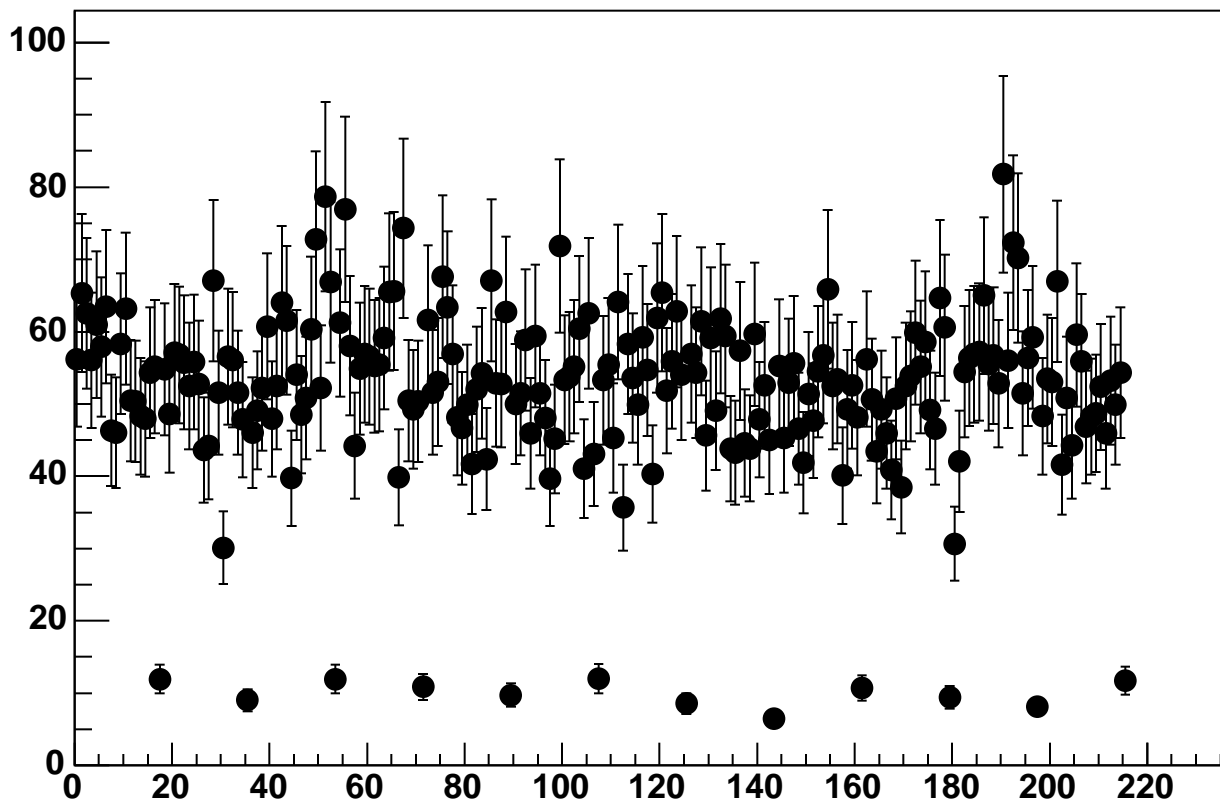
Enable 3, Hold=30, DAC=700, ADC Noise vs 18\*Chip+Chan



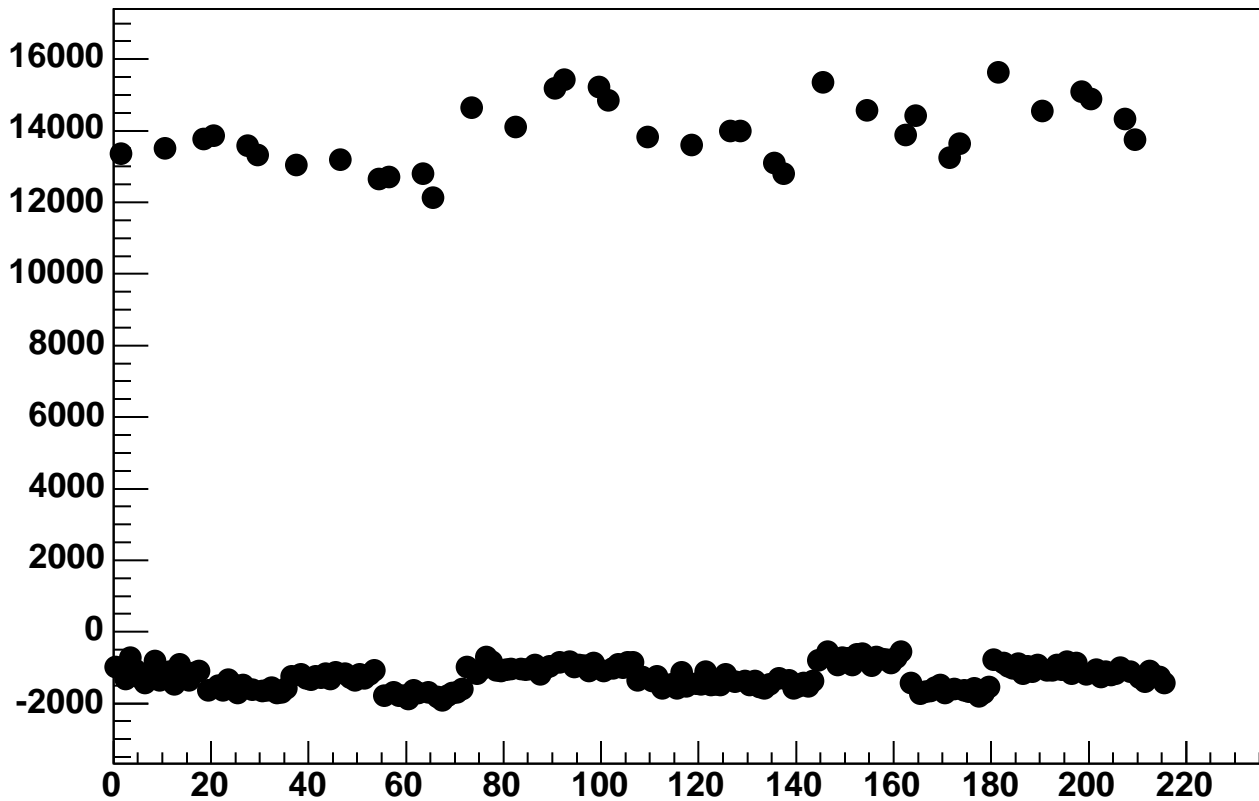
Enable 3, Hold=30, DAC=750, ADC Mean vs 18\*Chip+Chan



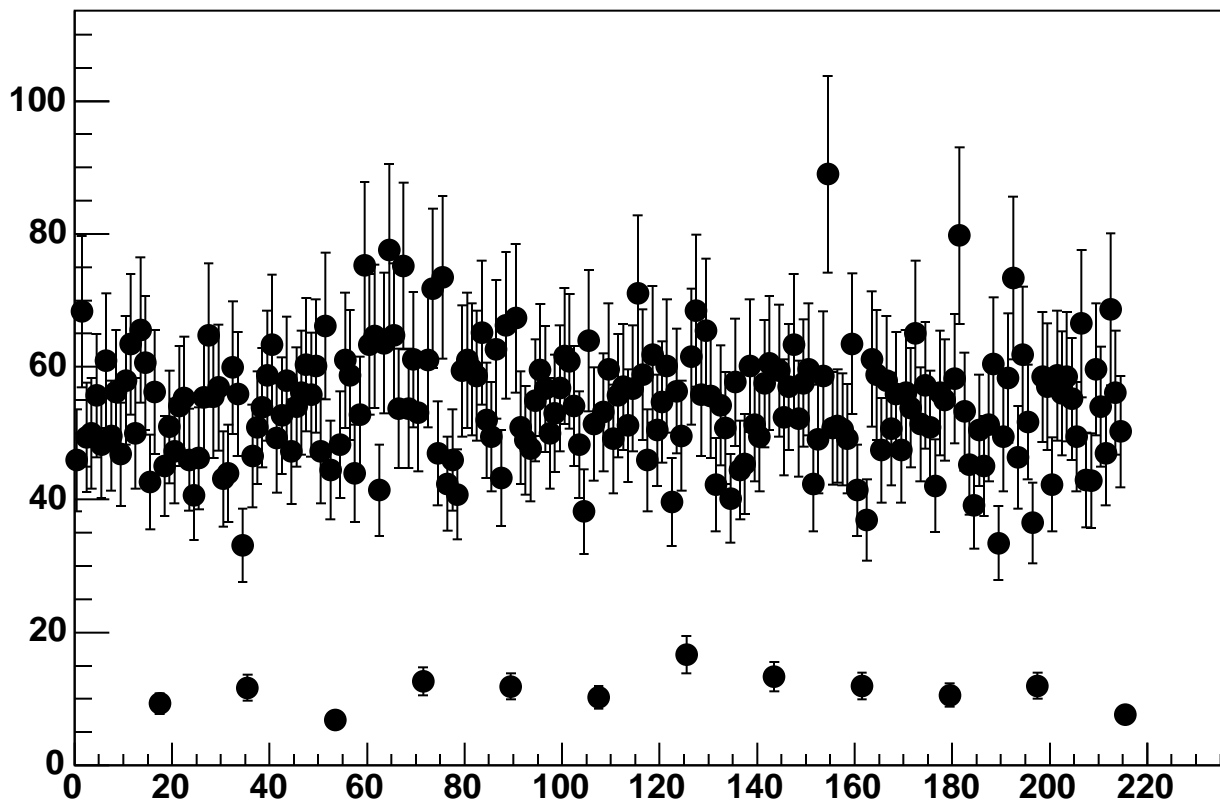
Enable 3, Hold=30, DAC=750, ADC Noise vs 18\*Chip+Chan



Enable 3, Hold=30, DAC=800, ADC Mean vs 18\*Chip+Chan

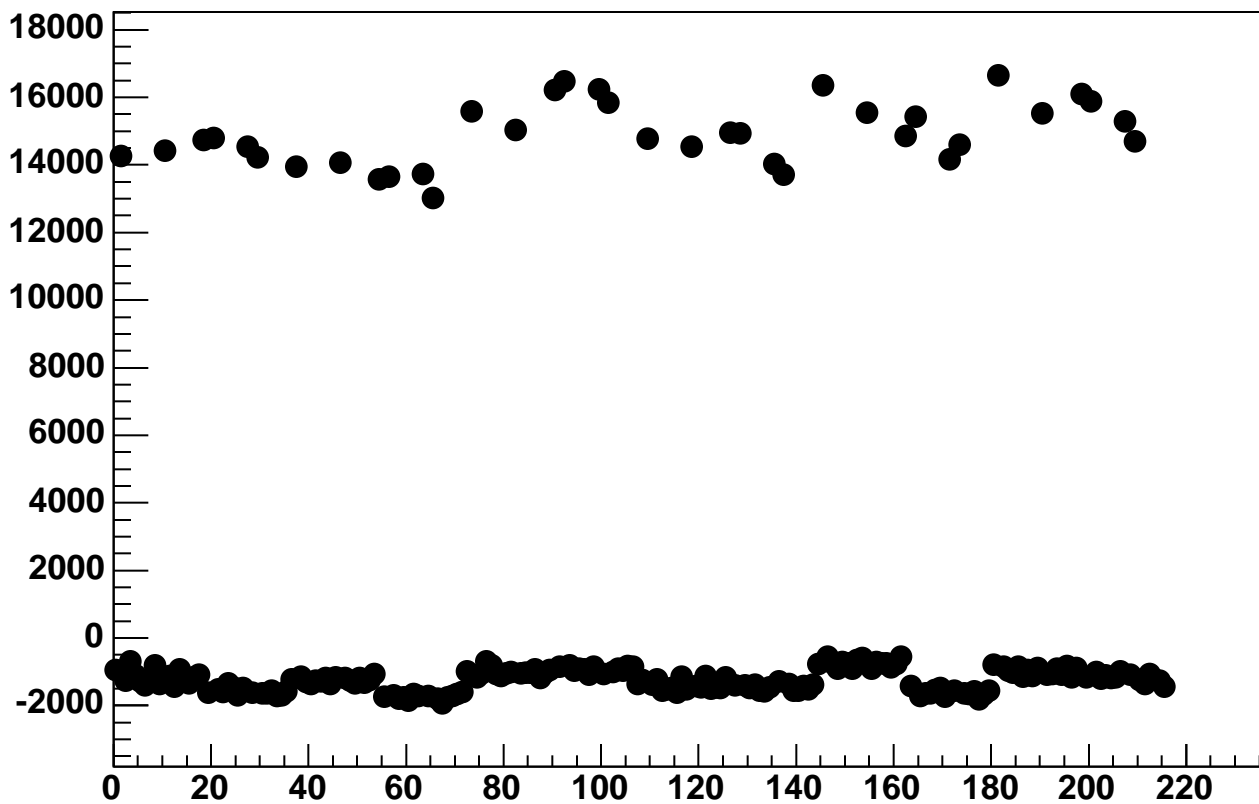


Enable 3, Hold=30, DAC=800, ADC Noise vs 18\*Chip+Chan

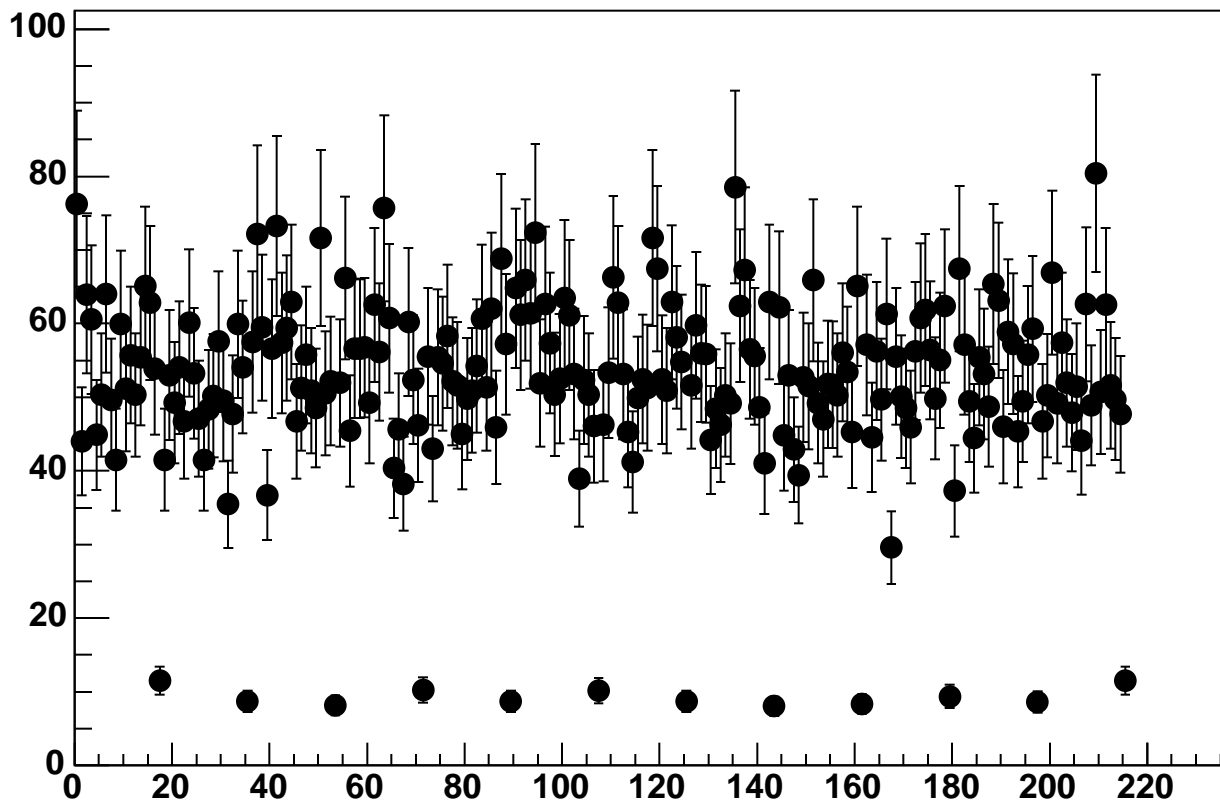




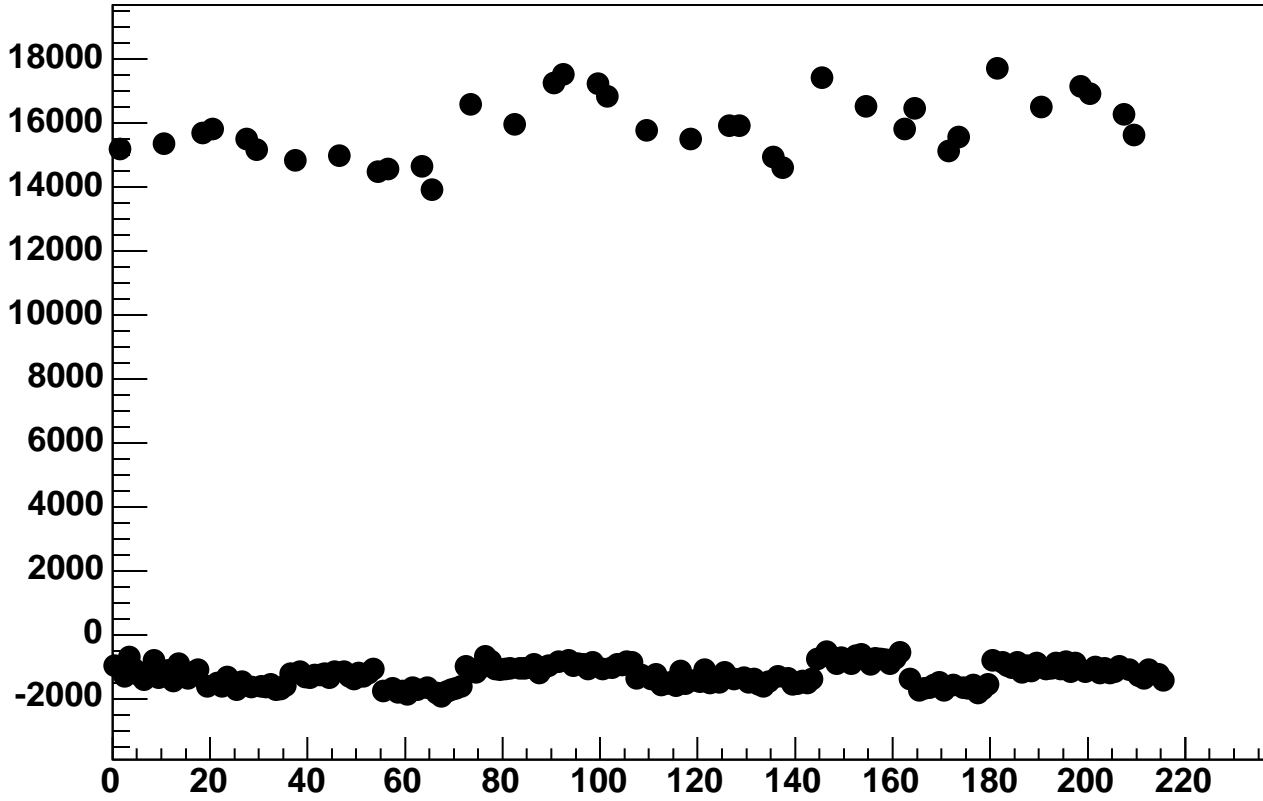
Enable 3, Hold=30, DAC=850, ADC Mean vs 18\*Chip+Chan



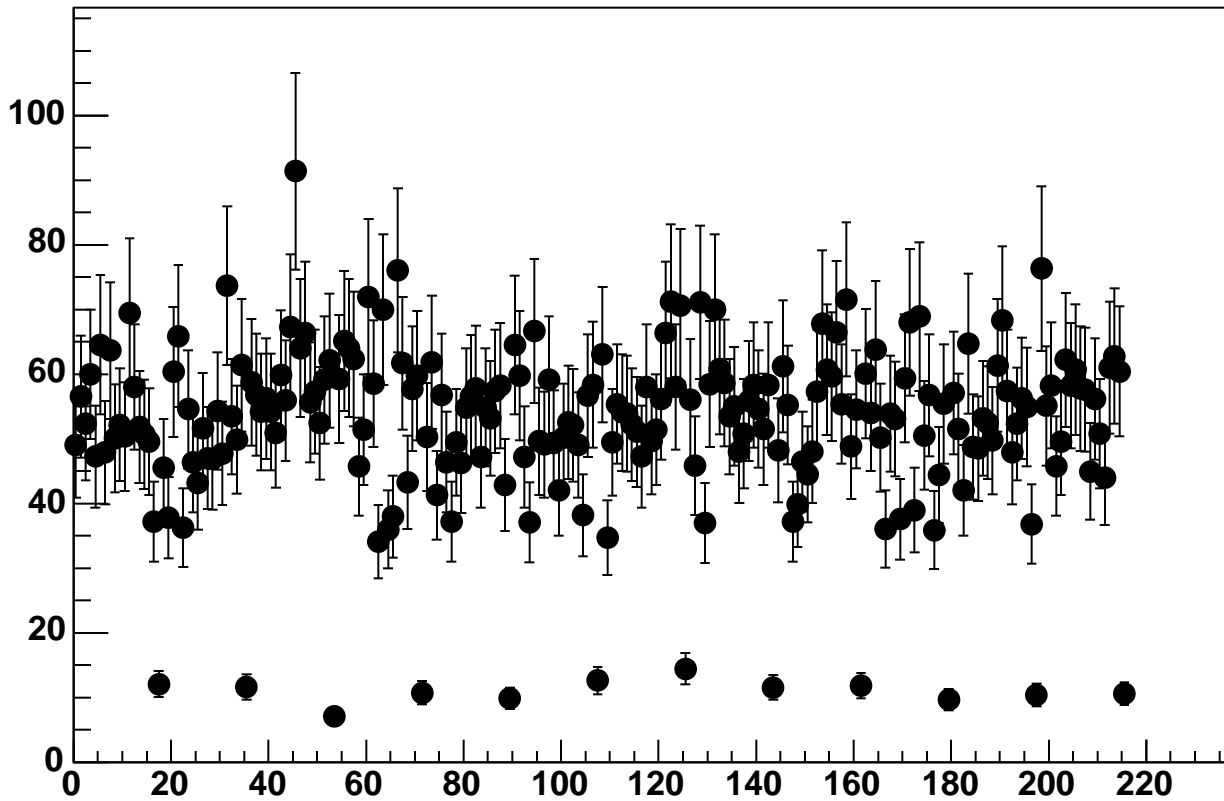
Enable 3, Hold=30, DAC=850, ADC Noise vs 18\*Chip+Chan



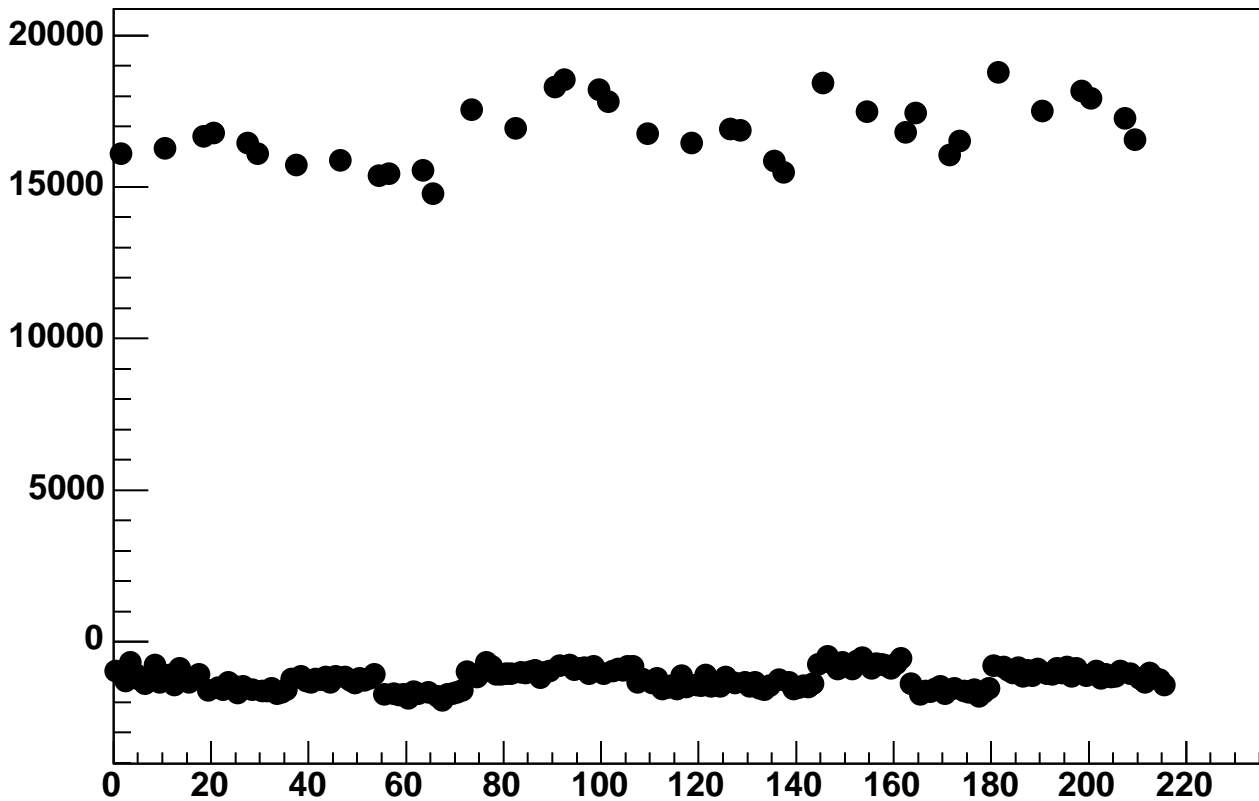
Enable 3, Hold=30, DAC=900, ADC Mean vs 18\*Chip+Chan



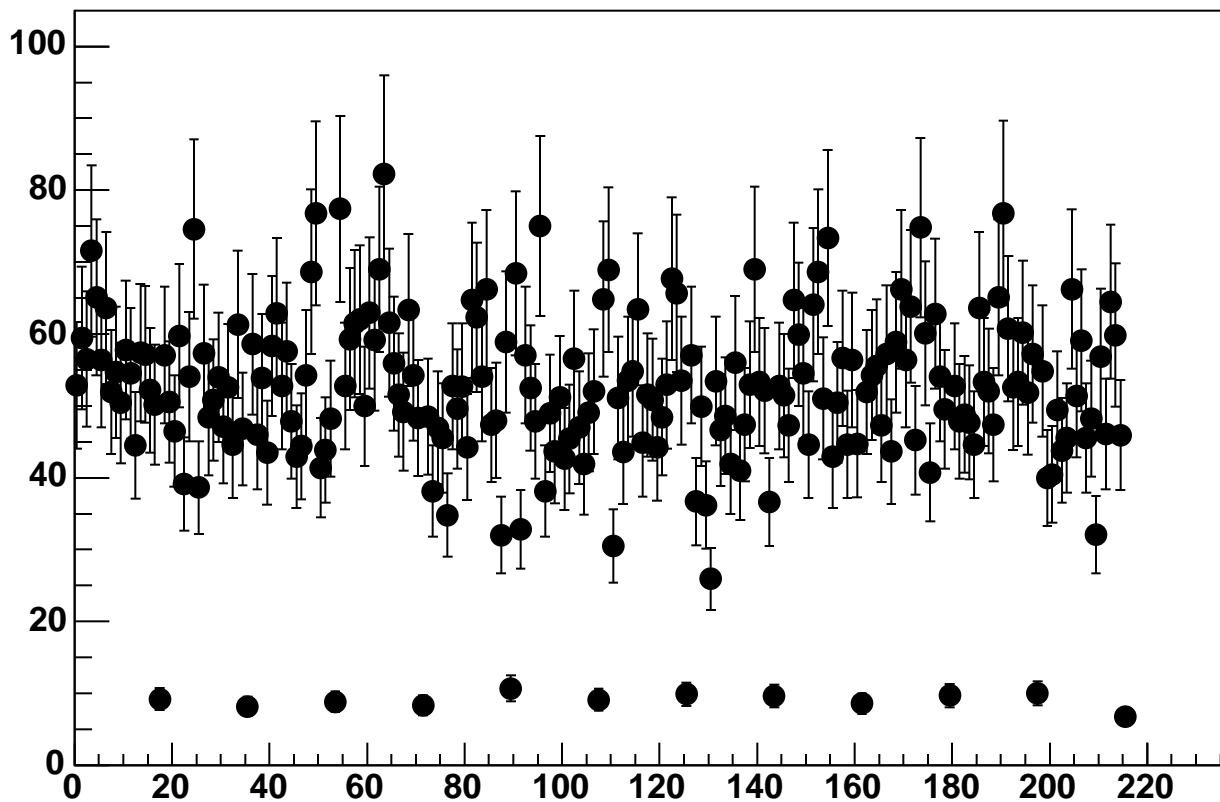
Enable 3, Hold=30, DAC=900, ADC Noise vs 18\*Chip+Chan



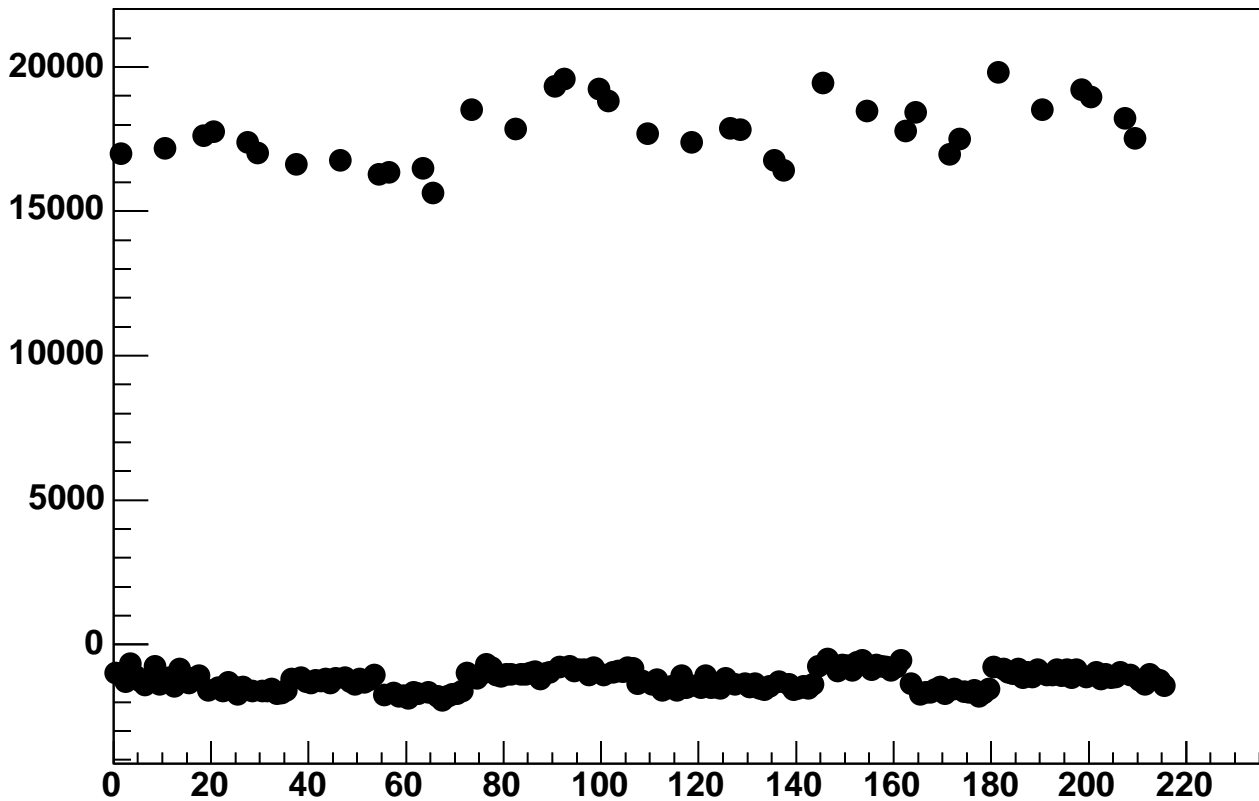
Enable 3, Hold=30, DAC=950, ADC Mean vs 18\*Chip+Chan



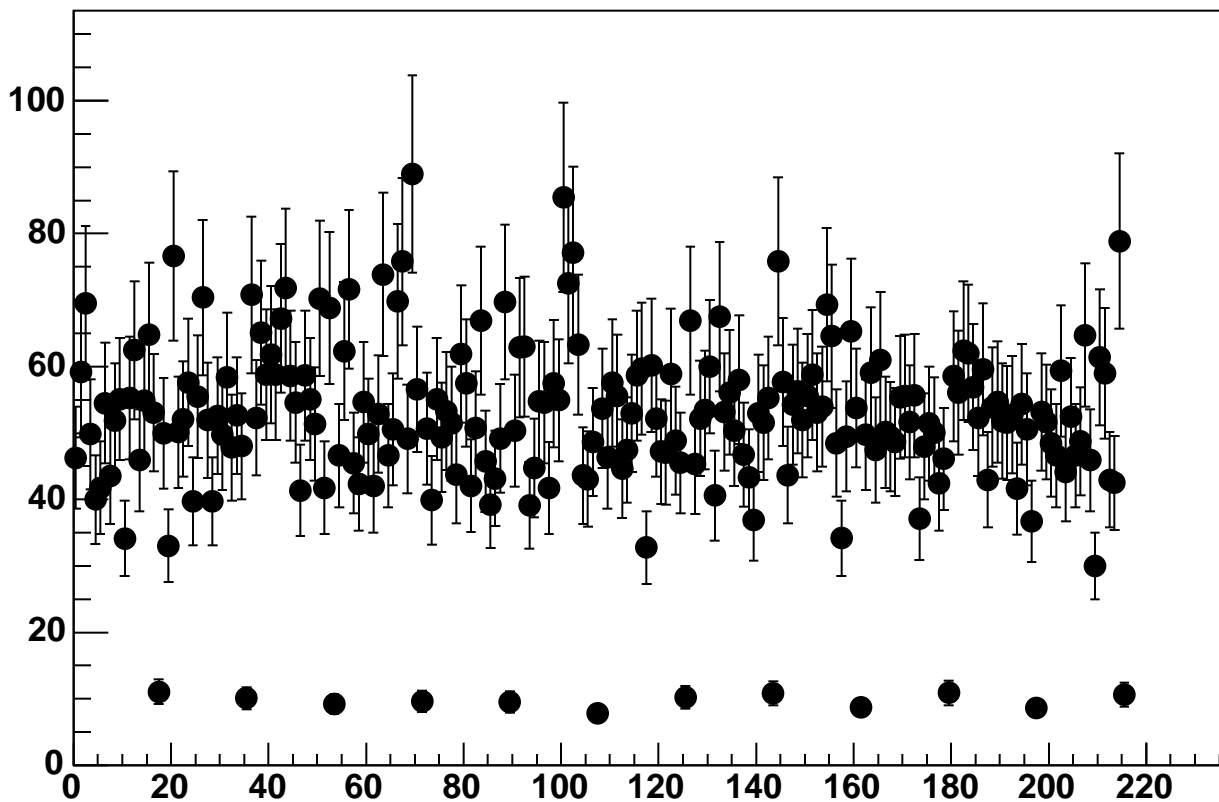
Enable 3, Hold=30, DAC=950, ADC Noise vs 18\*Chip+Chan



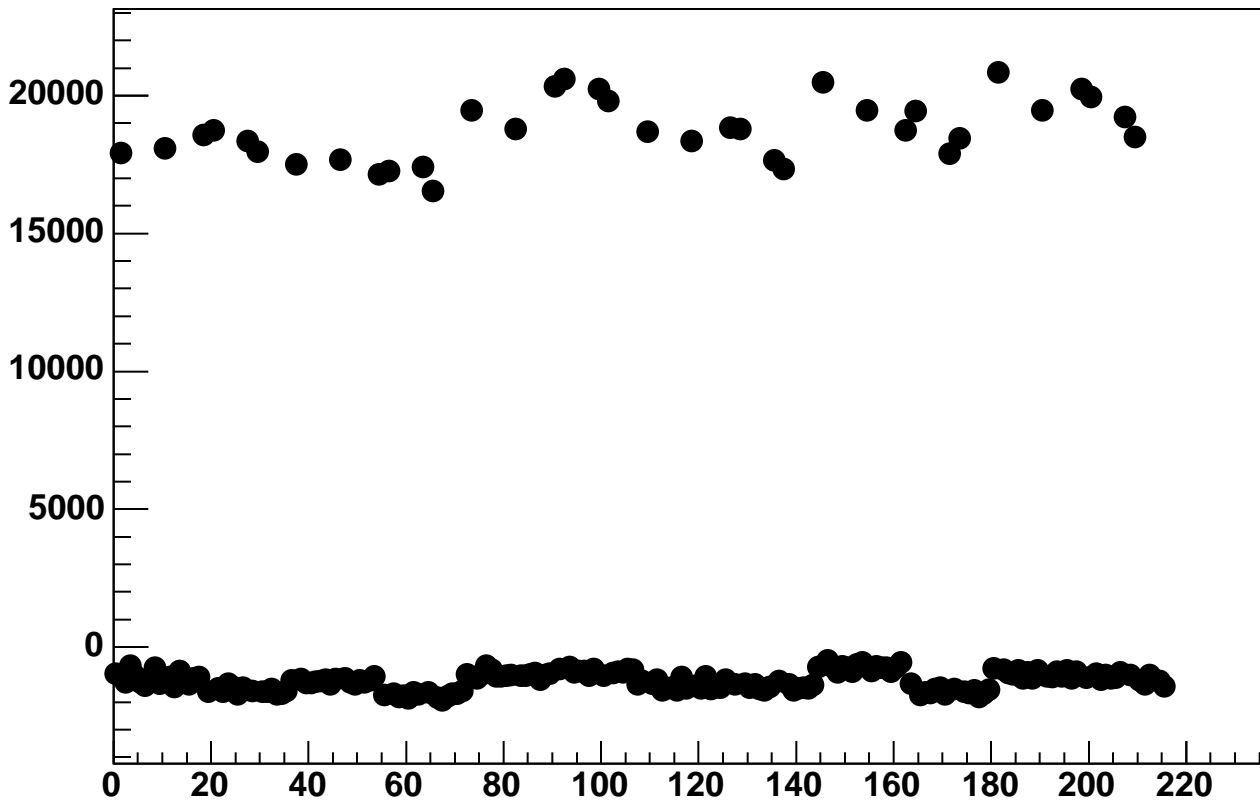
Enable 3, Hold=30, DAC=1000, ADC Mean vs 18\*Chip+Chan



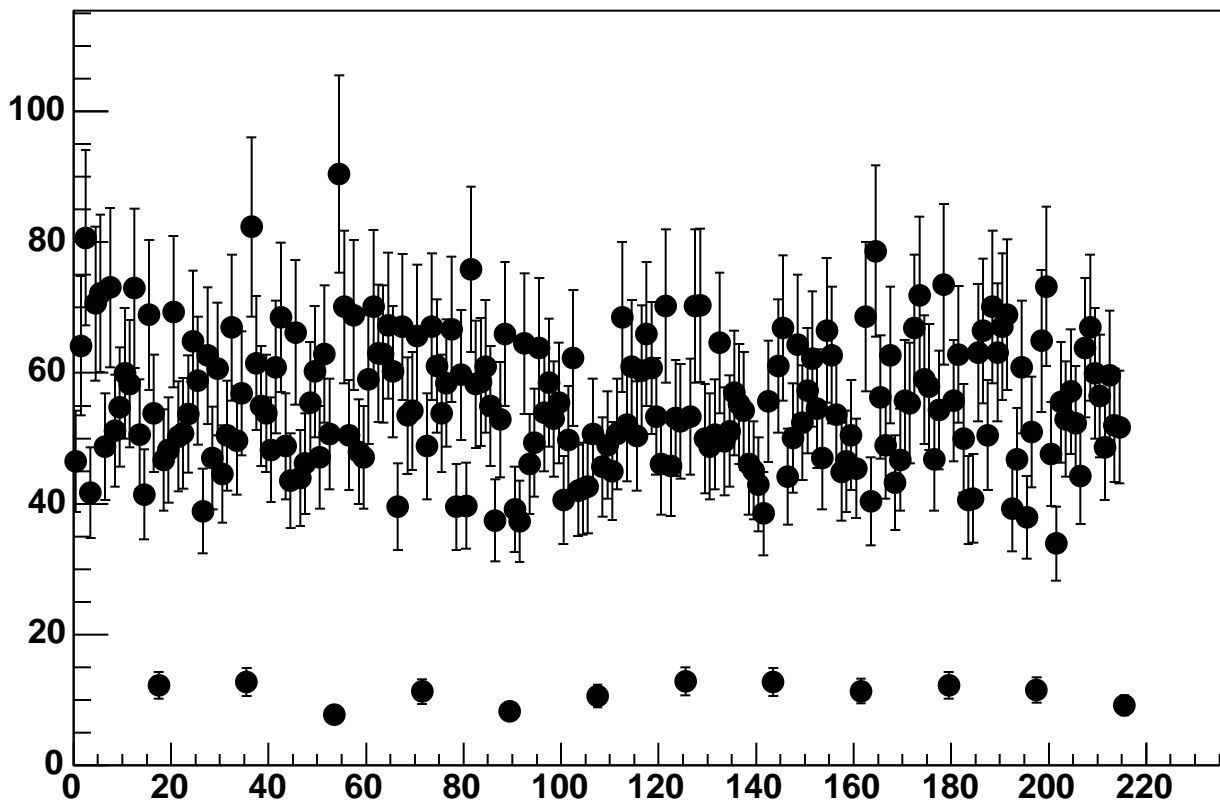
Enable 3, Hold=30, DAC=1000, ADC Noise vs 18\*Chip+Chan



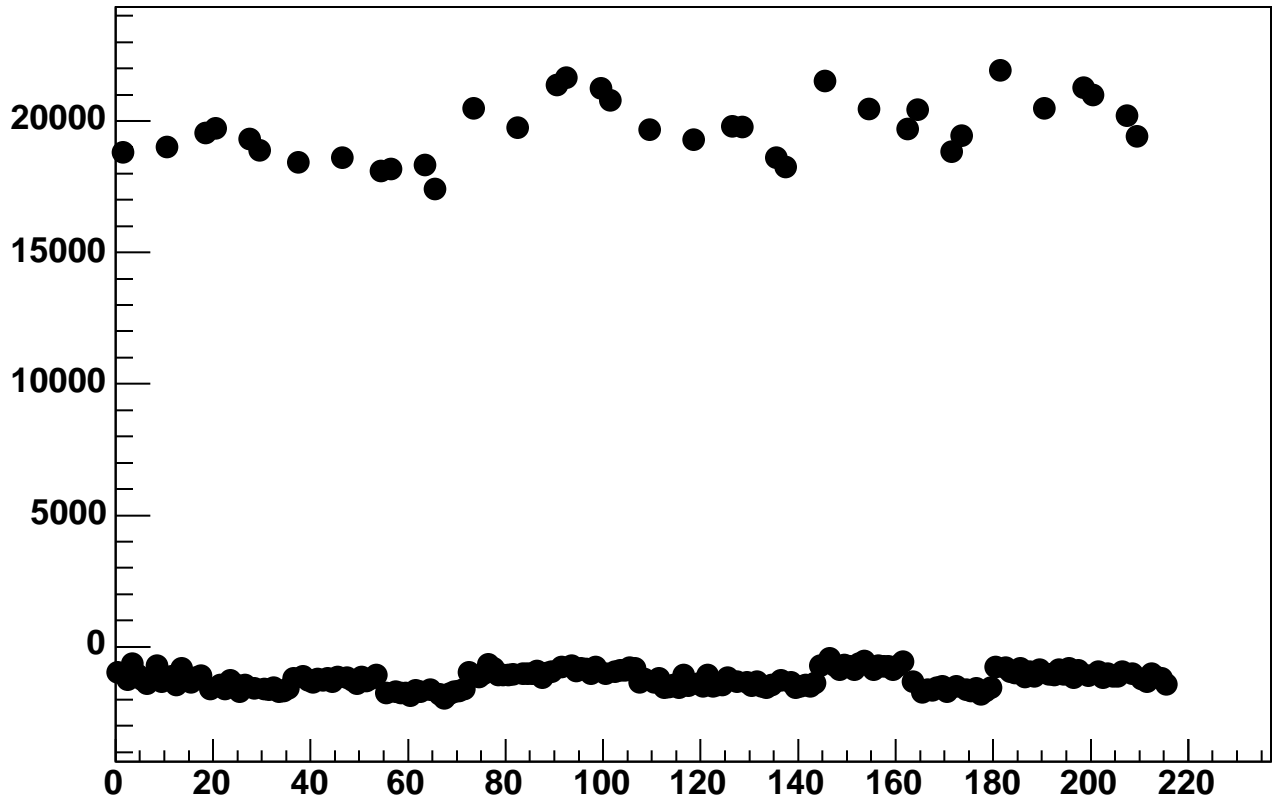
Enable 3, Hold=30, DAC=1050, ADC Mean vs 18\*Chip+Chan



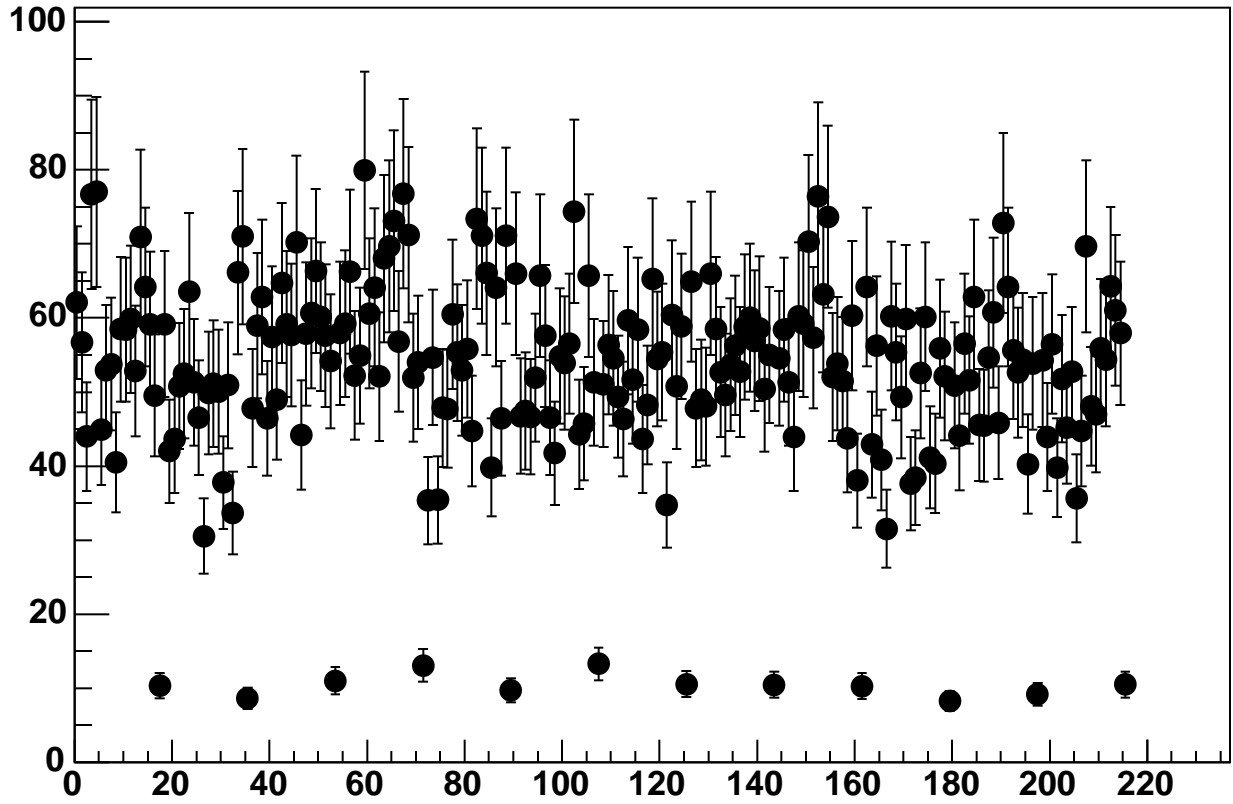
Enable 3, Hold=30, DAC=1050, ADC Noise vs 18\*Chip+Chan



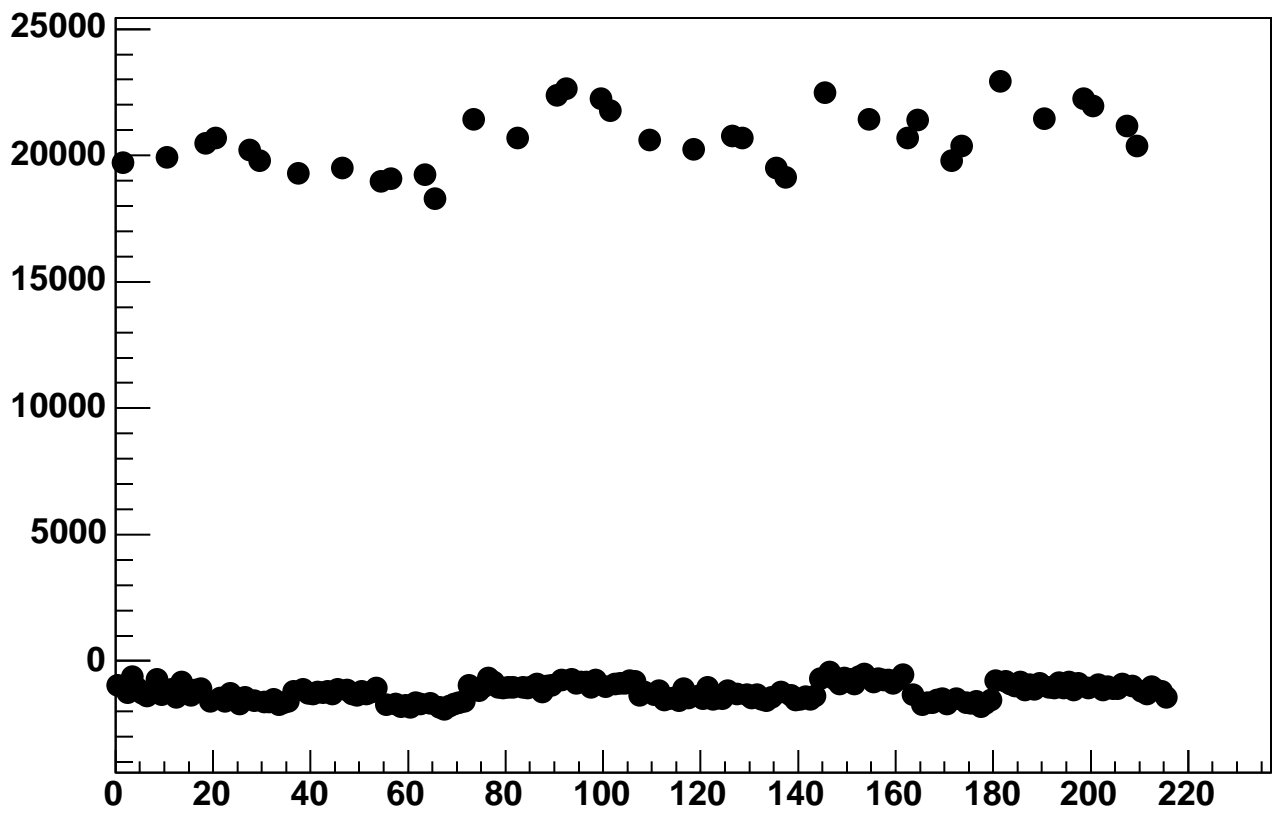
Enable 3, Hold=30, DAC=1100, ADC Mean vs 18\*Chip+Chan



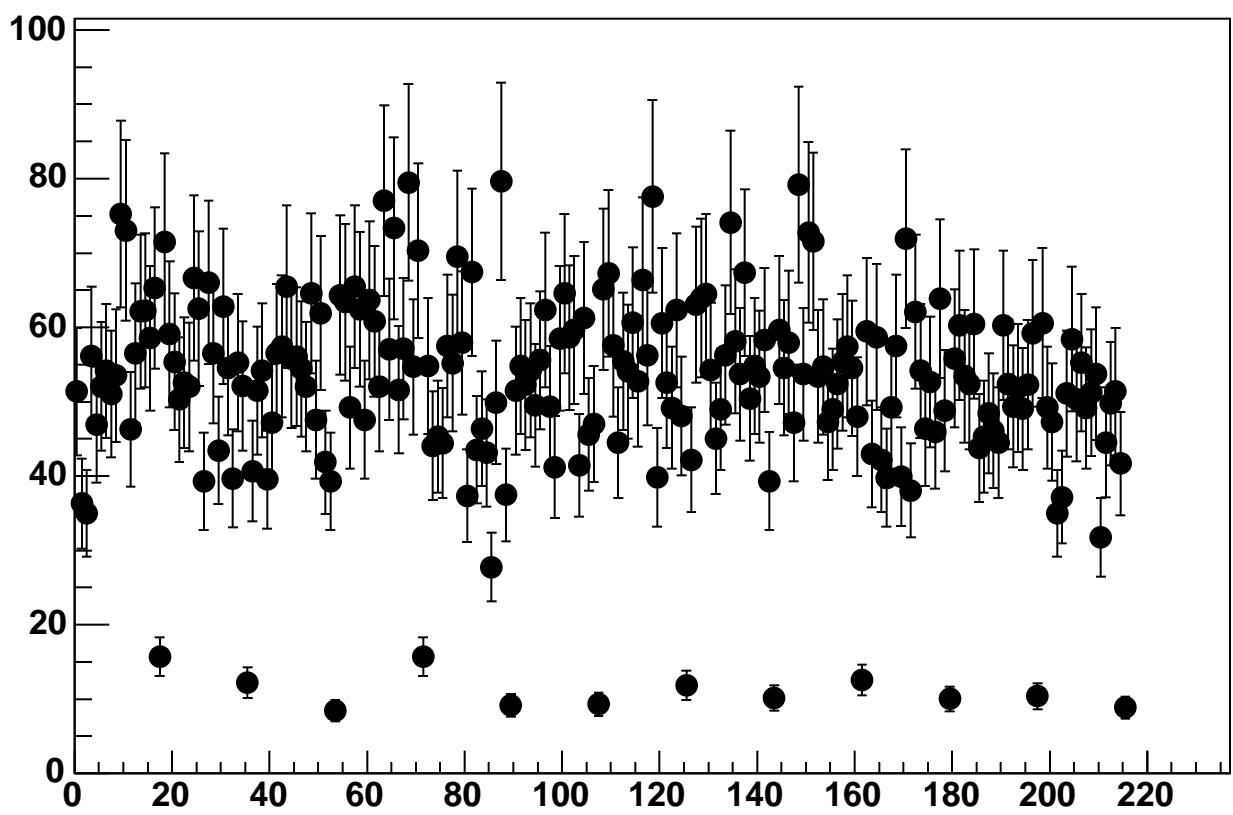
Enable 3, Hold=30, DAC=1100, ADC Noise vs 18\*Chip+Chan



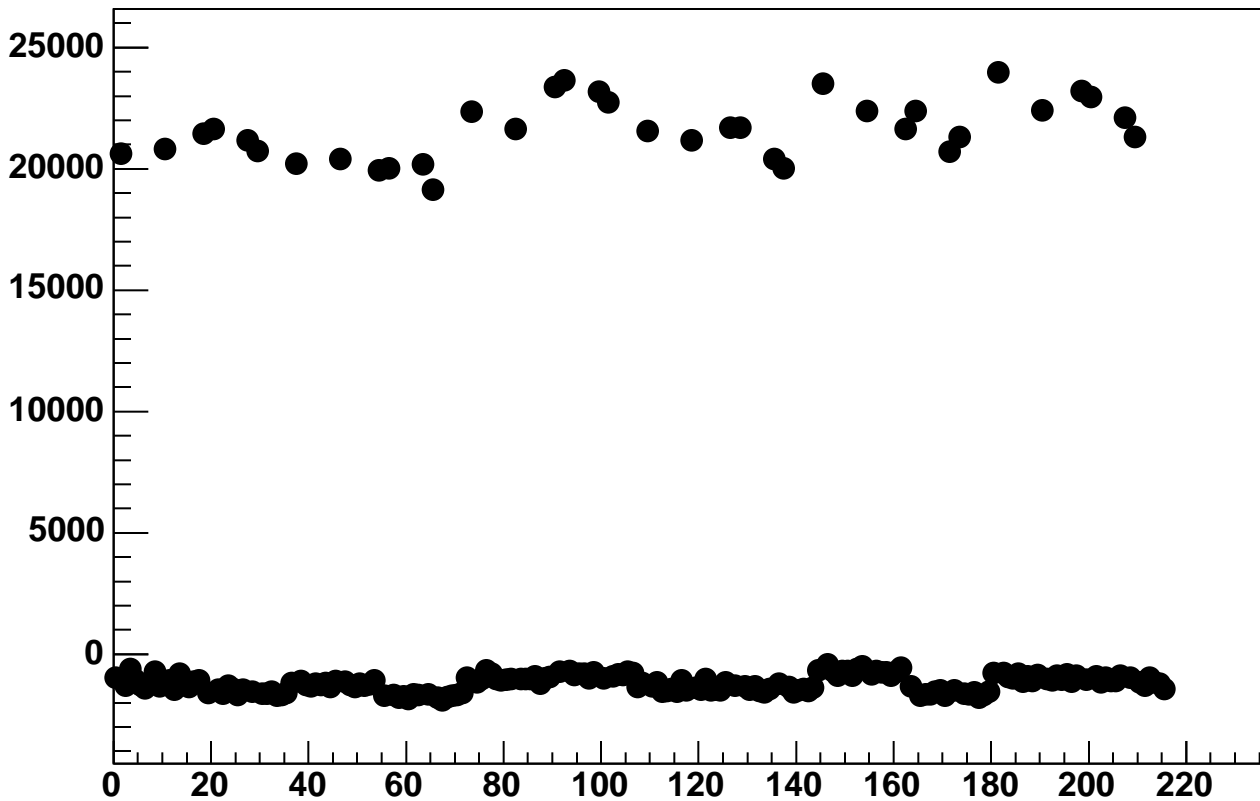
Enable 3, Hold=30, DAC=1150, ADC Mean vs 18\*Chip+Chan



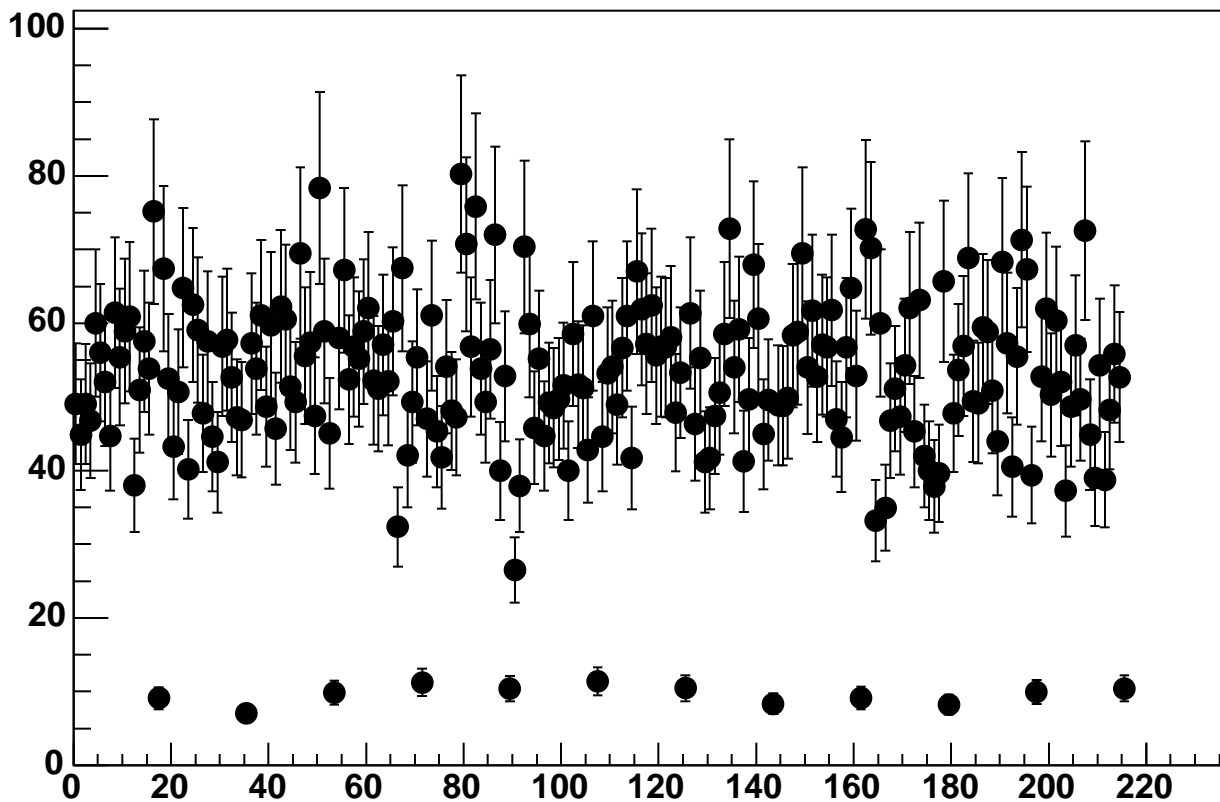
Enable 3, Hold=30, DAC=1150, ADC Noise vs 18\*Chip+Chan



Enable 3, Hold=30, DAC=1200, ADC Mean vs 18\*Chip+Chan

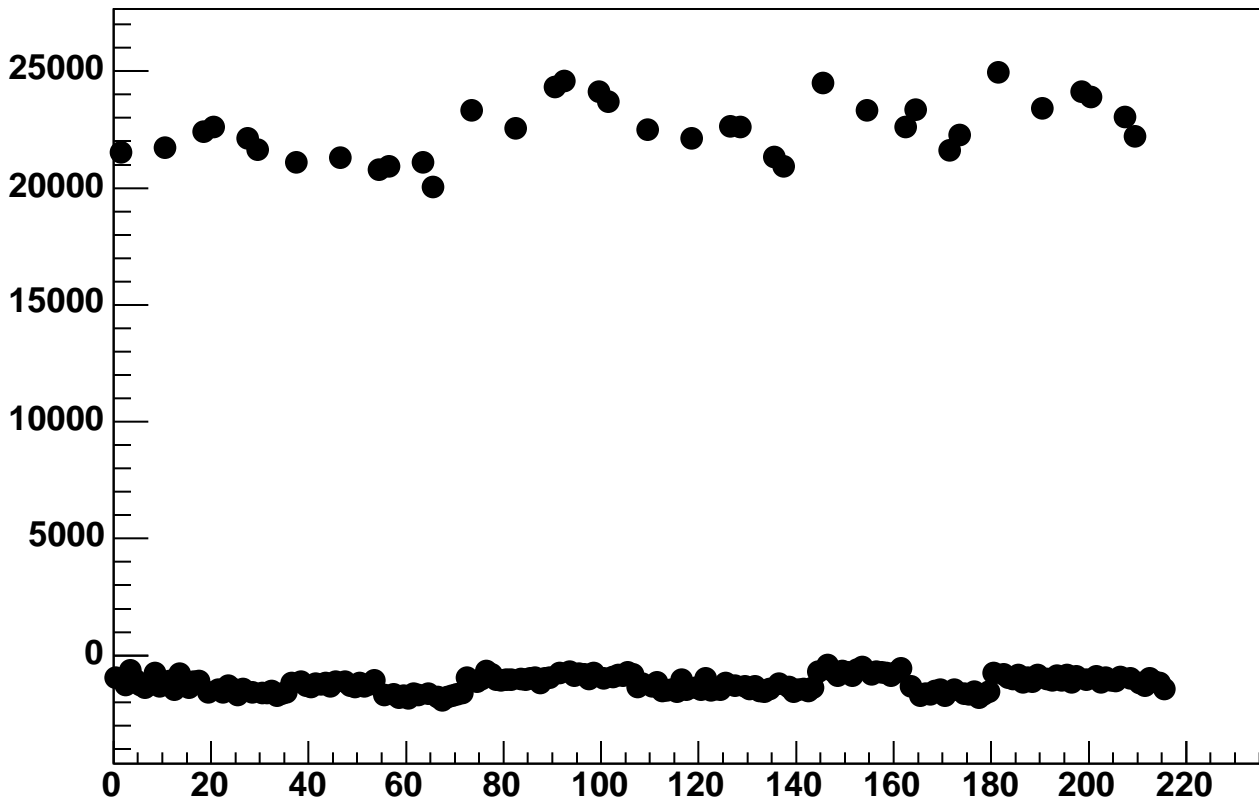


Enable 3, Hold=30, DAC=1200, ADC Noise vs 18\*Chip+Chan

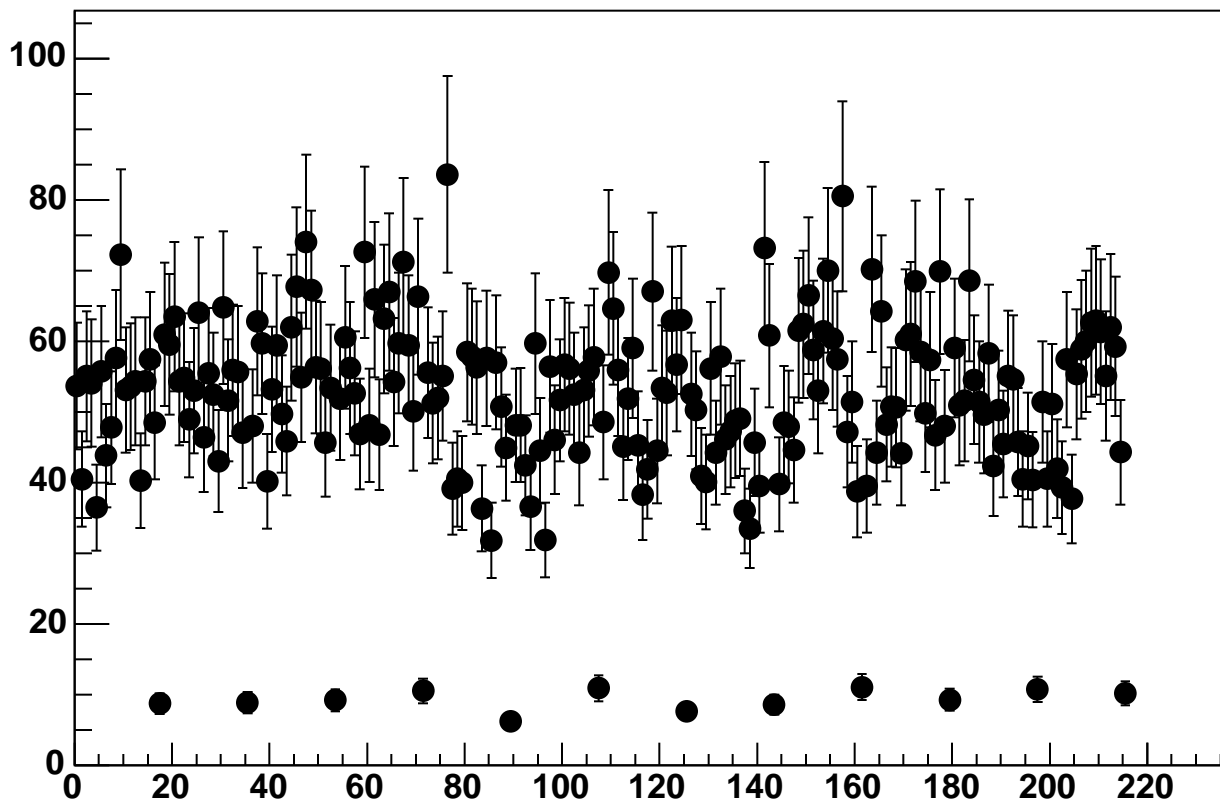




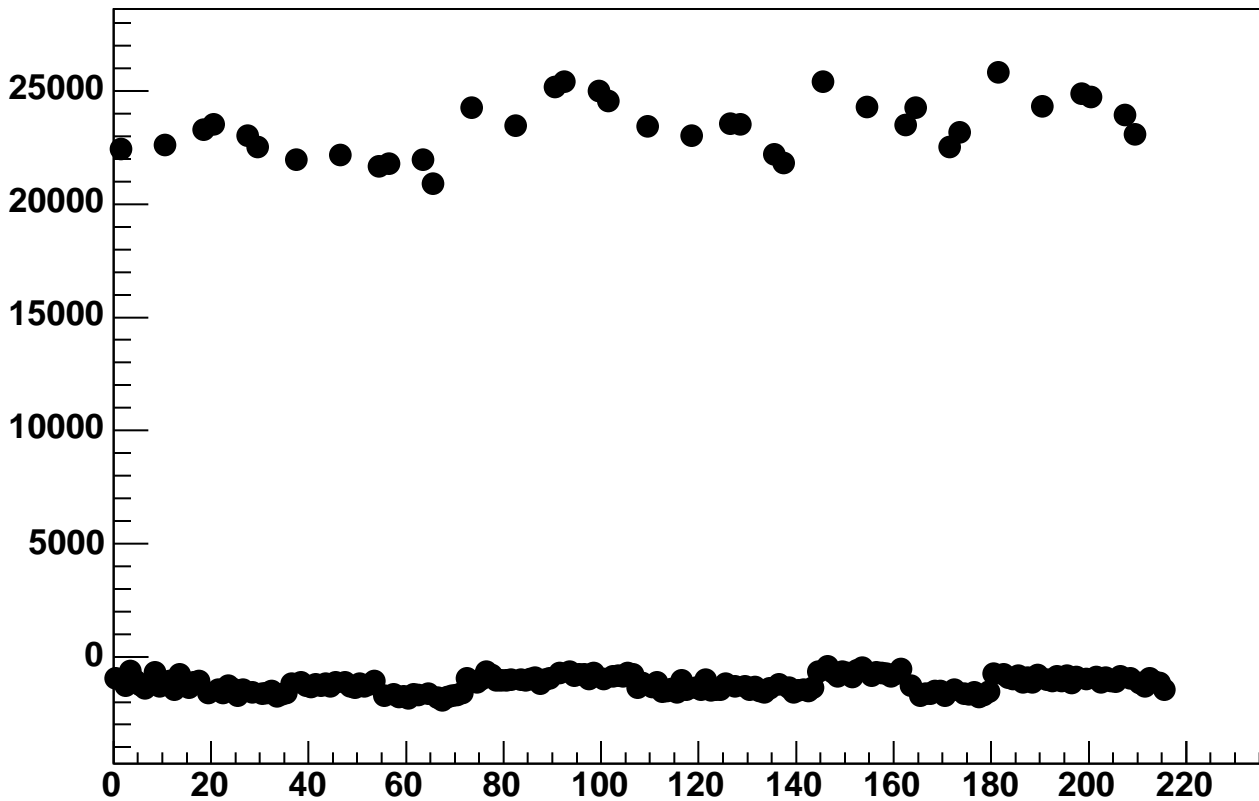
Enable 3, Hold=30, DAC=1250, ADC Mean vs 18\*Chip+Chan



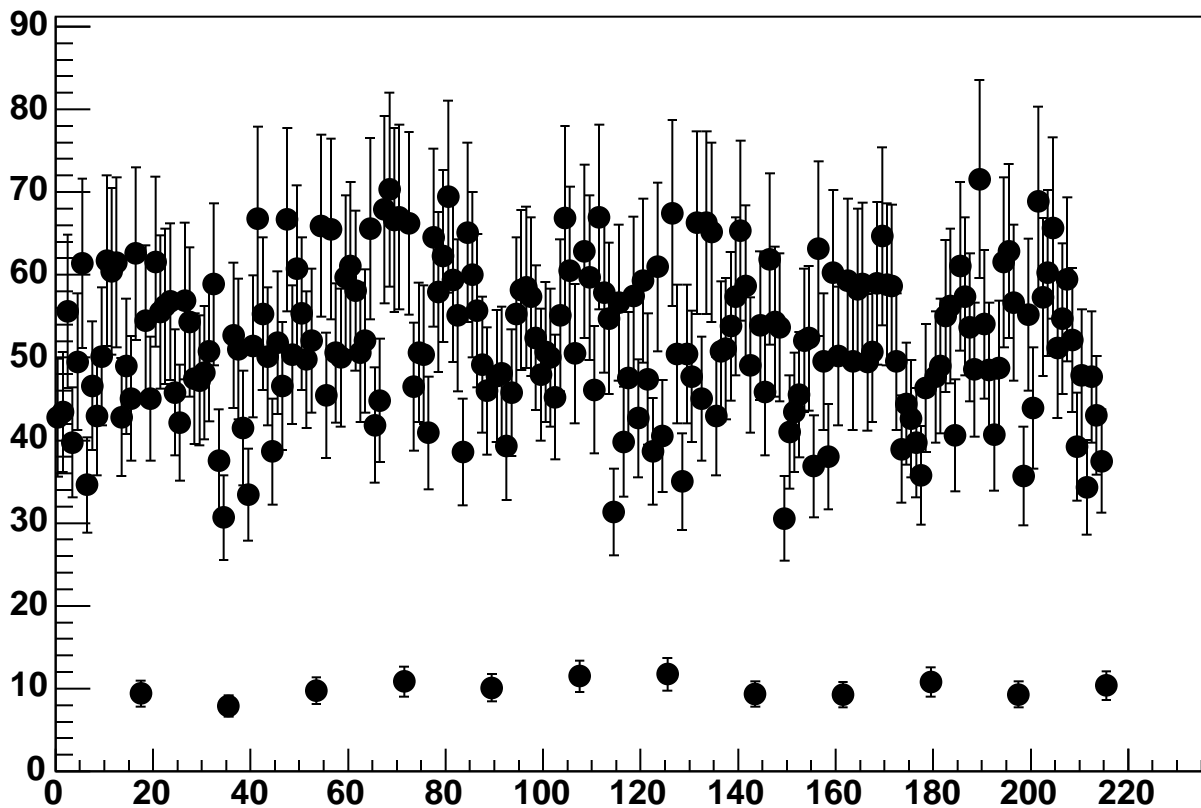
Enable 3, Hold=30, DAC=1250, ADC Noise vs 18\*Chip+Chan



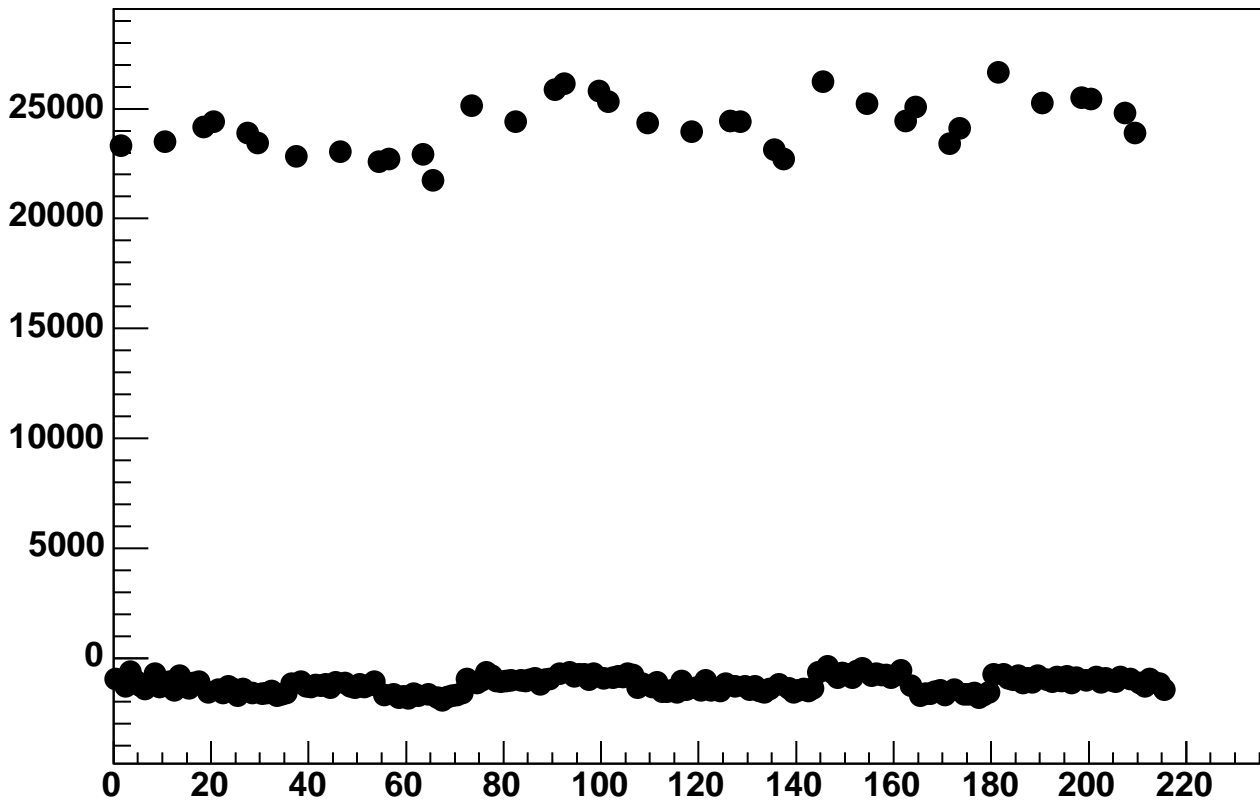
Enable 3, Hold=30, DAC=1300, ADC Mean vs 18\*Chip+Chan



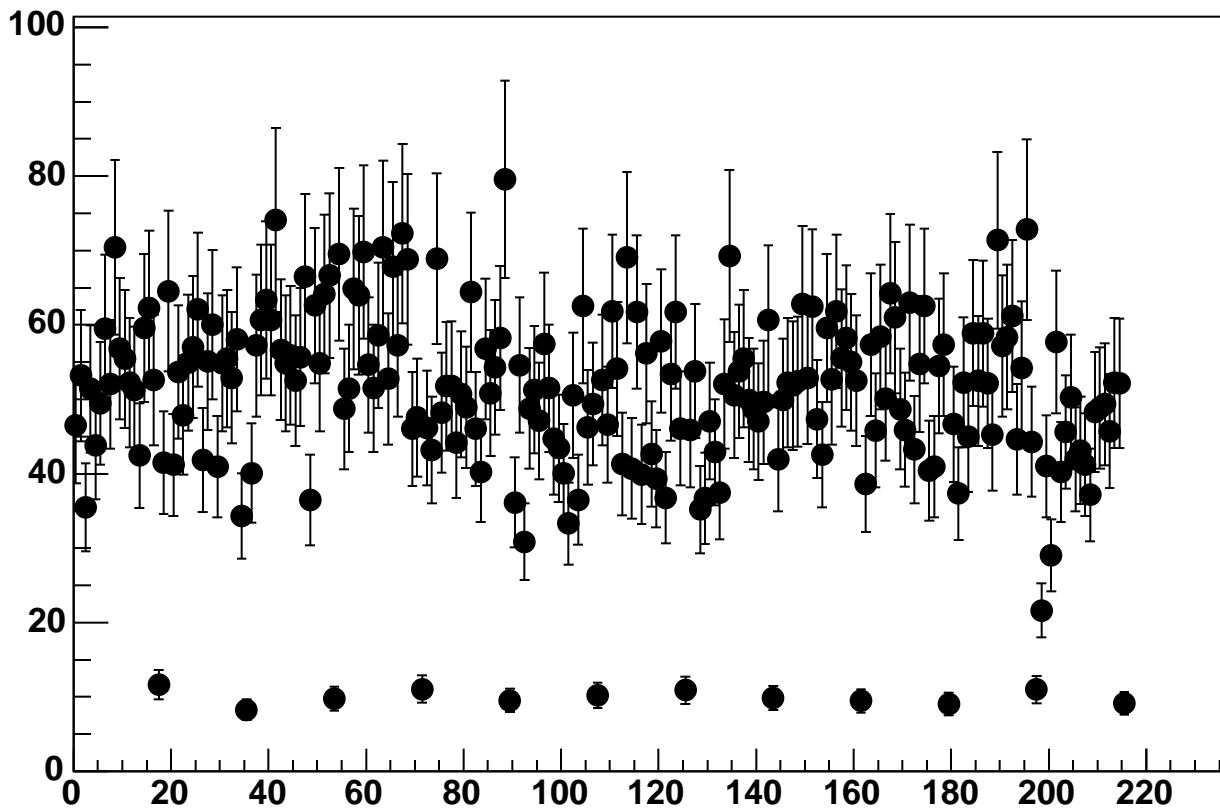
Enable 3, Hold=30, DAC=1300, ADC Noise vs 18\*Chip+Chan



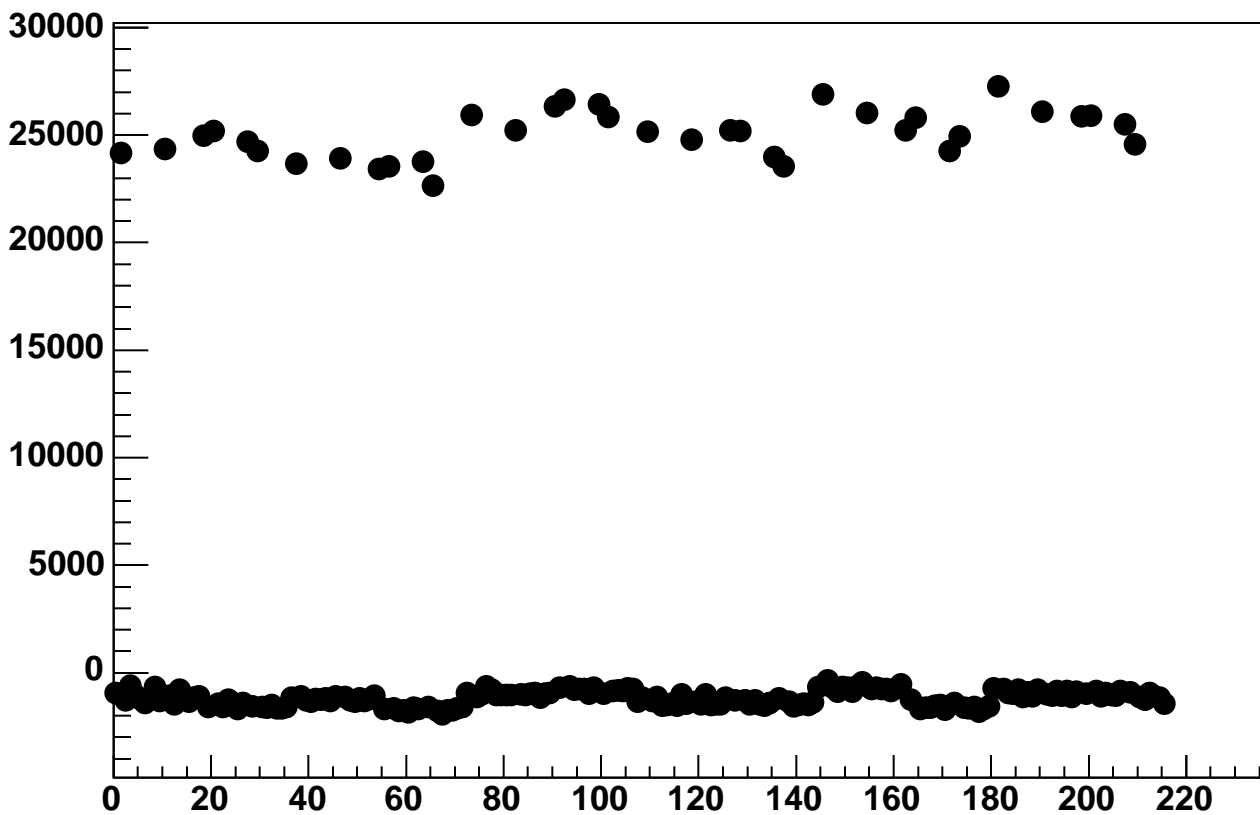
Enable 3, Hold=30, DAC=1350, ADC Mean vs 18\*Chip+Chan



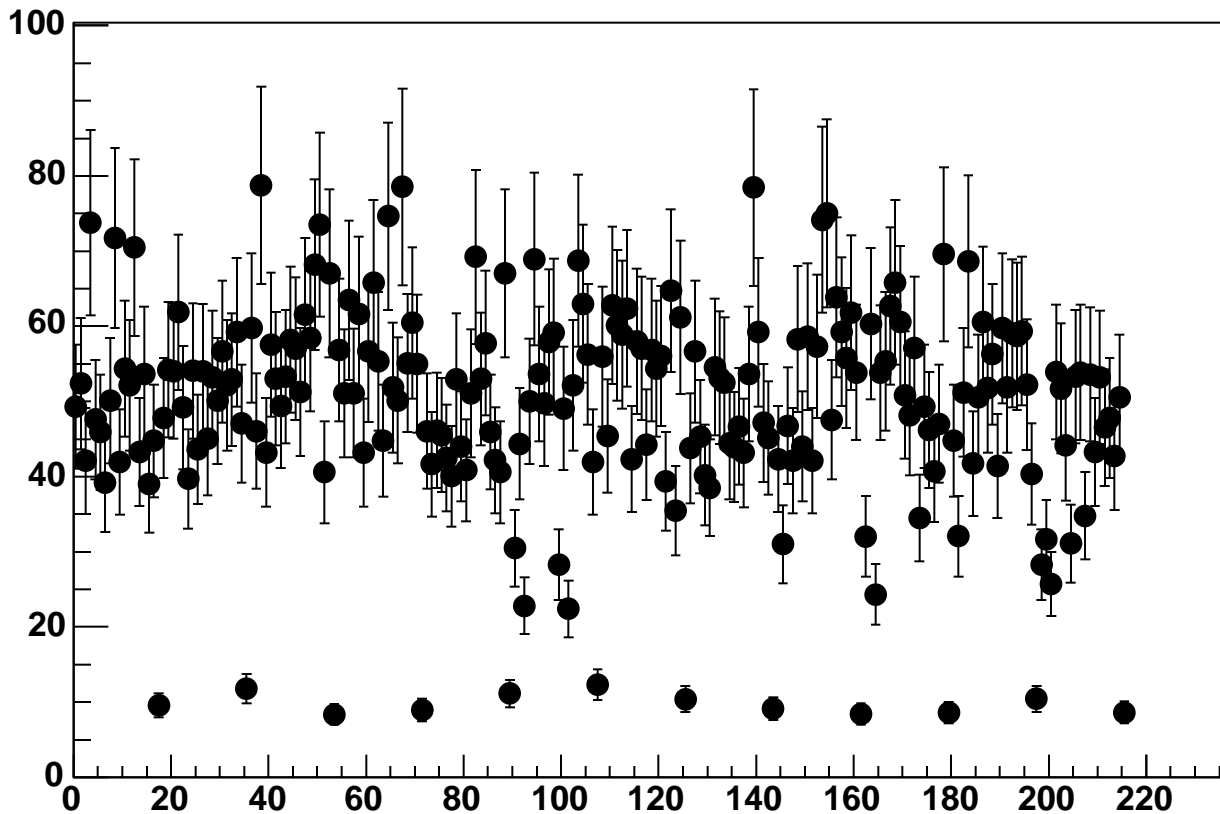
Enable 3, Hold=30, DAC=1350, ADC Noise vs 18\*Chip+Chan



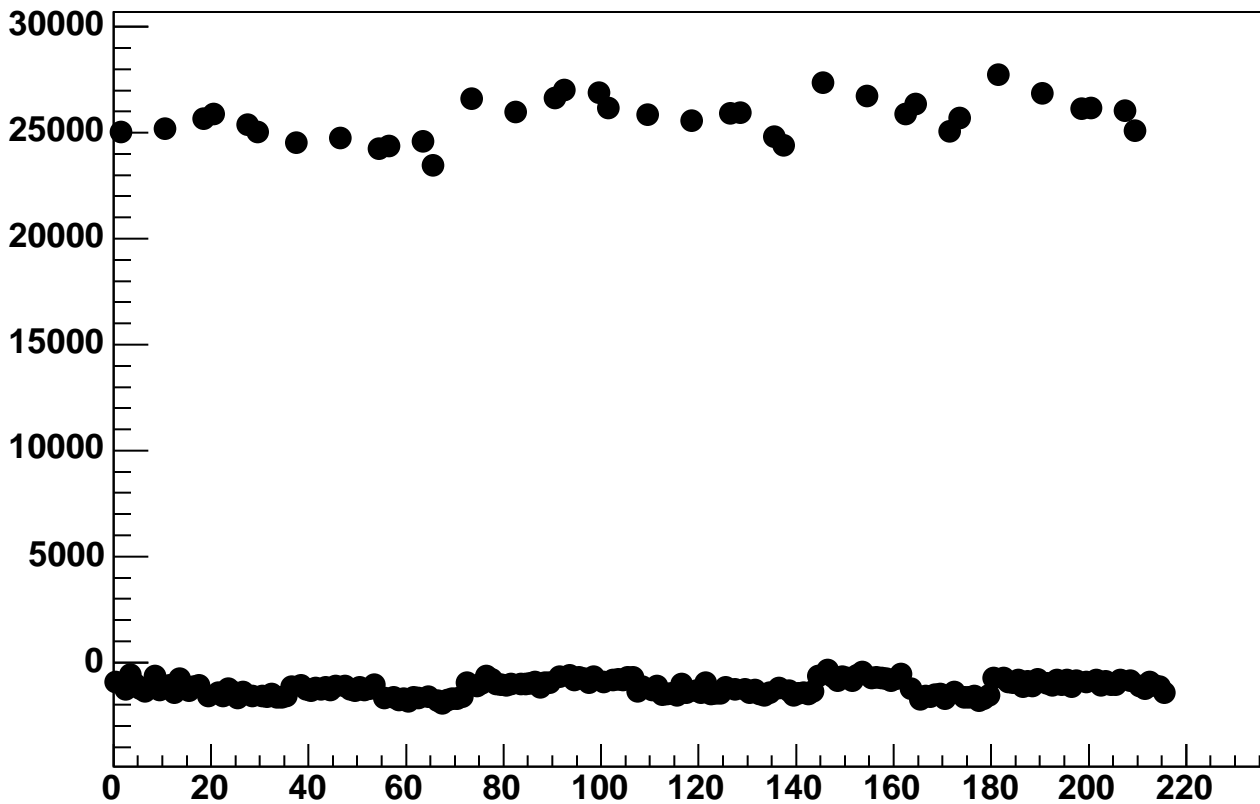
Enable 3, Hold=30, DAC=1400, ADC Mean vs 18\*Chip+Chan



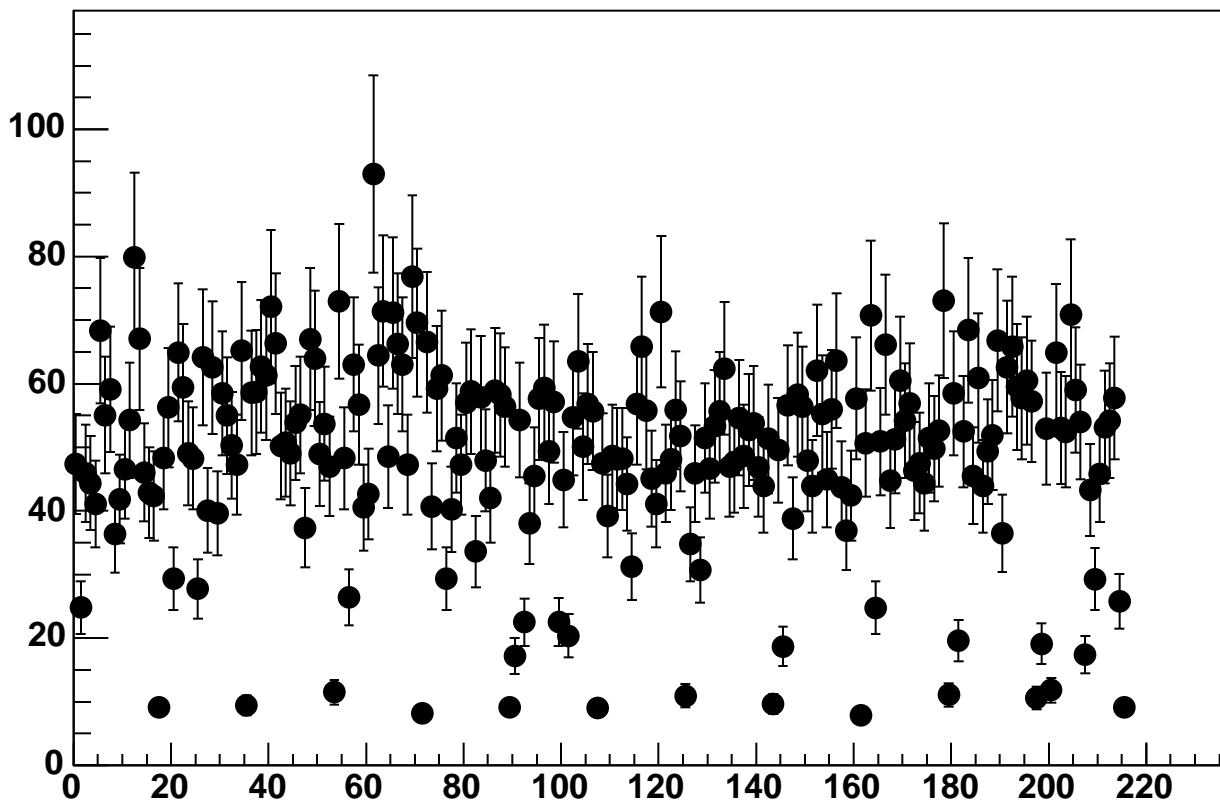
Enable 3, Hold=30, DAC=1400, ADC Noise vs 18\*Chip+Chan



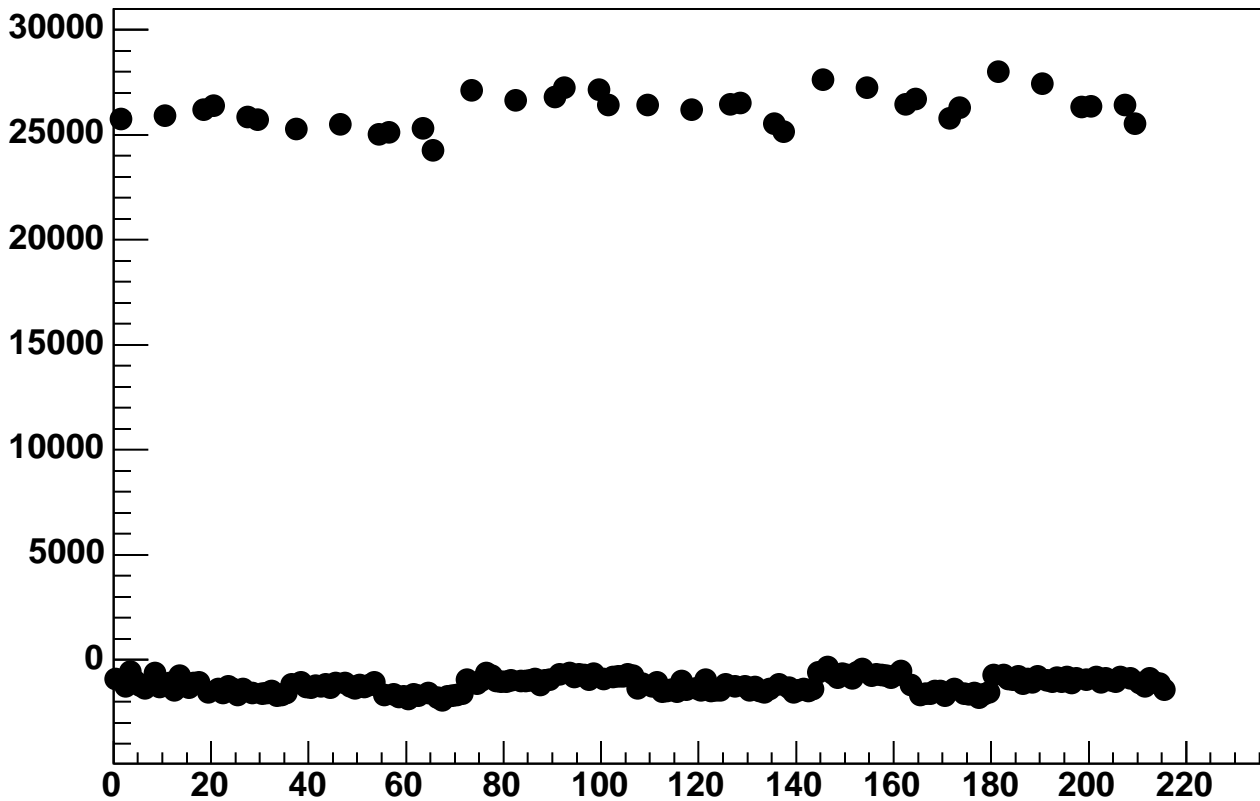
Enable 3, Hold=30, DAC=1450, ADC Mean vs 18\*Chip+Chan



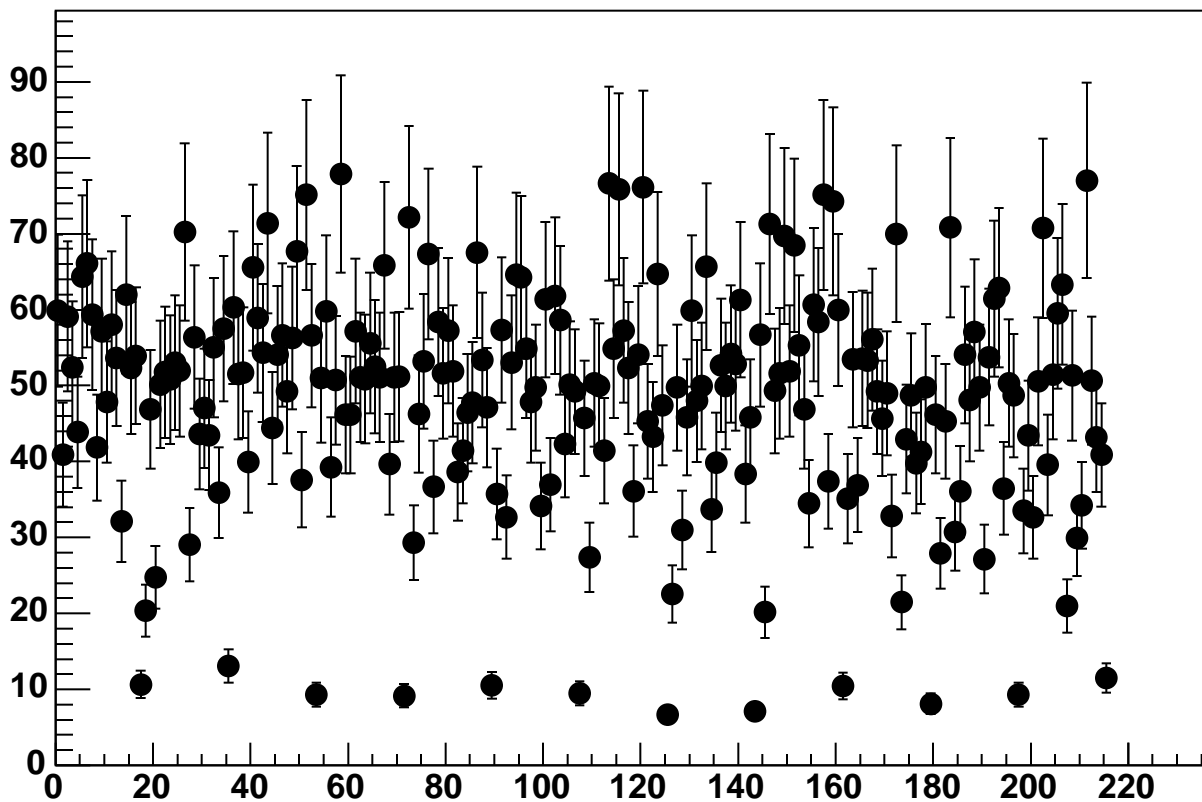
Enable 3, Hold=30, DAC=1450, ADC Noise vs 18\*Chip+Chan



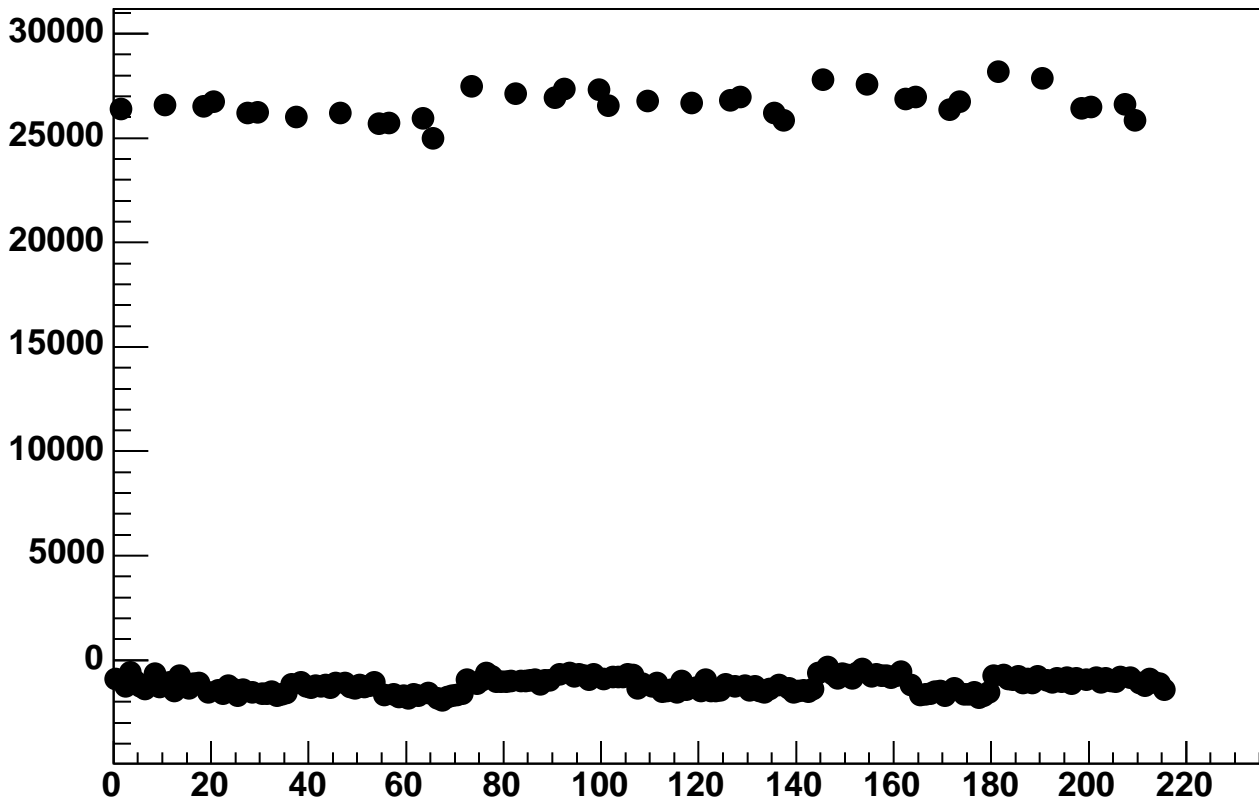
Enable 3, Hold=30, DAC=1500, ADC Mean vs 18\*Chip+Chan



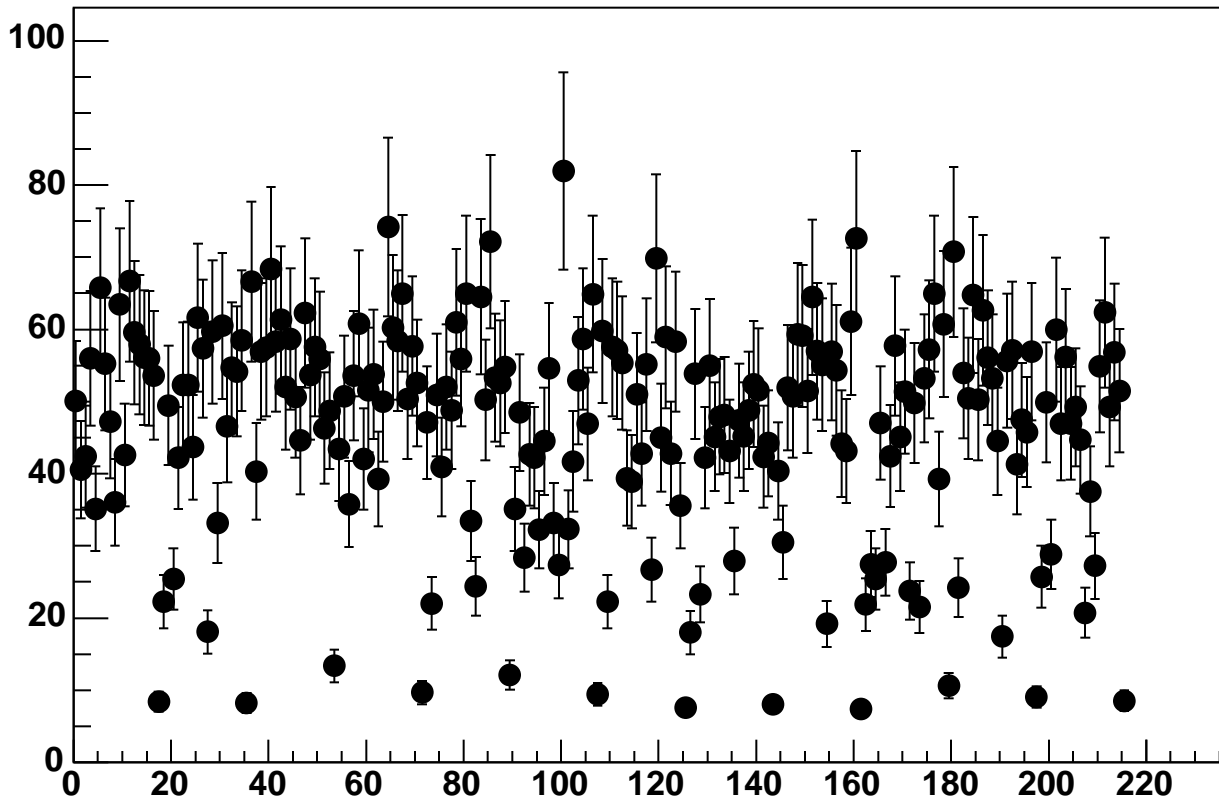
Enable 3, Hold=30, DAC=1500, ADC Noise vs 18\*Chip+Chan



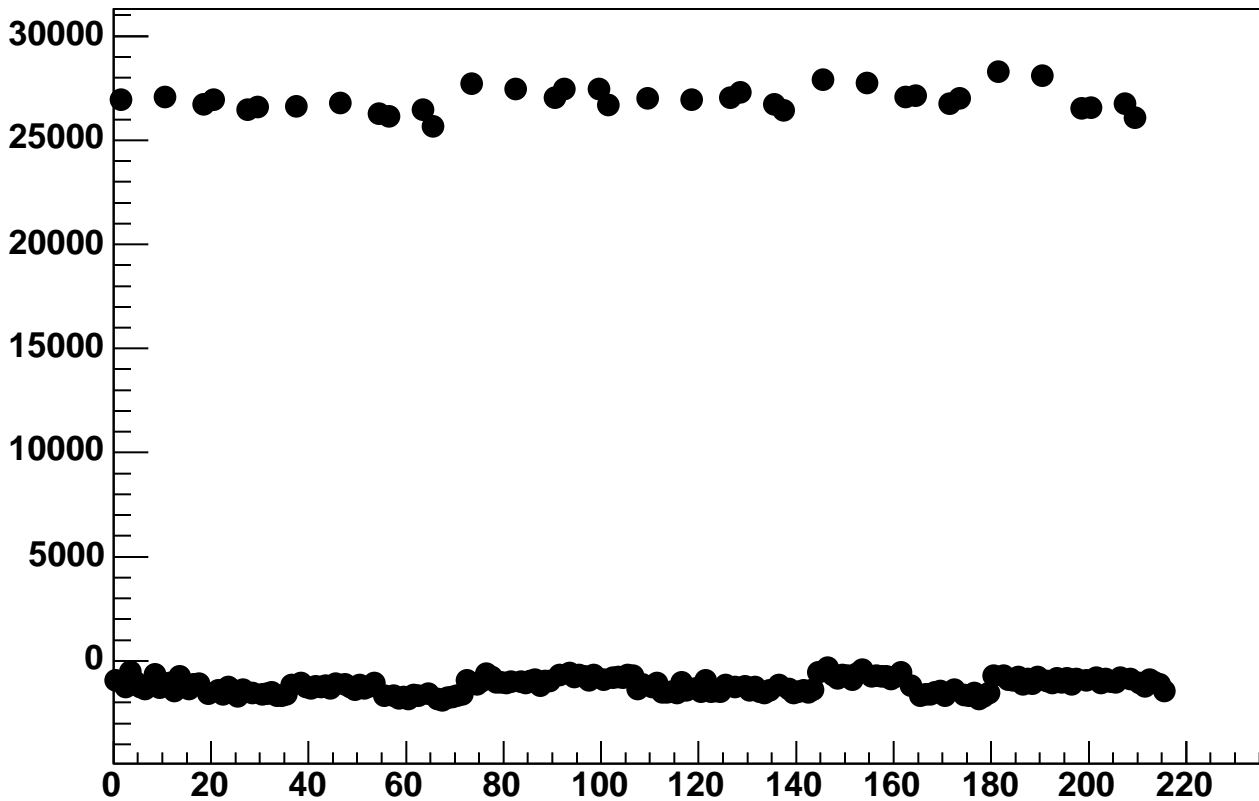
Enable 3, Hold=30, DAC=1550, ADC Mean vs 18\*Chip+Chan



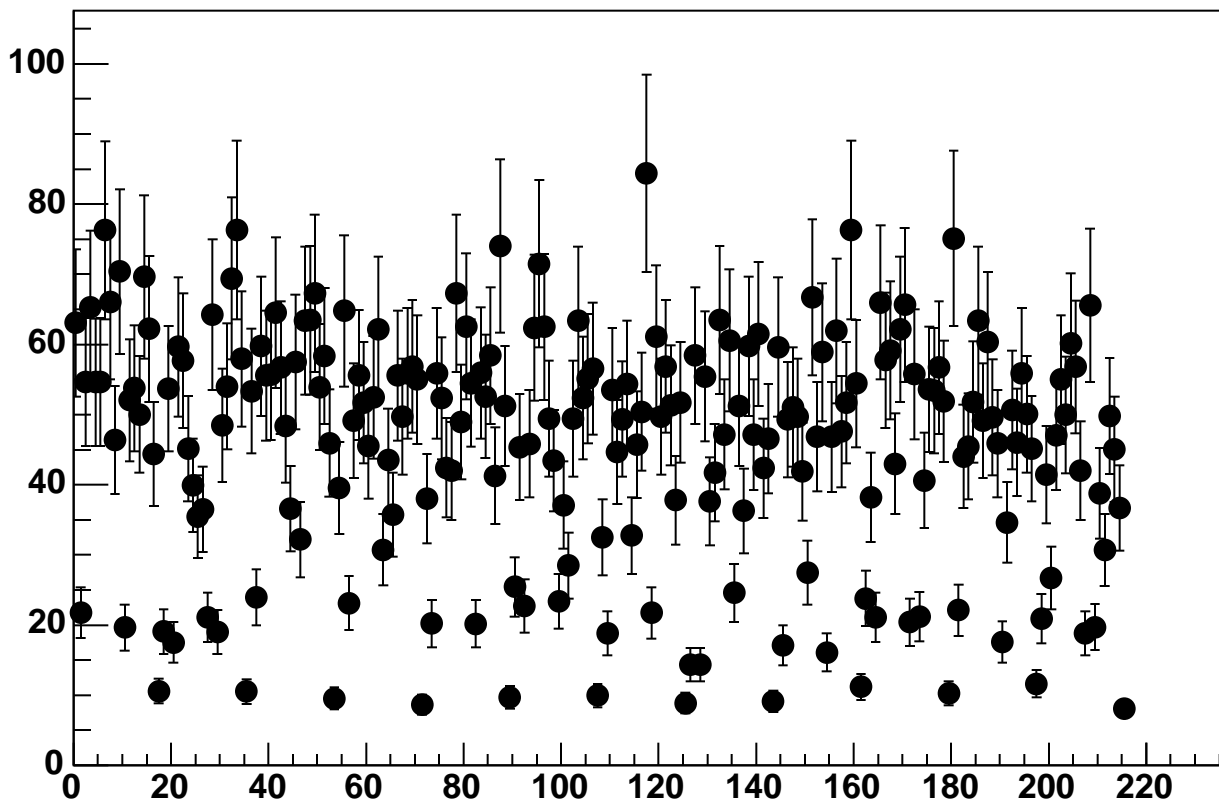
Enable 3, Hold=30, DAC=1550, ADC Noise vs 18\*Chip+Chan



Enable 3, Hold=30, DAC=1600, ADC Mean vs 18\*Chip+Chan

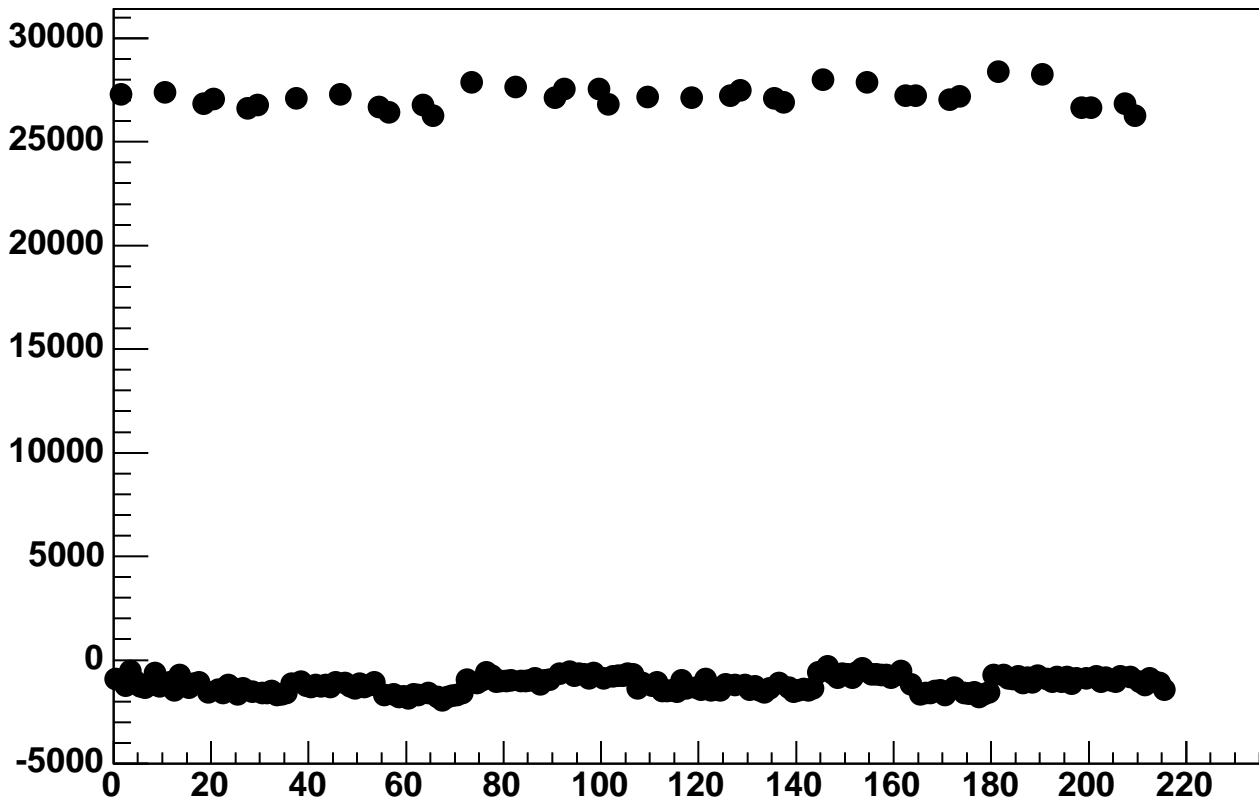


Enable 3, Hold=30, DAC=1600, ADC Noise vs 18\*Chip+Chan

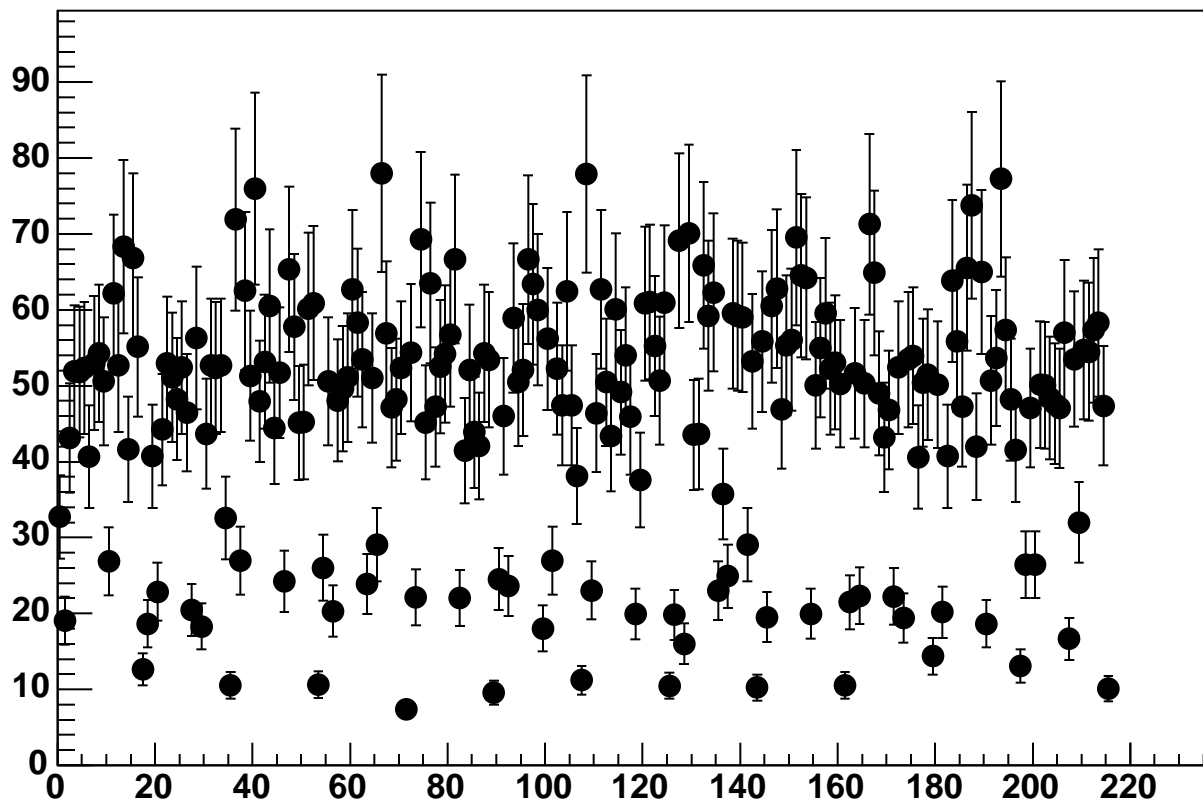




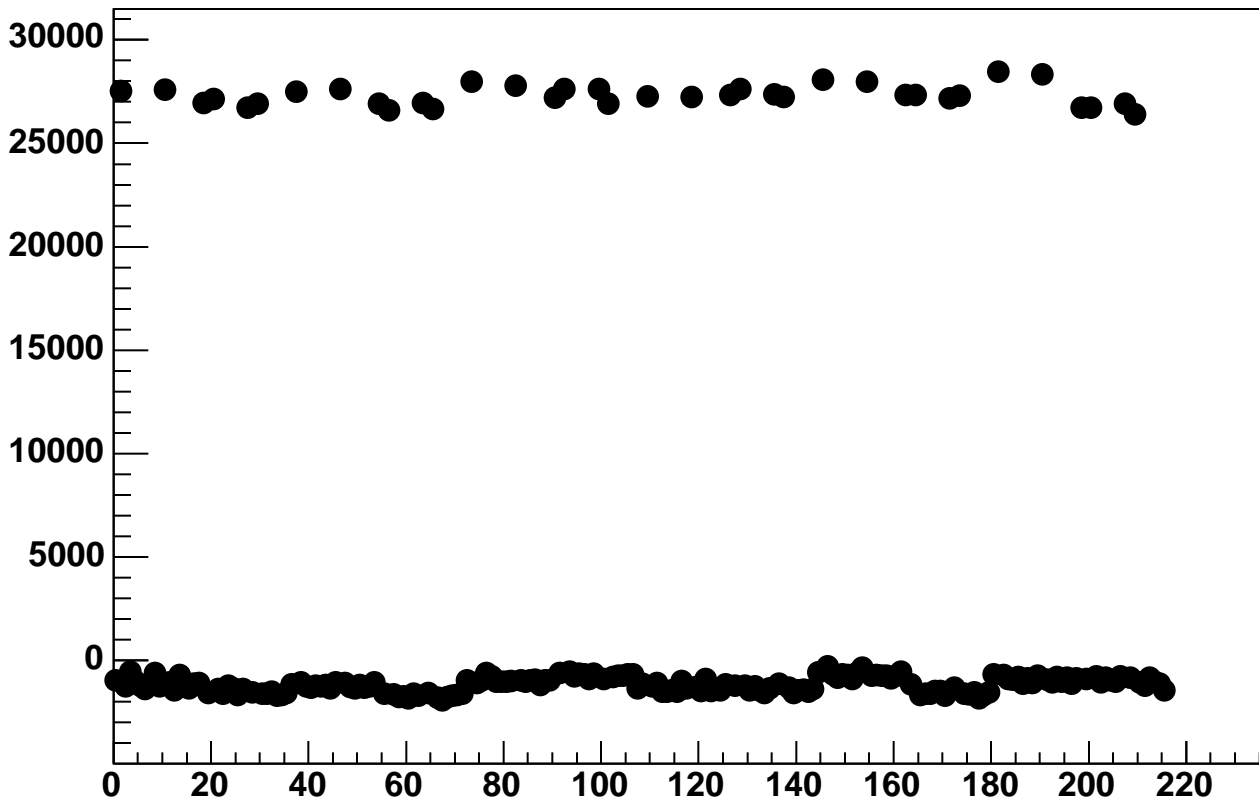
Enable 3, Hold=30, DAC=1650, ADC Mean vs 18\*Chip+Chan



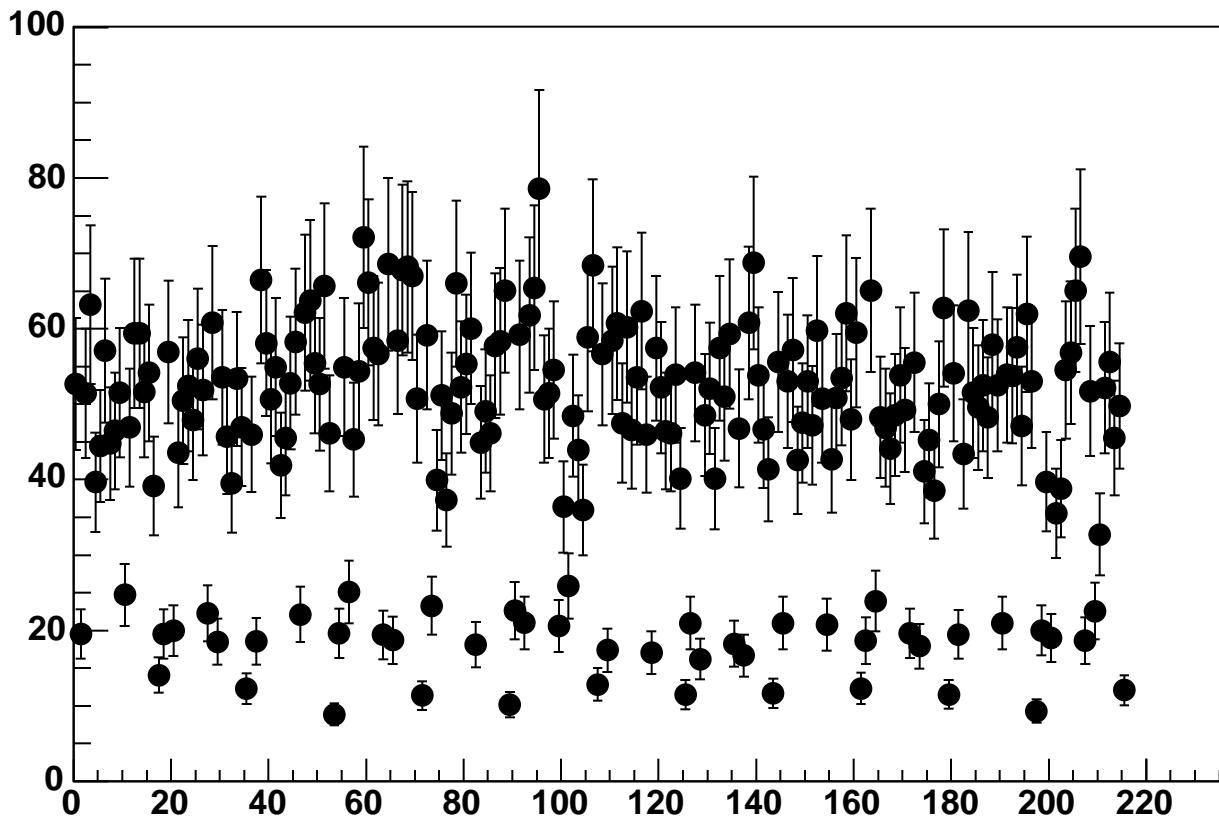
Enable 3, Hold=30, DAC=1650, ADC Noise vs 18\*Chip+Chan



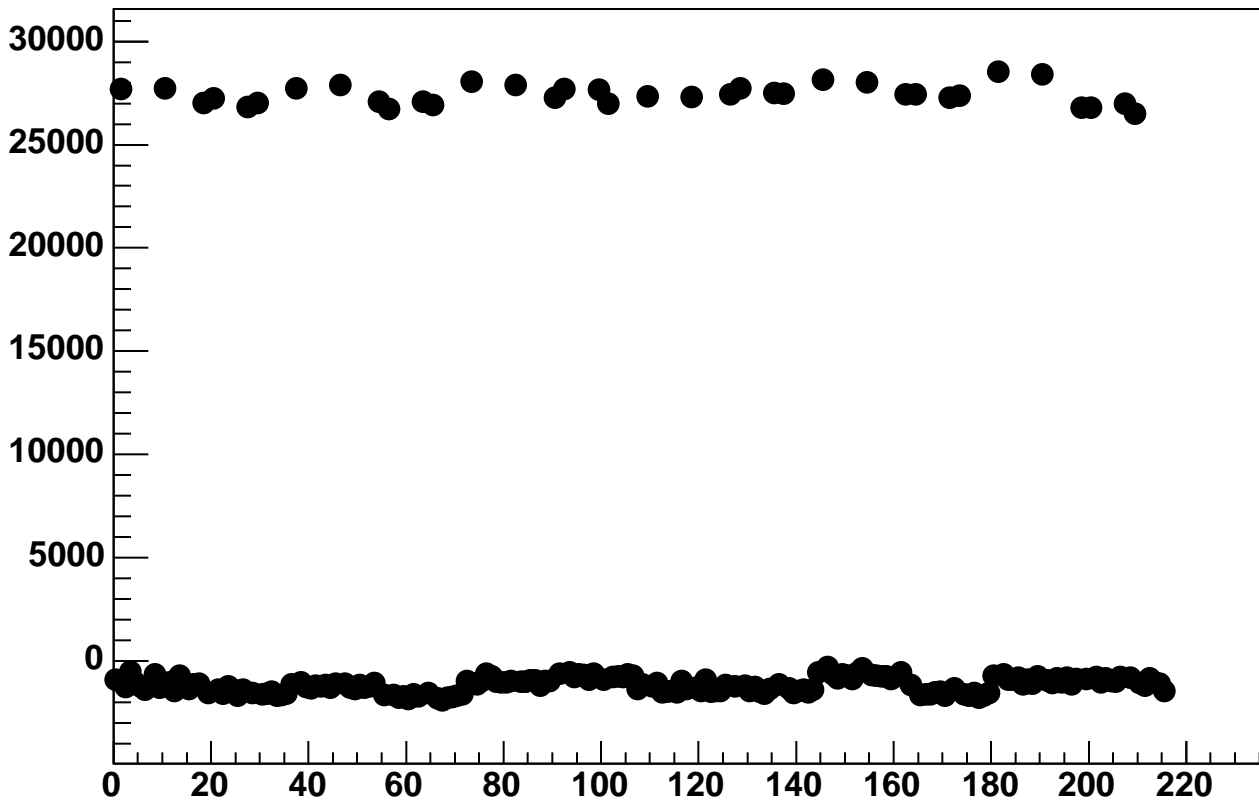
Enable 3, Hold=30, DAC=1700, ADC Mean vs 18\*Chip+Chan



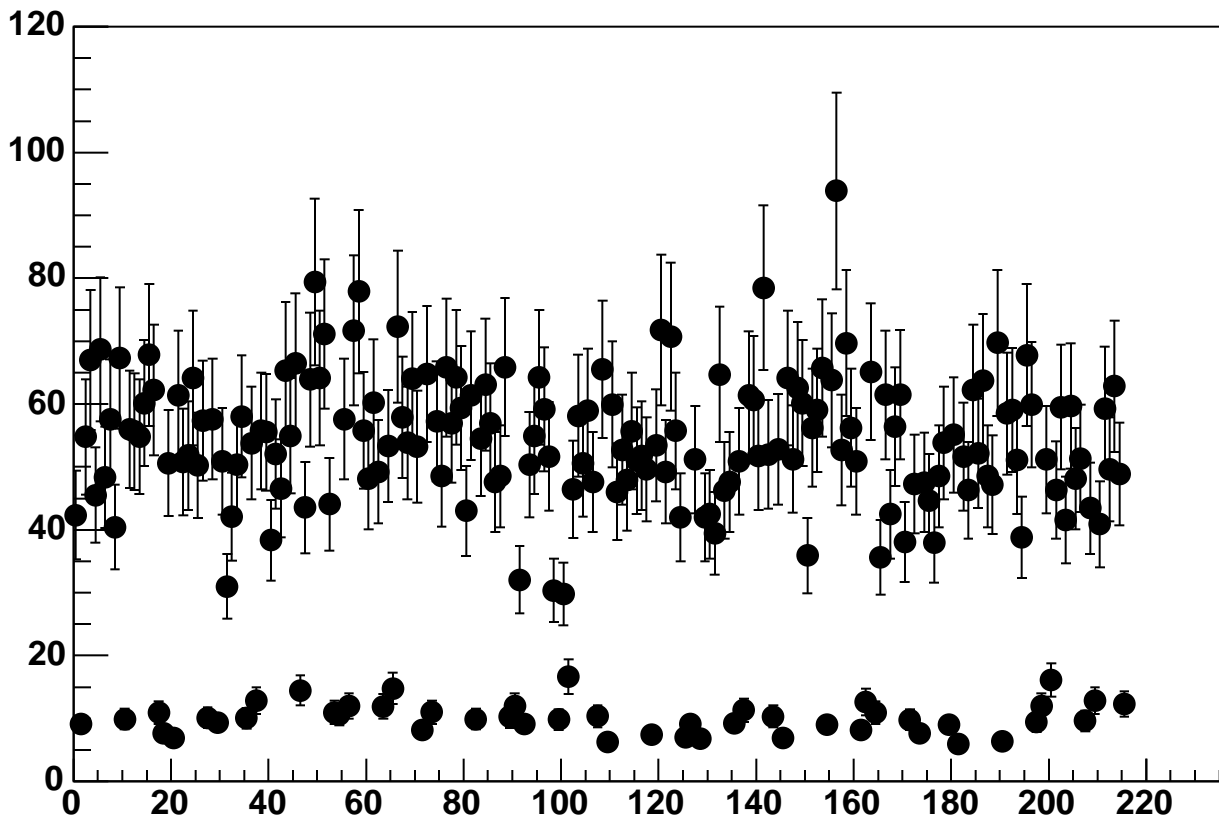
Enable 3, Hold=30, DAC=1700, ADC Noise vs 18\*Chip+Chan



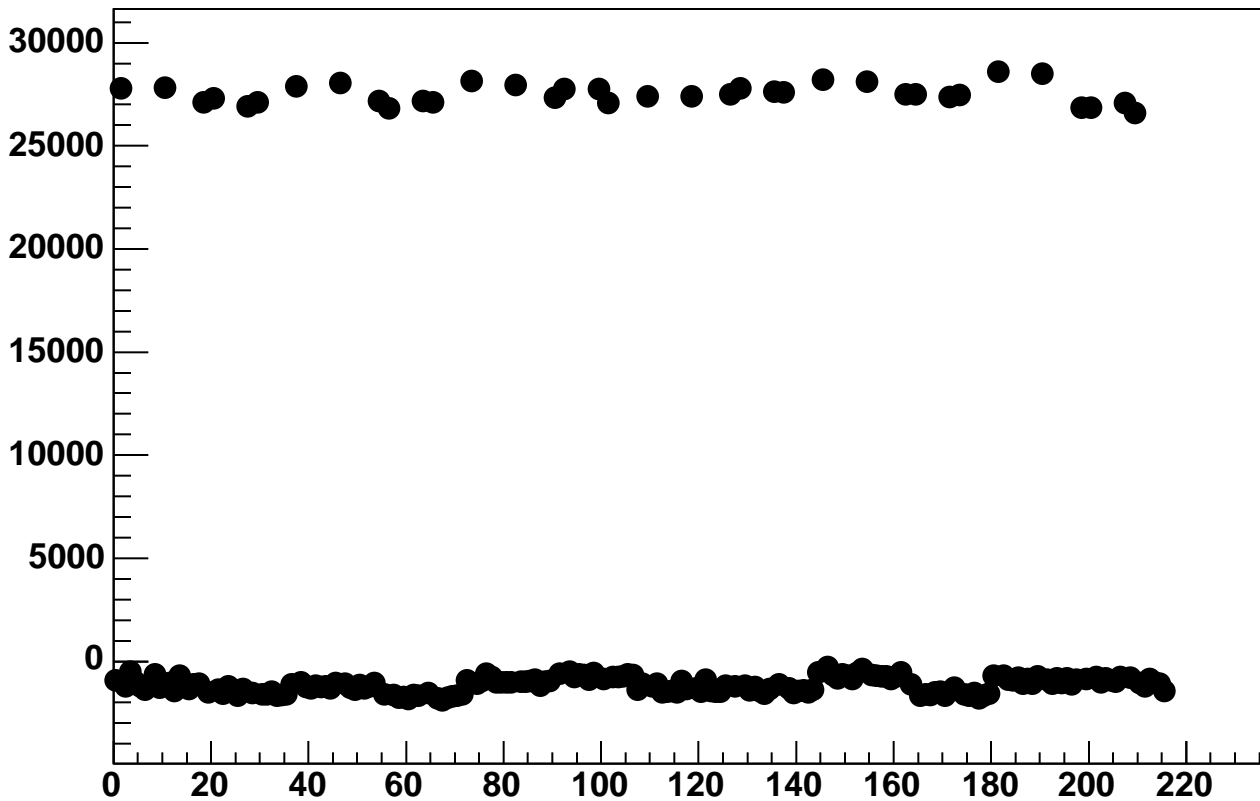
Enable 3, Hold=30, DAC=1750, ADC Mean vs 18\*Chip+Chan



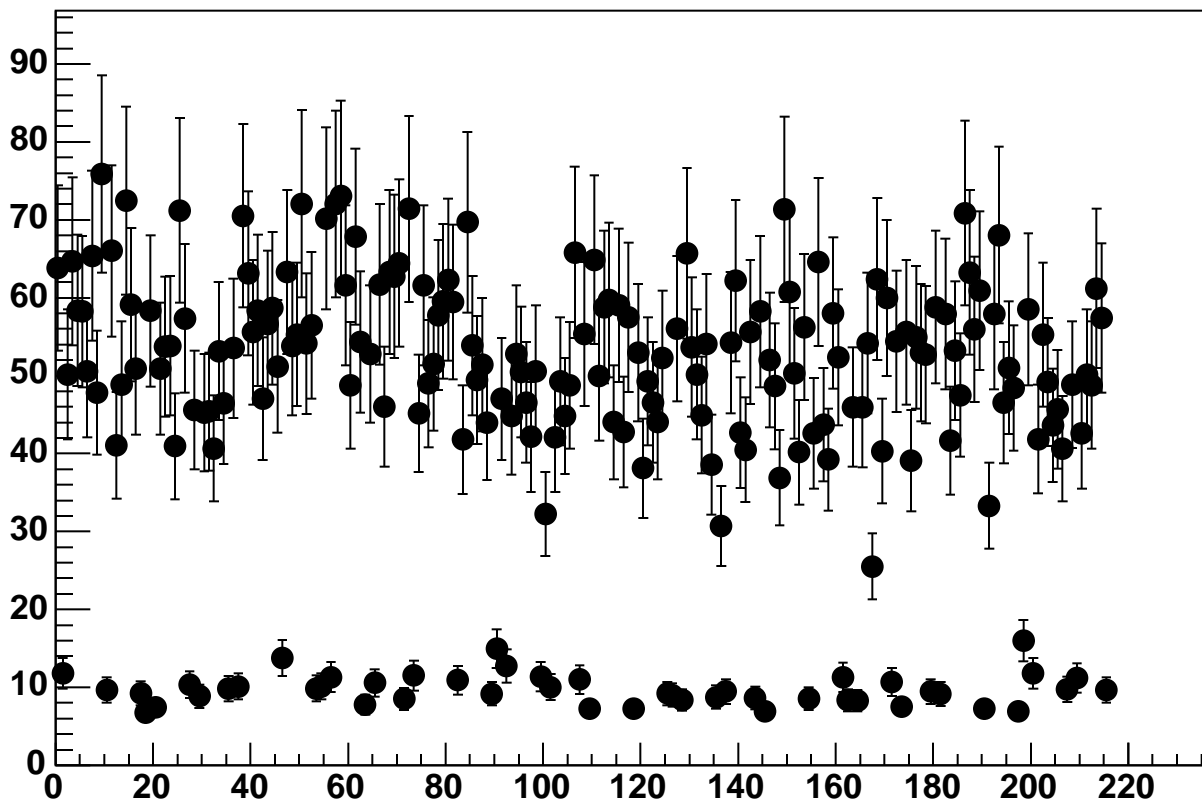
Enable 3, Hold=30, DAC=1750, ADC Noise vs 18\*Chip+Chan



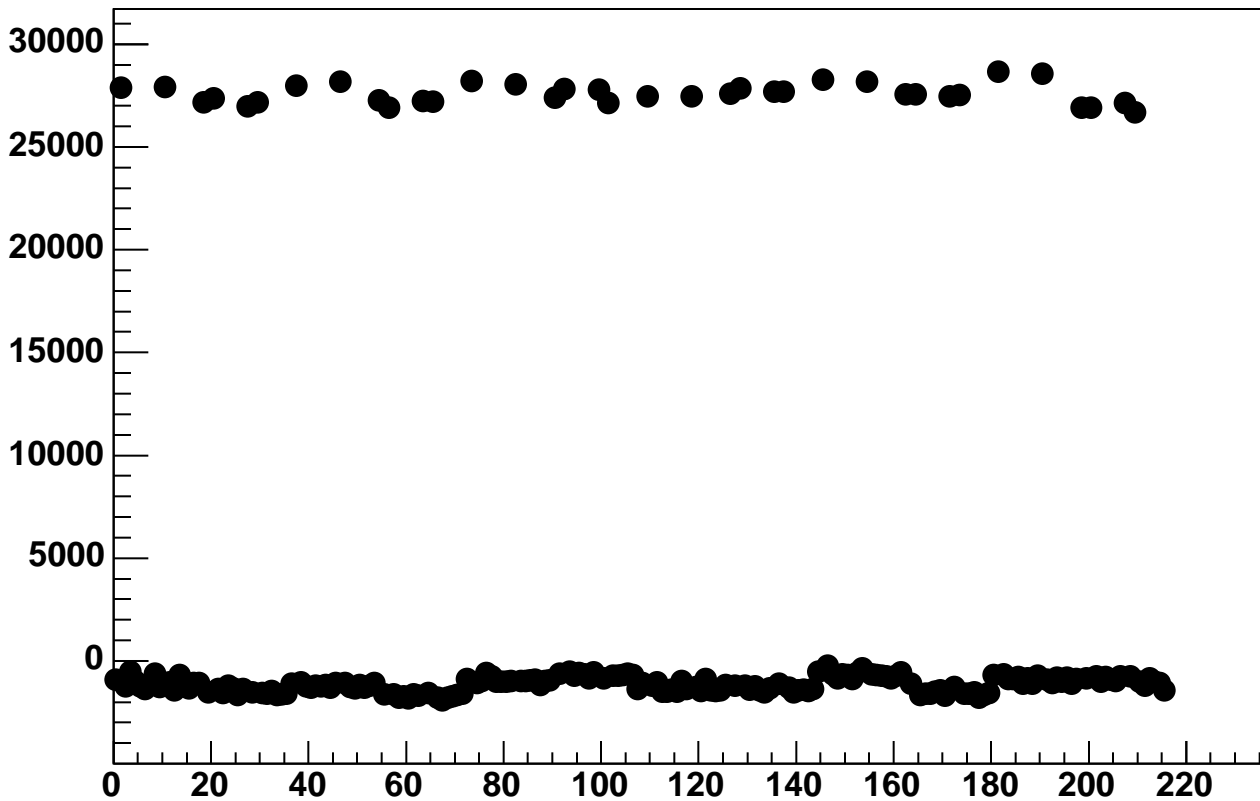
Enable 3, Hold=30, DAC=1800, ADC Mean vs 18\*Chip+Chan



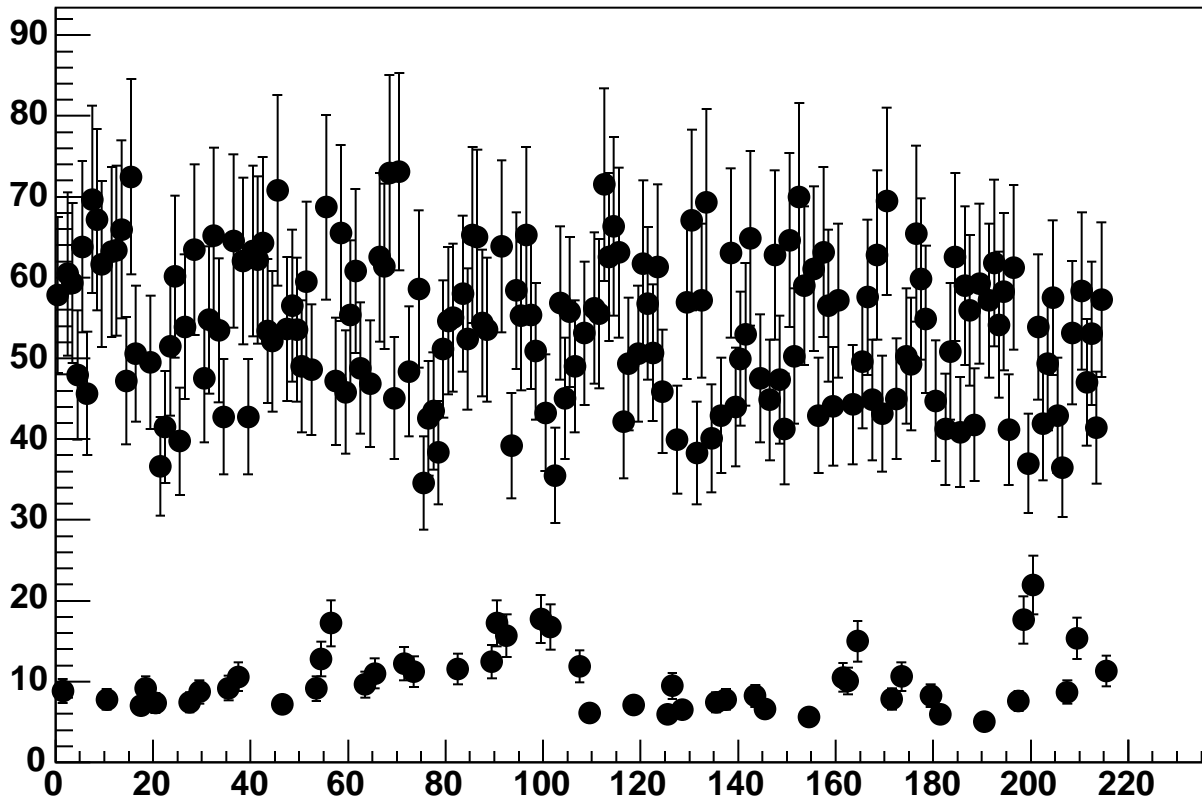
Enable 3, Hold=30, DAC=1800, ADC Noise vs 18\*Chip+Chan



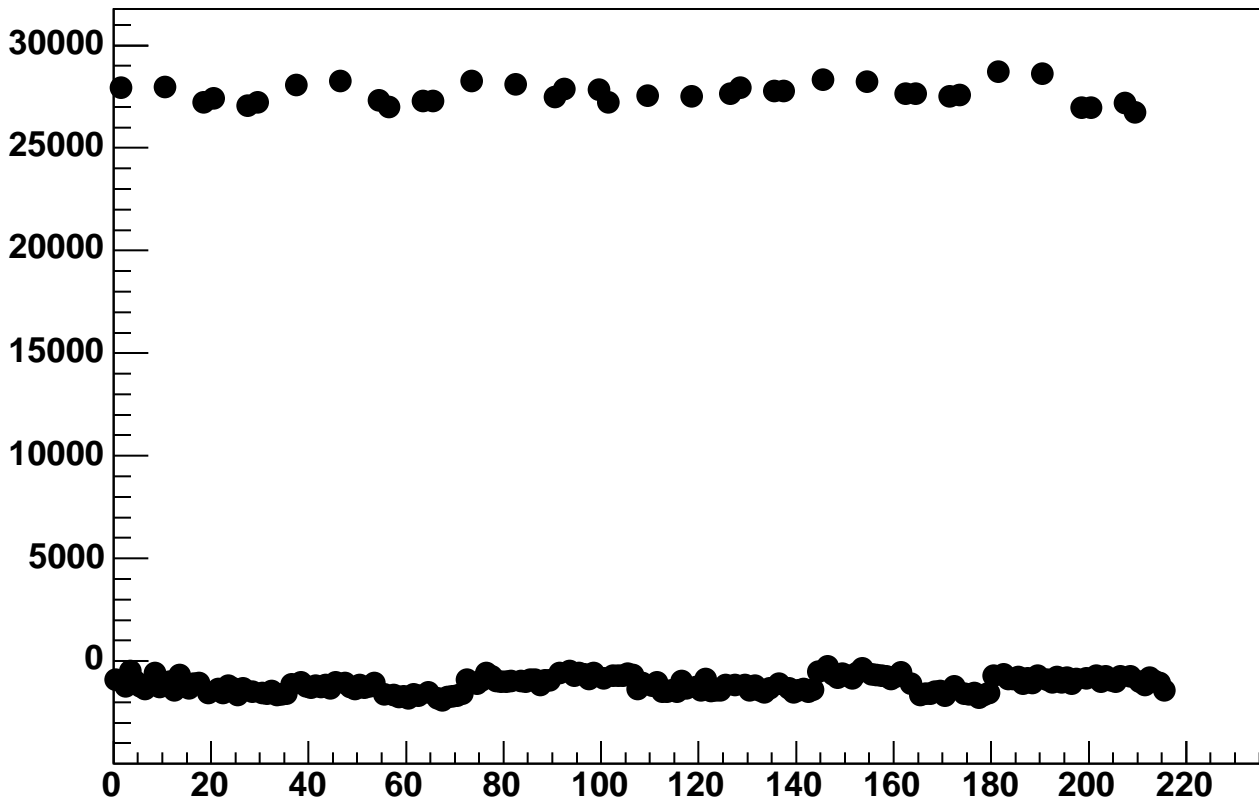
Enable 3, Hold=30, DAC=1850, ADC Mean vs 18\*Chip+Chan



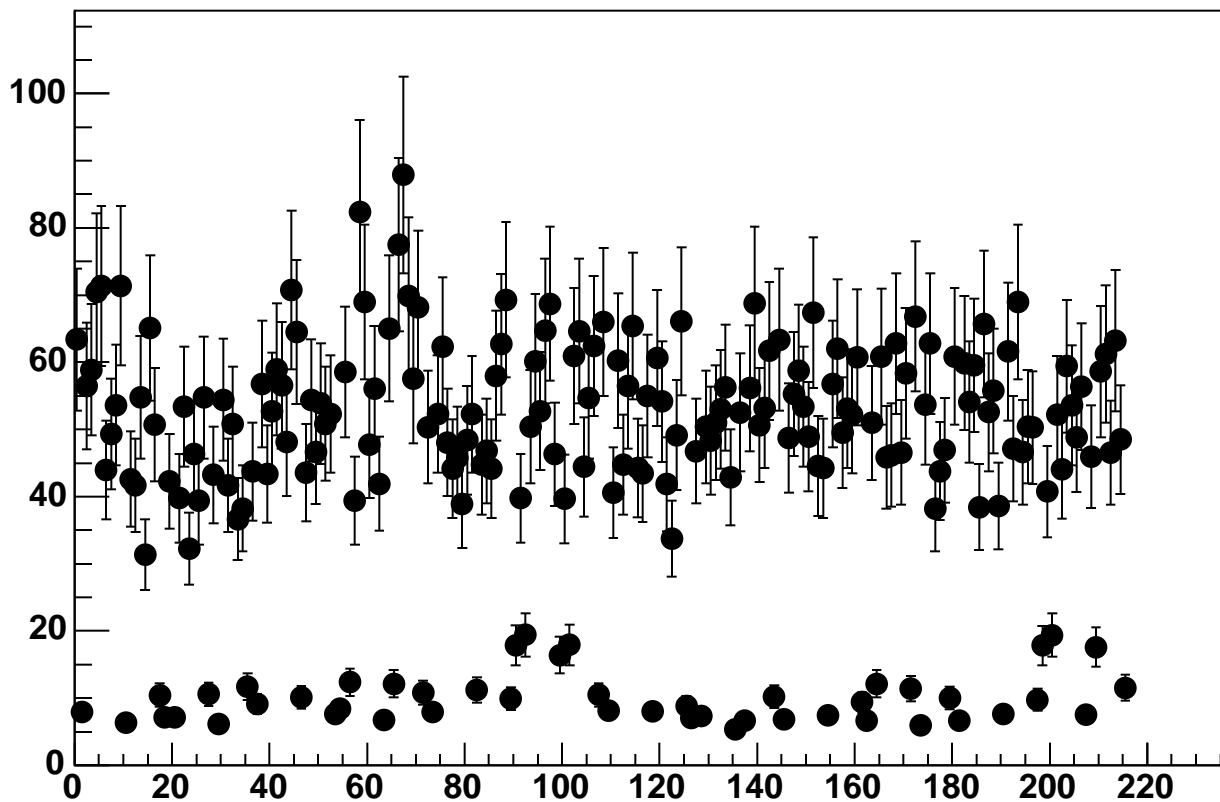
Enable 3, Hold=30, DAC=1850, ADC Noise vs 18\*Chip+Chan



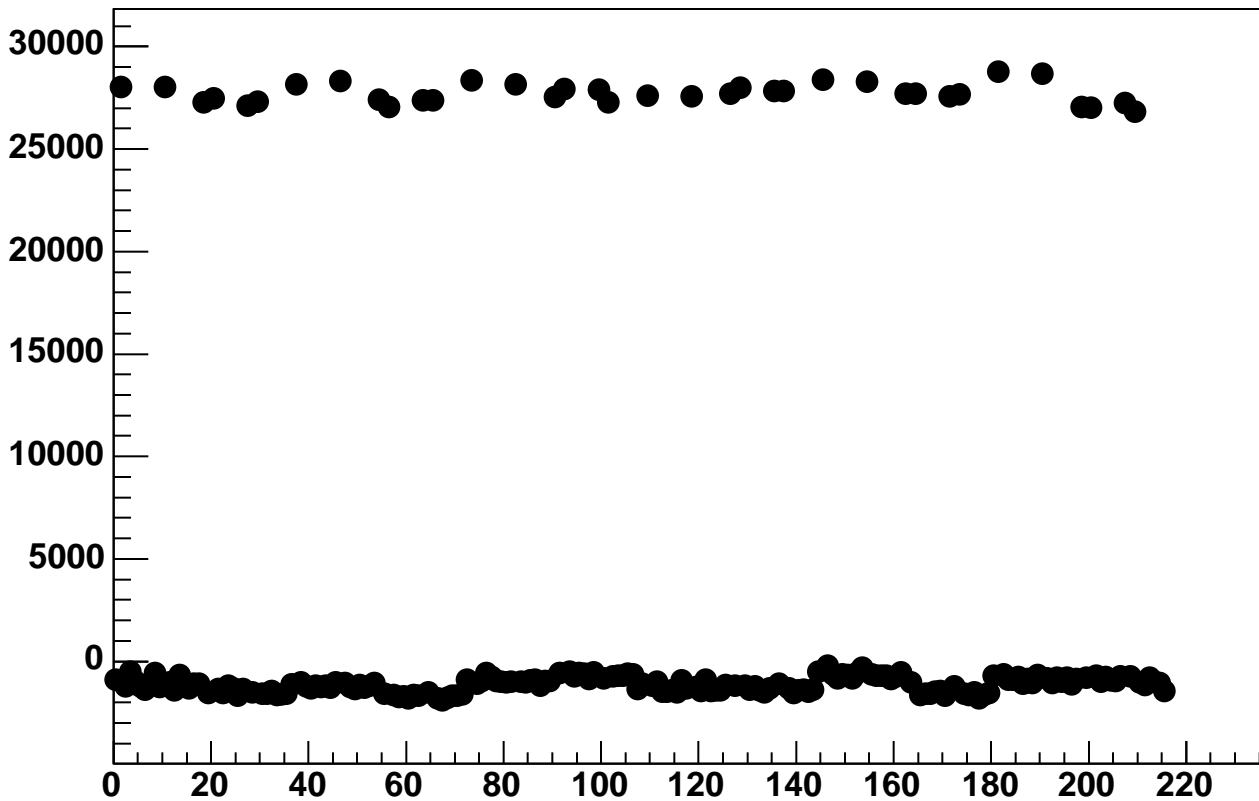
Enable 3, Hold=30, DAC=1900, ADC Mean vs 18\*Chip+Chan



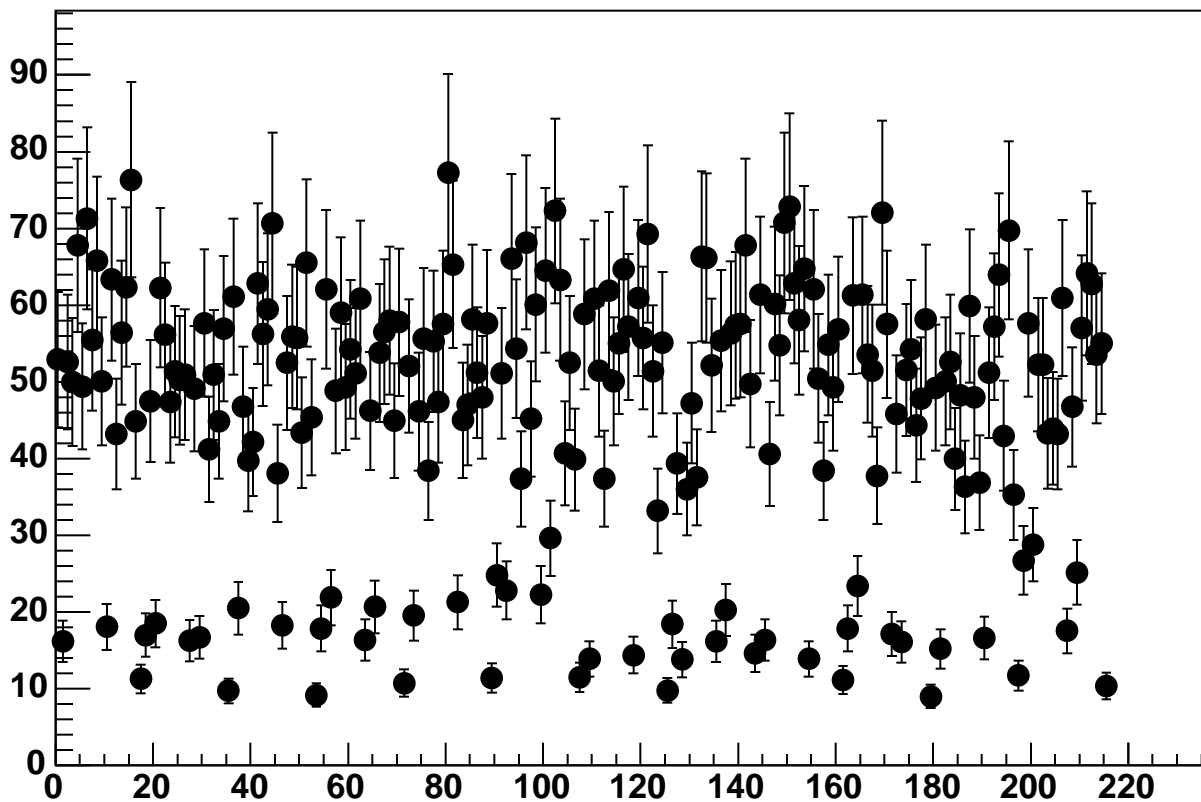
Enable 3, Hold=30, DAC=1900, ADC Noise vs 18\*Chip+Chan



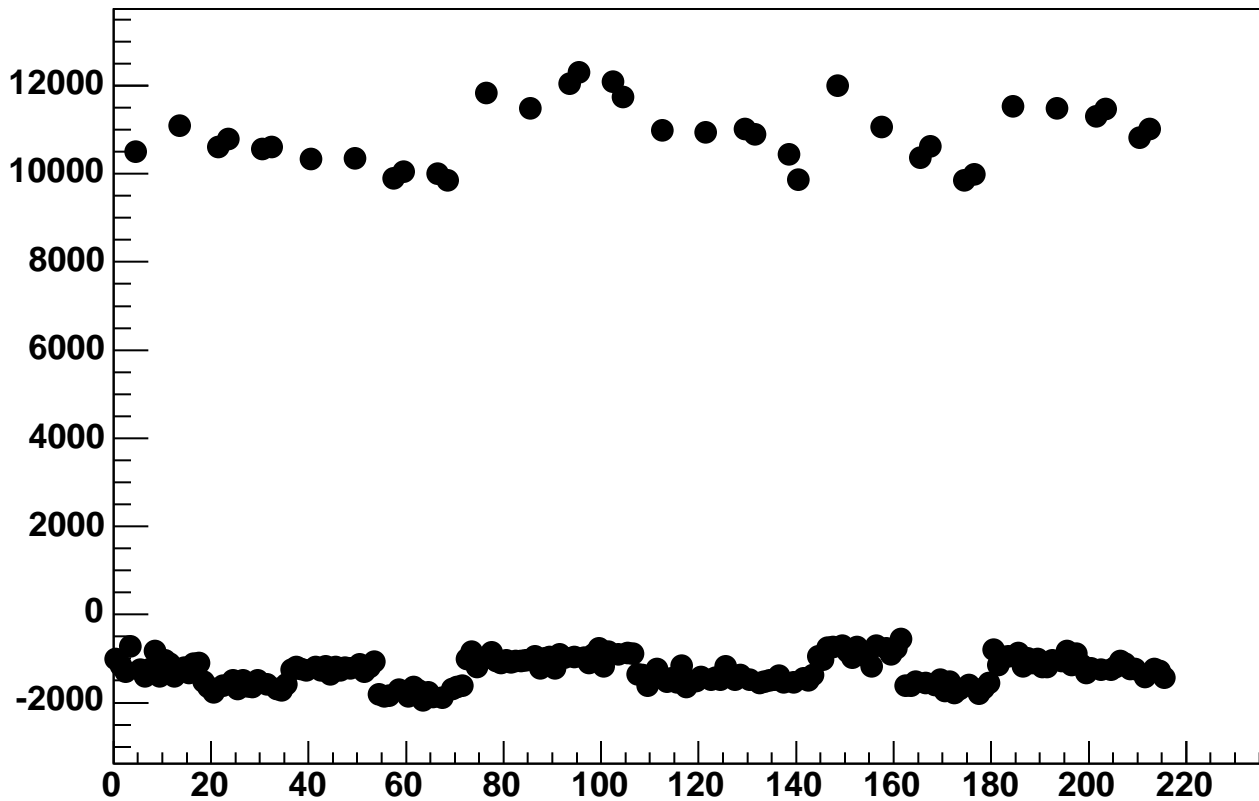
Enable 3, Hold=30, DAC=1950, ADC Mean vs 18\*Chip+Chan



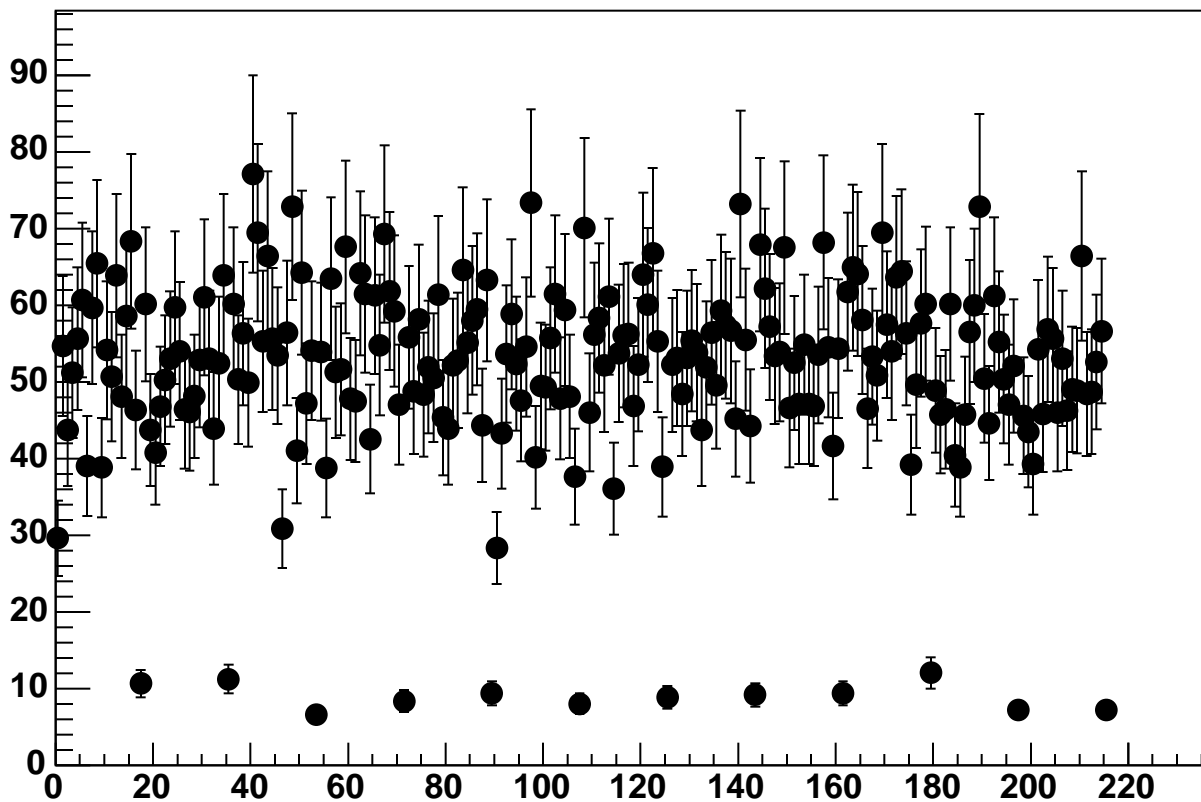
Enable 3, Hold=30, DAC=1950, ADC Noise vs 18\*Chip+Chan



Enable 4, Hold=30, DAC=0, ADC Mean vs 18\*Chip+Chan

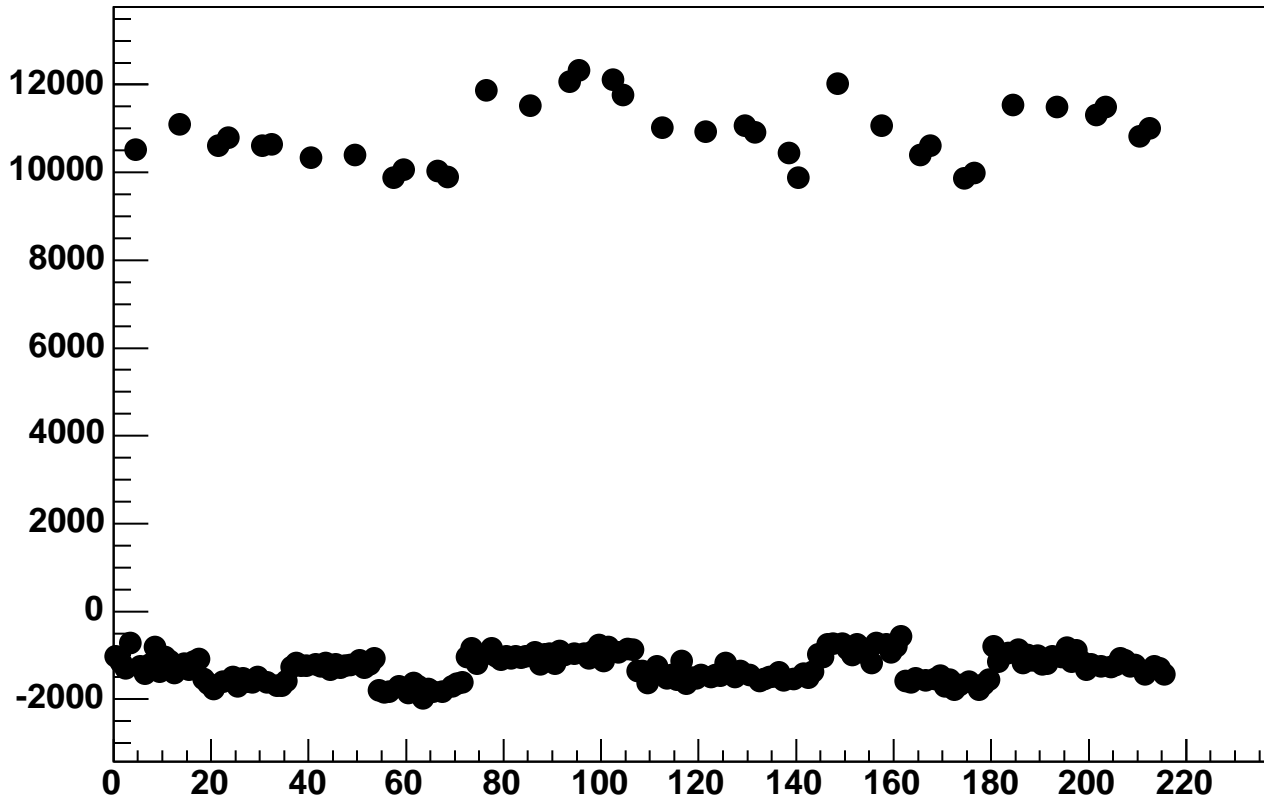


Enable 4, Hold=30, DAC=0, ADC Noise vs 18\*Chip+Chan

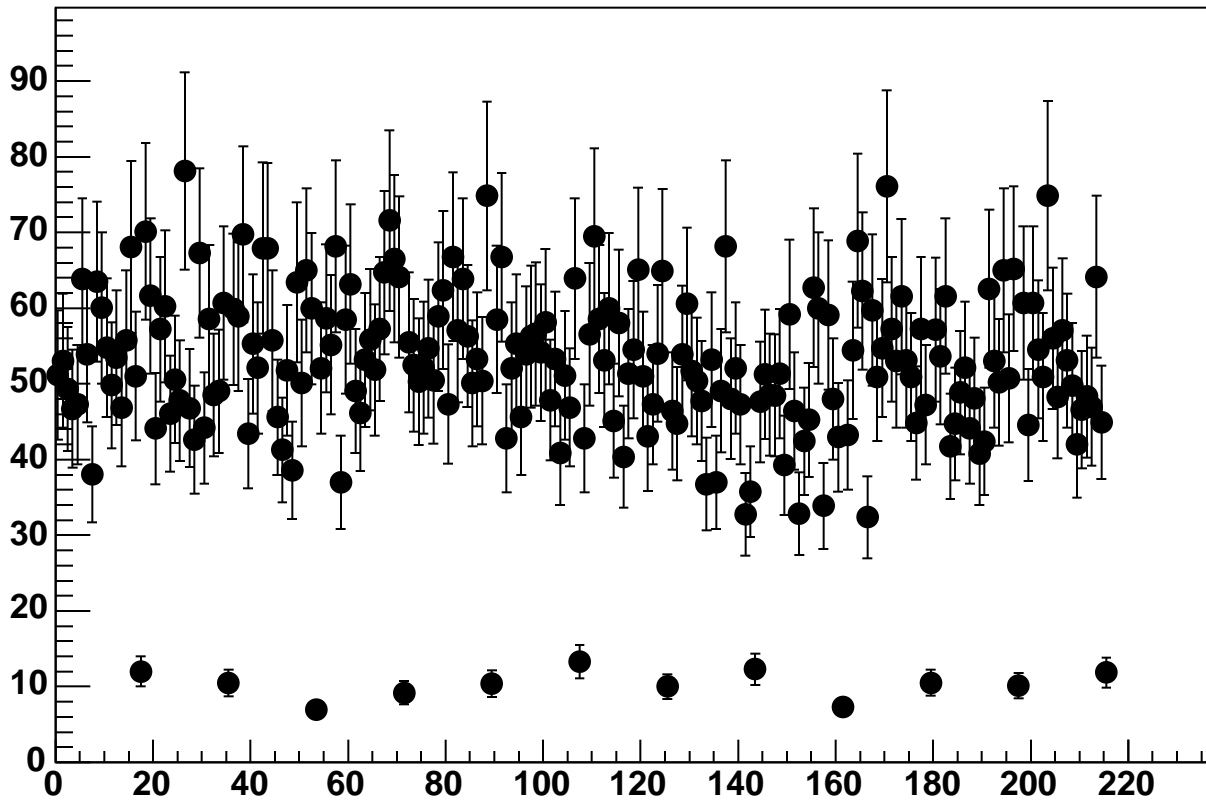




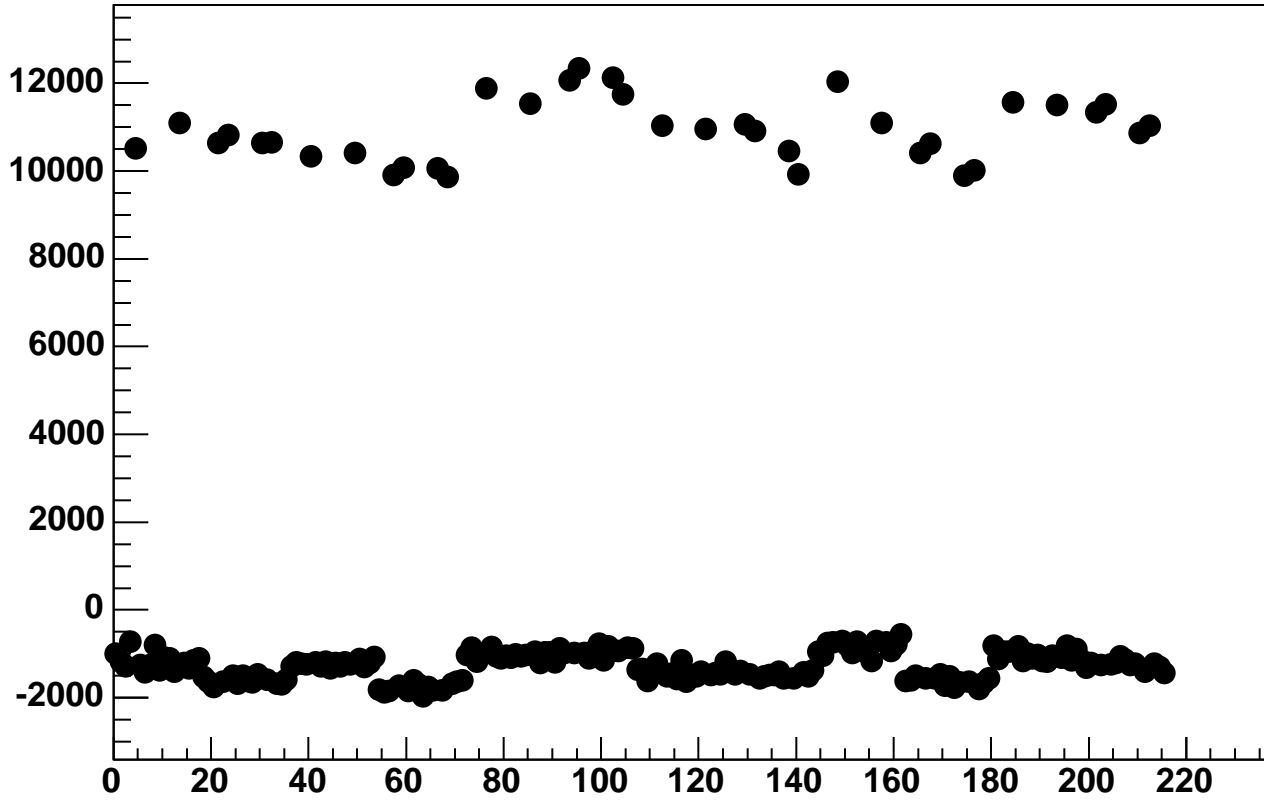
Enable 4, Hold=30, DAC=50, ADC Mean vs 18\*Chip+Chan



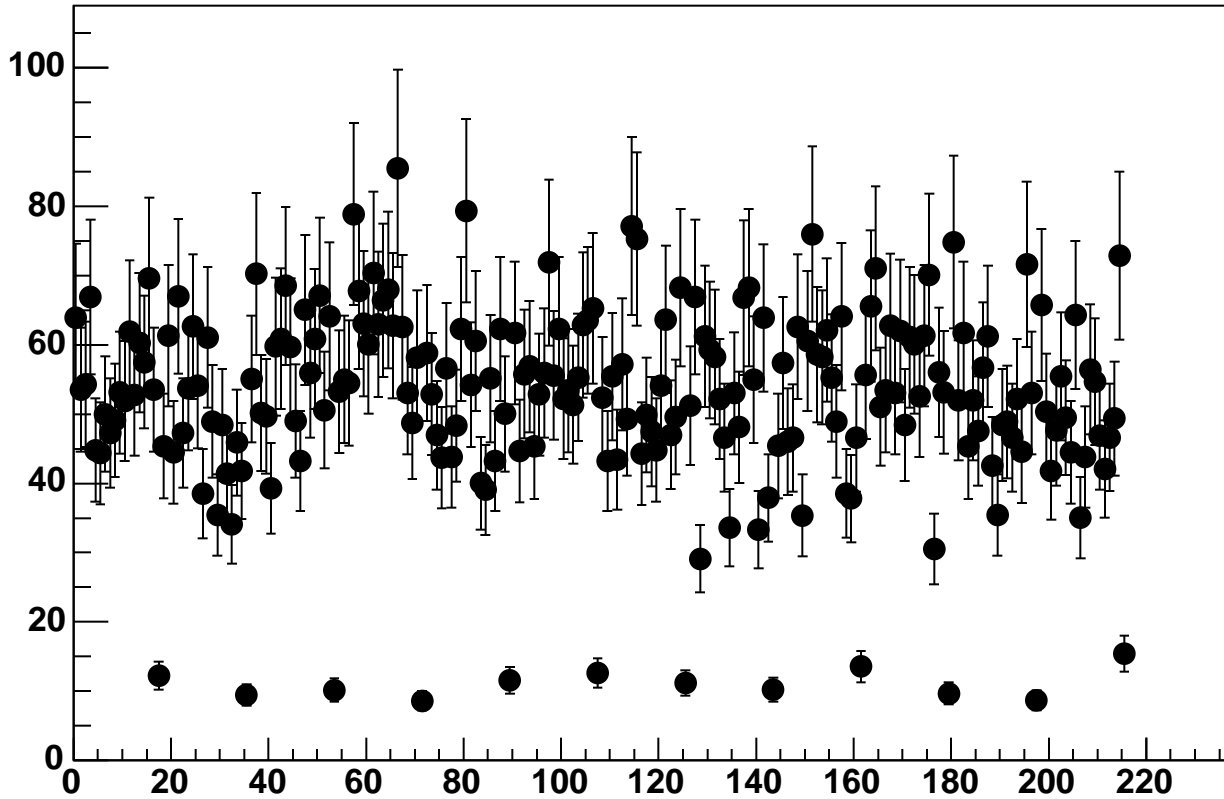
Enable 4, Hold=30, DAC=50, ADC Noise vs 18\*Chip+Chan



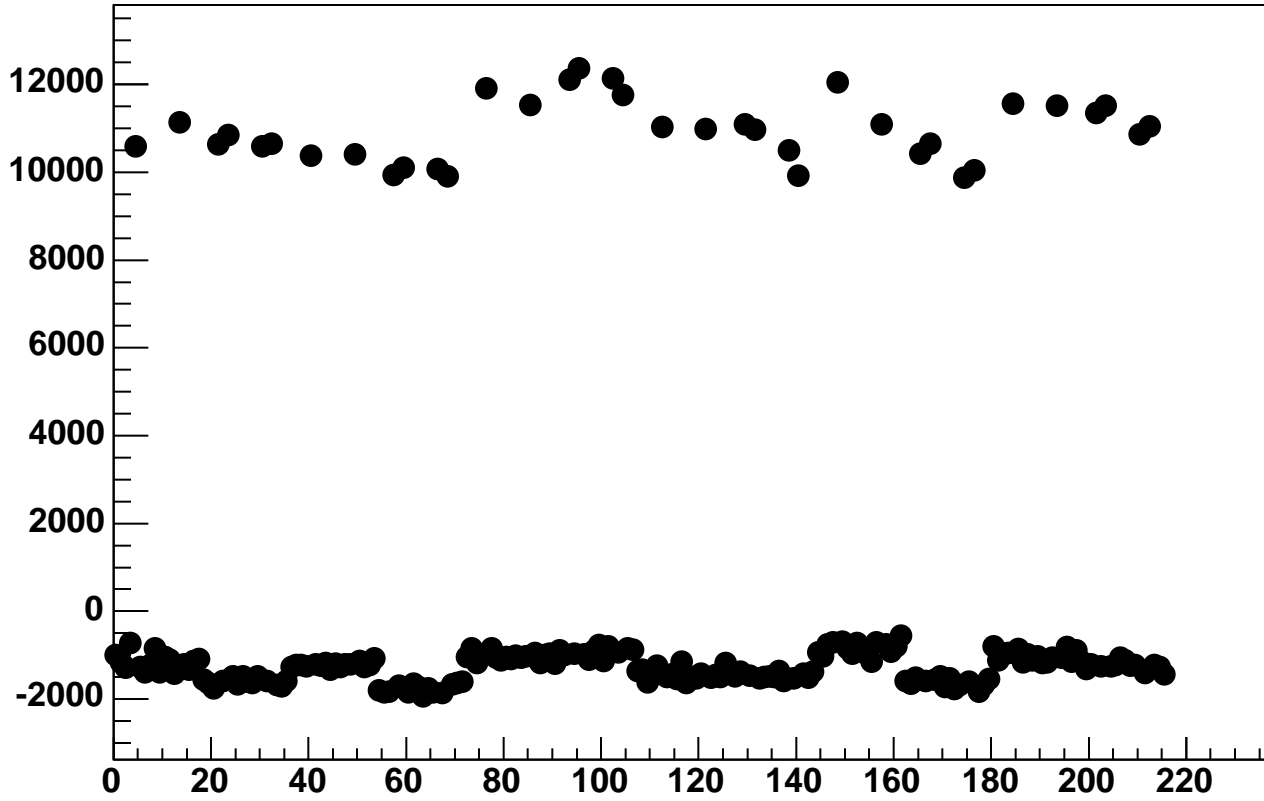
Enable 4, Hold=30, DAC=100, ADC Mean vs 18\*Chip+Chan



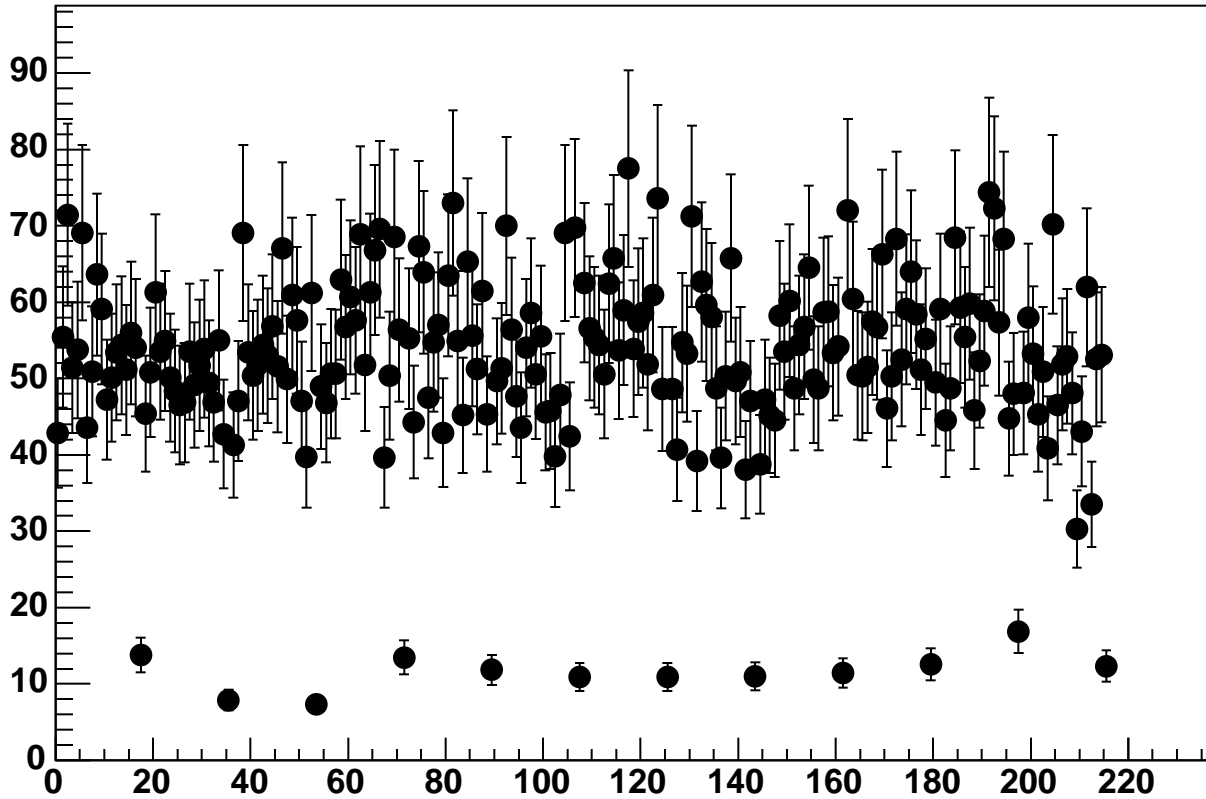
Enable 4, Hold=30, DAC=100, ADC Noise vs 18\*Chip+Chan



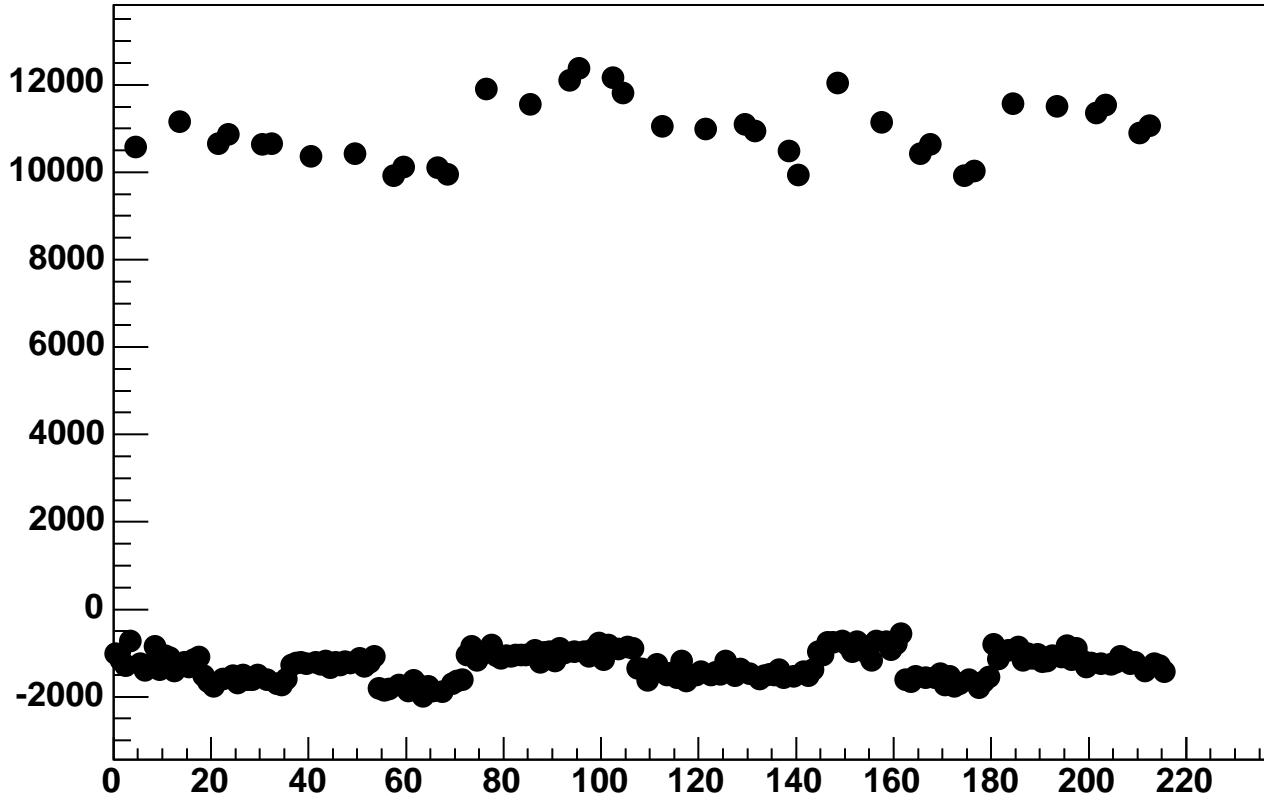
Enable 4, Hold=30, DAC=150, ADC Mean vs 18\*Chip+Chan



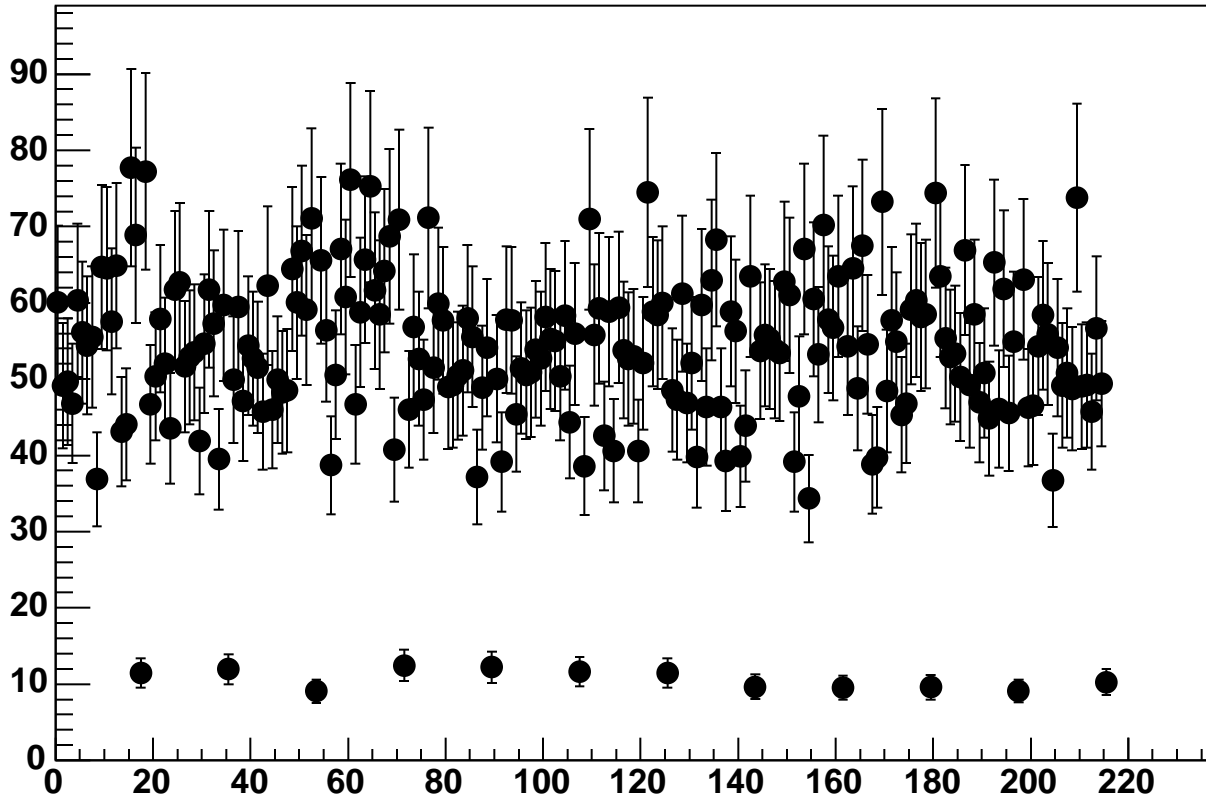
Enable 4, Hold=30, DAC=150, ADC Noise vs 18\*Chip+Chan



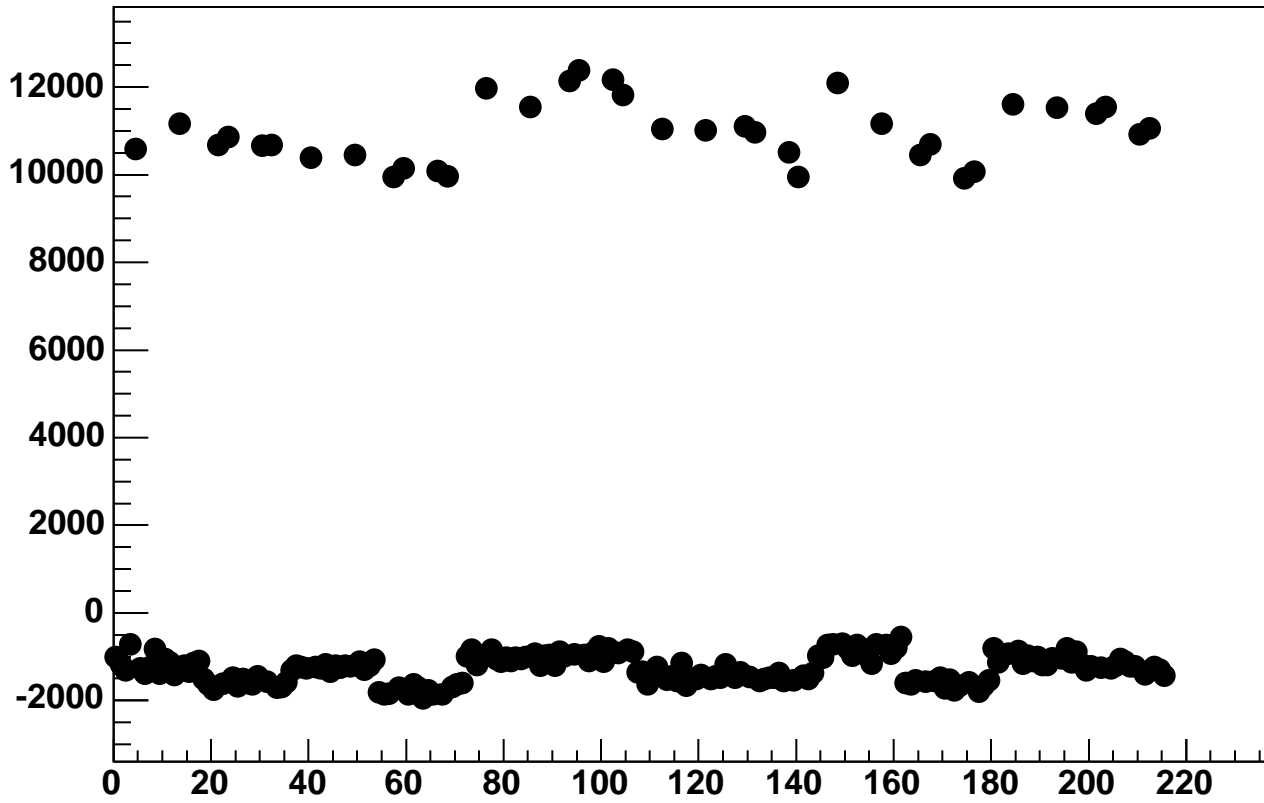
Enable 4, Hold=30, DAC=200, ADC Mean vs 18\*Chip+Chan



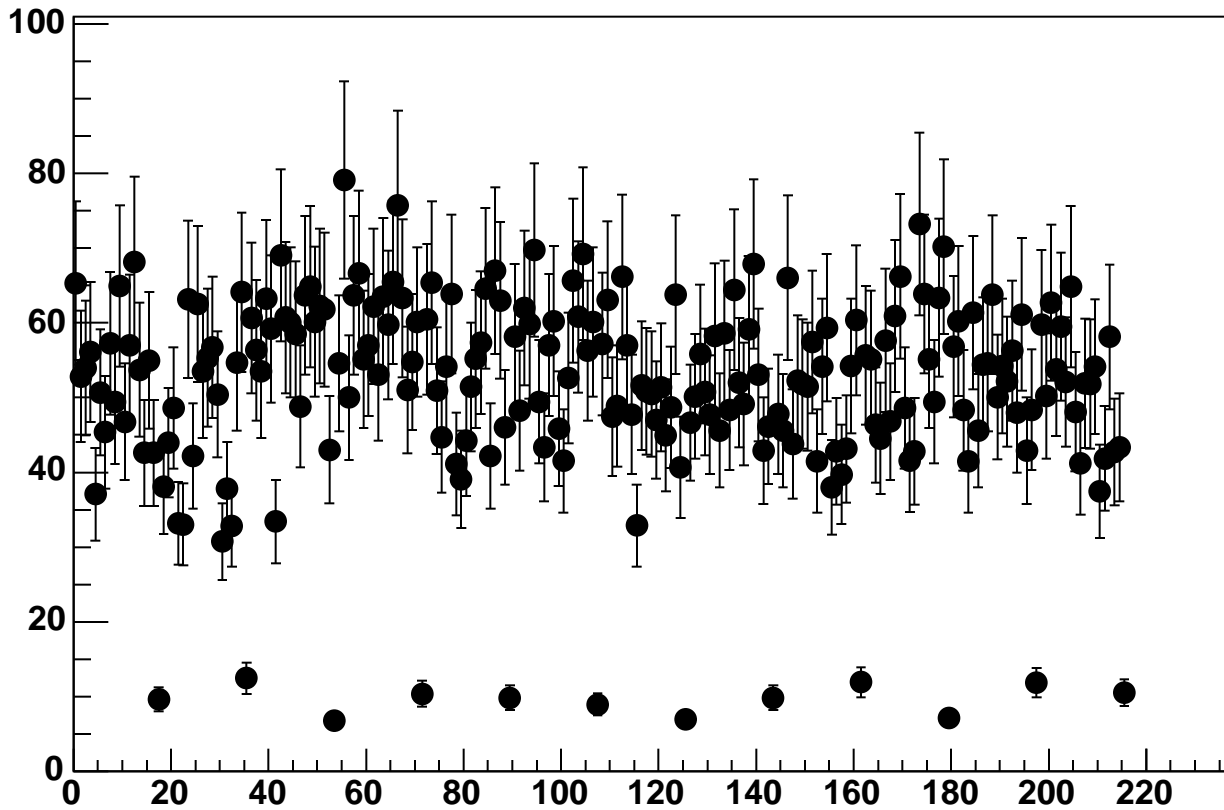
Enable 4, Hold=30, DAC=200, ADC Noise vs 18\*Chip+Chan



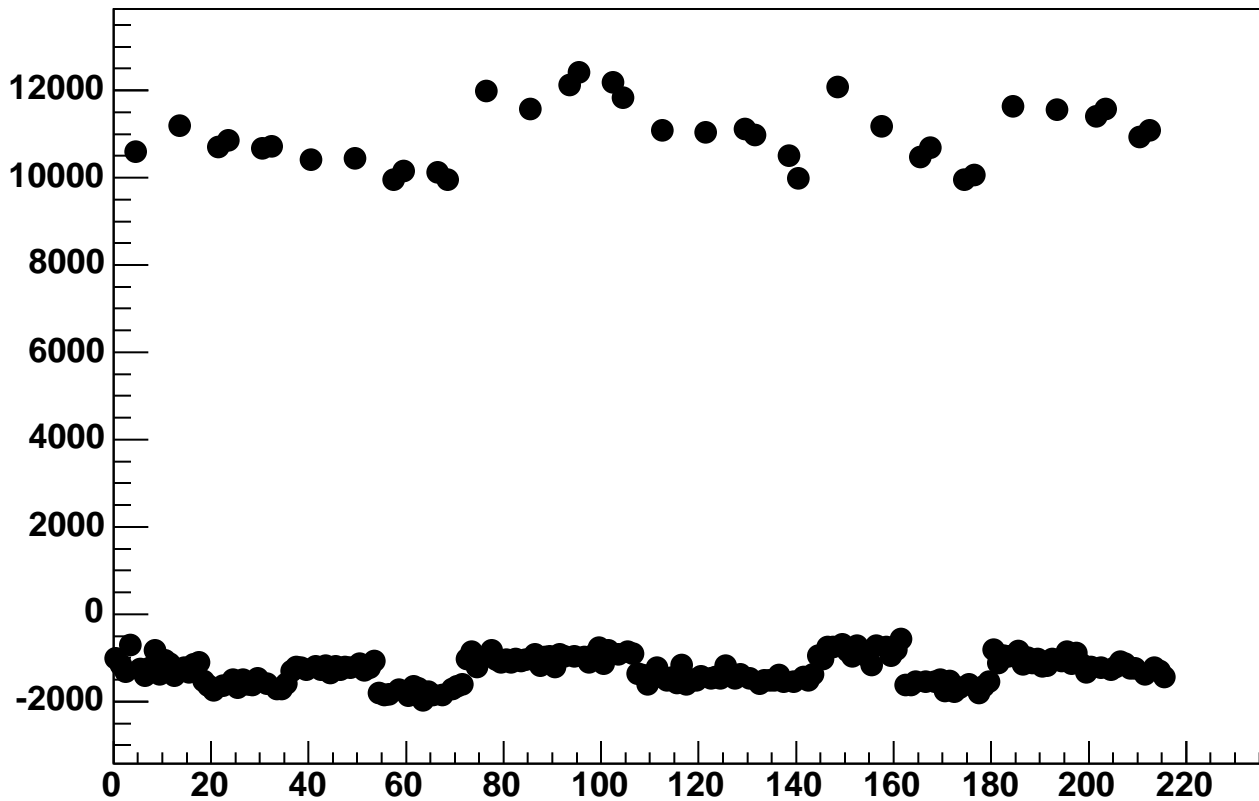
Enable 4, Hold=30, DAC=250, ADC Mean vs 18\*Chip+Chan



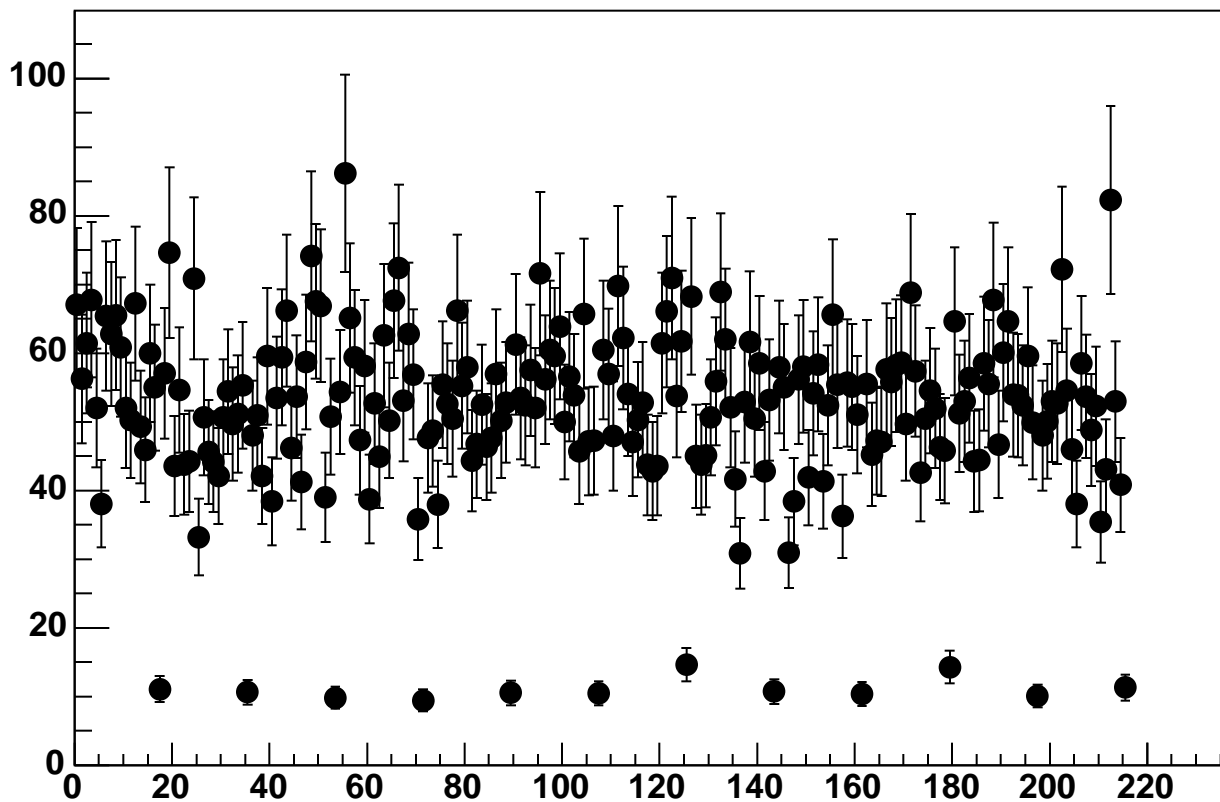
Enable 4, Hold=30, DAC=250, ADC Noise vs 18\*Chip+Chan



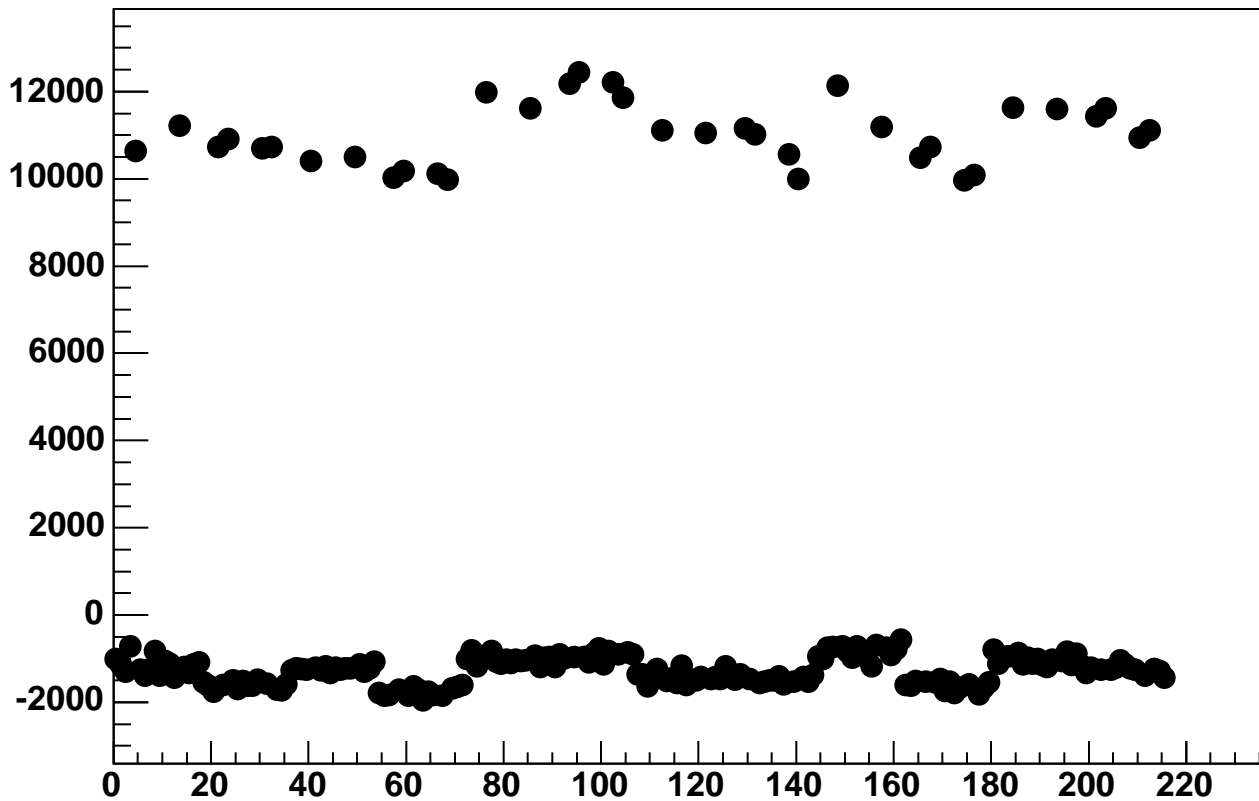
Enable 4, Hold=30, DAC=300, ADC Mean vs 18\*Chip+Chan



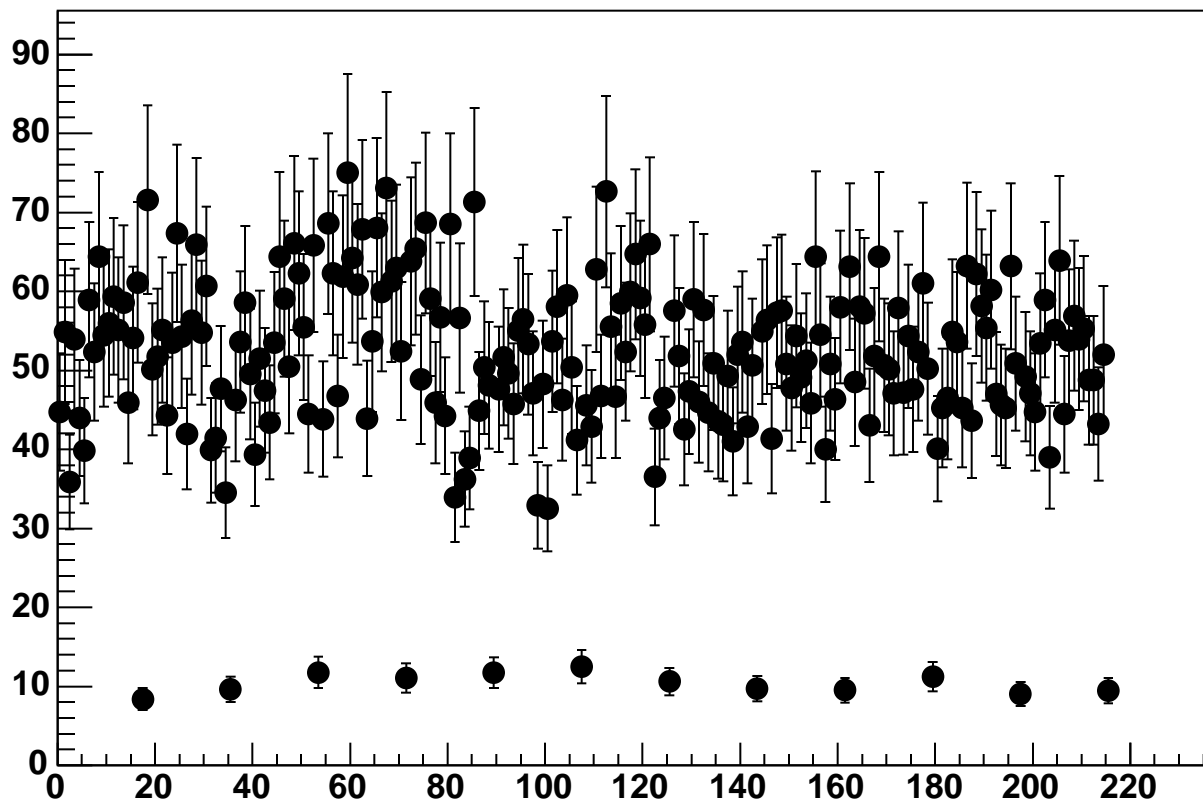
Enable 4, Hold=30, DAC=300, ADC Noise vs 18\*Chip+Chan



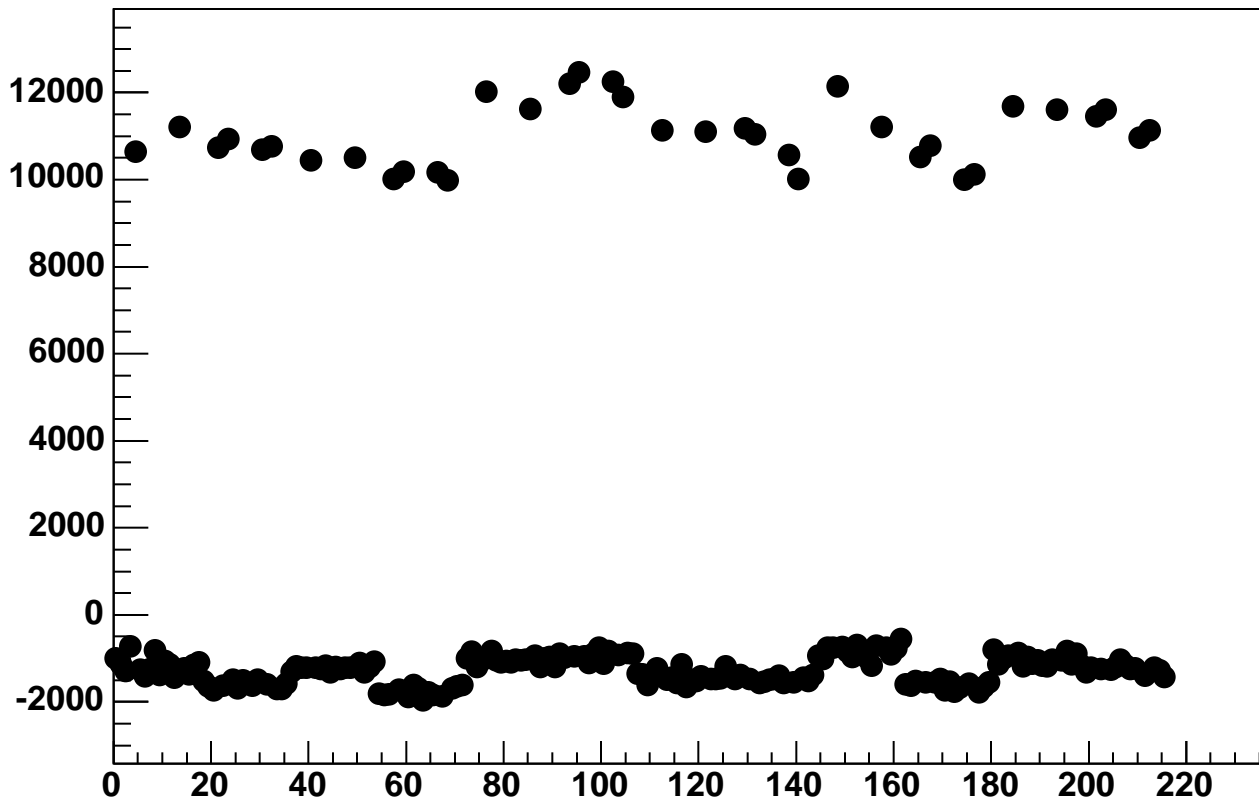
Enable 4, Hold=30, DAC=350, ADC Mean vs 18\*Chip+Chan



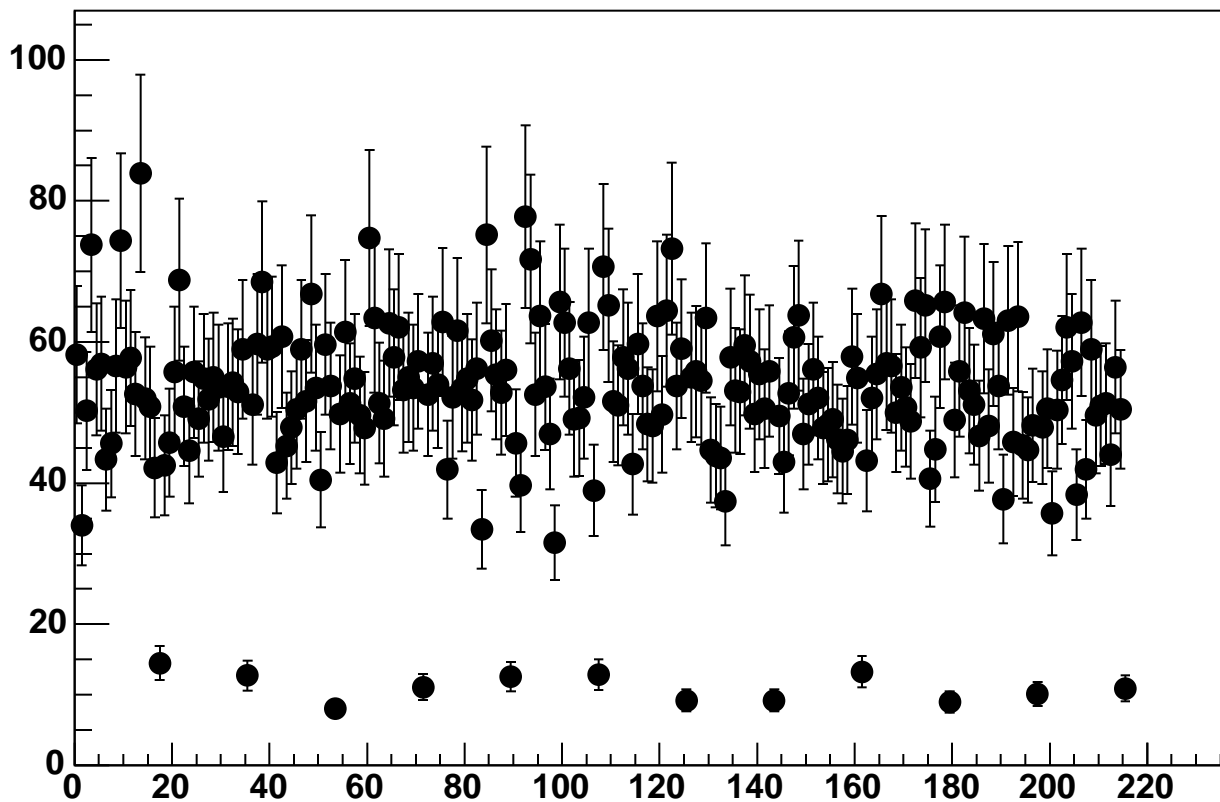
Enable 4, Hold=30, DAC=350, ADC Noise vs 18\*Chip+Chan



Enable 4, Hold=30, DAC=400, ADC Mean vs 18\*Chip+Chan

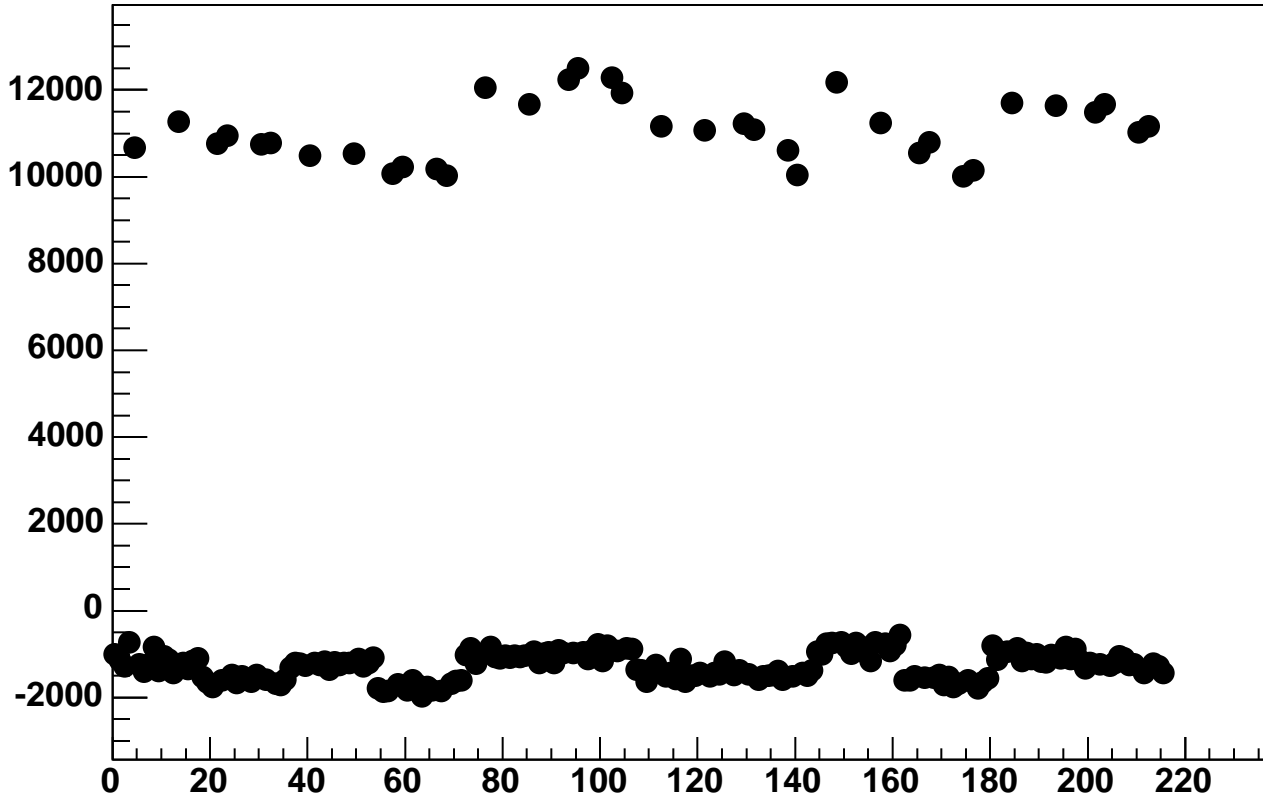


Enable 4, Hold=30, DAC=400, ADC Noise vs 18\*Chip+Chan

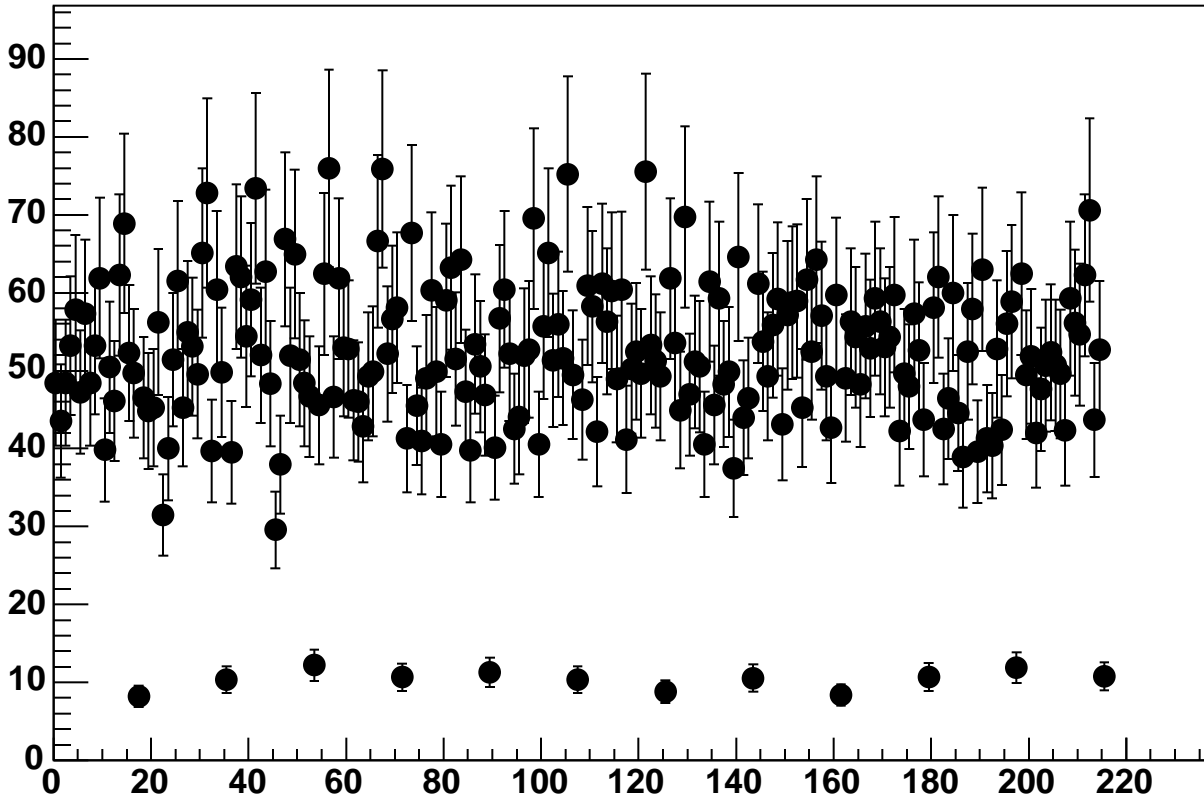




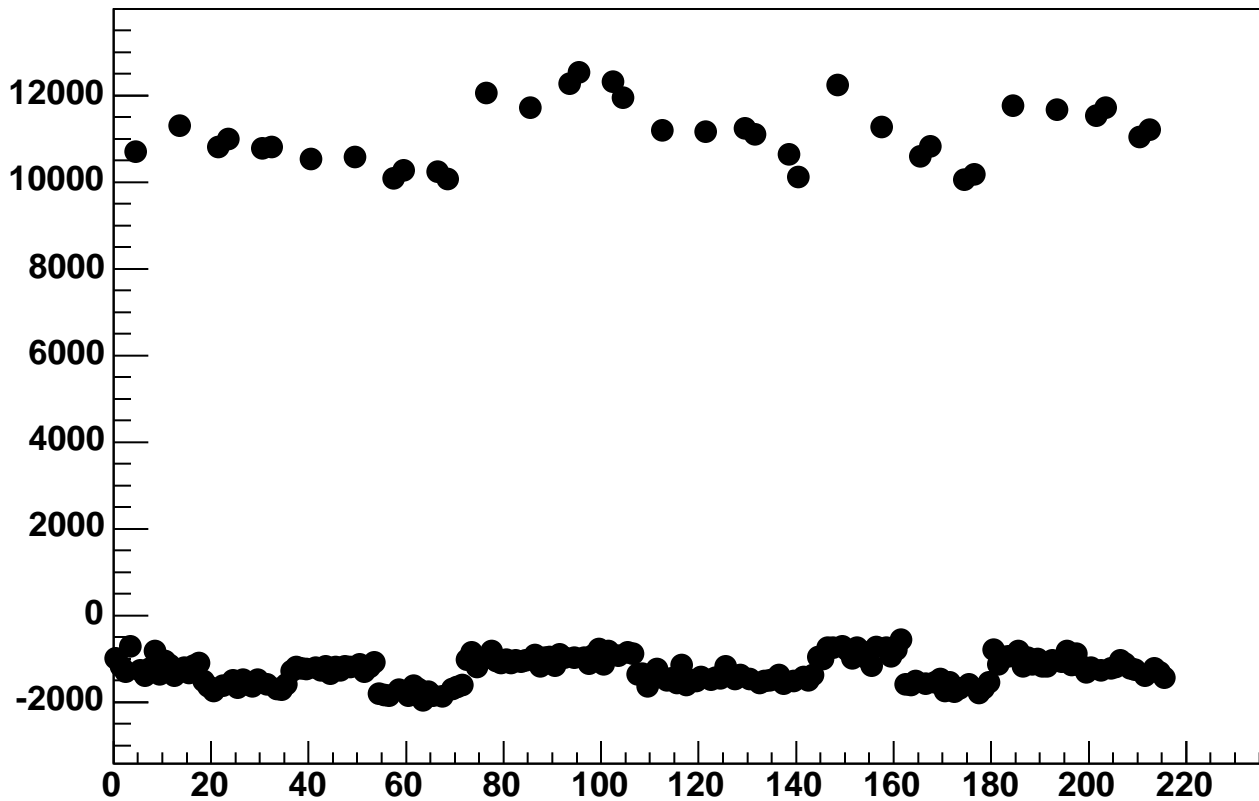
Enable 4, Hold=30, DAC=450, ADC Mean vs 18\*Chip+Chan



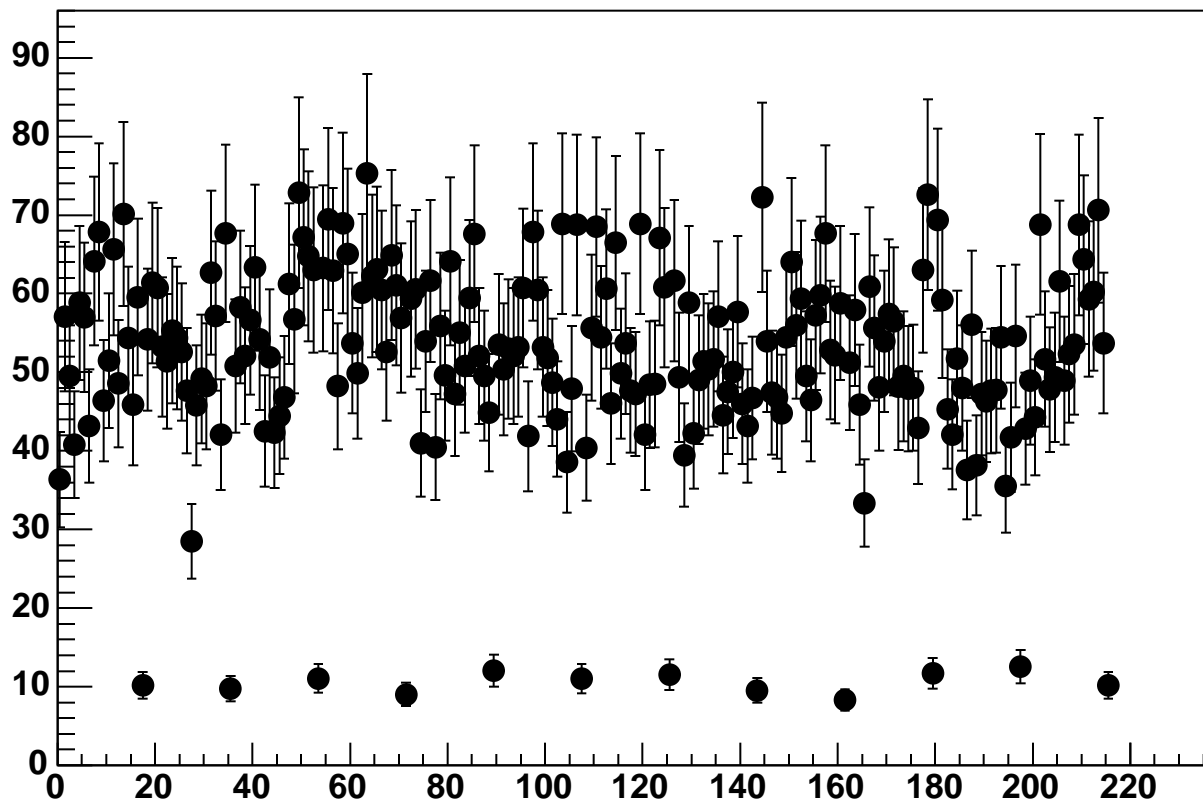
Enable 4, Hold=30, DAC=450, ADC Noise vs 18\*Chip+Chan



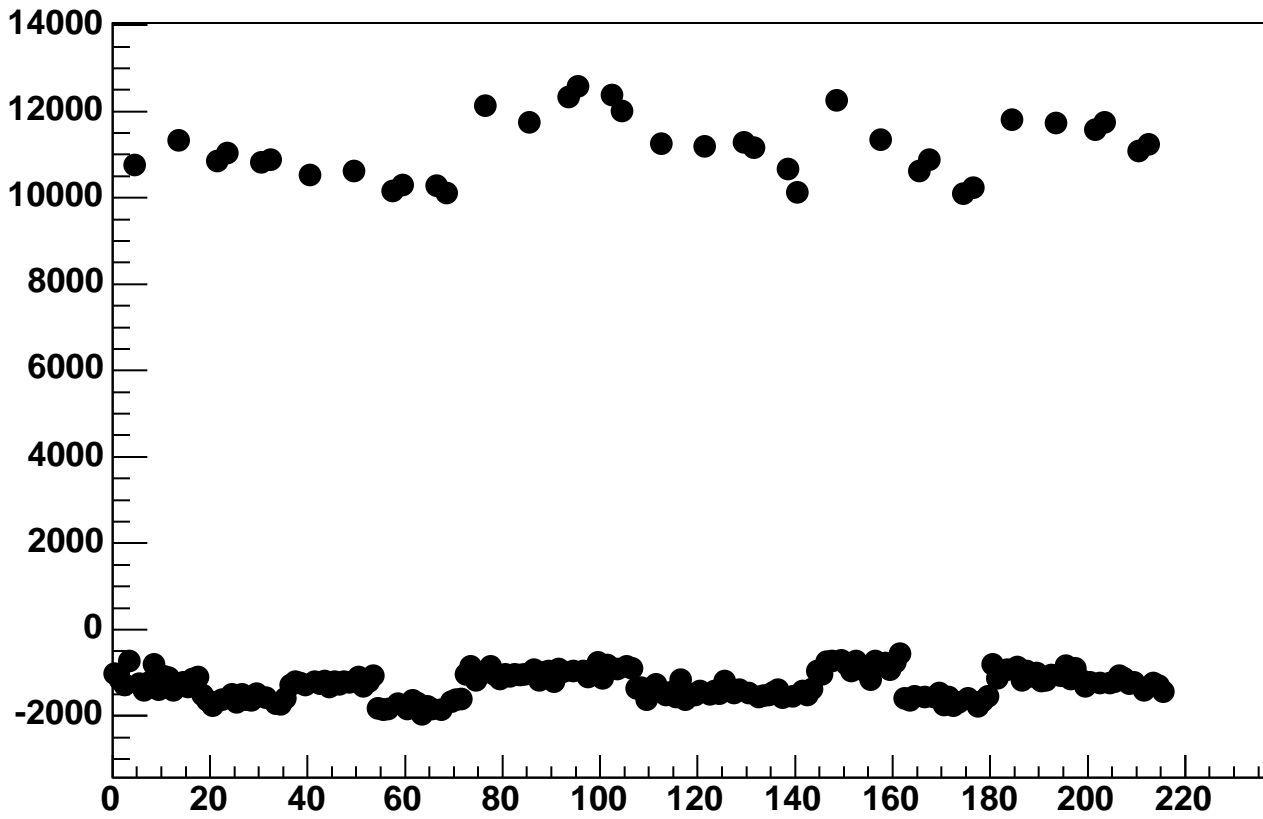
Enable 4, Hold=30, DAC=500, ADC Mean vs 18\*Chip+Chan



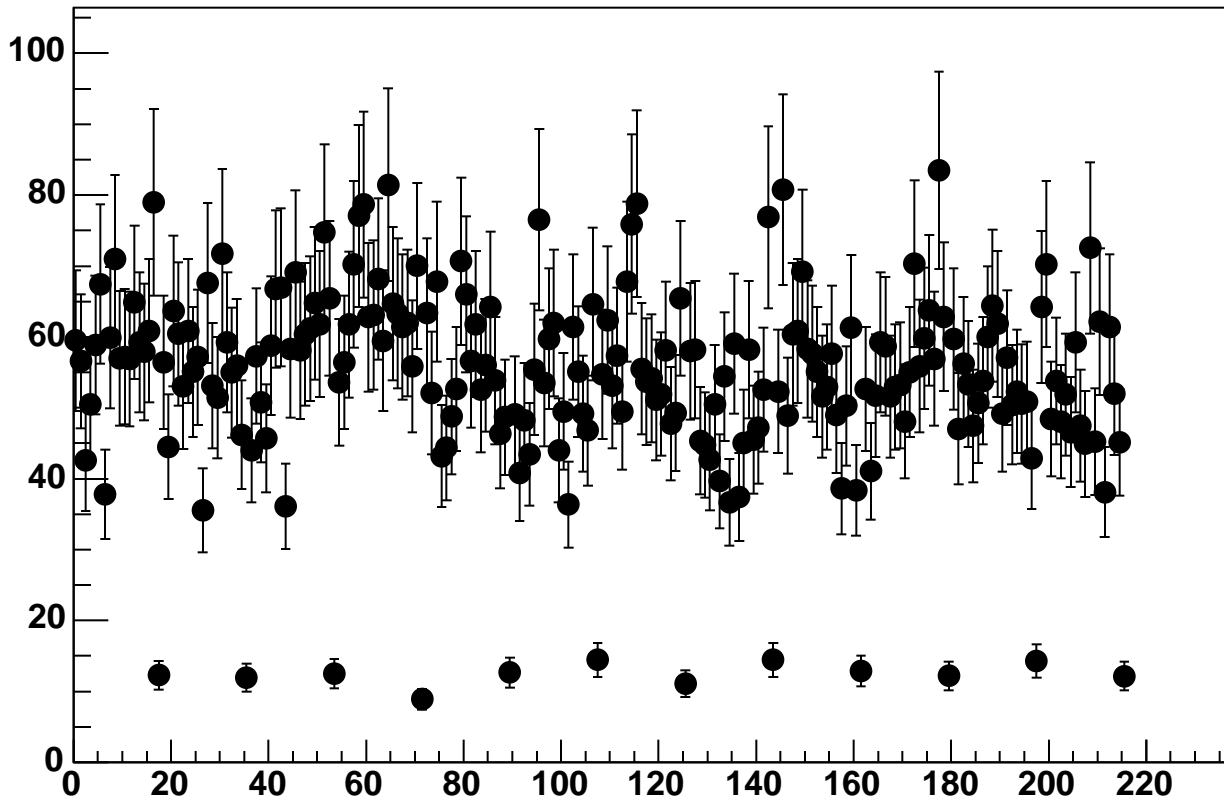
Enable 4, Hold=30, DAC=500, ADC Noise vs 18\*Chip+Chan



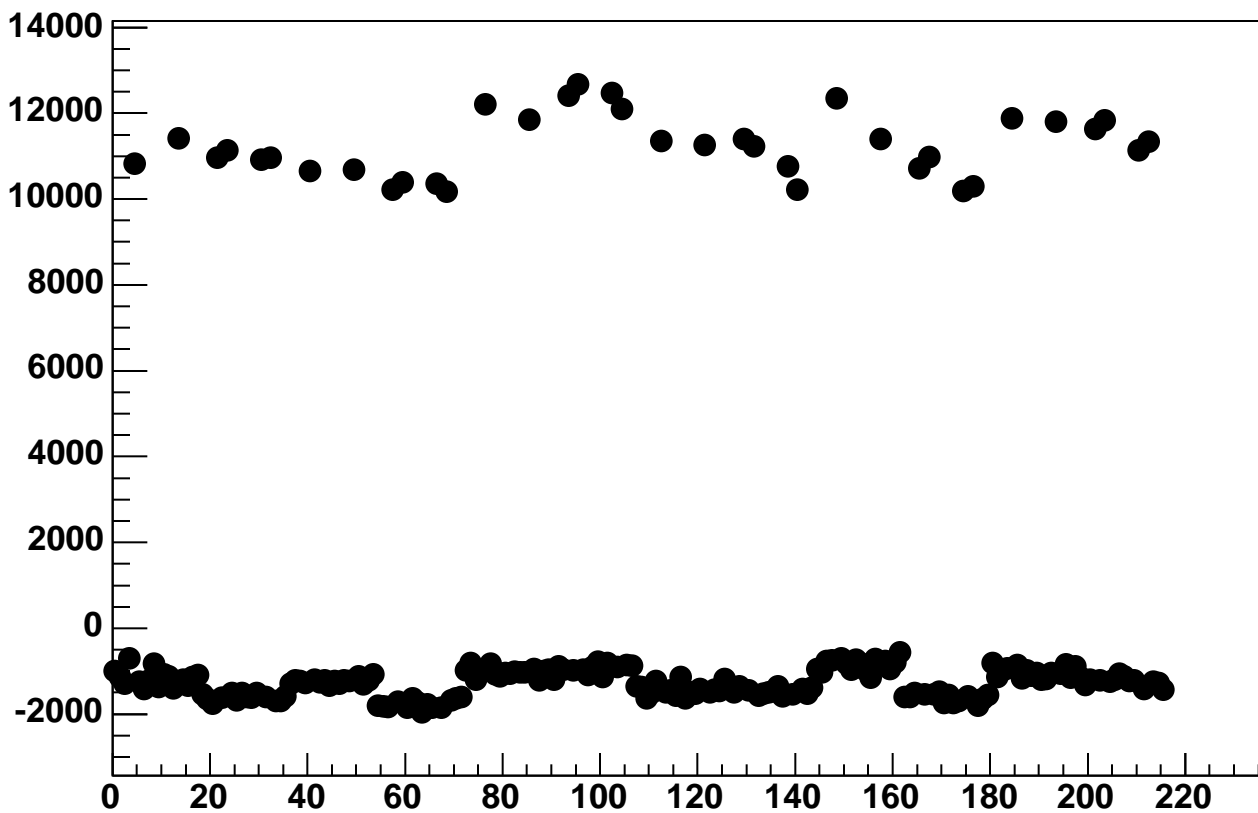
Enable 4, Hold=30, DAC=550, ADC Mean vs 18\*Chip+Chan



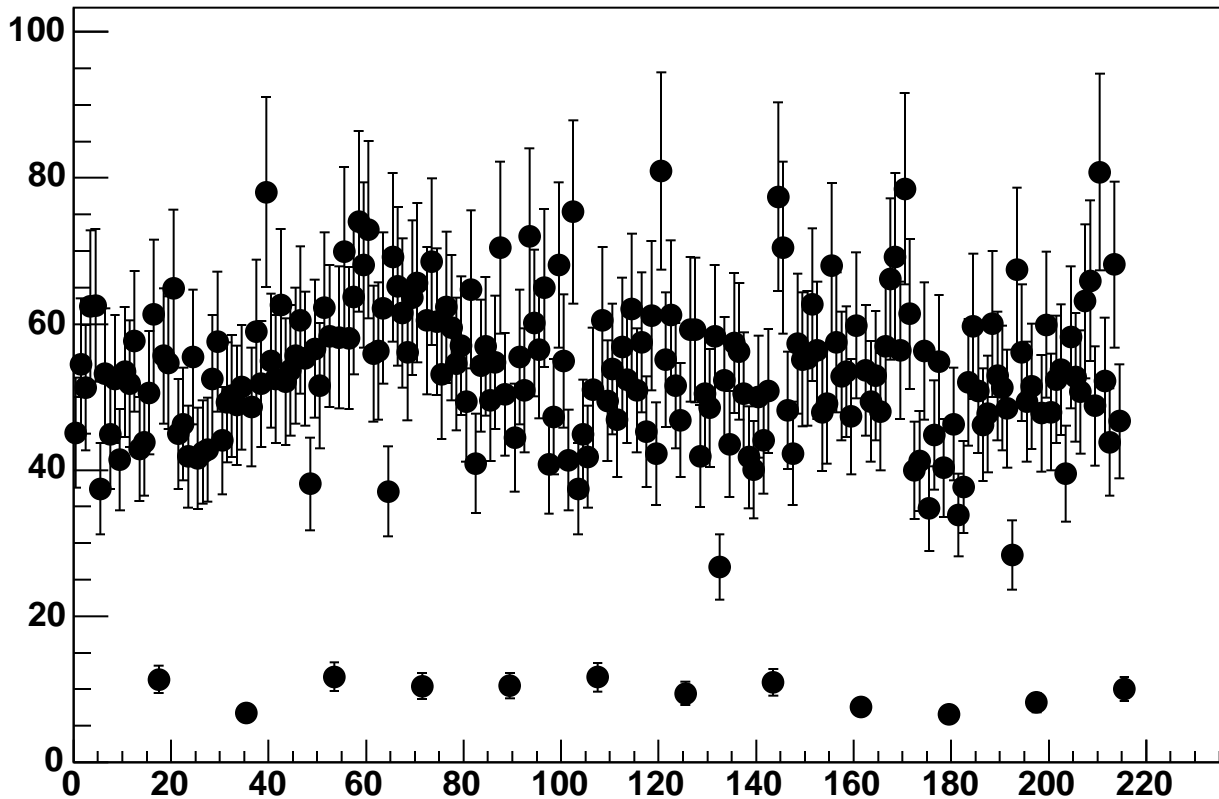
Enable 4, Hold=30, DAC=550, ADC Noise vs 18\*Chip+Chan



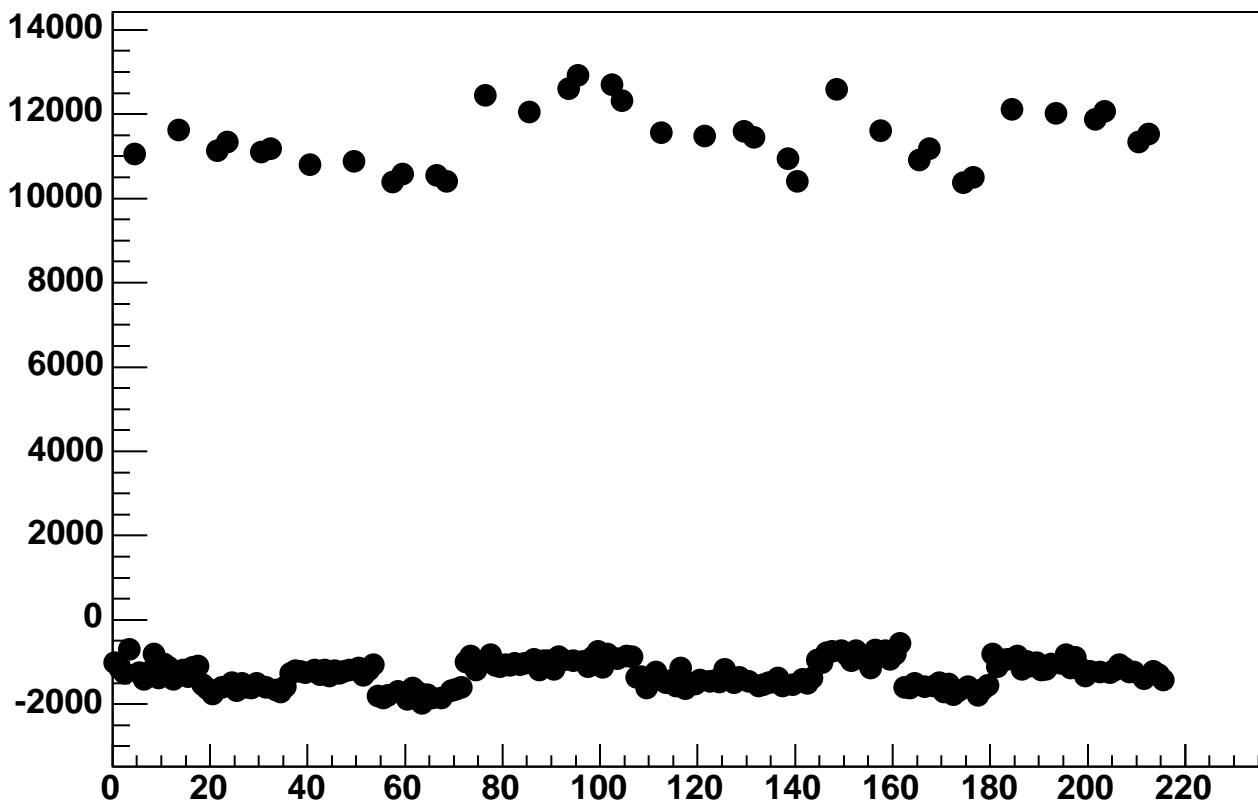
Enable 4, Hold=30, DAC=600, ADC Mean vs 18\*Chip+Chan



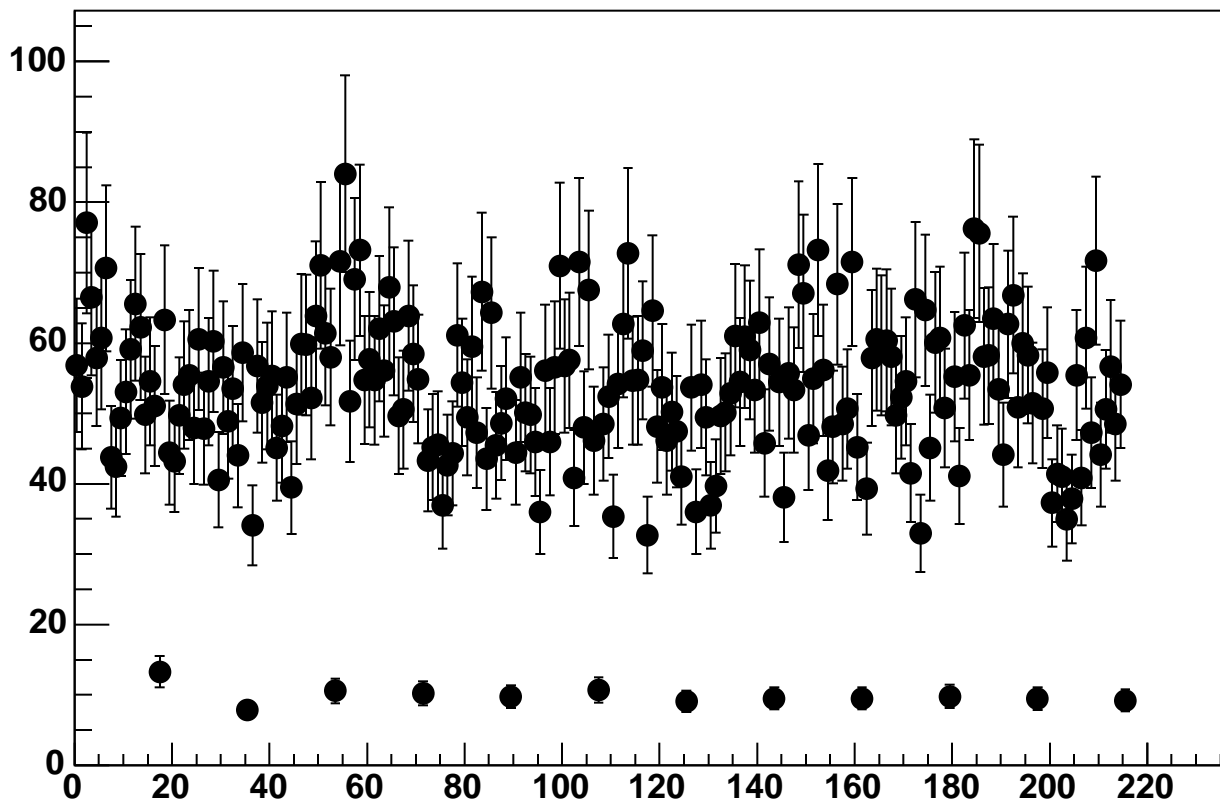
Enable 4, Hold=30, DAC=600, ADC Noise vs 18\*Chip+Chan



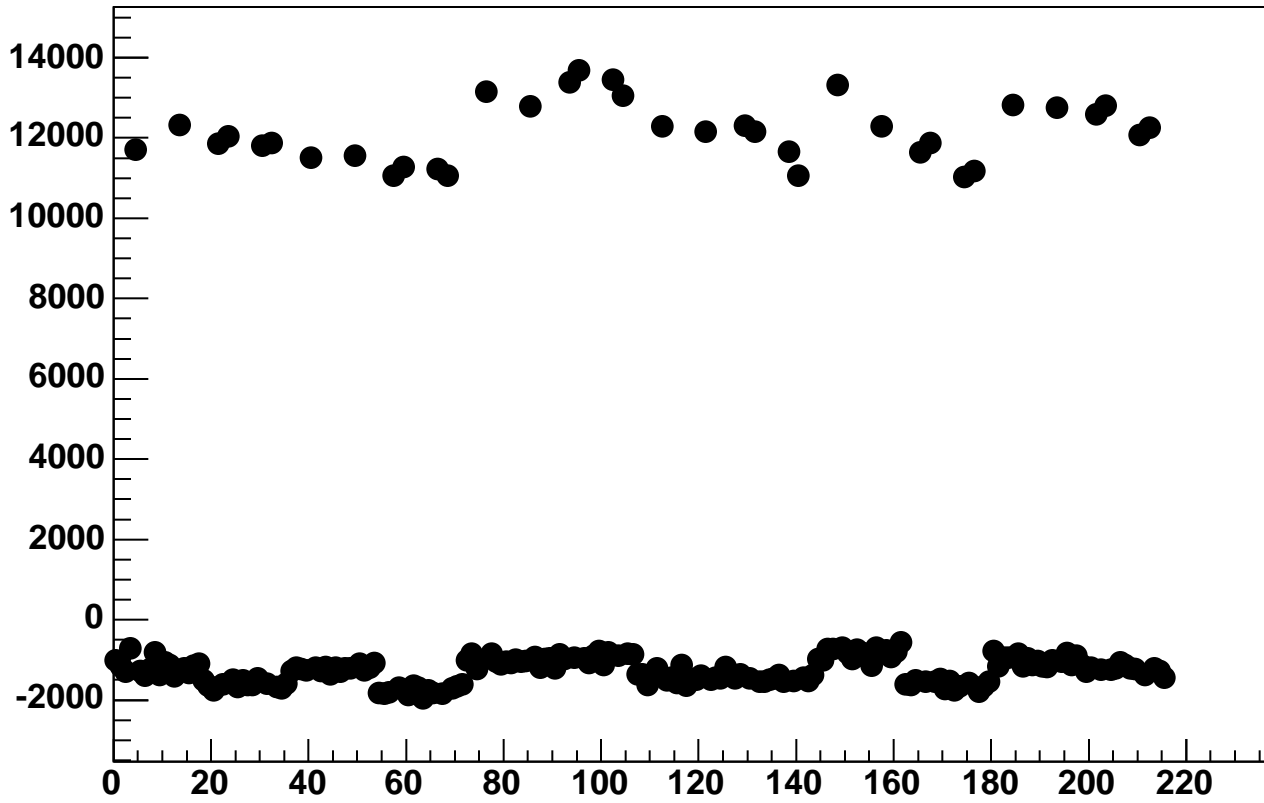
Enable 4, Hold=30, DAC=650, ADC Mean vs 18\*Chip+Chan



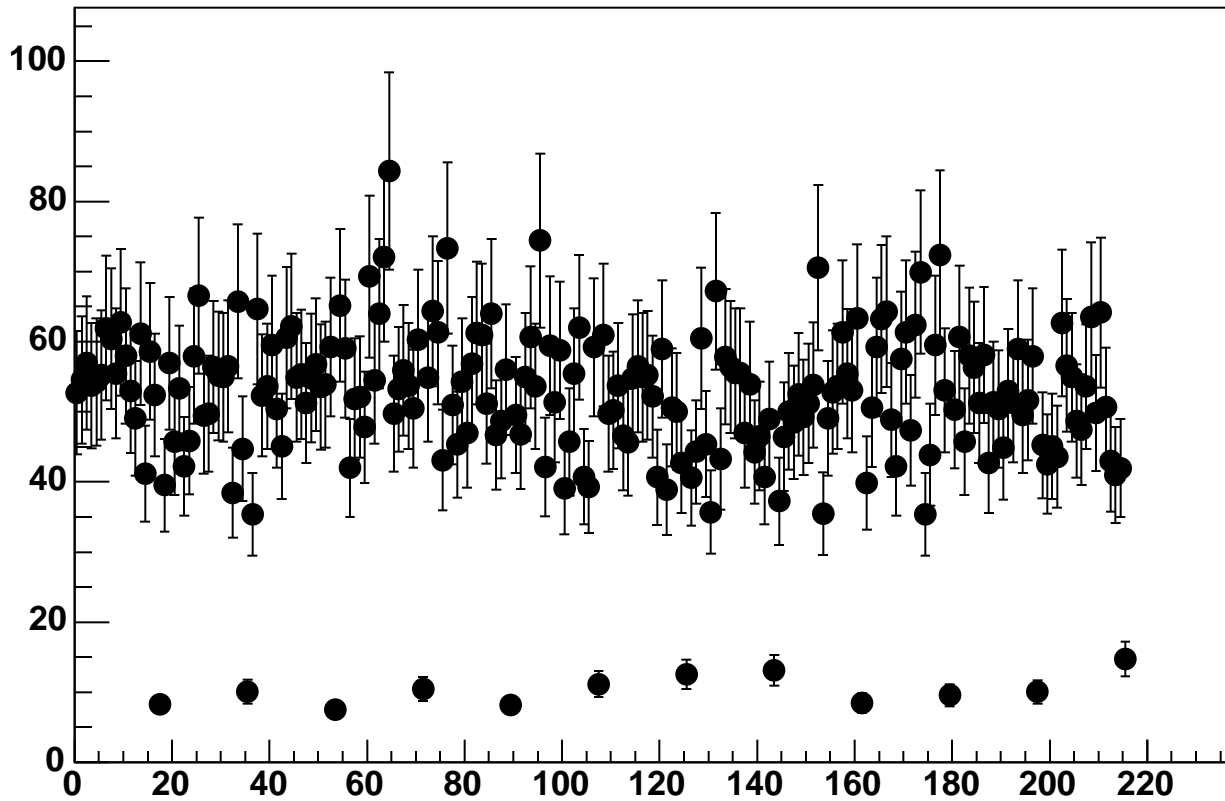
Enable 4, Hold=30, DAC=650, ADC Noise vs 18\*Chip+Chan



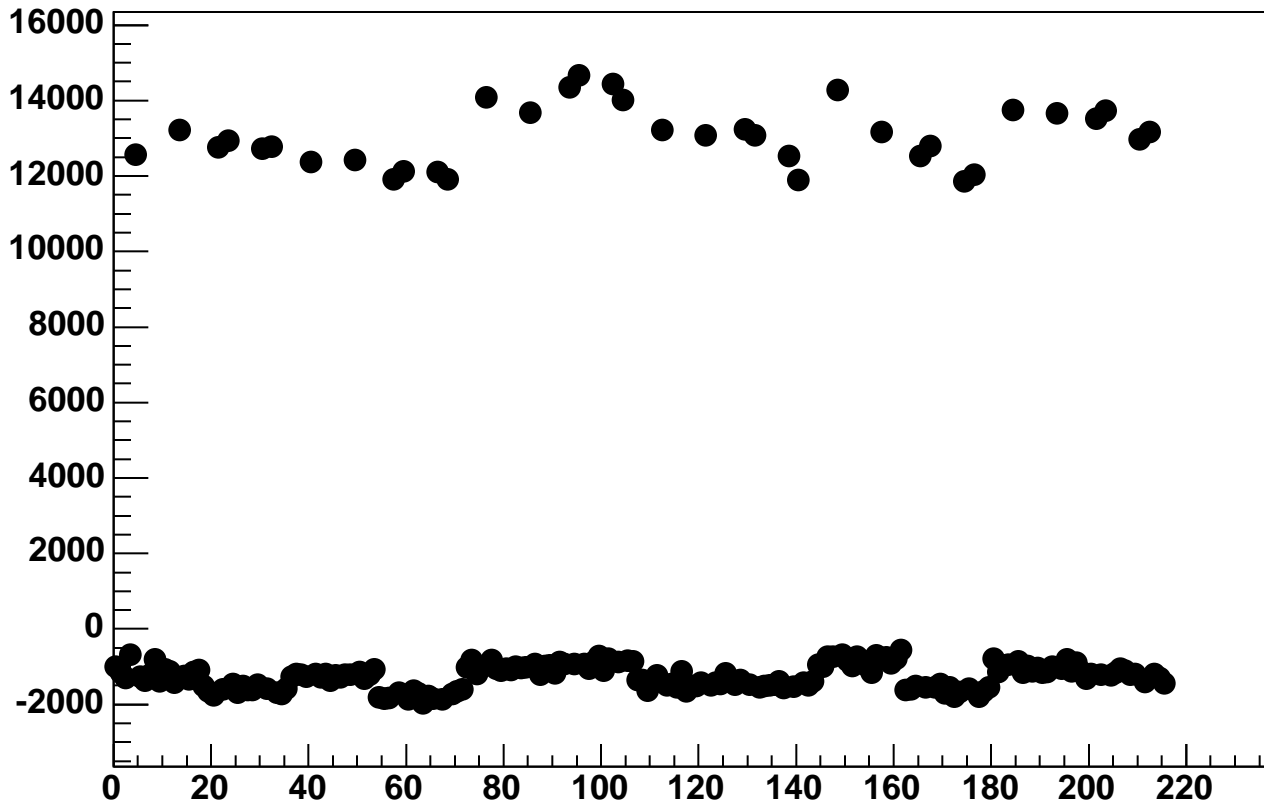
Enable 4, Hold=30, DAC=700, ADC Mean vs 18\*Chip+Chan



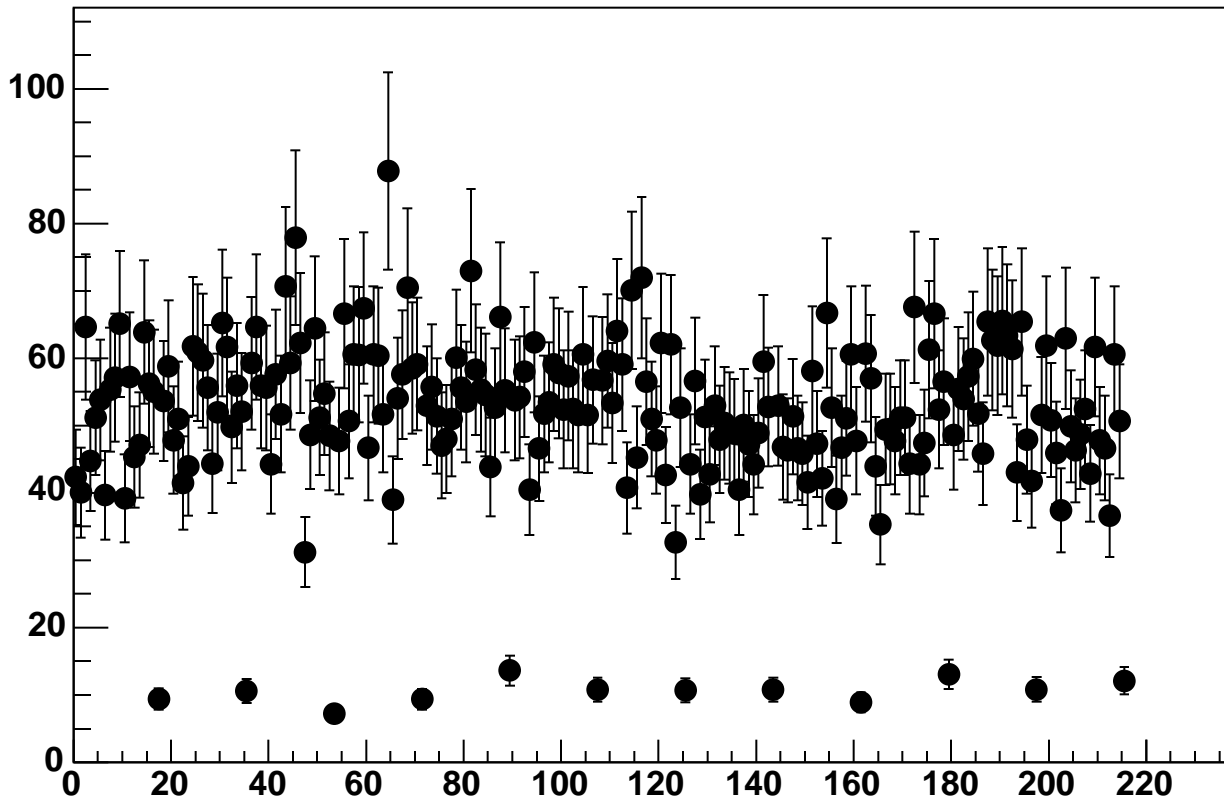
Enable 4, Hold=30, DAC=700, ADC Noise vs 18\*Chip+Chan



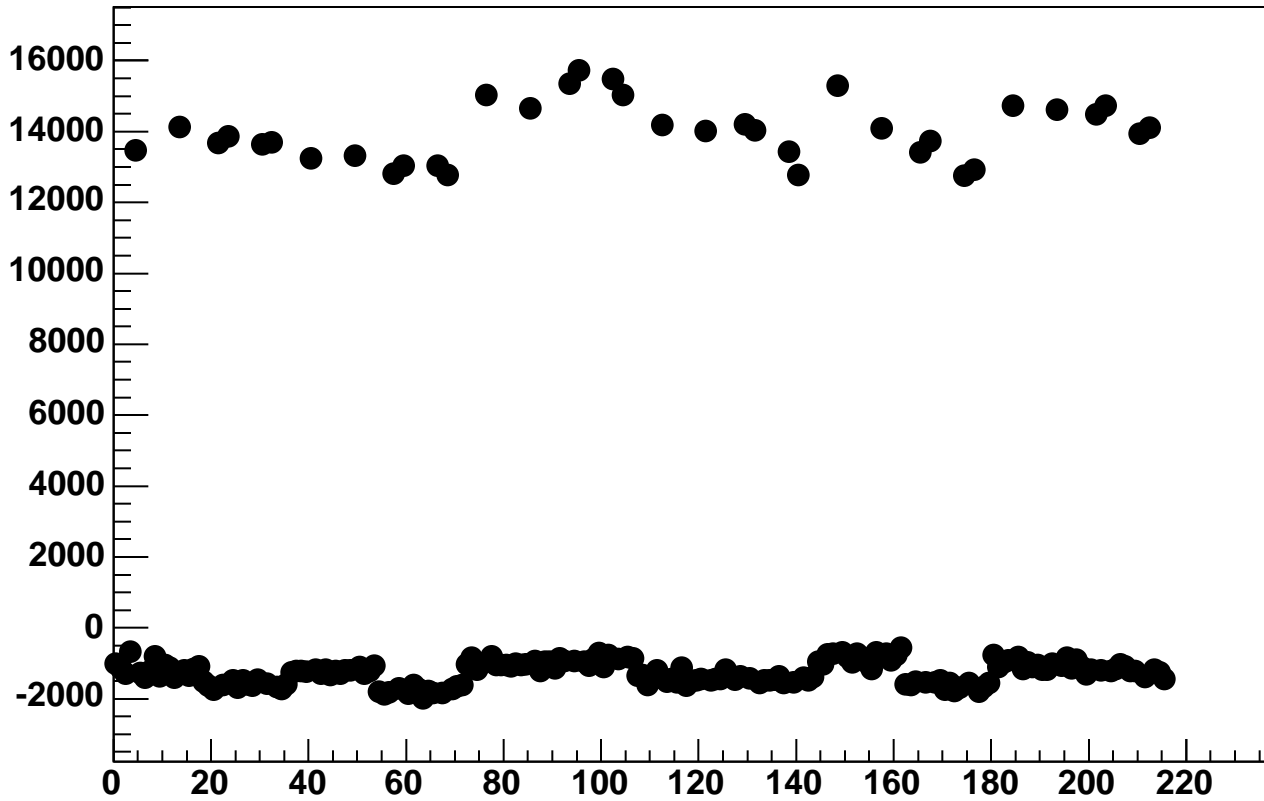
Enable 4, Hold=30, DAC=750, ADC Mean vs 18\*Chip+Chan



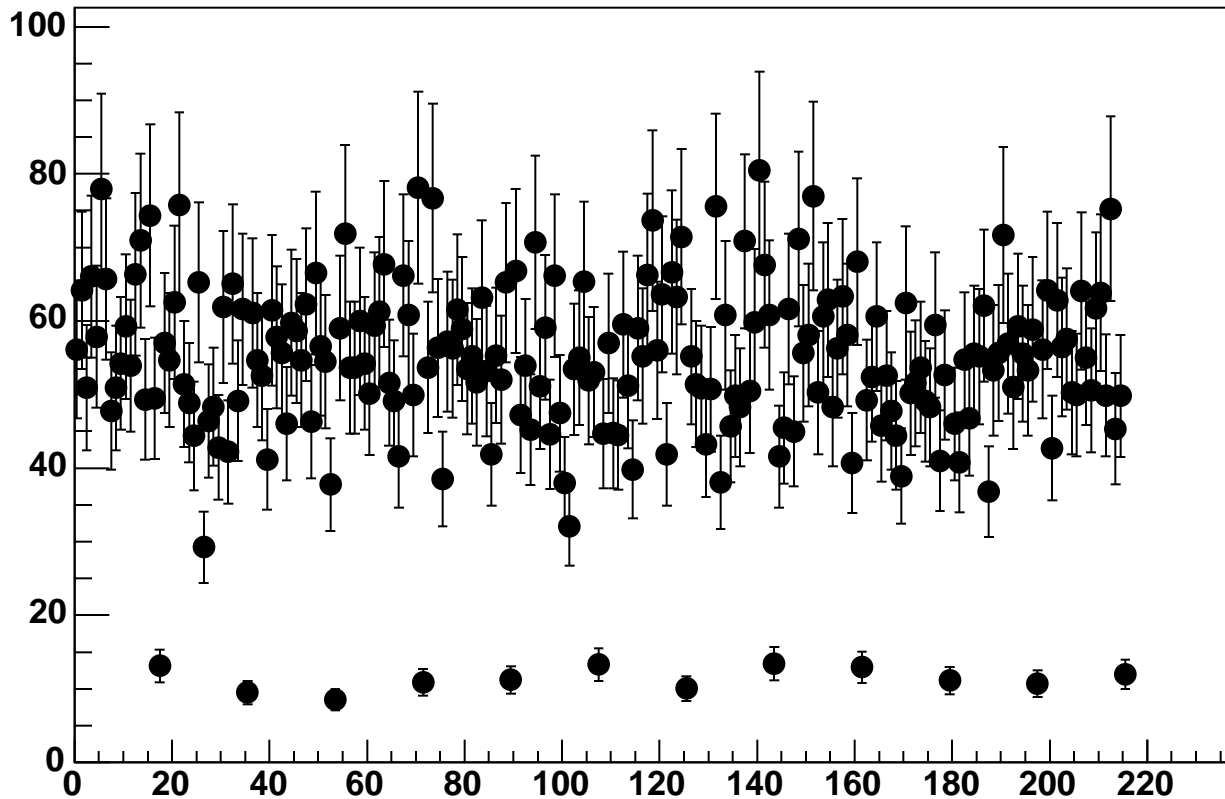
Enable 4, Hold=30, DAC=750, ADC Noise vs 18\*Chip+Chan



Enable 4, Hold=30, DAC=800, ADC Mean vs 18\*Chip+Chan

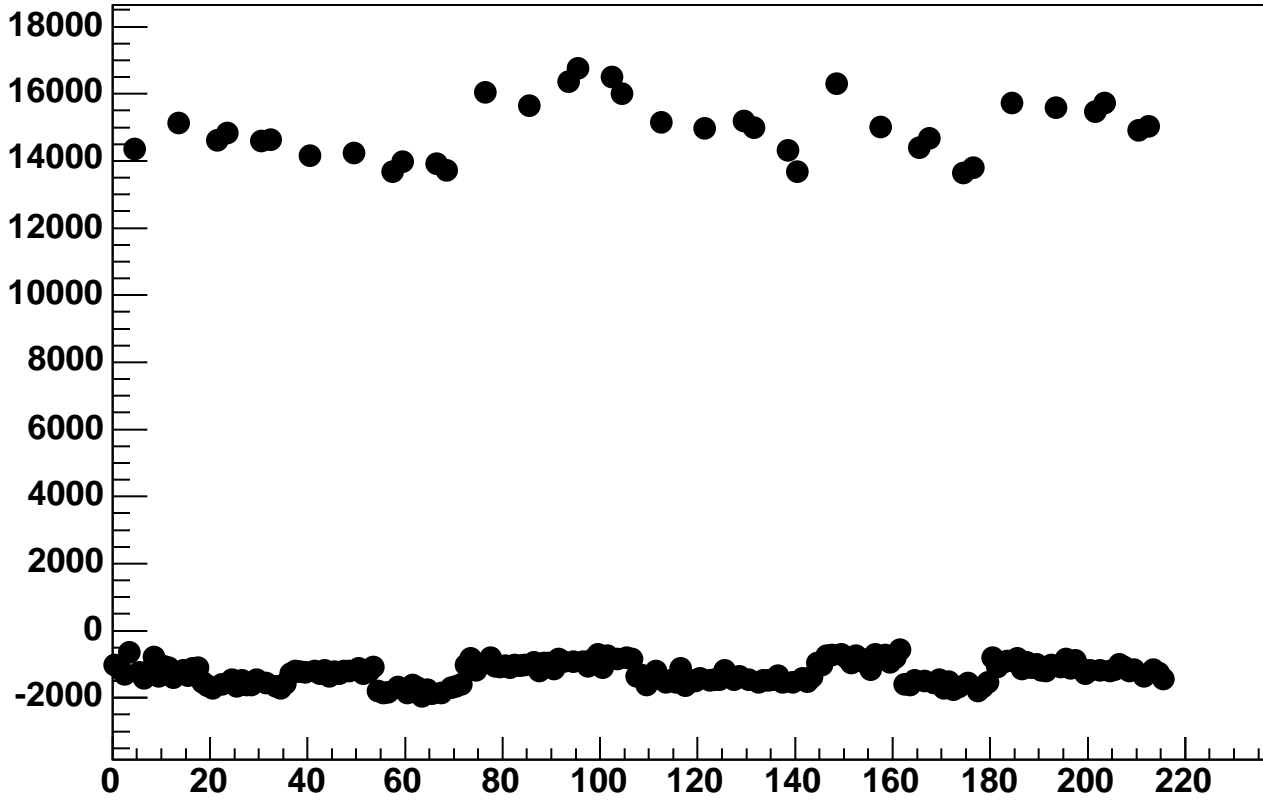


Enable 4, Hold=30, DAC=800, ADC Noise vs 18\*Chip+Chan

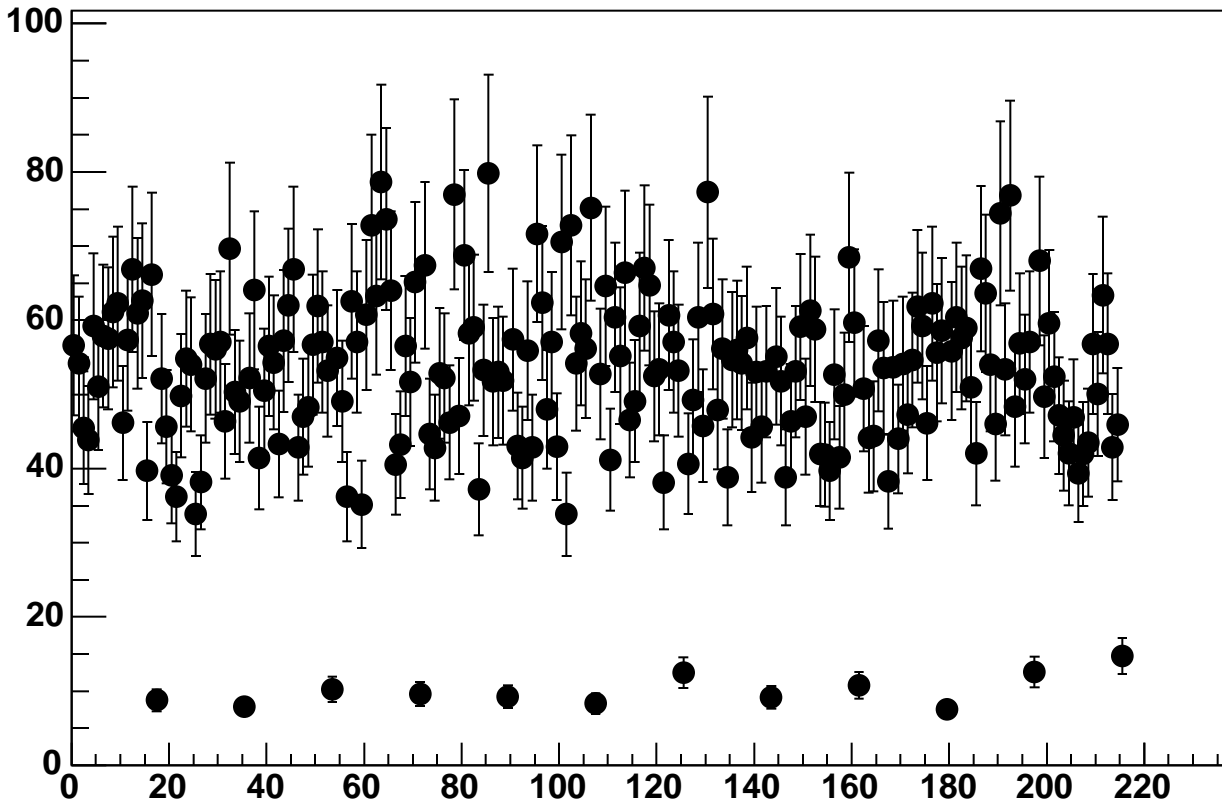




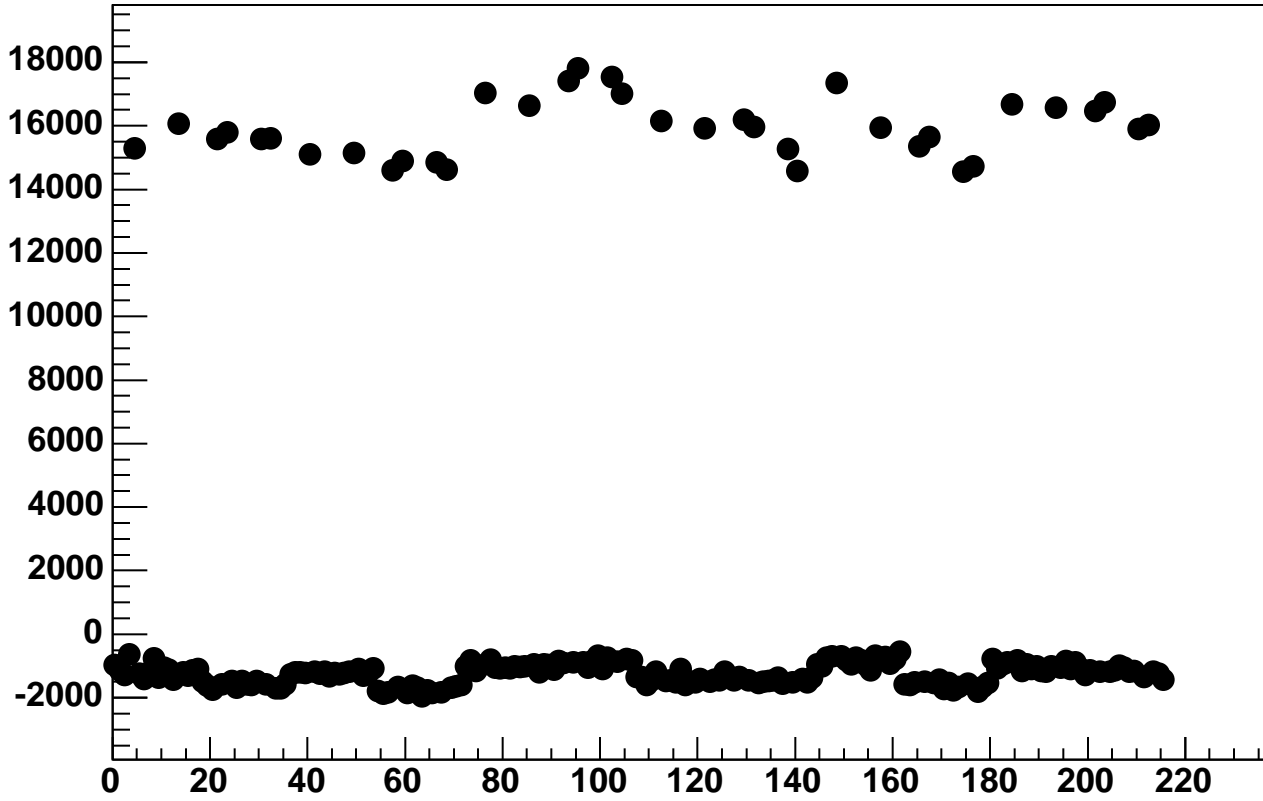
Enable 4, Hold=30, DAC=850, ADC Mean vs 18\*Chip+Chan



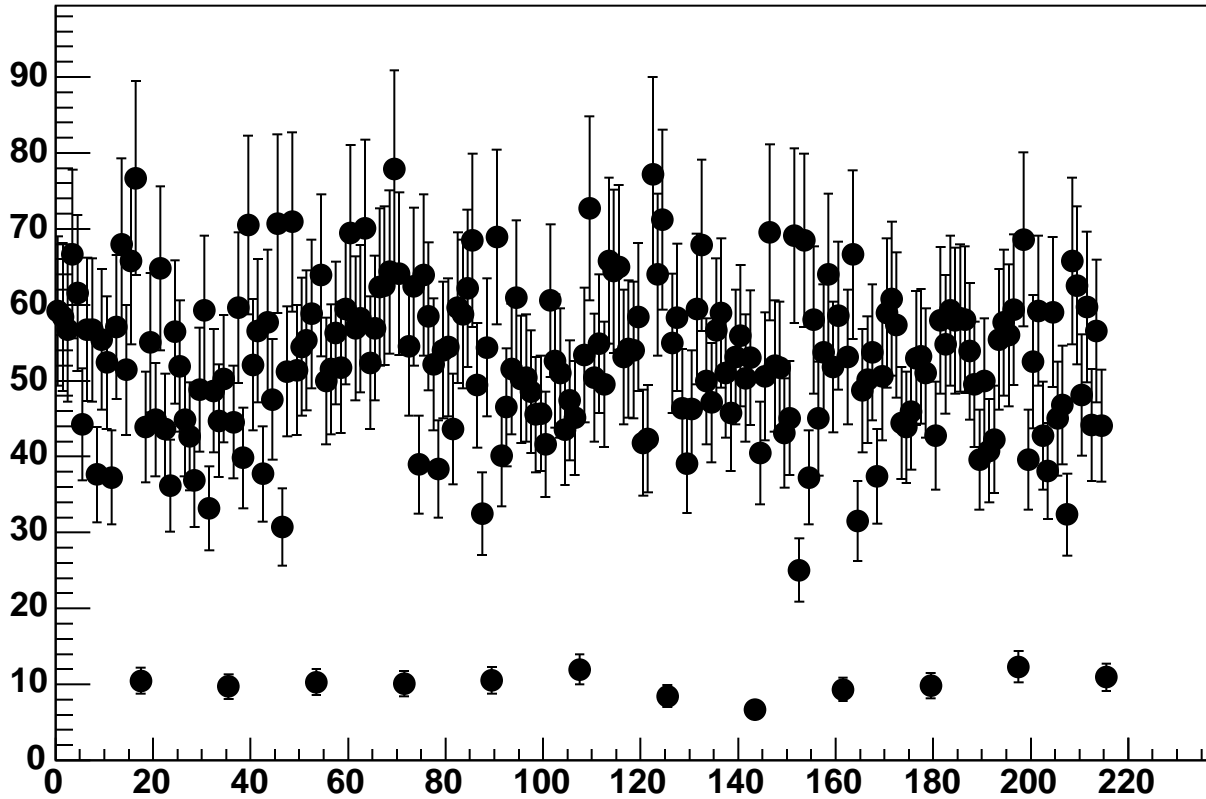
Enable 4, Hold=30, DAC=850, ADC Noise vs 18\*Chip+Chan



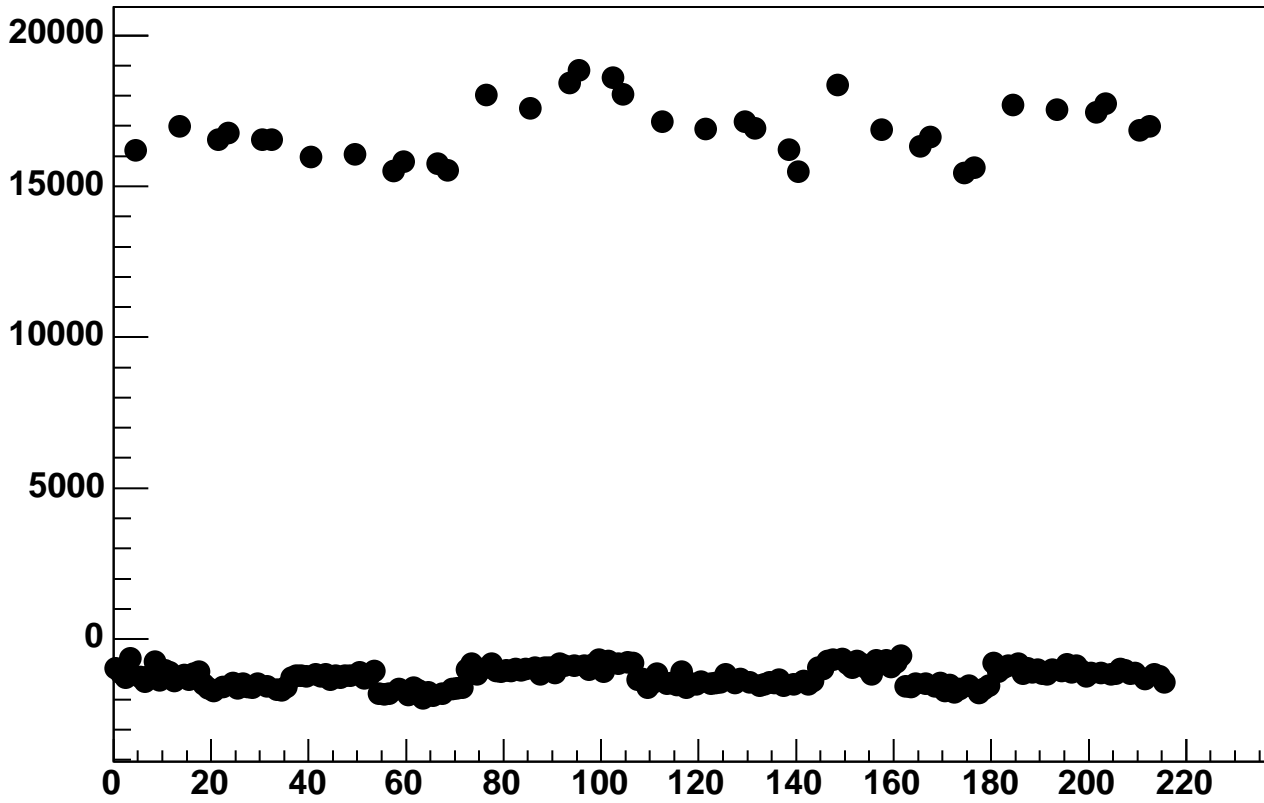
Enable 4, Hold=30, DAC=900, ADC Mean vs 18\*Chip+Chan



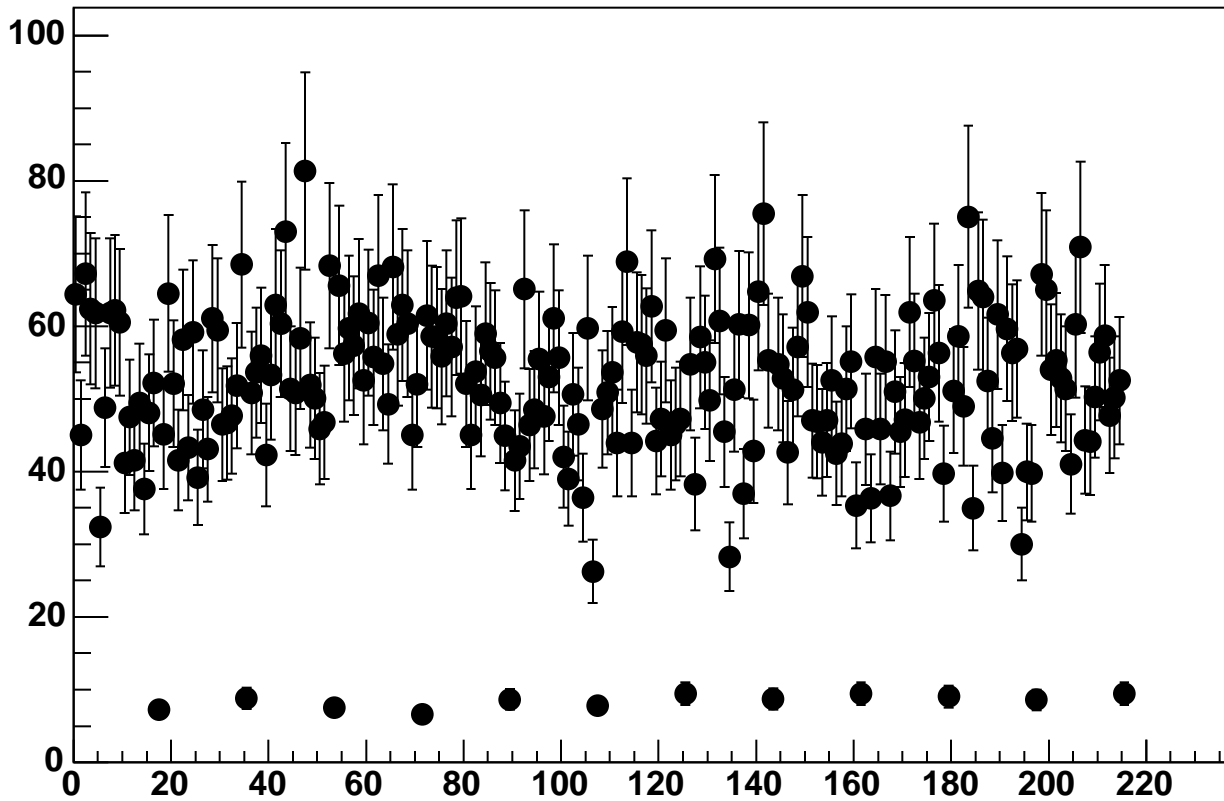
Enable 4, Hold=30, DAC=900, ADC Noise vs 18\*Chip+Chan



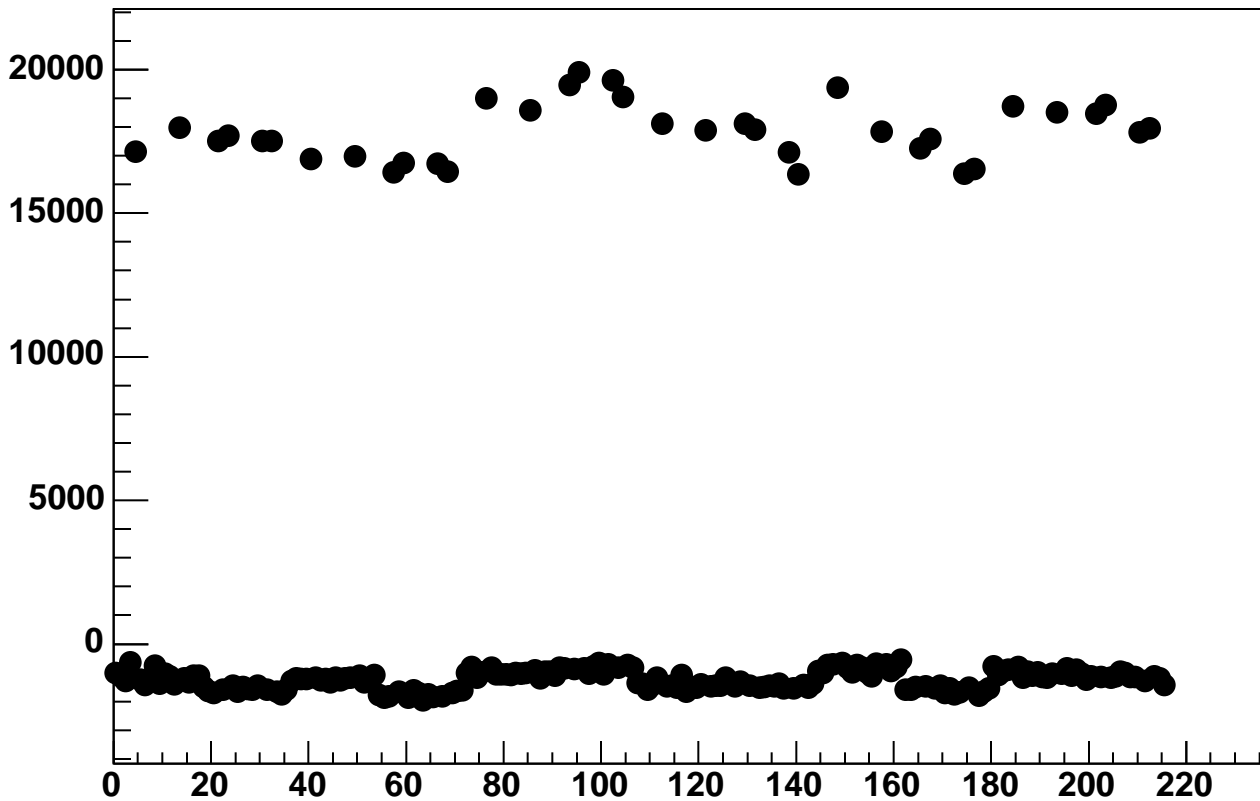
Enable 4, Hold=30, DAC=950, ADC Mean vs 18\*Chip+Chan



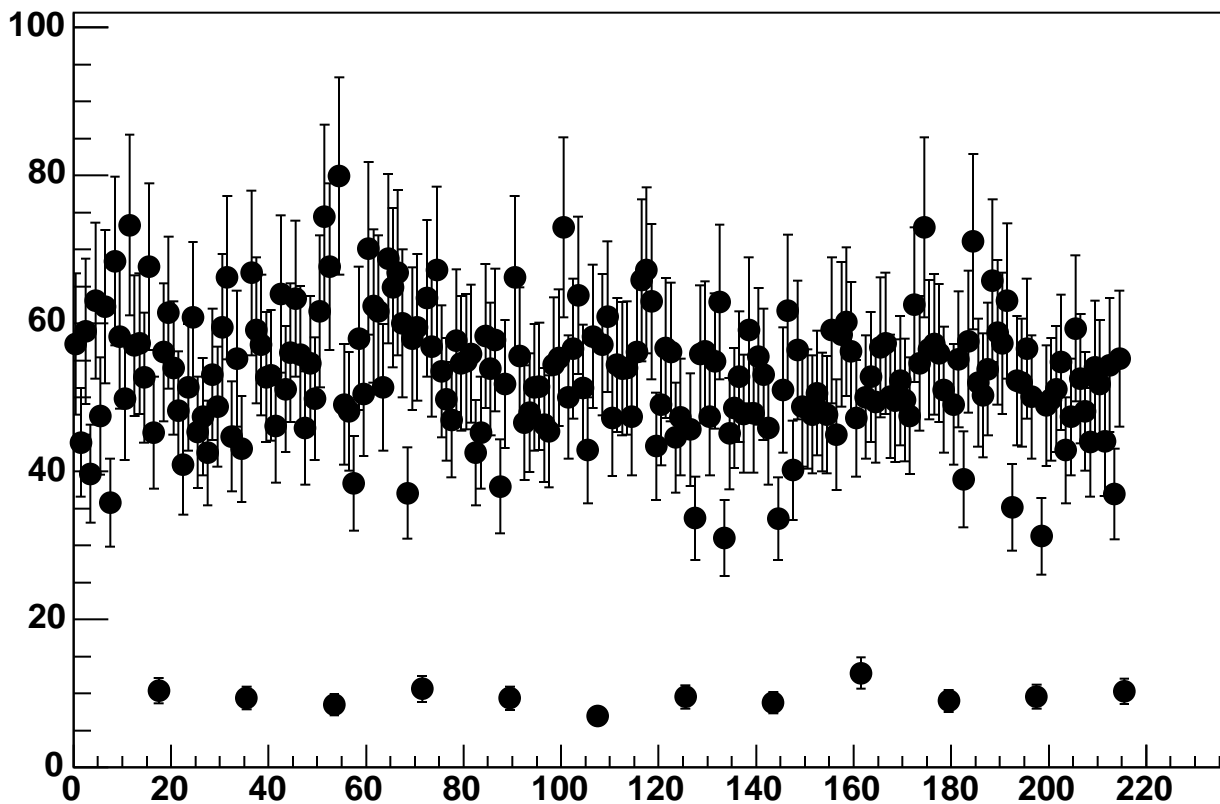
Enable 4, Hold=30, DAC=950, ADC Noise vs 18\*Chip+Chan



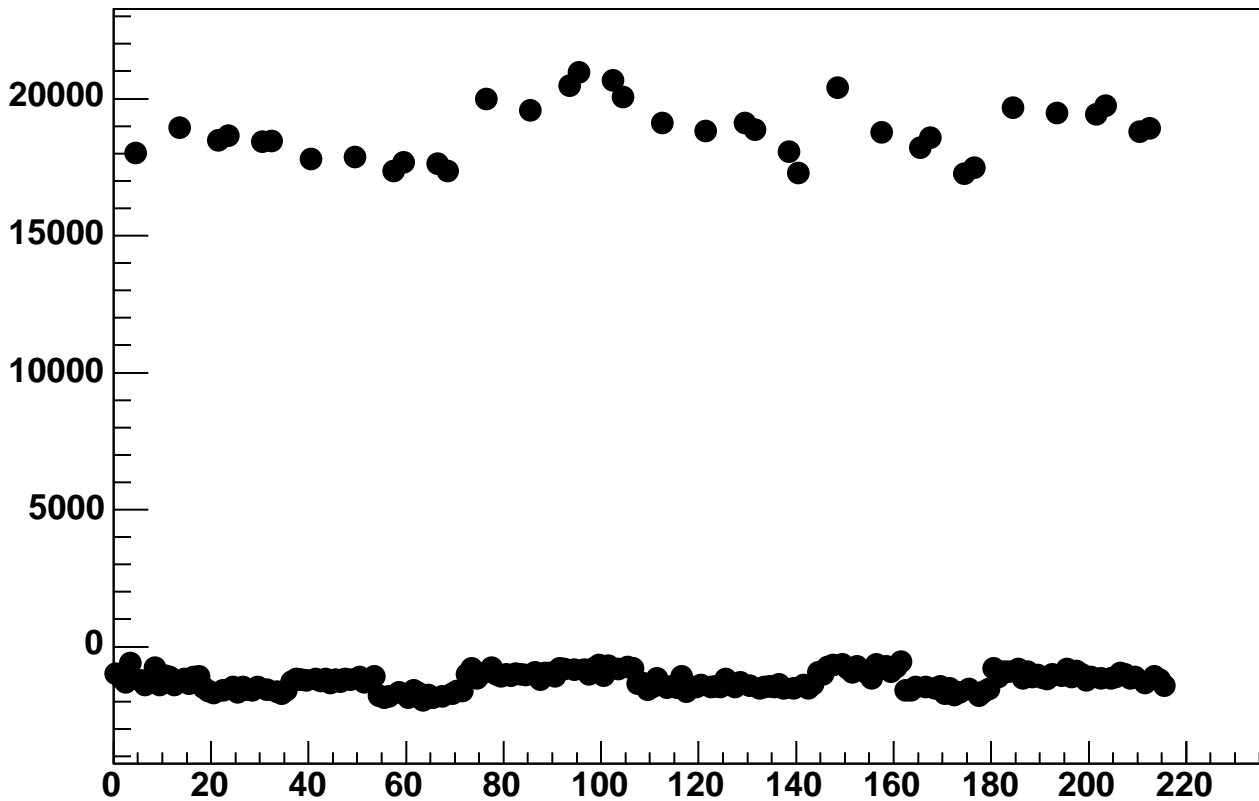
Enable 4, Hold=30, DAC=1000, ADC Mean vs 18\*Chip+Chan



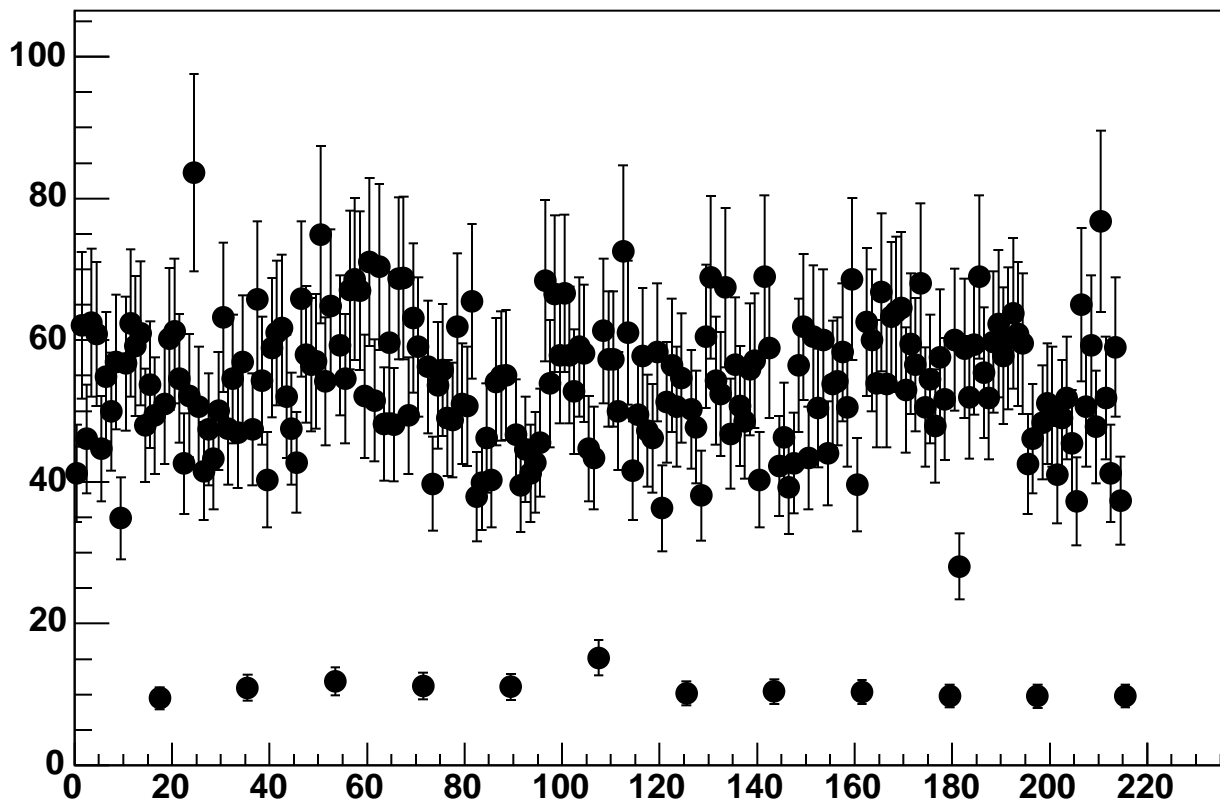
Enable 4, Hold=30, DAC=1000, ADC Noise vs 18\*Chip+Chan



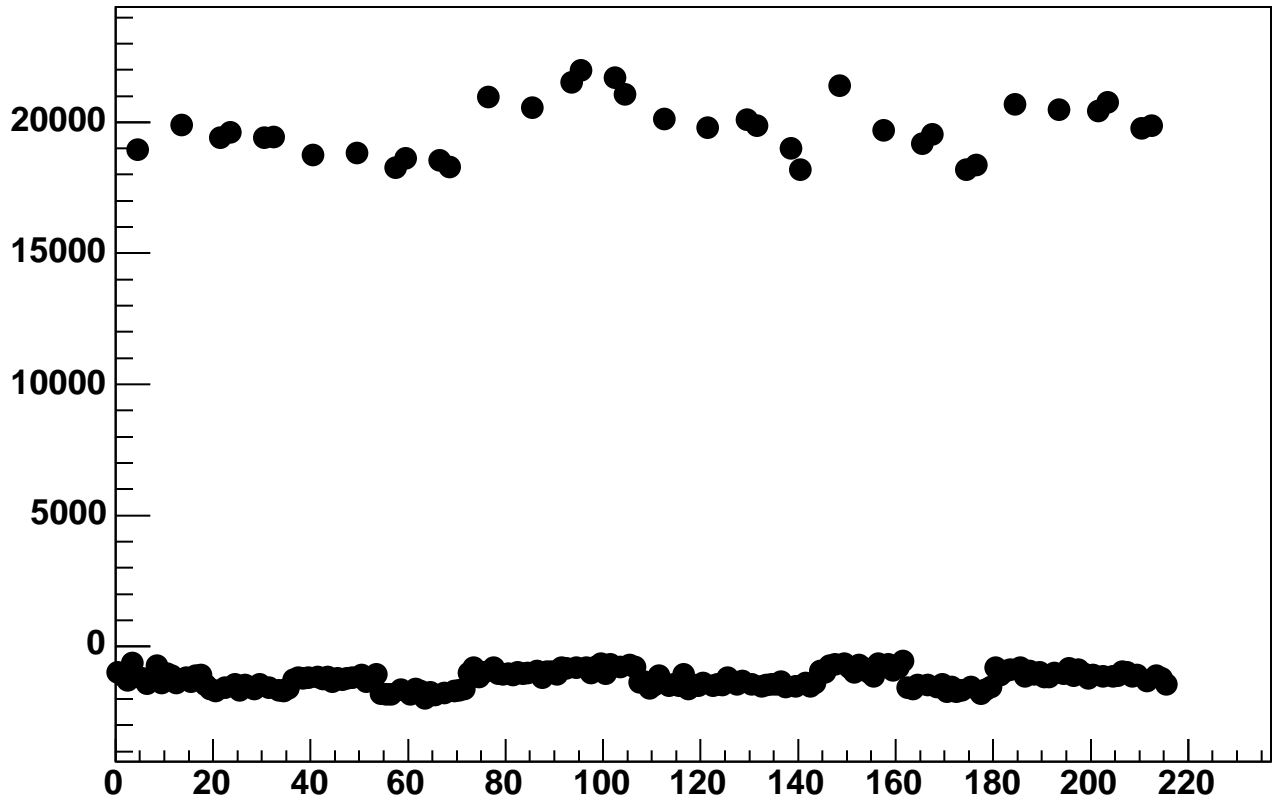
Enable 4, Hold=30, DAC=1050, ADC Mean vs 18\*Chip+Chan



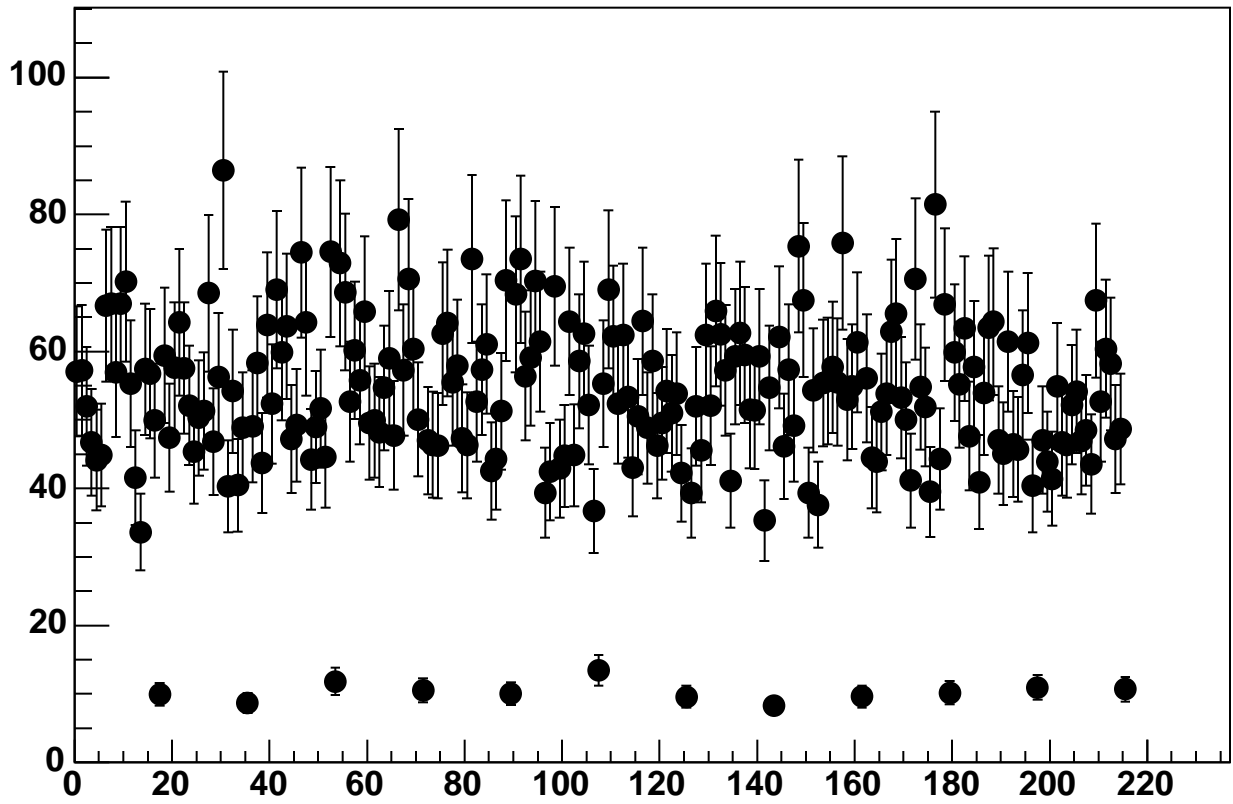
Enable 4, Hold=30, DAC=1050, ADC Noise vs 18\*Chip+Chan



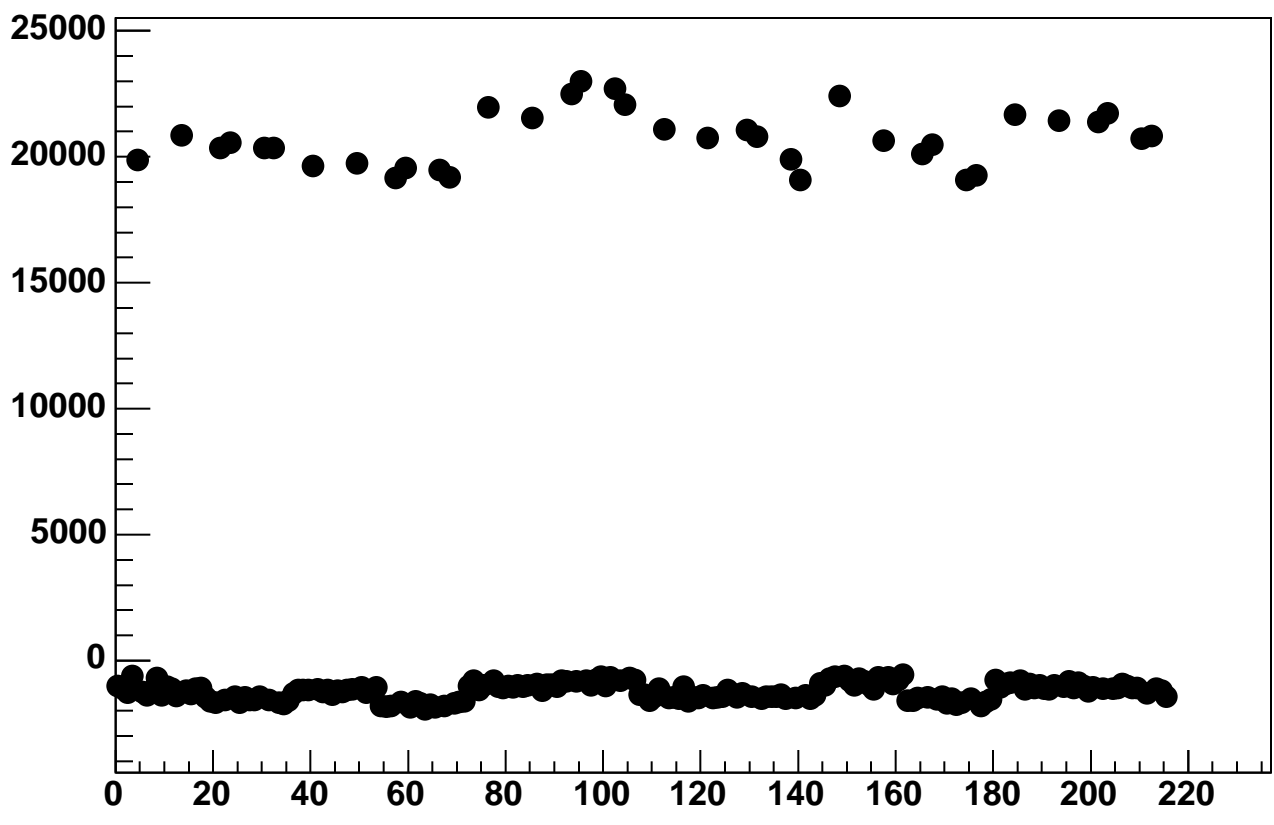
Enable 4, Hold=30, DAC=1100, ADC Mean vs 18\*Chip+Chan



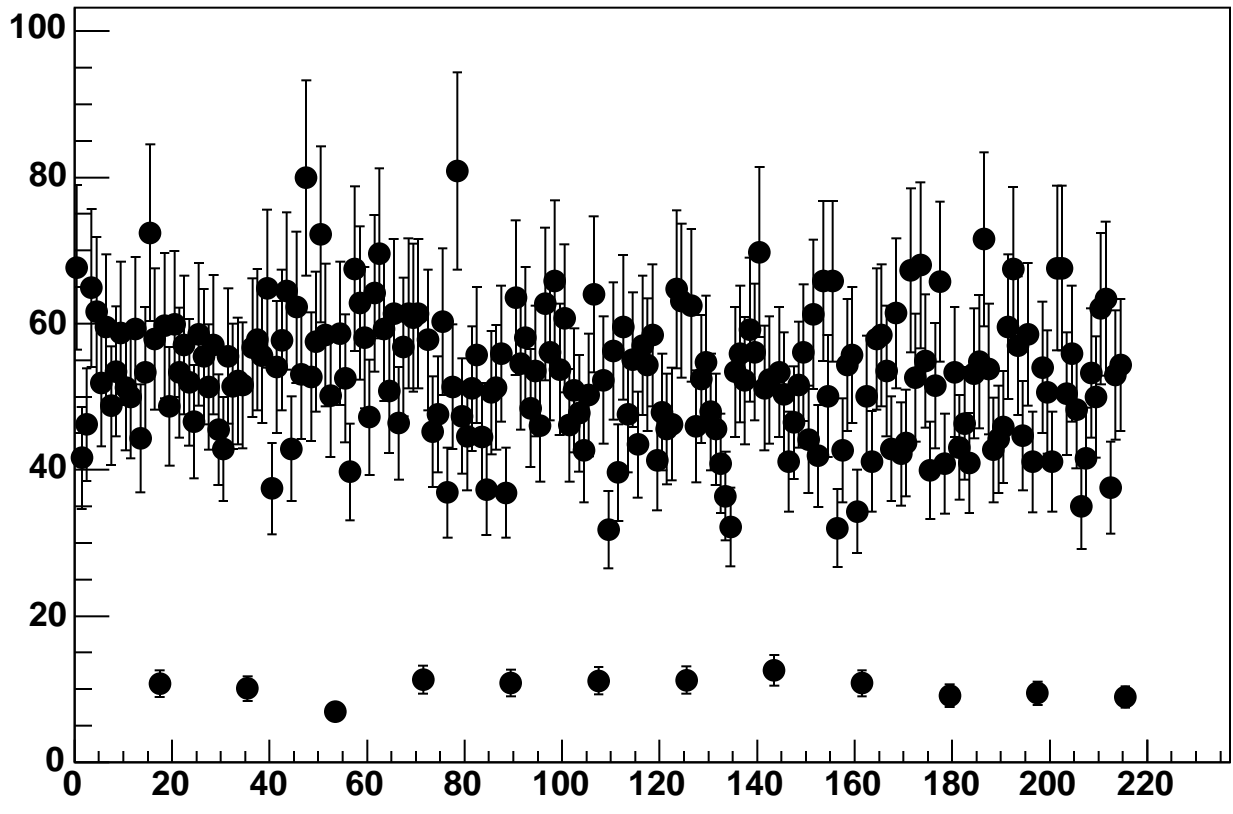
Enable 4, Hold=30, DAC=1100, ADC Noise vs 18\*Chip+Chan



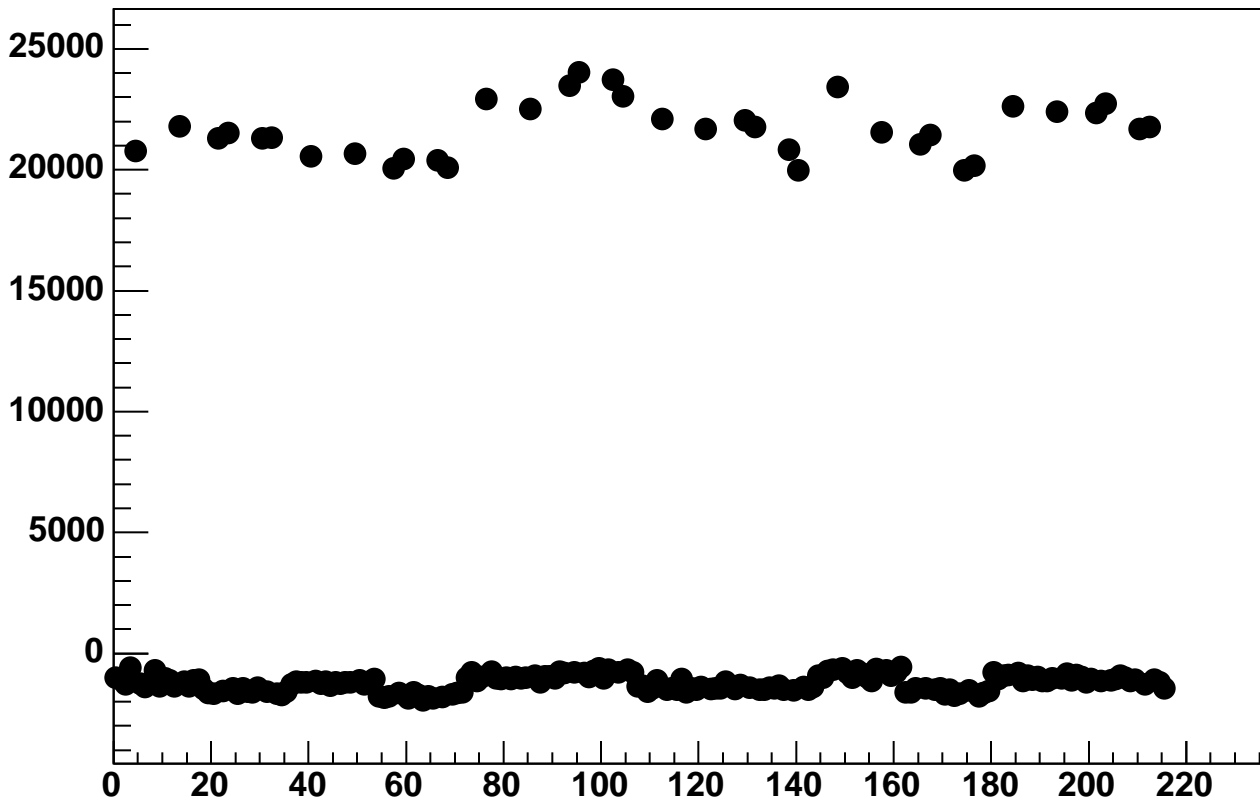
Enable 4, Hold=30, DAC=1150, ADC Mean vs 18\*Chip+Chan



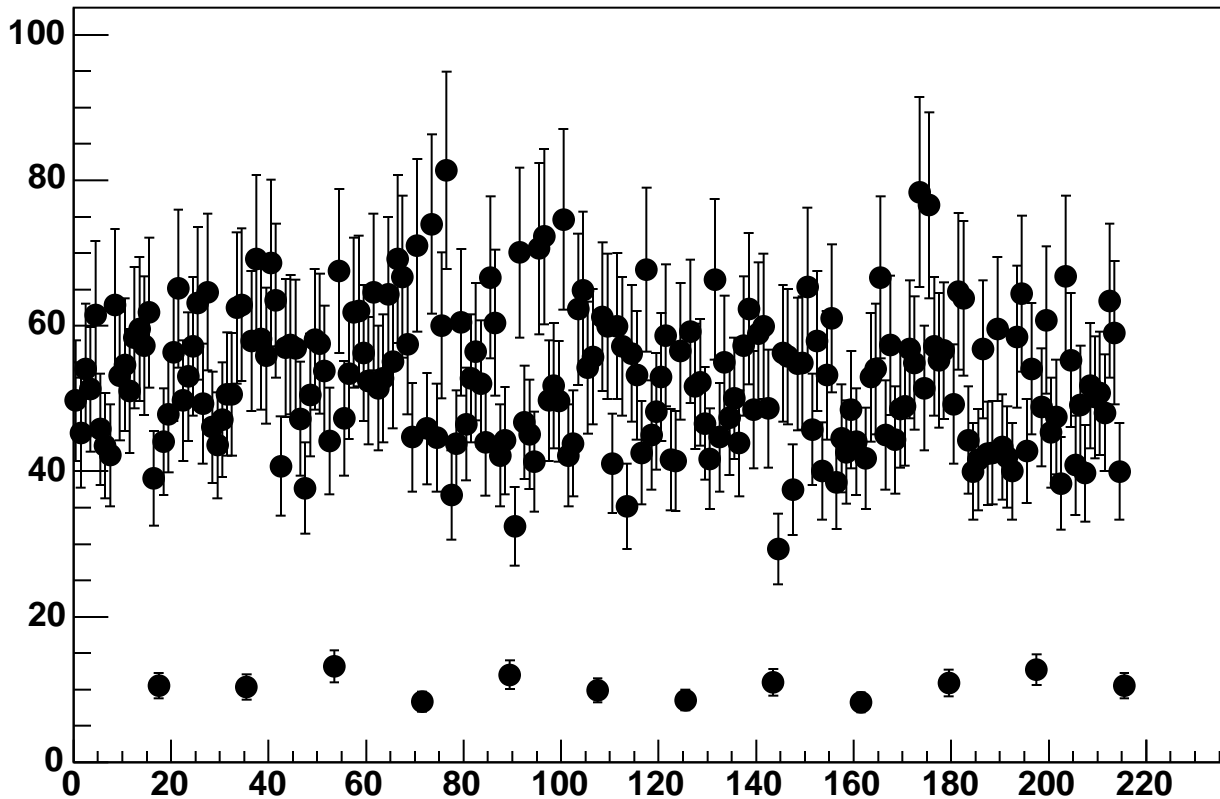
Enable 4, Hold=30, DAC=1150, ADC Noise vs 18\*Chip+Chan



Enable 4, Hold=30, DAC=1200, ADC Mean vs 18\*Chip+Chan

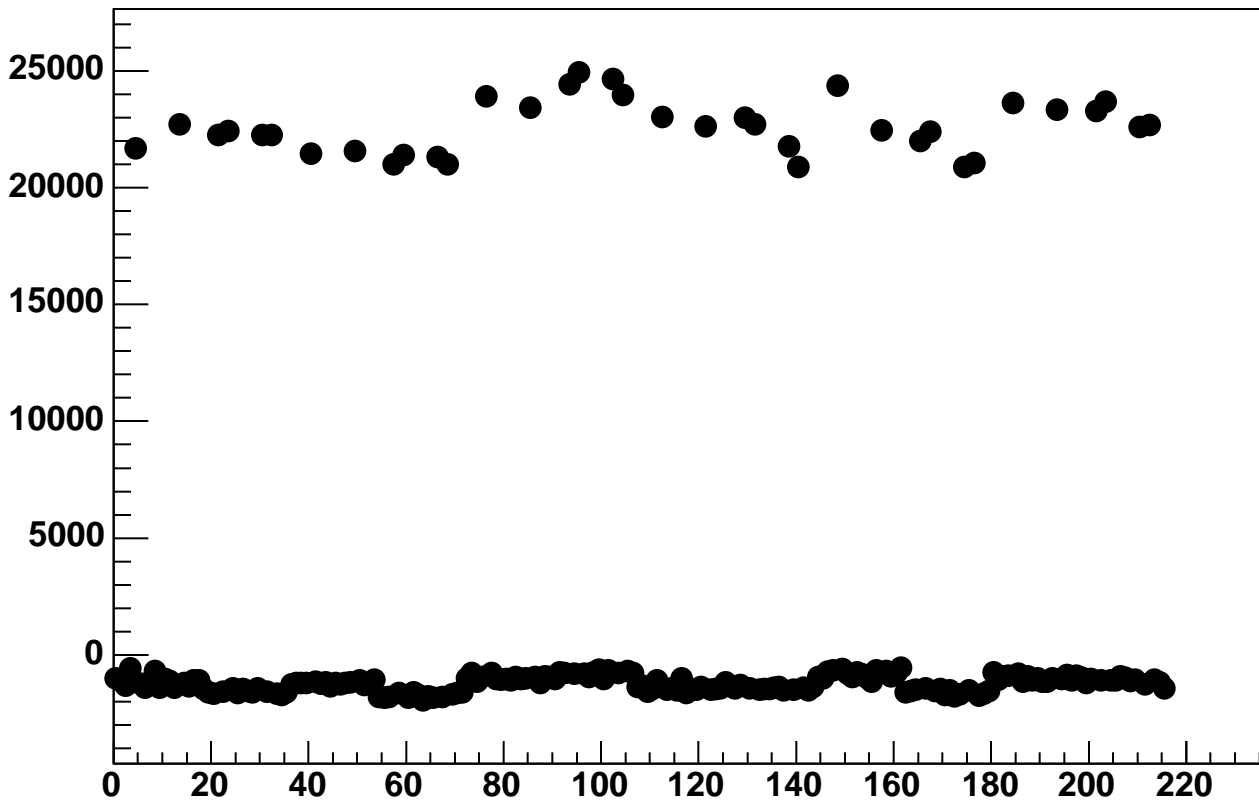


Enable 4, Hold=30, DAC=1200, ADC Noise vs 18\*Chip+Chan

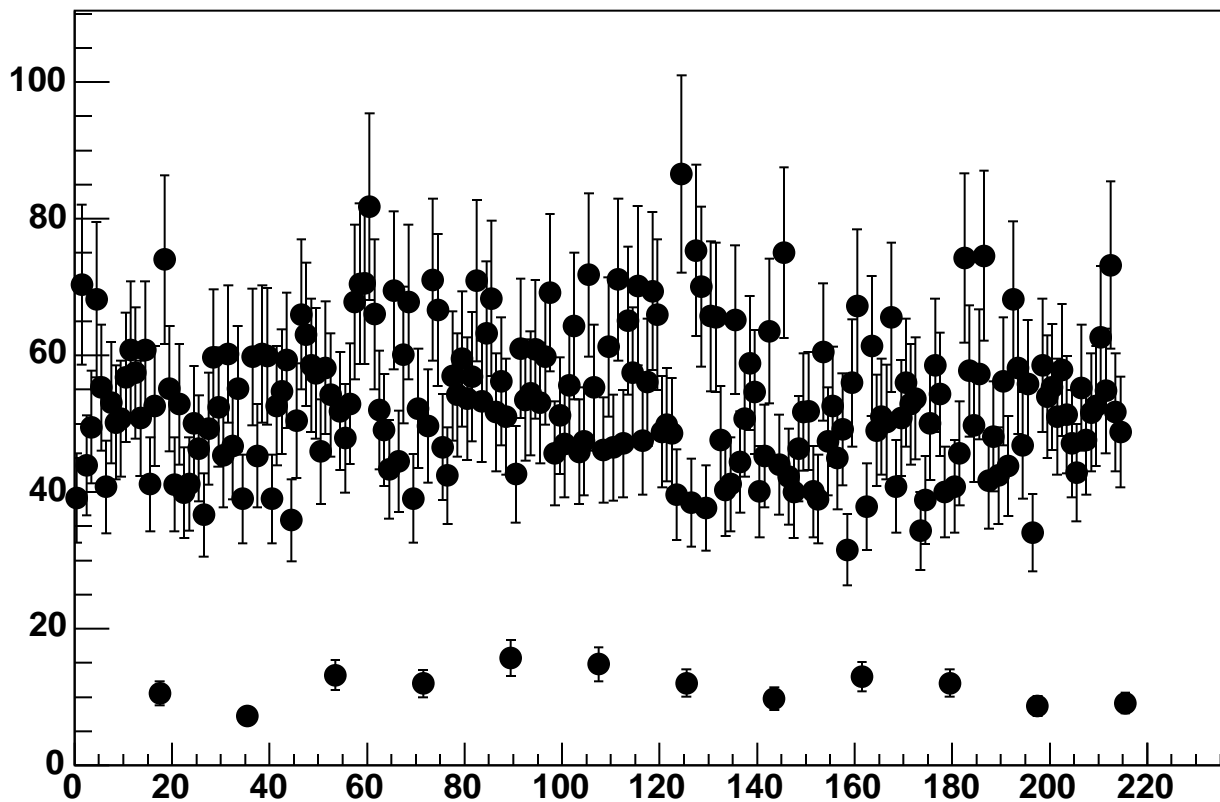




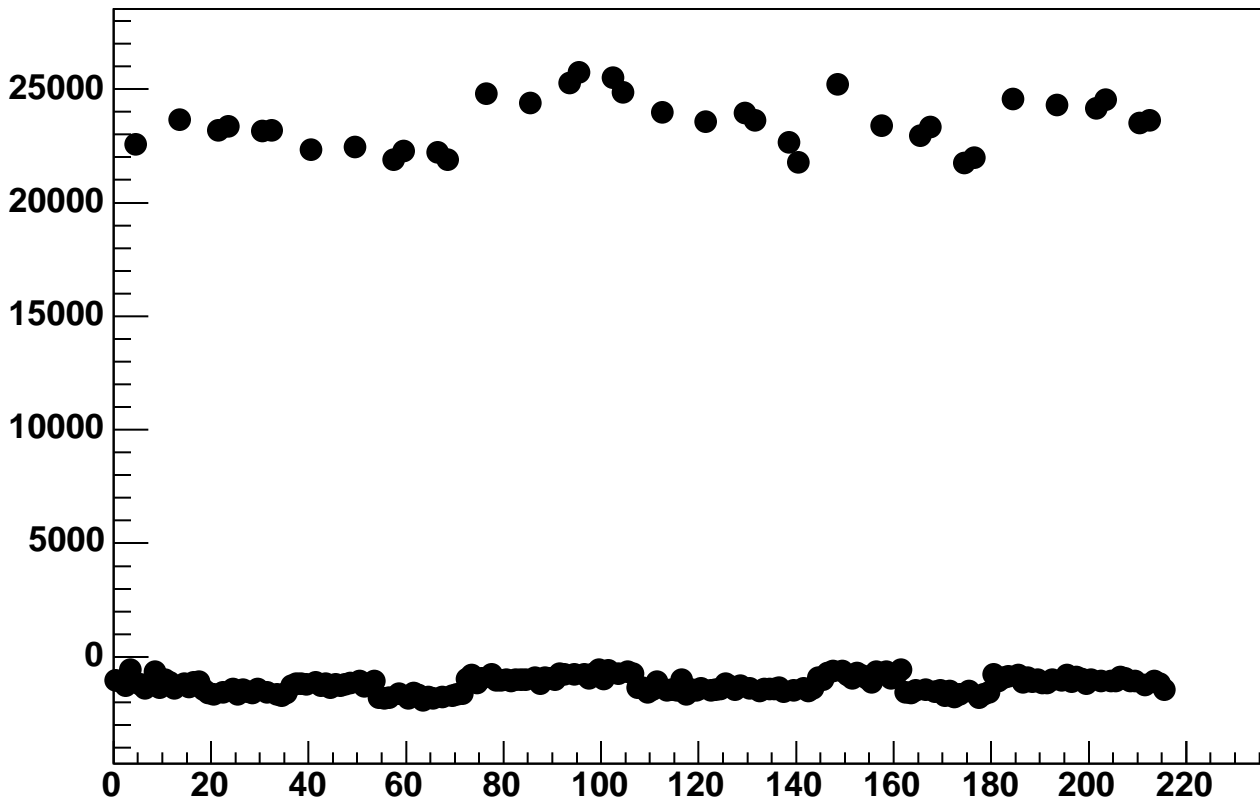
Enable 4, Hold=30, DAC=1250, ADC Mean vs 18\*Chip+Chan



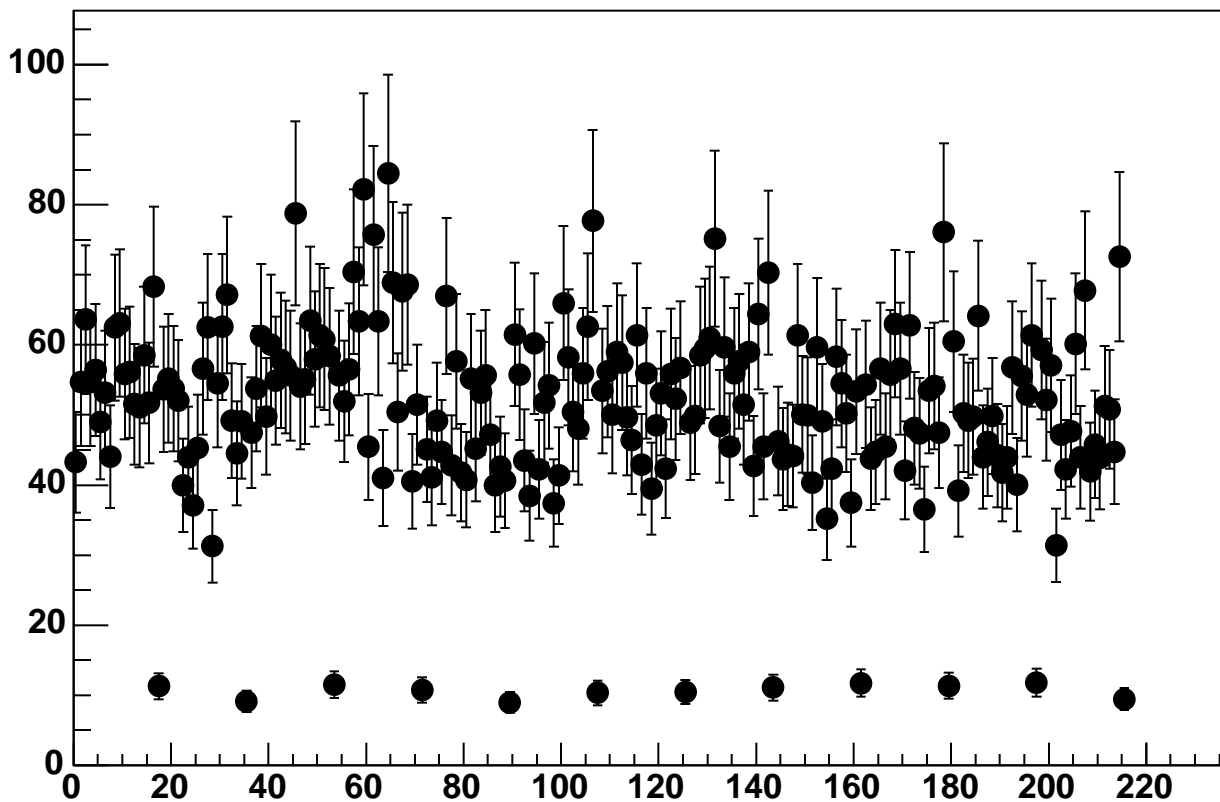
Enable 4, Hold=30, DAC=1250, ADC Noise vs 18\*Chip+Chan



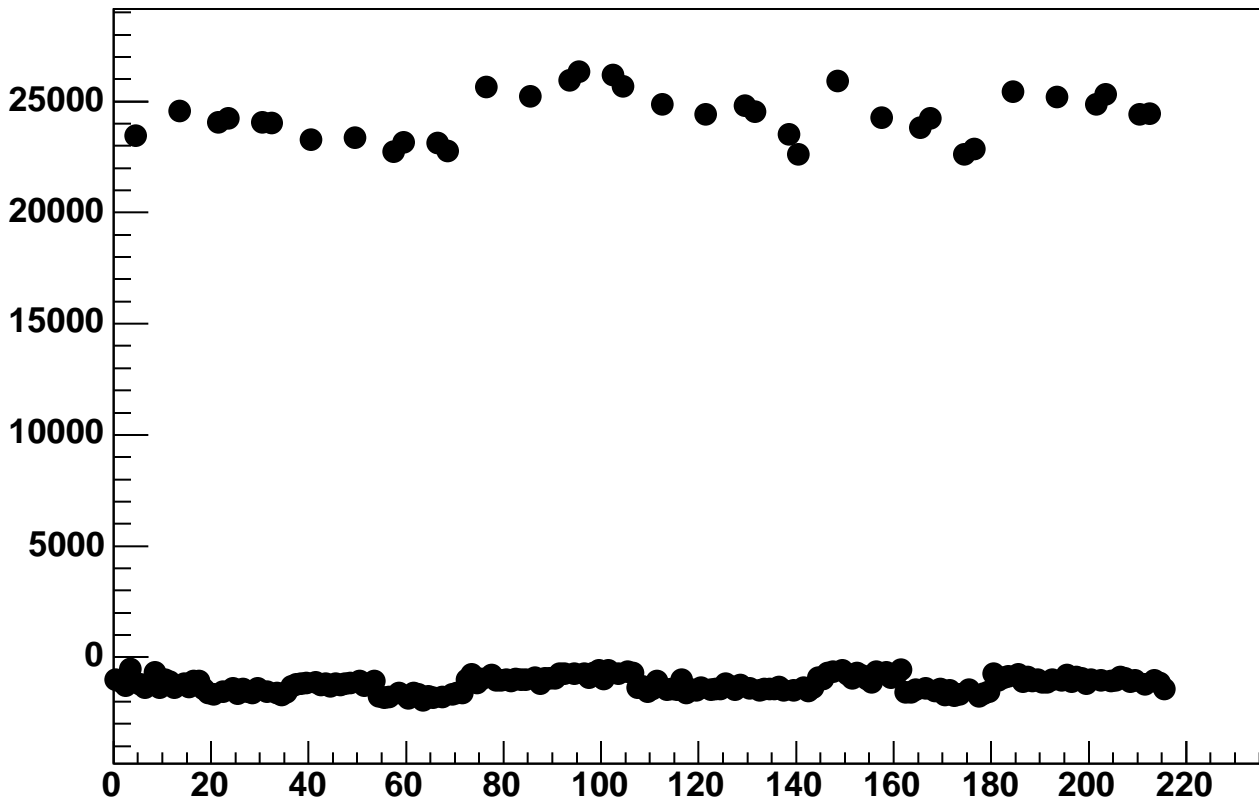
Enable 4, Hold=30, DAC=1300, ADC Mean vs 18\*Chip+Chan



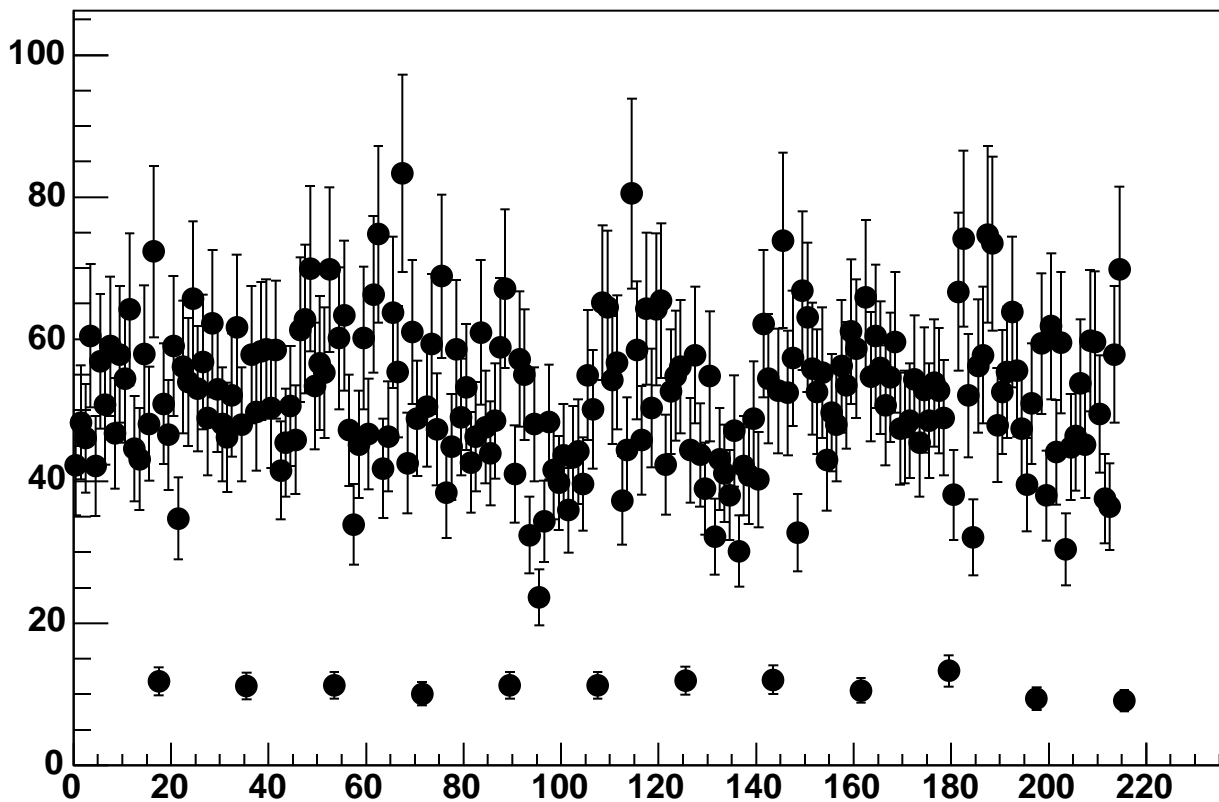
Enable 4, Hold=30, DAC=1300, ADC Noise vs 18\*Chip+Chan



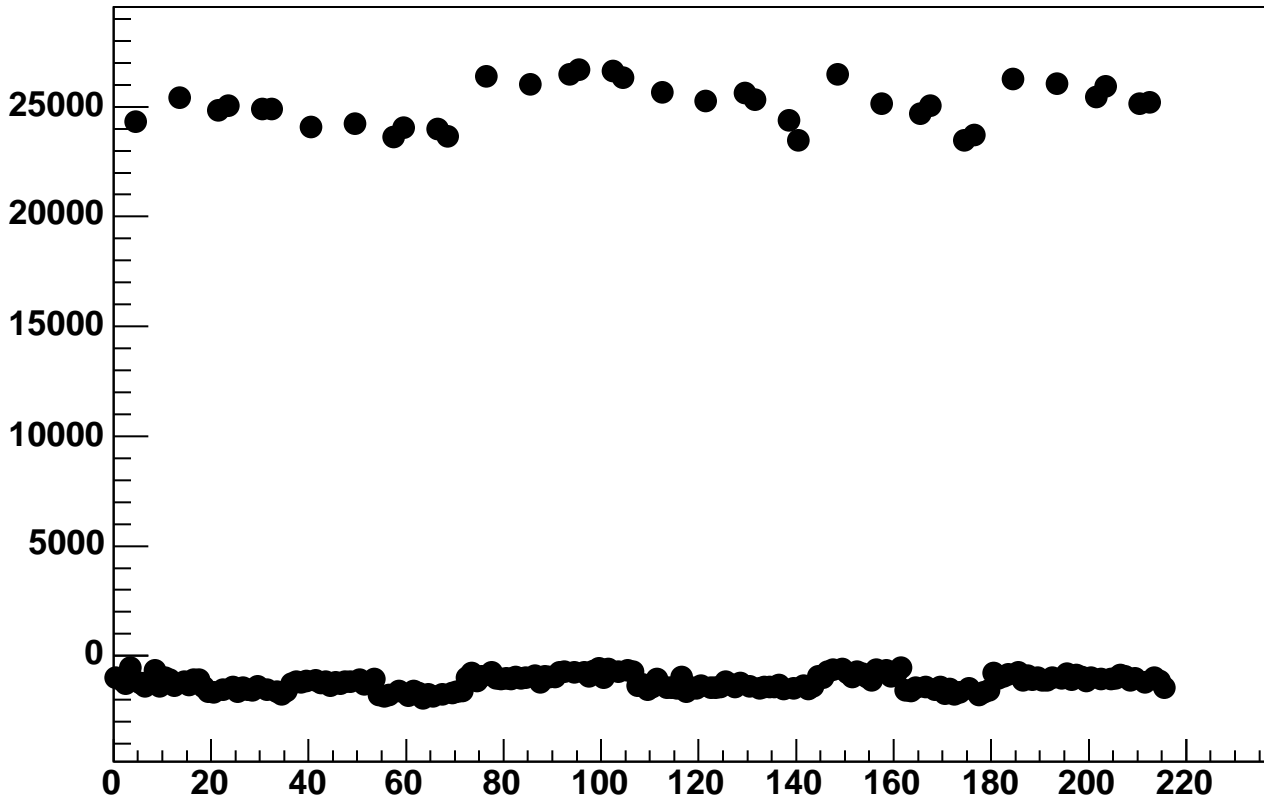
Enable 4, Hold=30, DAC=1350, ADC Mean vs 18\*Chip+Chan



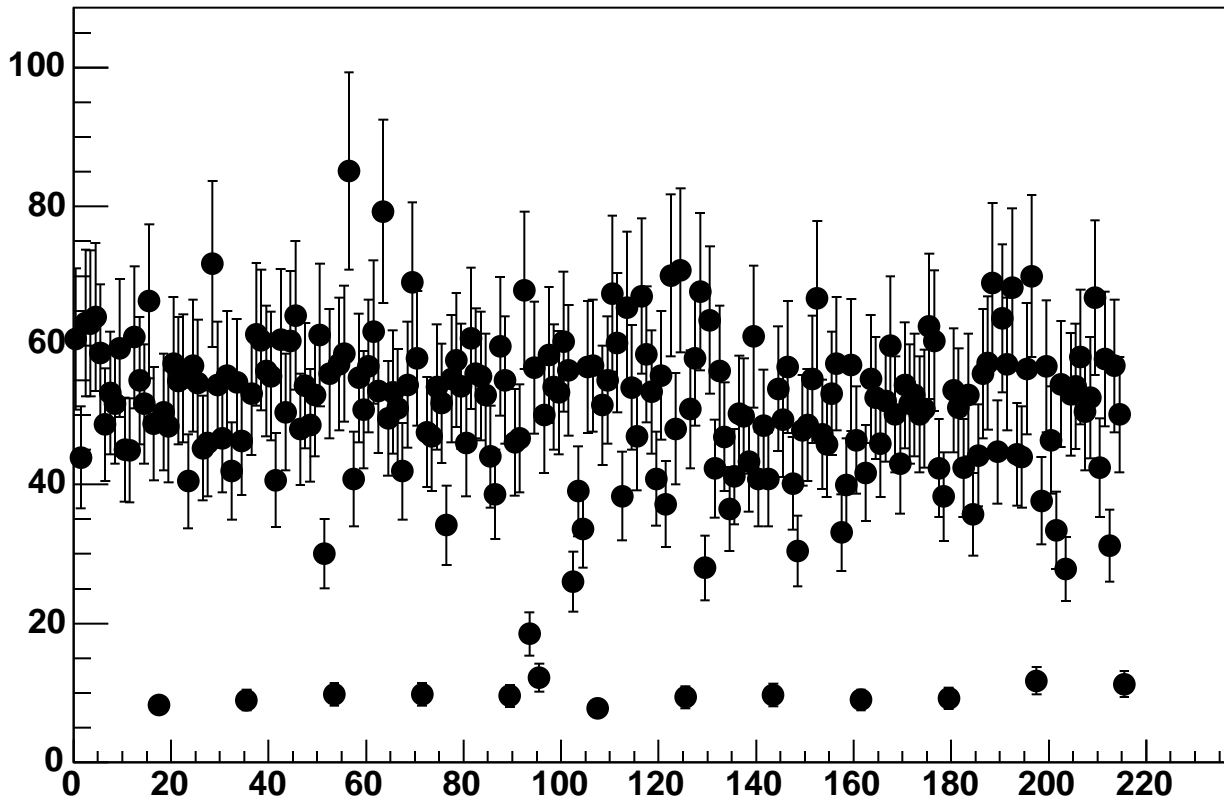
Enable 4, Hold=30, DAC=1350, ADC Noise vs 18\*Chip+Chan



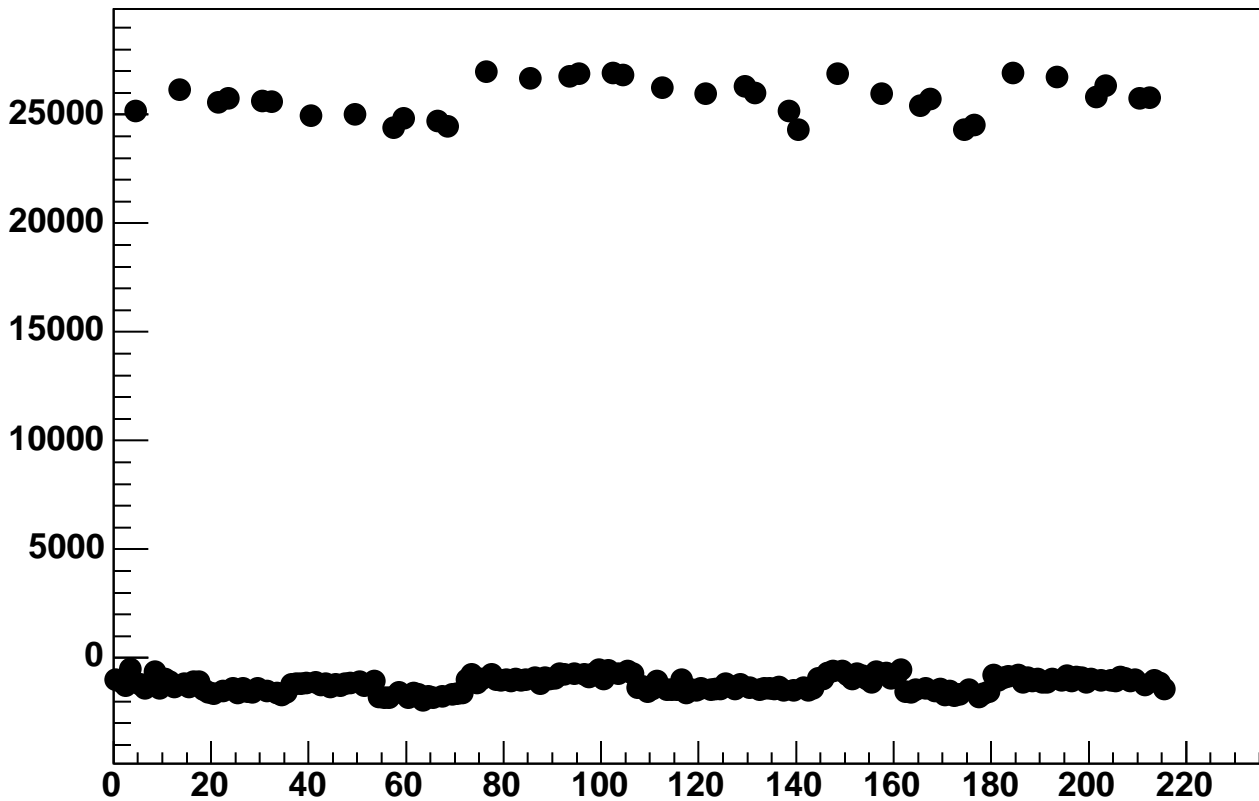
Enable 4, Hold=30, DAC=1400, ADC Mean vs 18\*Chip+Chan



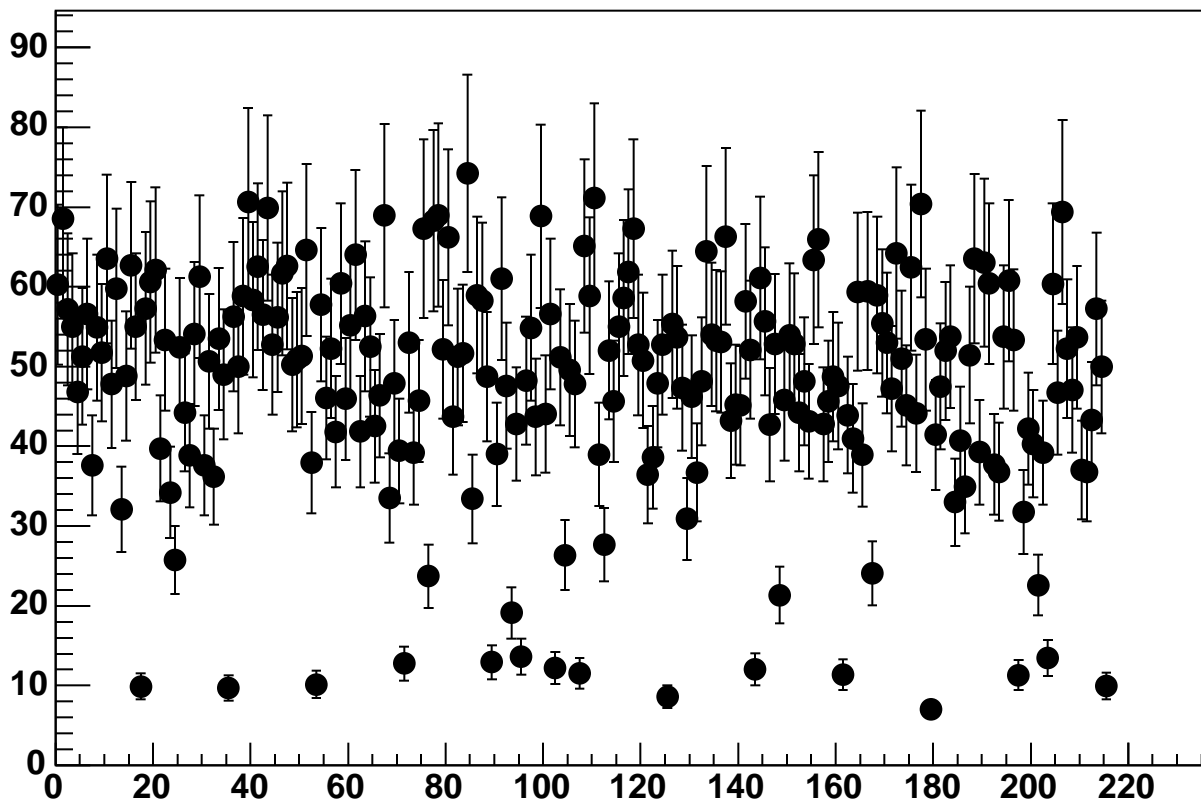
Enable 4, Hold=30, DAC=1400, ADC Noise vs 18\*Chip+Chan



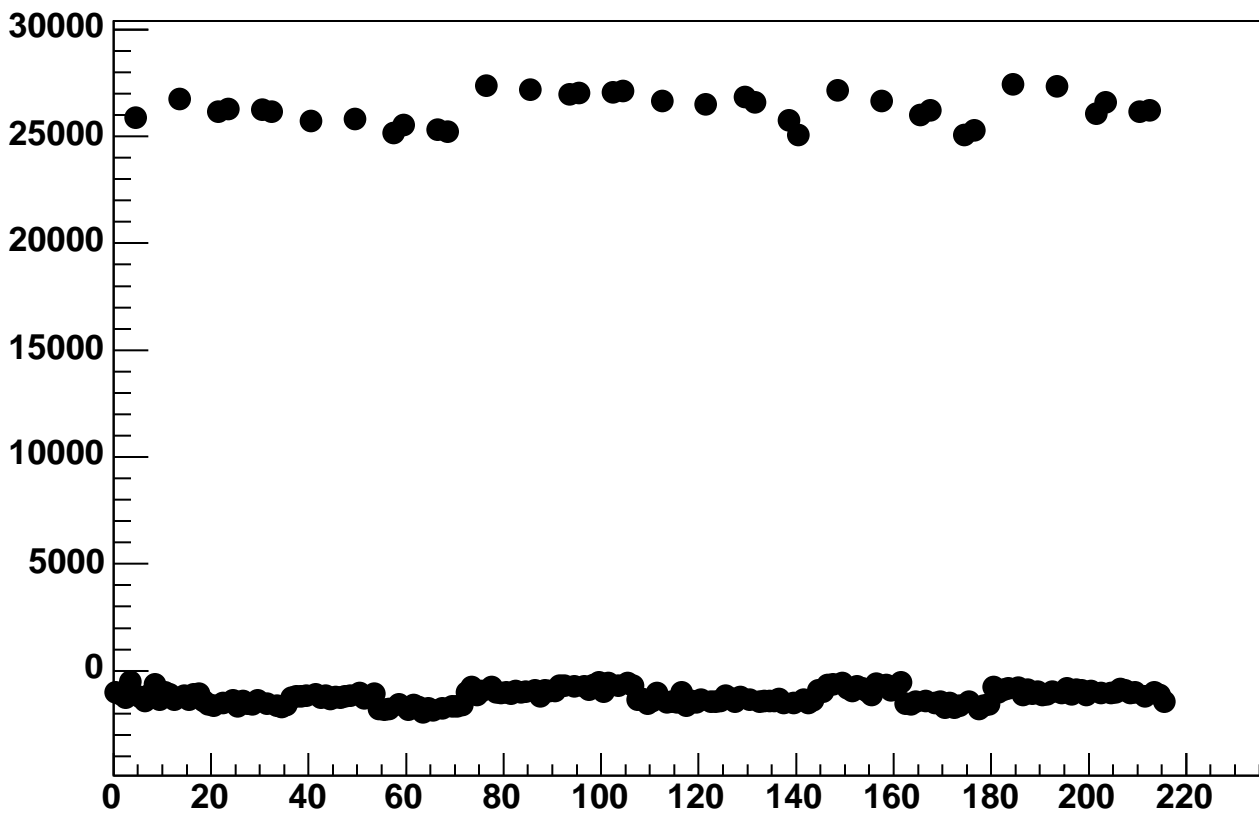
Enable 4, Hold=30, DAC=1450, ADC Mean vs 18\*Chip+Chan



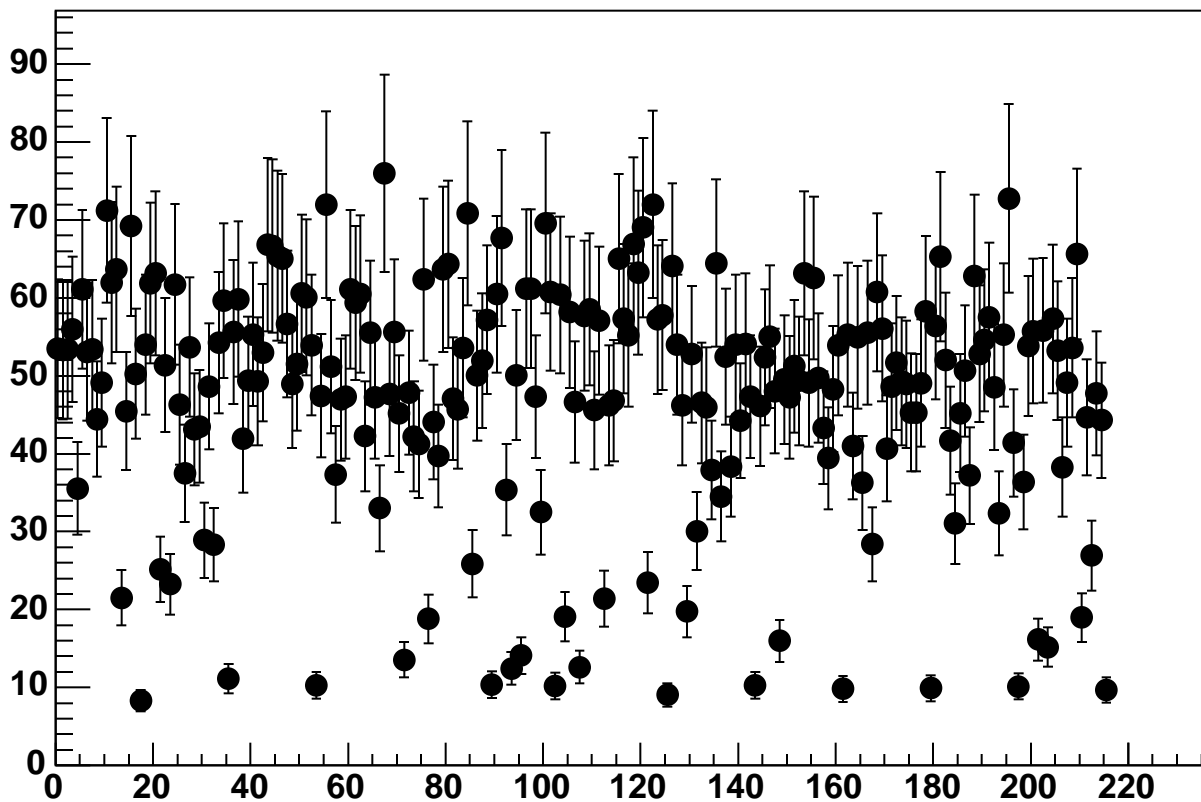
Enable 4, Hold=30, DAC=1450, ADC Noise vs 18\*Chip+Chan



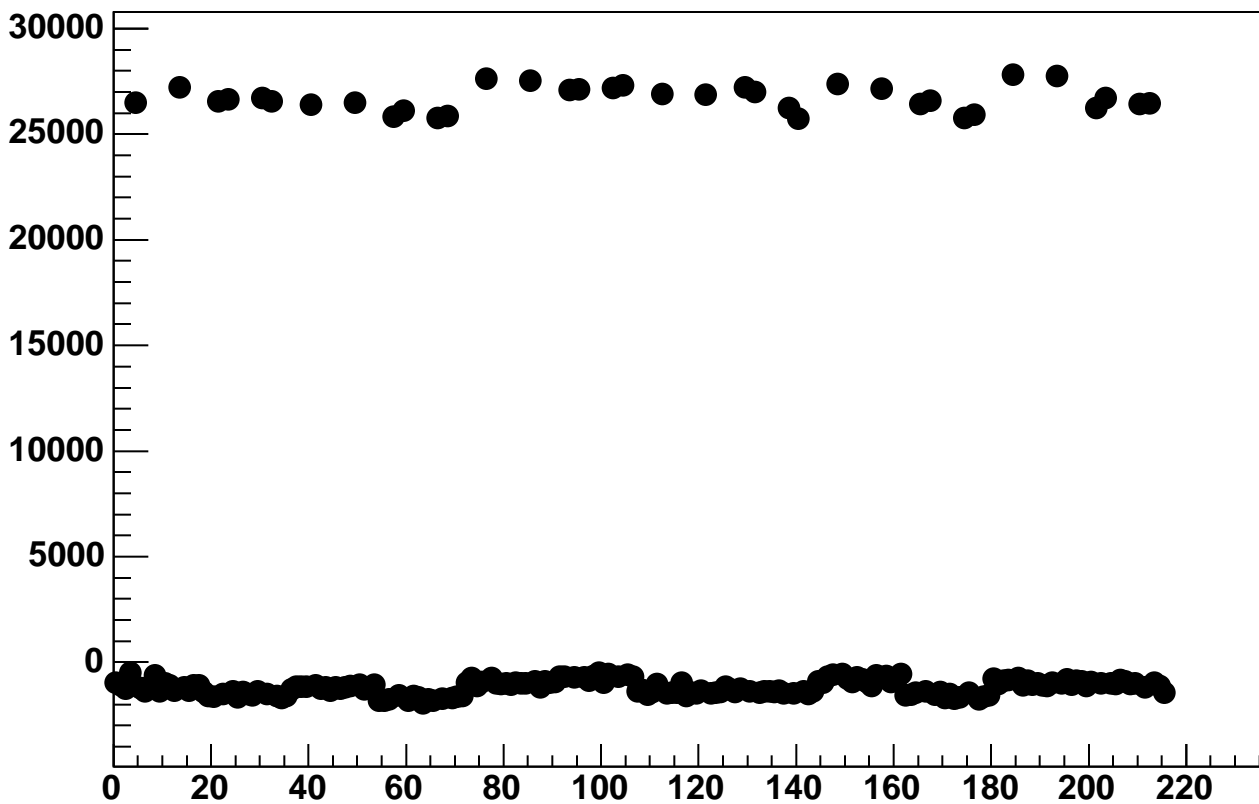
Enable 4, Hold=30, DAC=1500, ADC Mean vs 18\*Chip+Chan



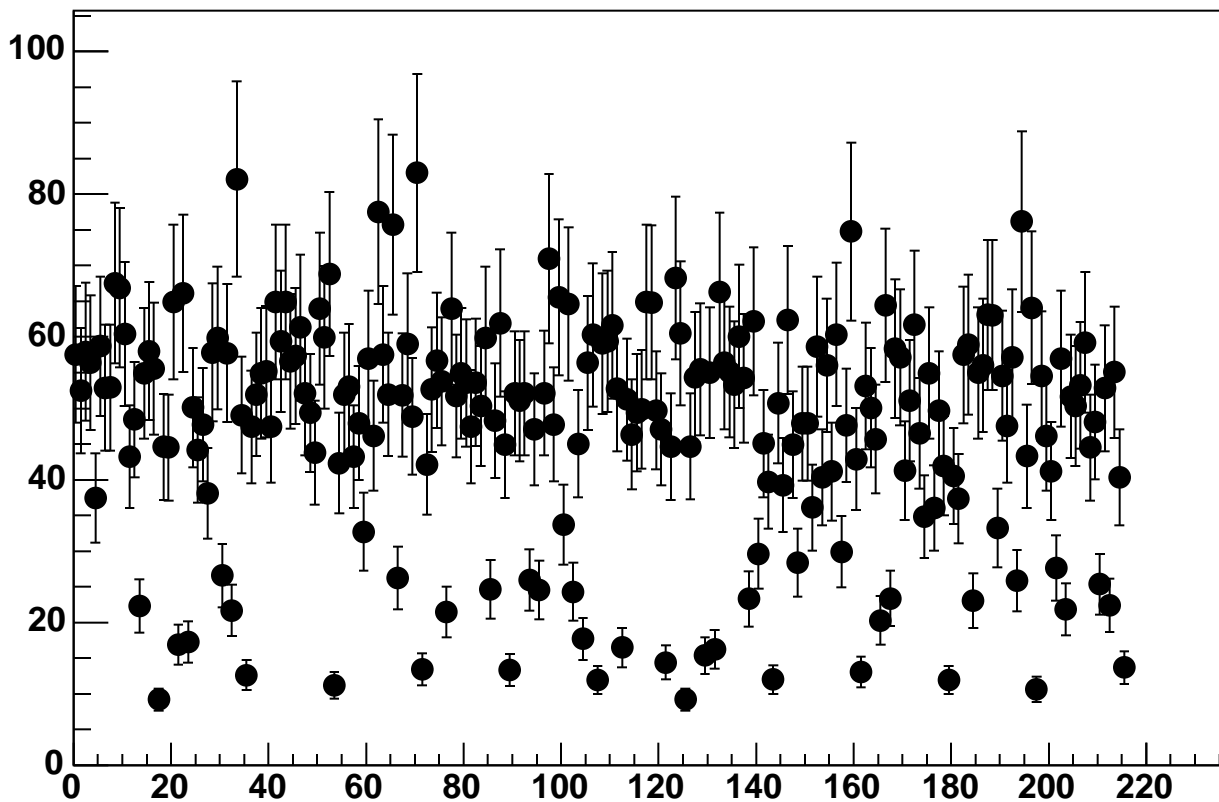
Enable 4, Hold=30, DAC=1500, ADC Noise vs 18\*Chip+Chan



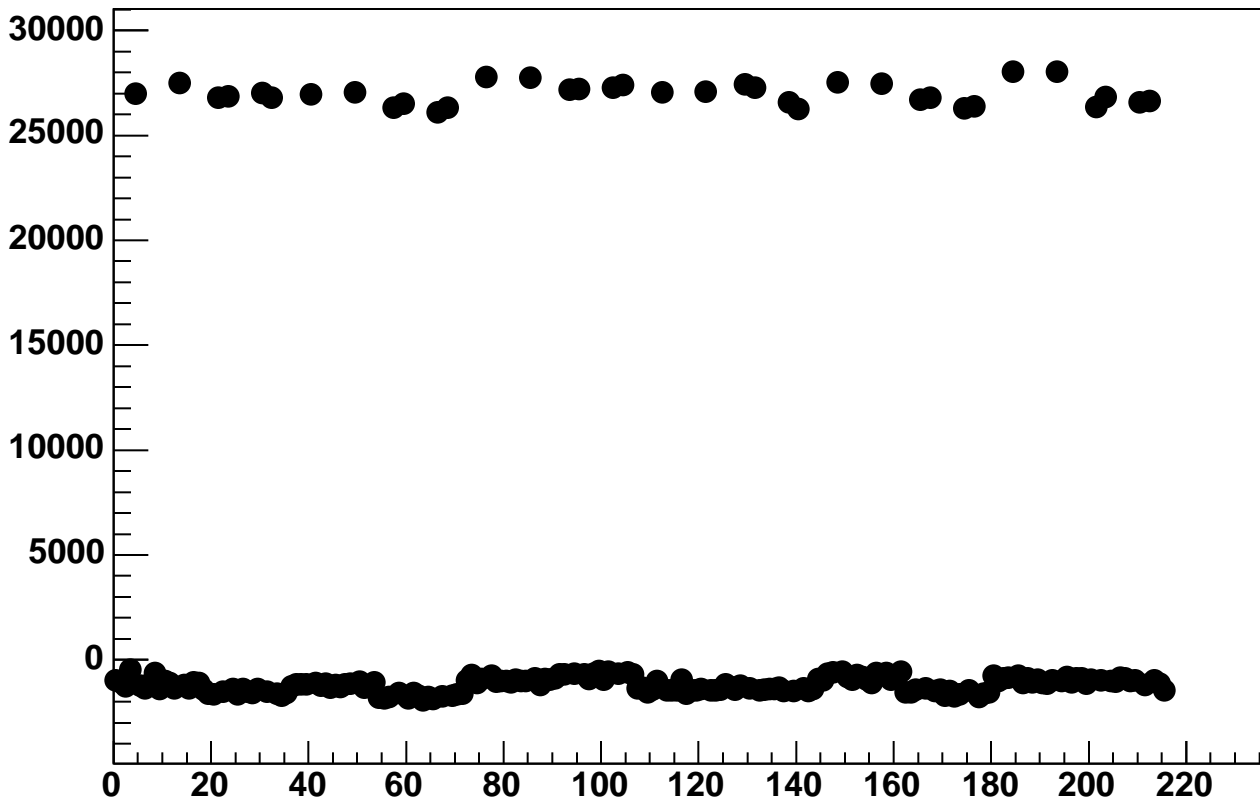
Enable 4, Hold=30, DAC=1550, ADC Mean vs 18\*Chip+Chan



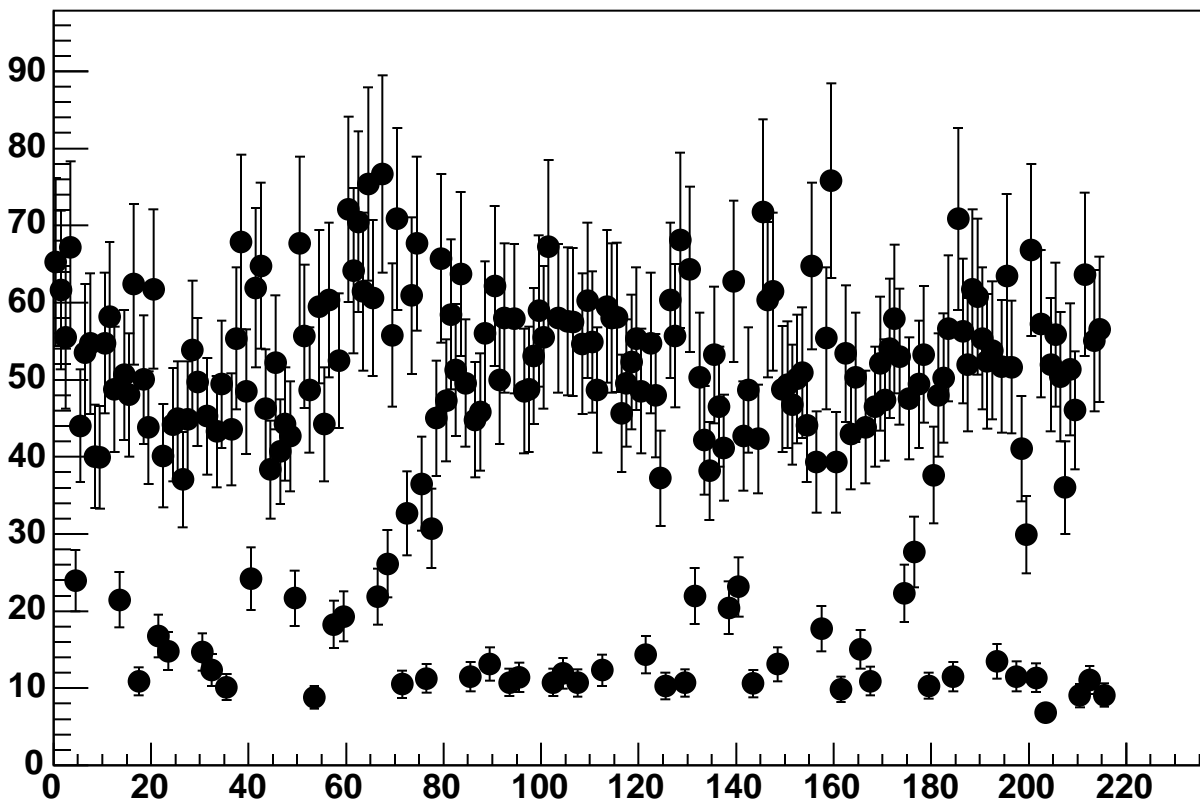
Enable 4, Hold=30, DAC=1550, ADC Noise vs 18\*Chip+Chan



Enable 4, Hold=30, DAC=1600, ADC Mean vs 18\*Chip+Chan

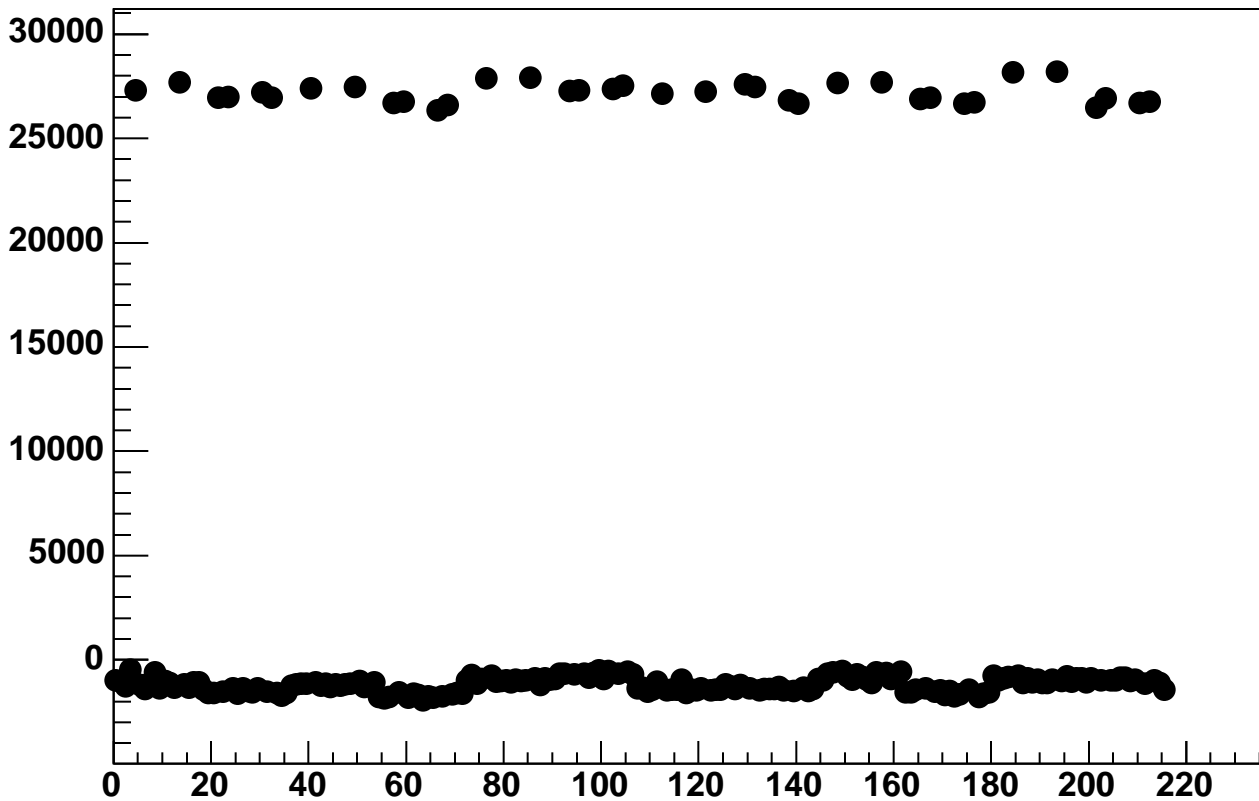


Enable 4, Hold=30, DAC=1600, ADC Noise vs 18\*Chip+Chan

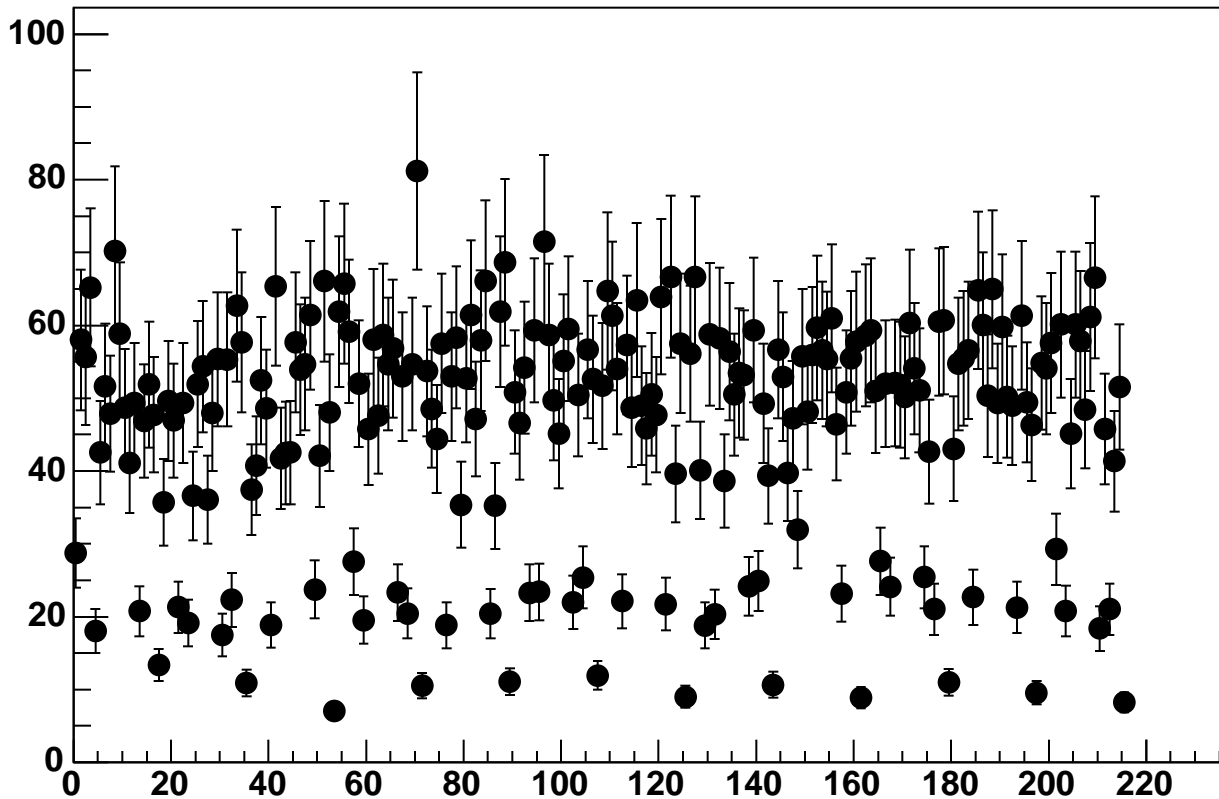




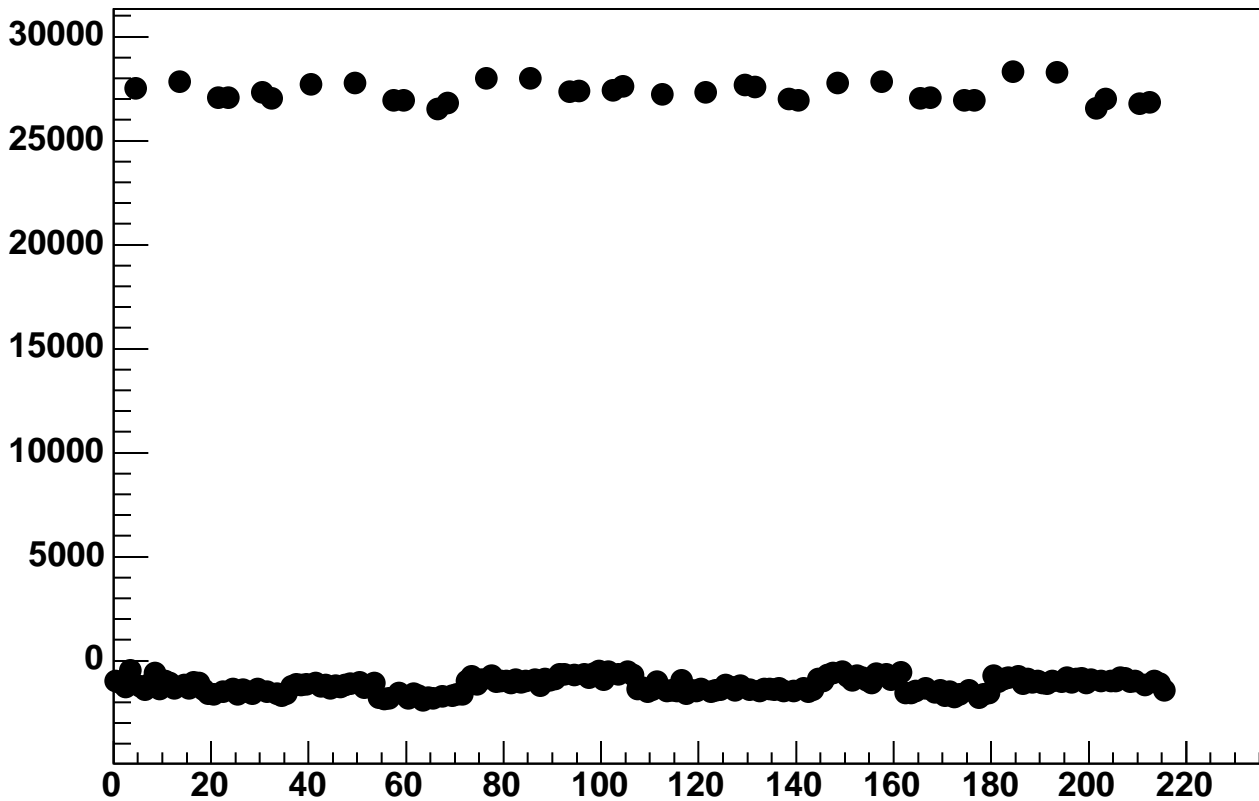
Enable 4, Hold=30, DAC=1650, ADC Mean vs 18\*Chip+Chan



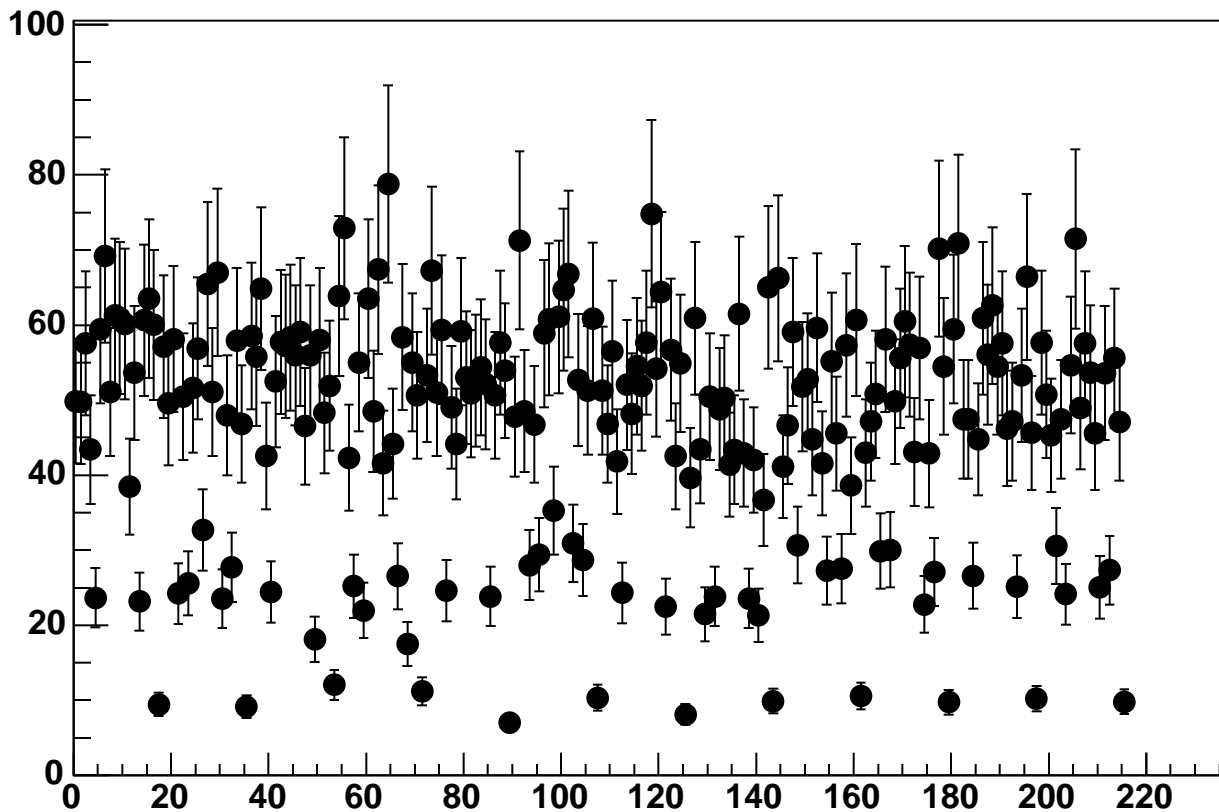
Enable 4, Hold=30, DAC=1650, ADC Noise vs 18\*Chip+Chan



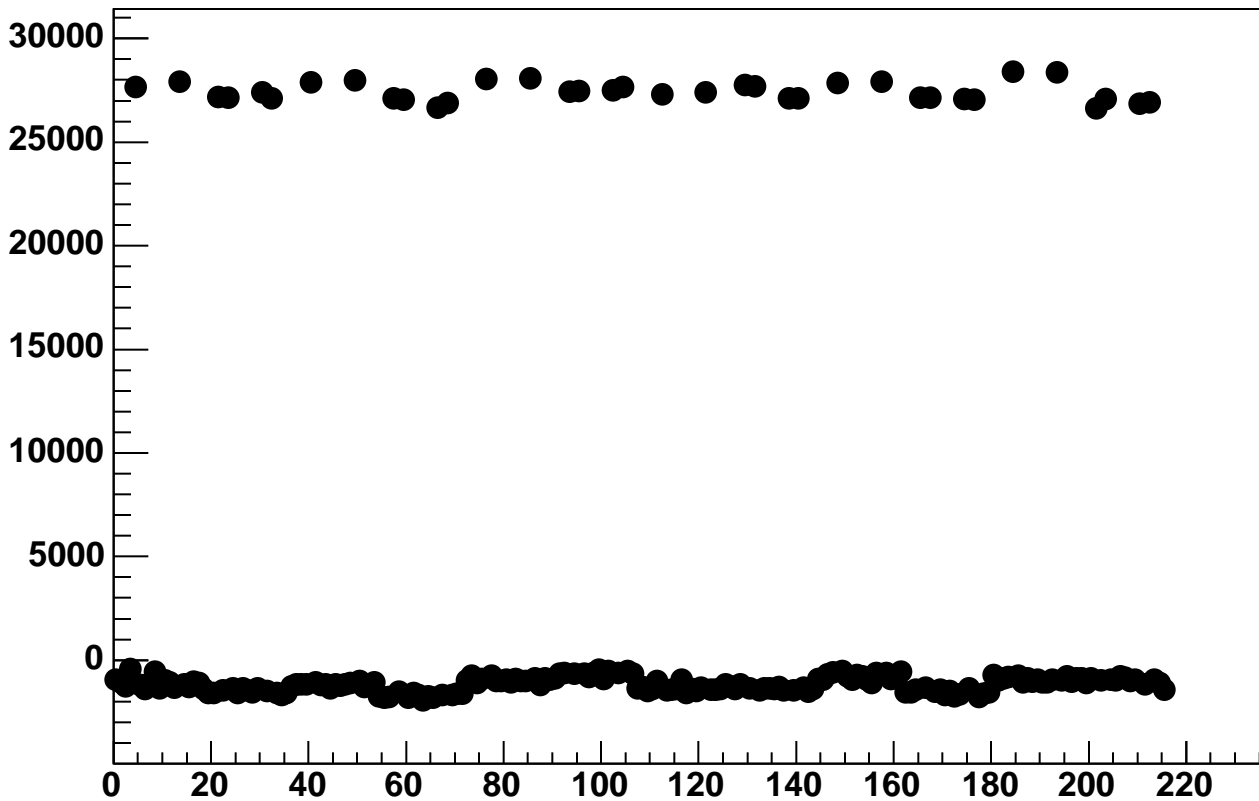
Enable 4, Hold=30, DAC=1700, ADC Mean vs 18\*Chip+Chan



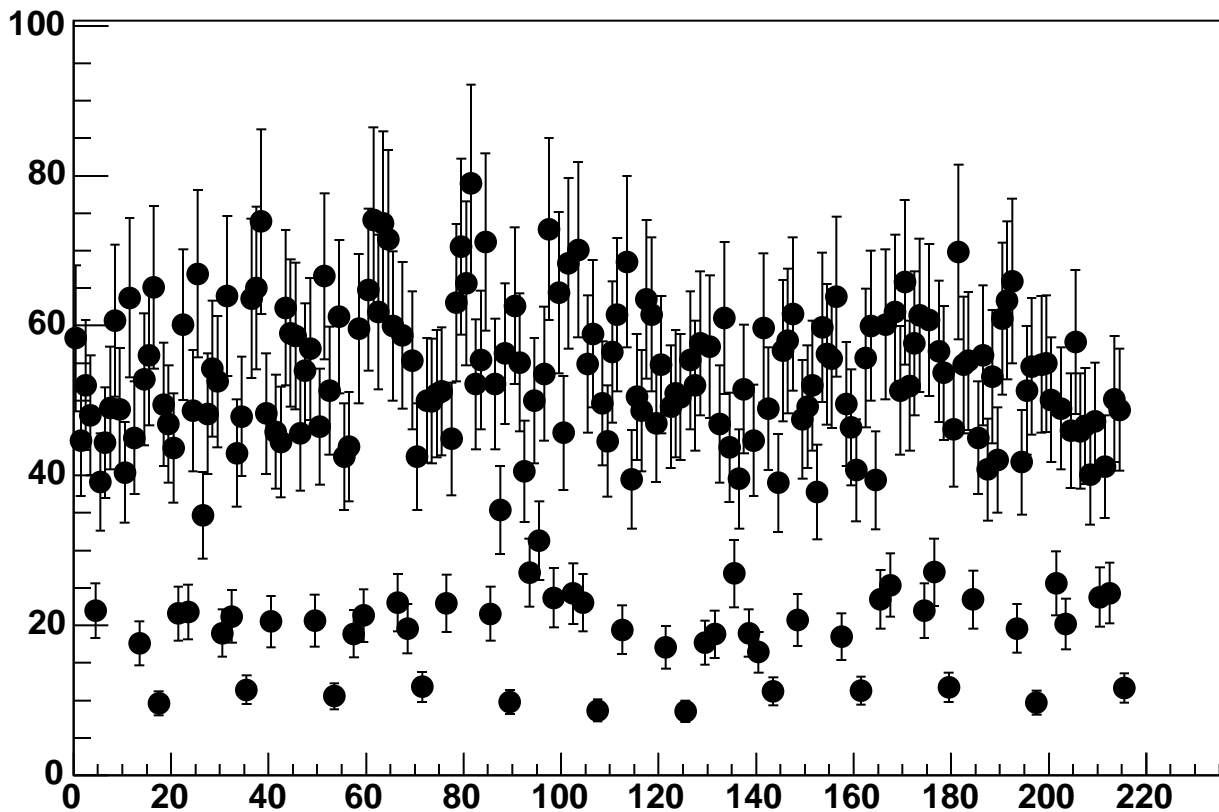
Enable 4, Hold=30, DAC=1700, ADC Noise vs 18\*Chip+Chan



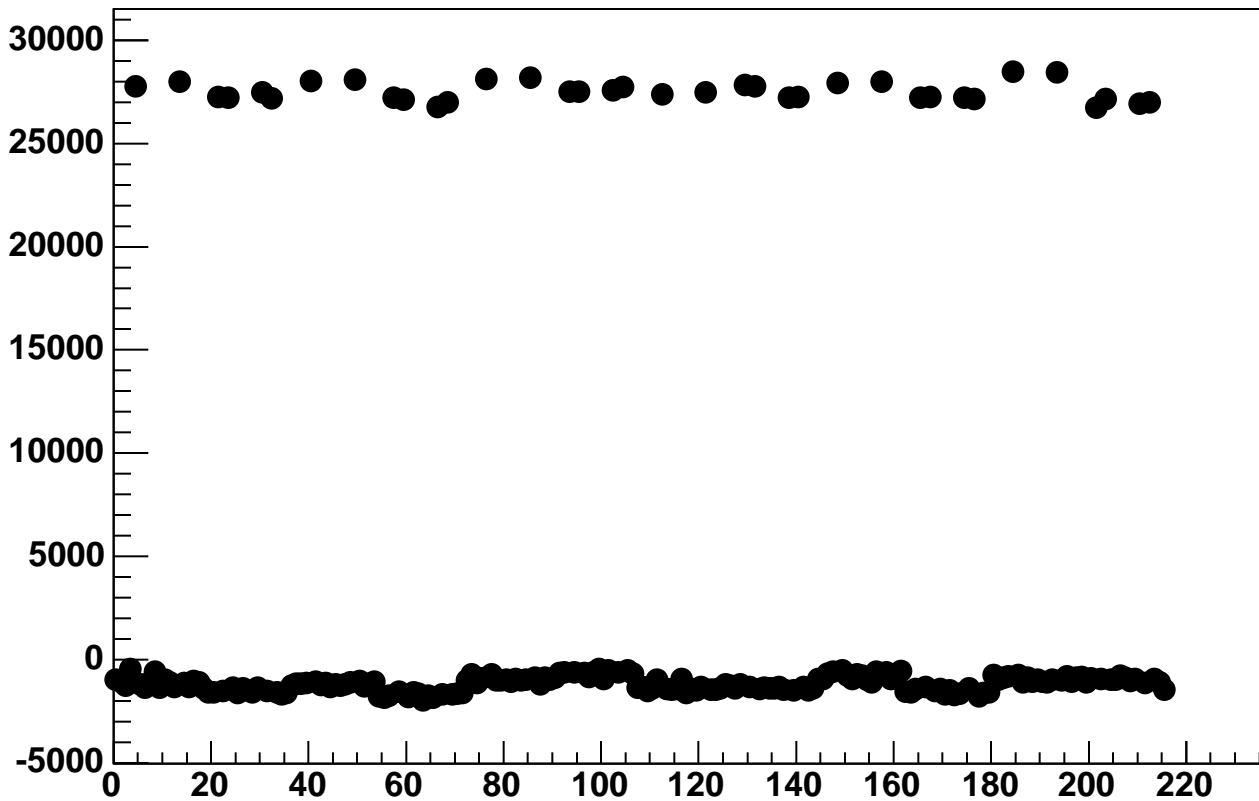
Enable 4, Hold=30, DAC=1750, ADC Mean vs 18\*Chip+Chan



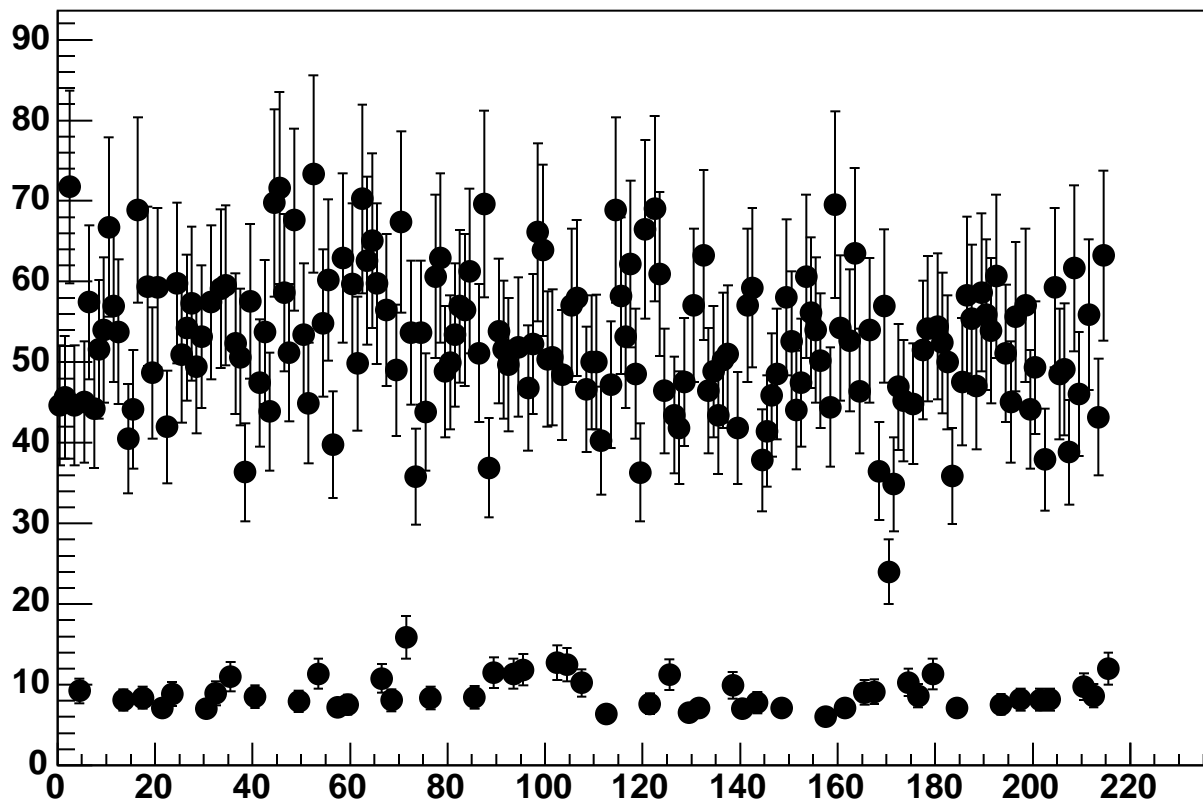
Enable 4, Hold=30, DAC=1750, ADC Noise vs 18\*Chip+Chan



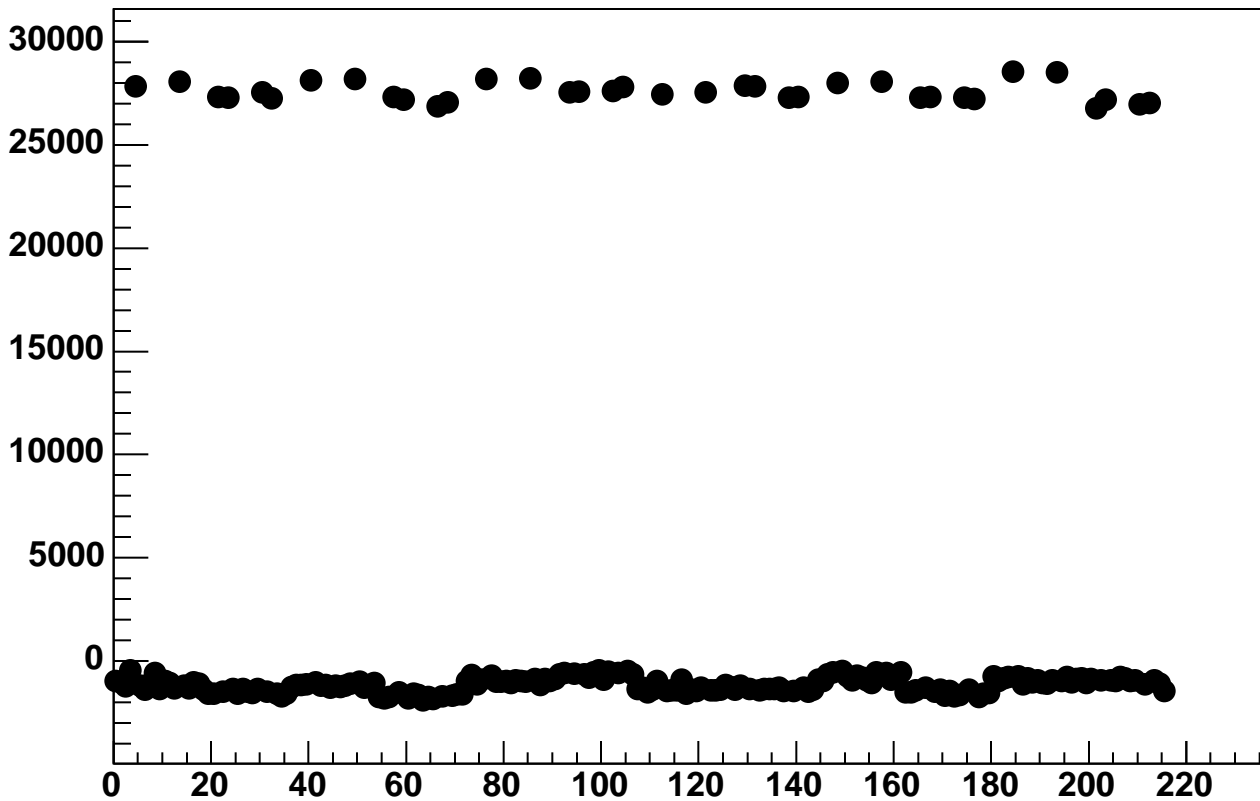
Enable 4, Hold=30, DAC=1800, ADC Mean vs 18\*Chip+Chan



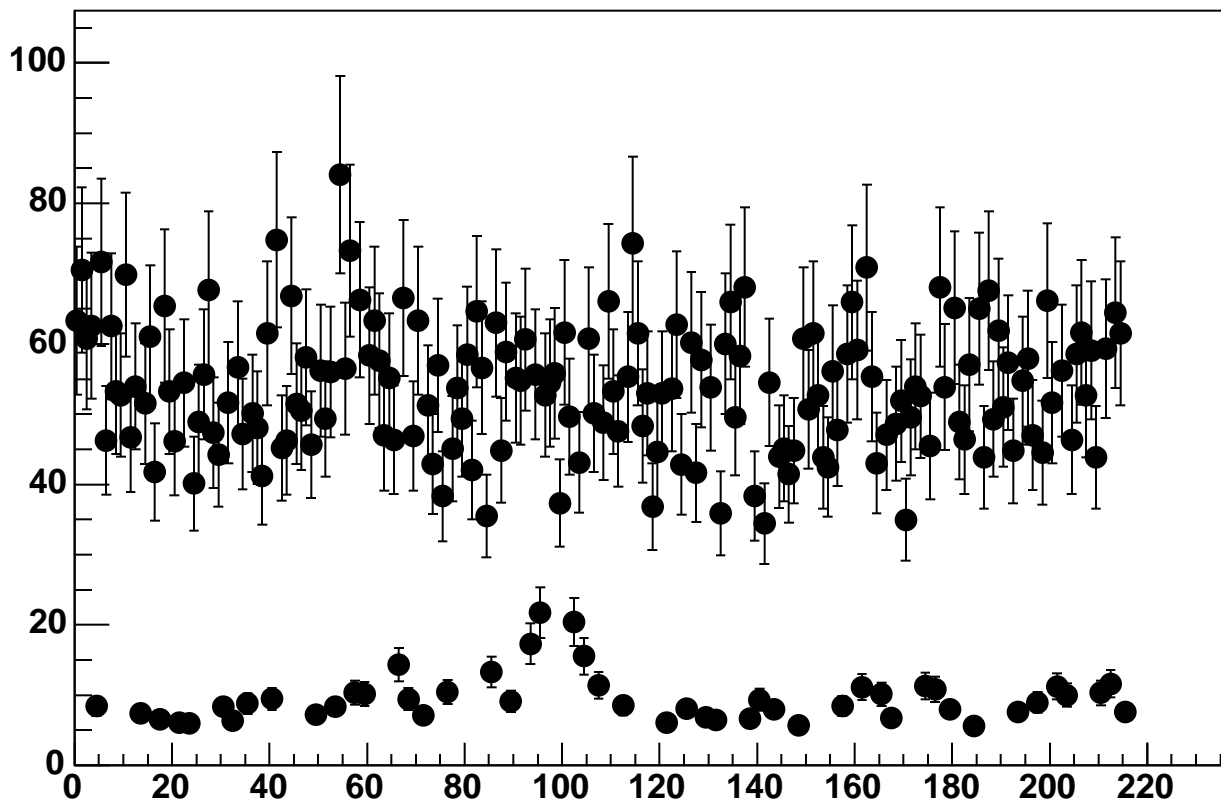
Enable 4, Hold=30, DAC=1800, ADC Noise vs 18\*Chip+Chan



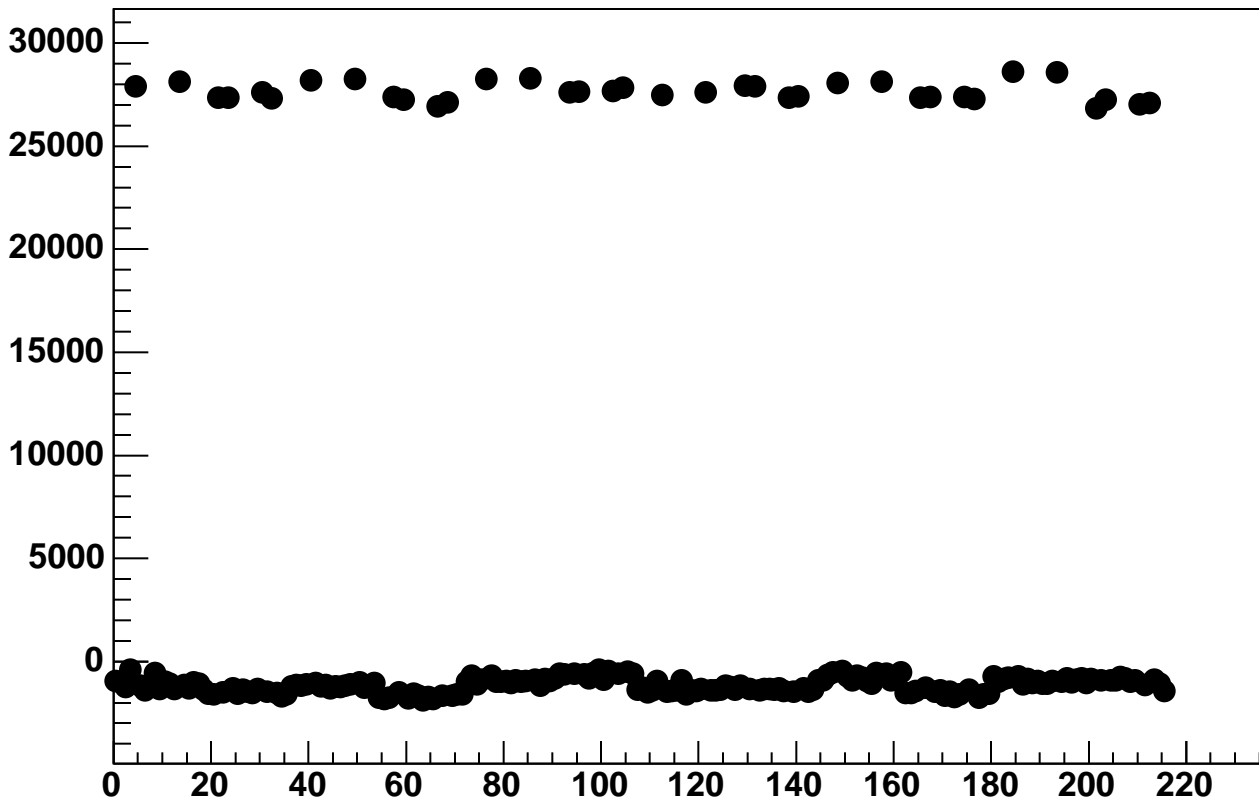
Enable 4, Hold=30, DAC=1850, ADC Mean vs 18\*Chip+Chan



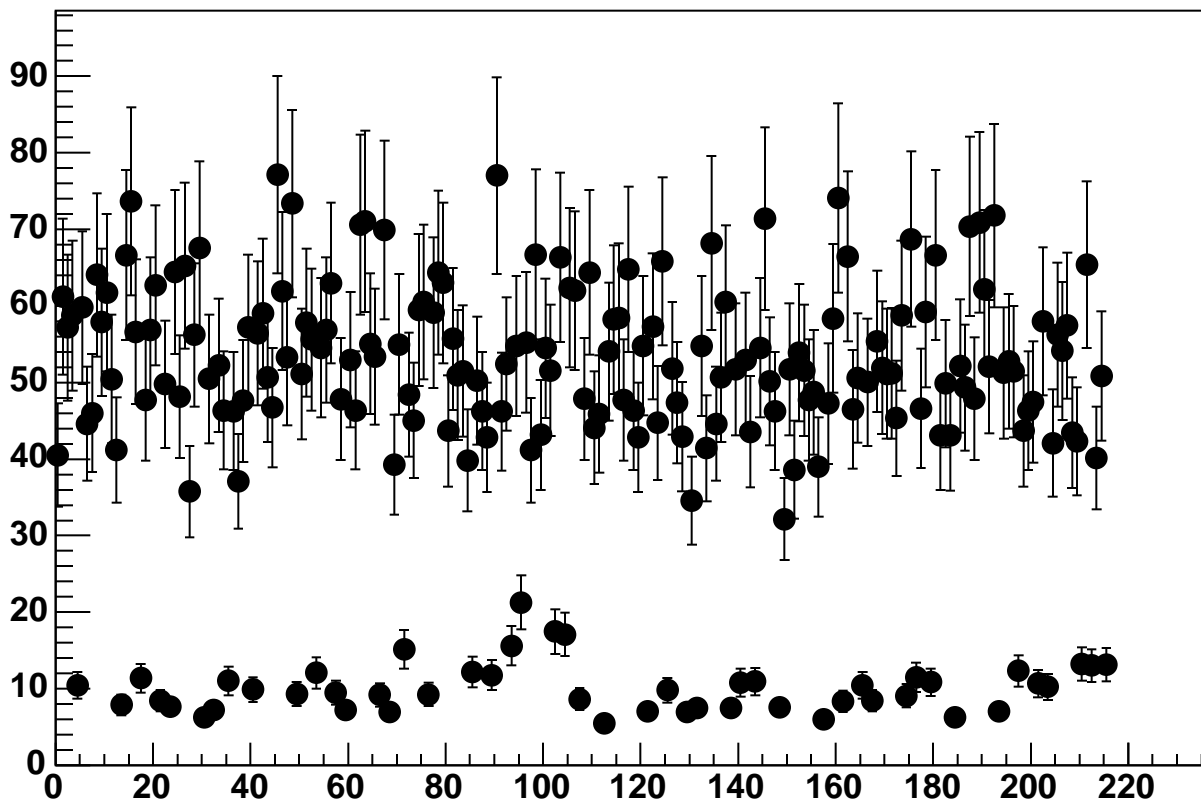
Enable 4, Hold=30, DAC=1850, ADC Noise vs 18\*Chip+Chan



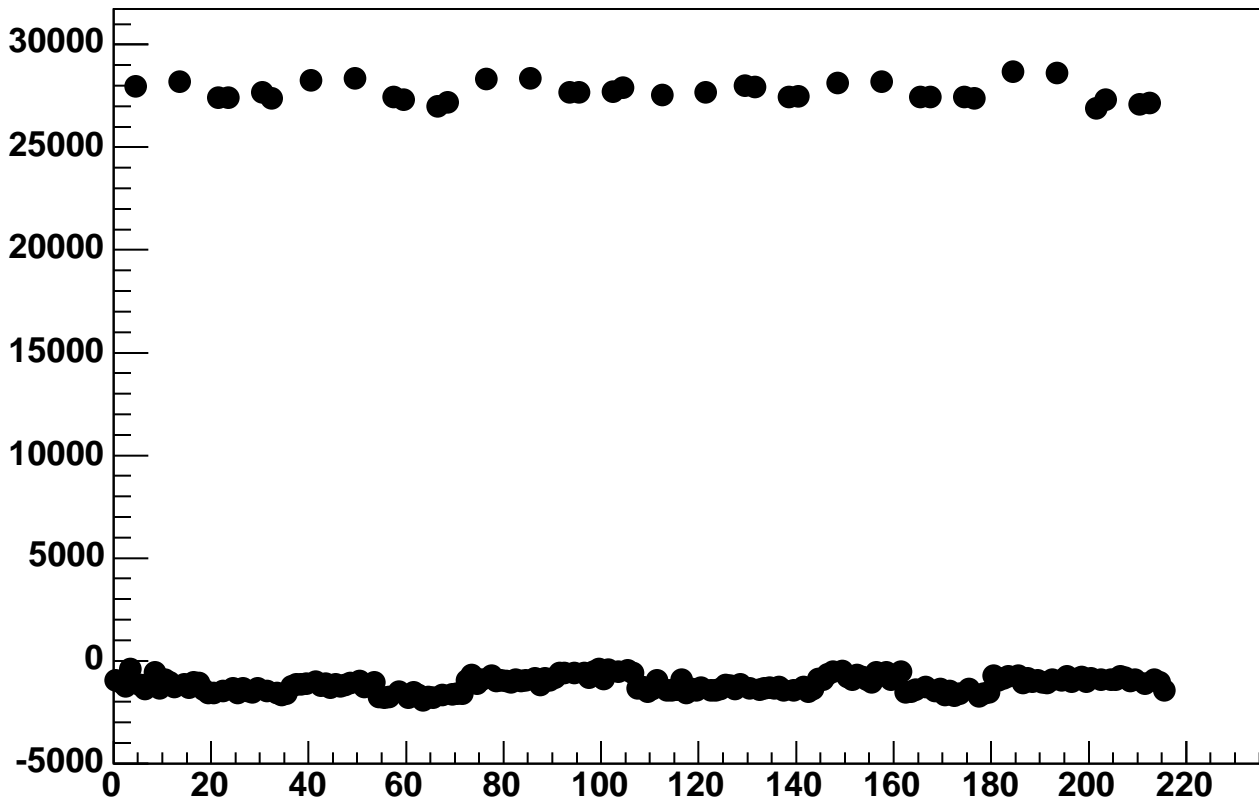
Enable 4, Hold=30, DAC=1900, ADC Mean vs 18\*Chip+Chan



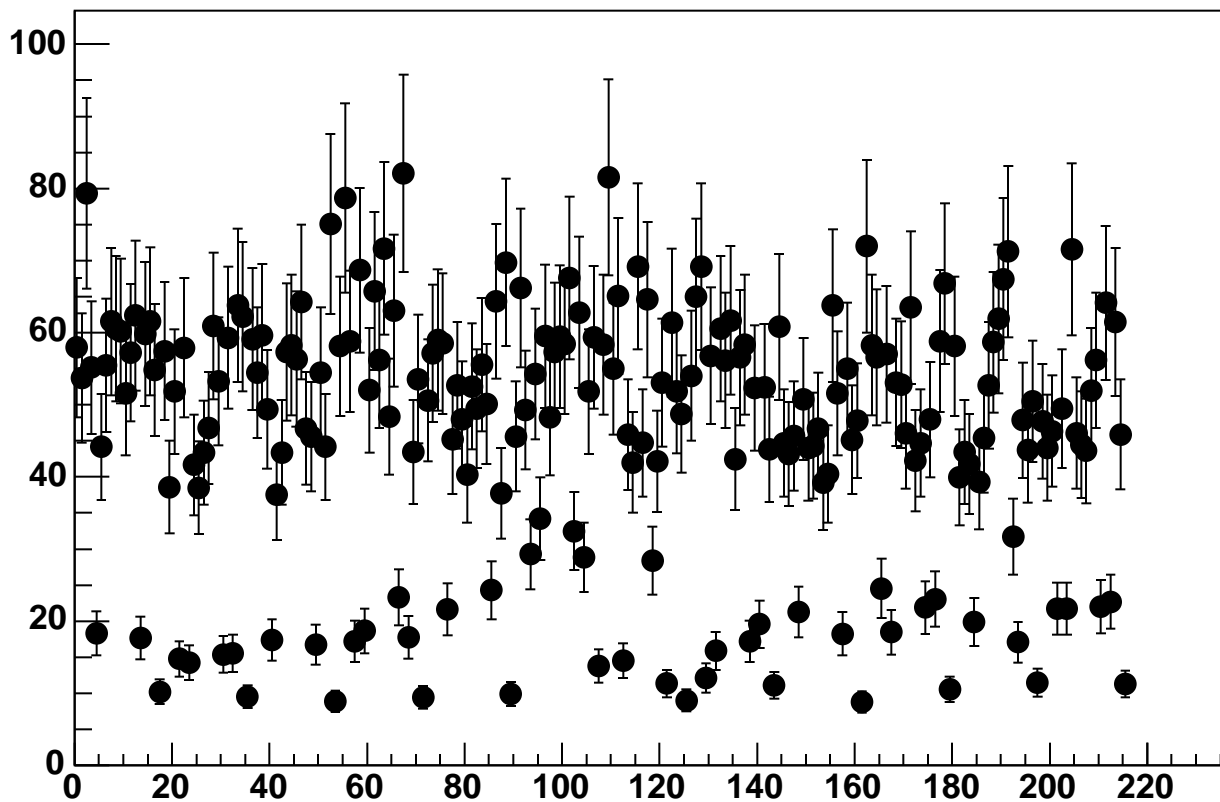
Enable 4, Hold=30, DAC=1900, ADC Noise vs 18\*Chip+Chan



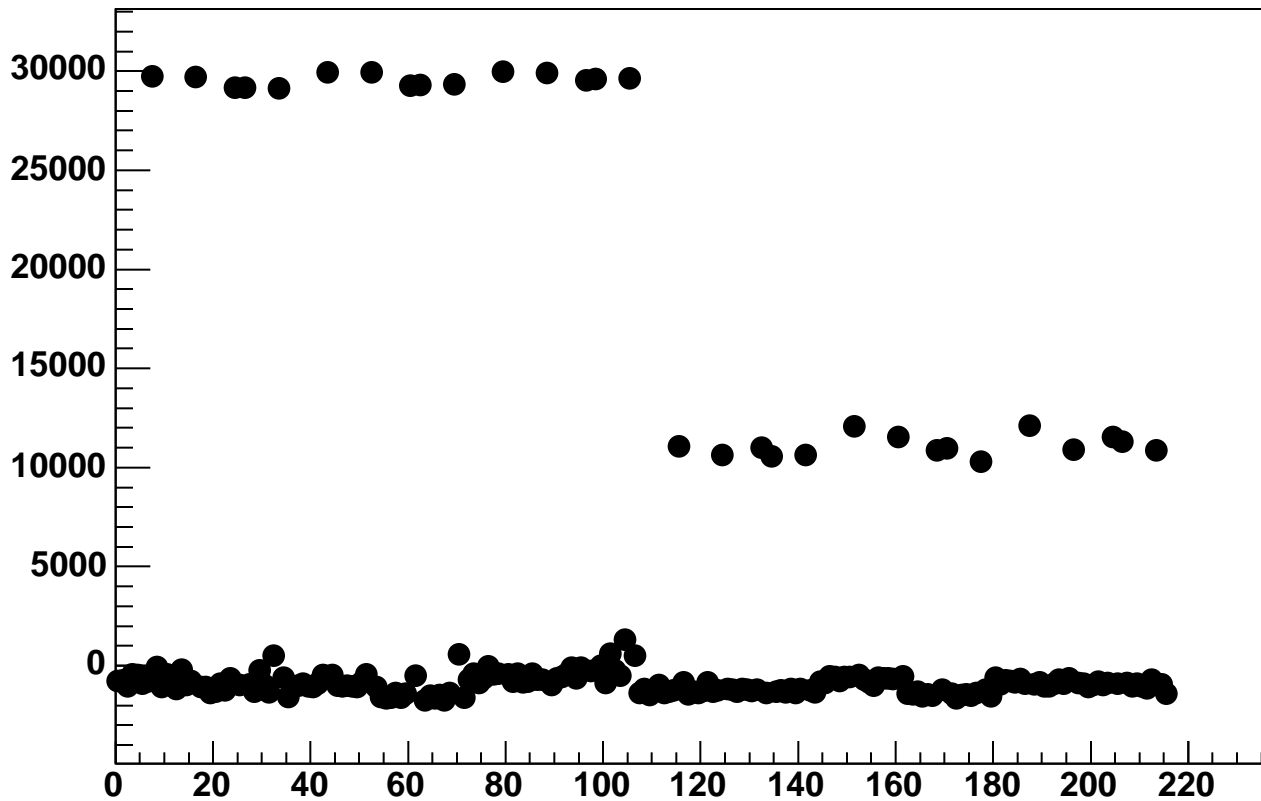
Enable 4, Hold=30, DAC=1950, ADC Mean vs 18\*Chip+Chan



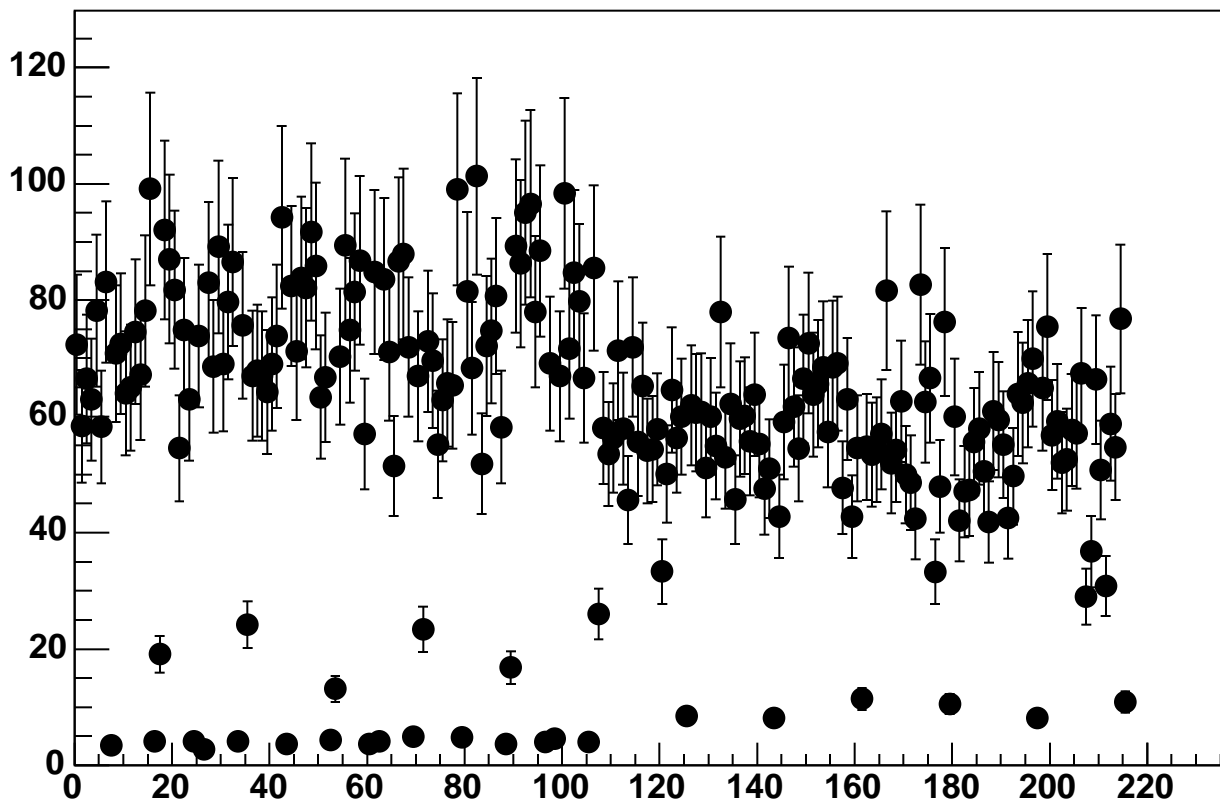
Enable 4, Hold=30, DAC=1950, ADC Noise vs 18\*Chip+Chan



Enable 5, Hold=30, DAC=0, ADC Mean vs 18\*Chip+Chan

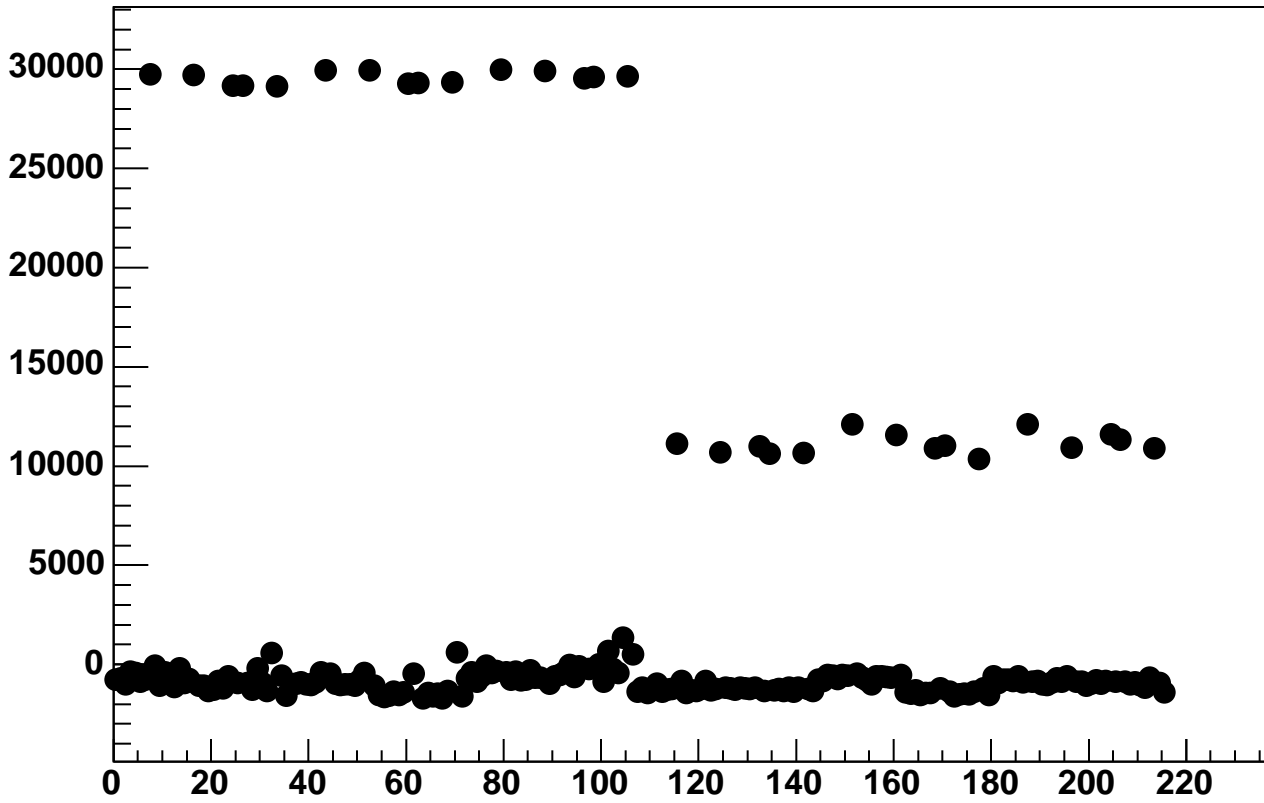


Enable 5, Hold=30, DAC=0, ADC Noise vs 18\*Chip+Chan

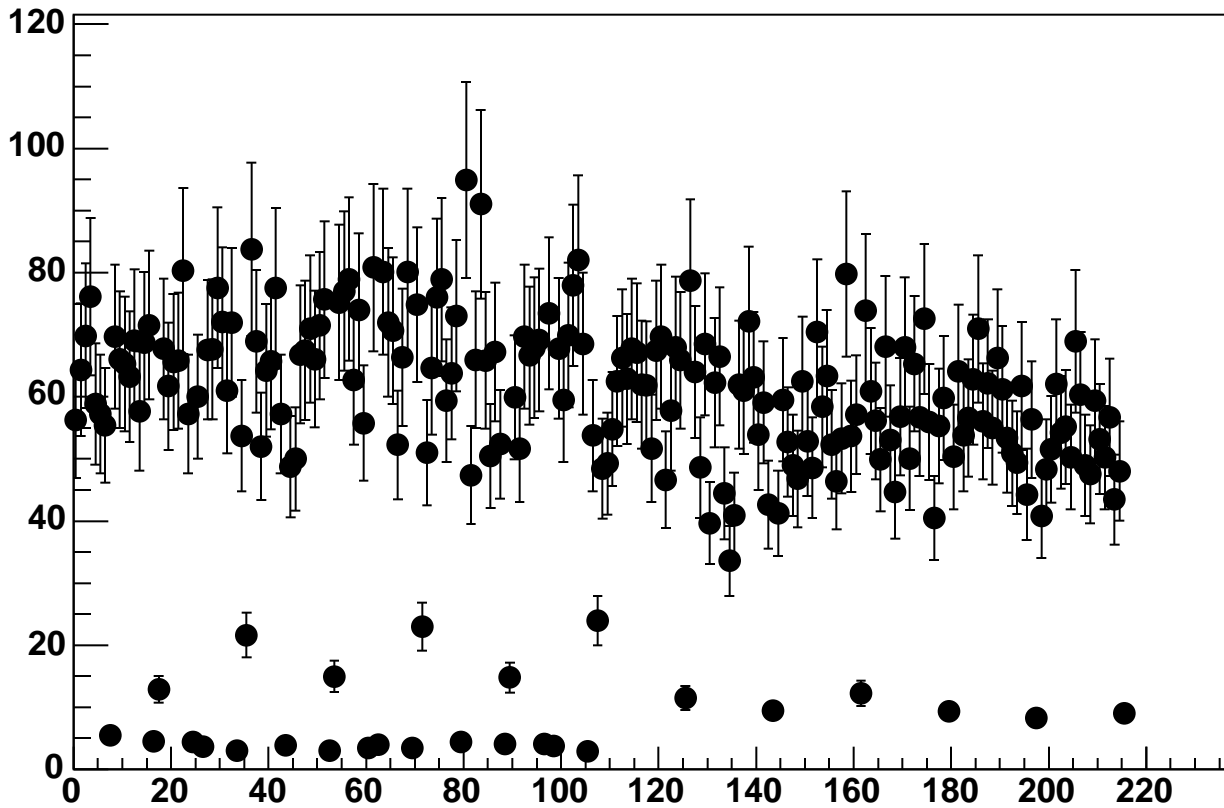




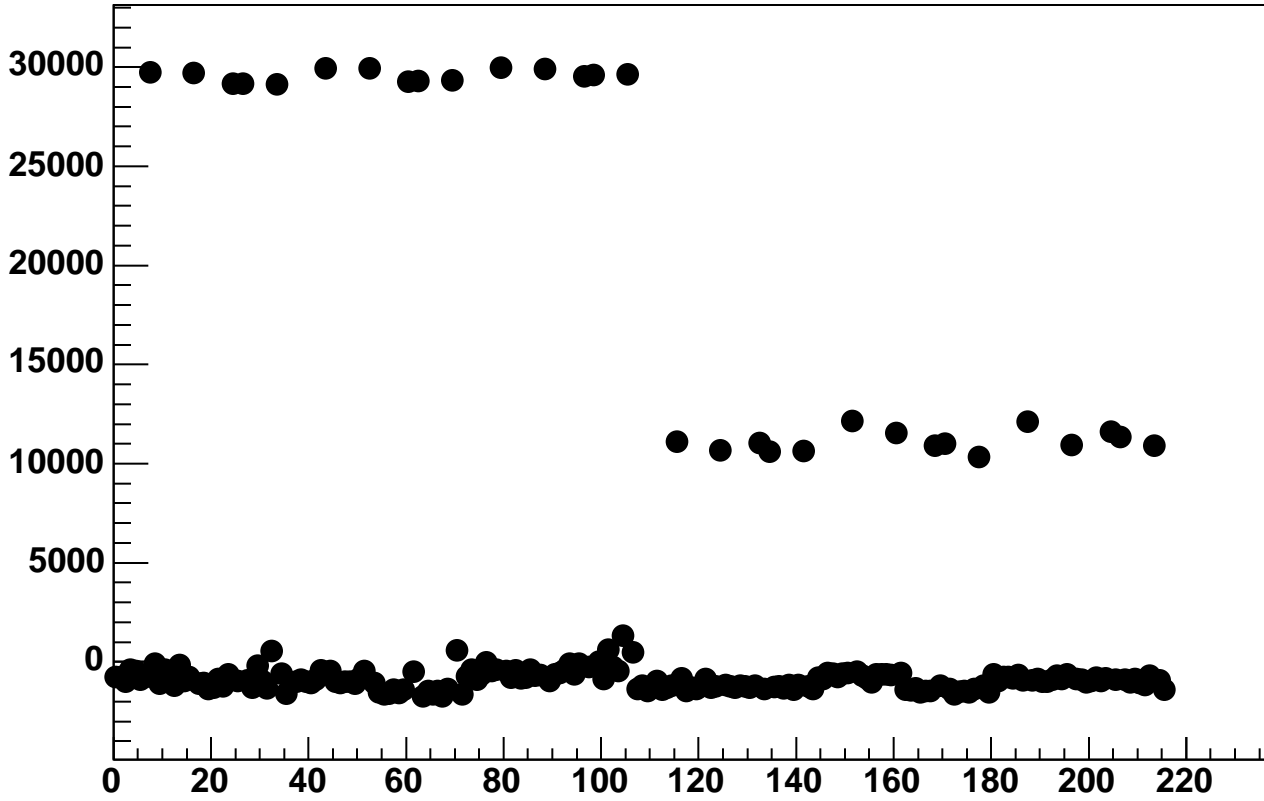
Enable 5, Hold=30, DAC=50, ADC Mean vs 18\*Chip+Chan



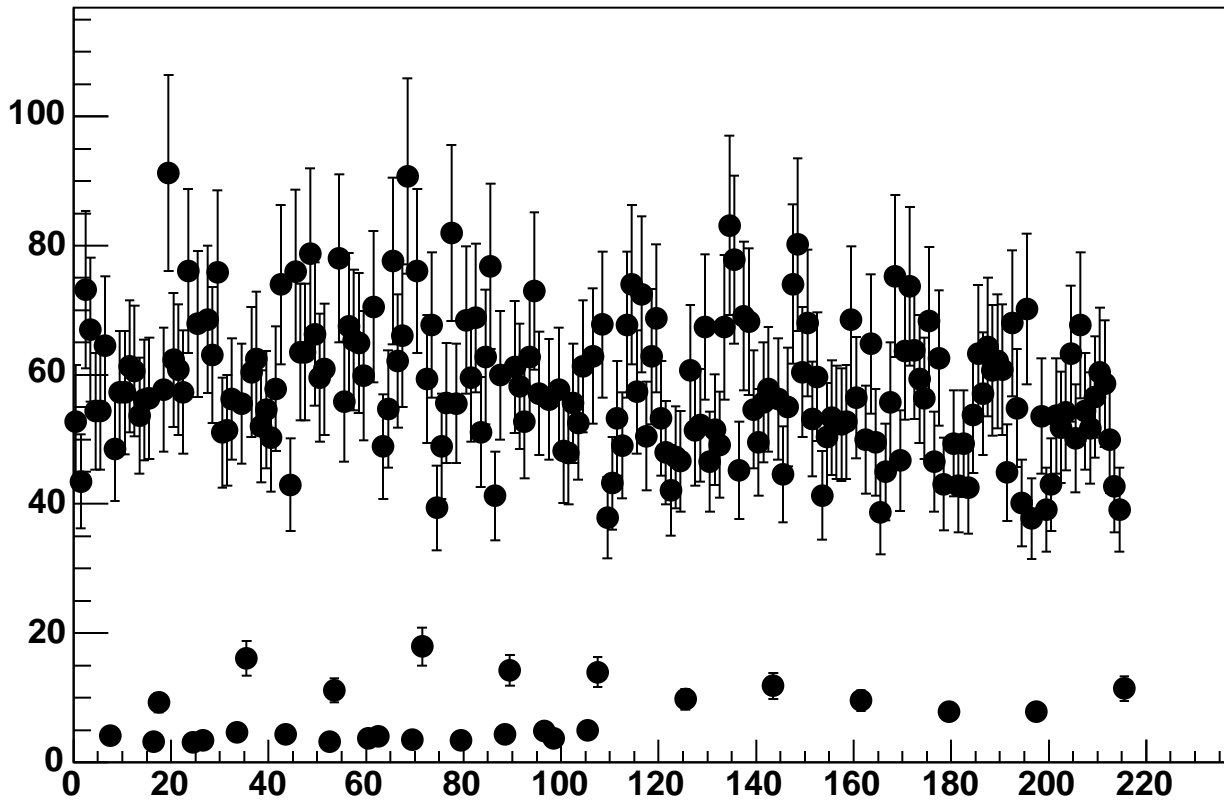
Enable 5, Hold=30, DAC=50, ADC Noise vs 18\*Chip+Chan



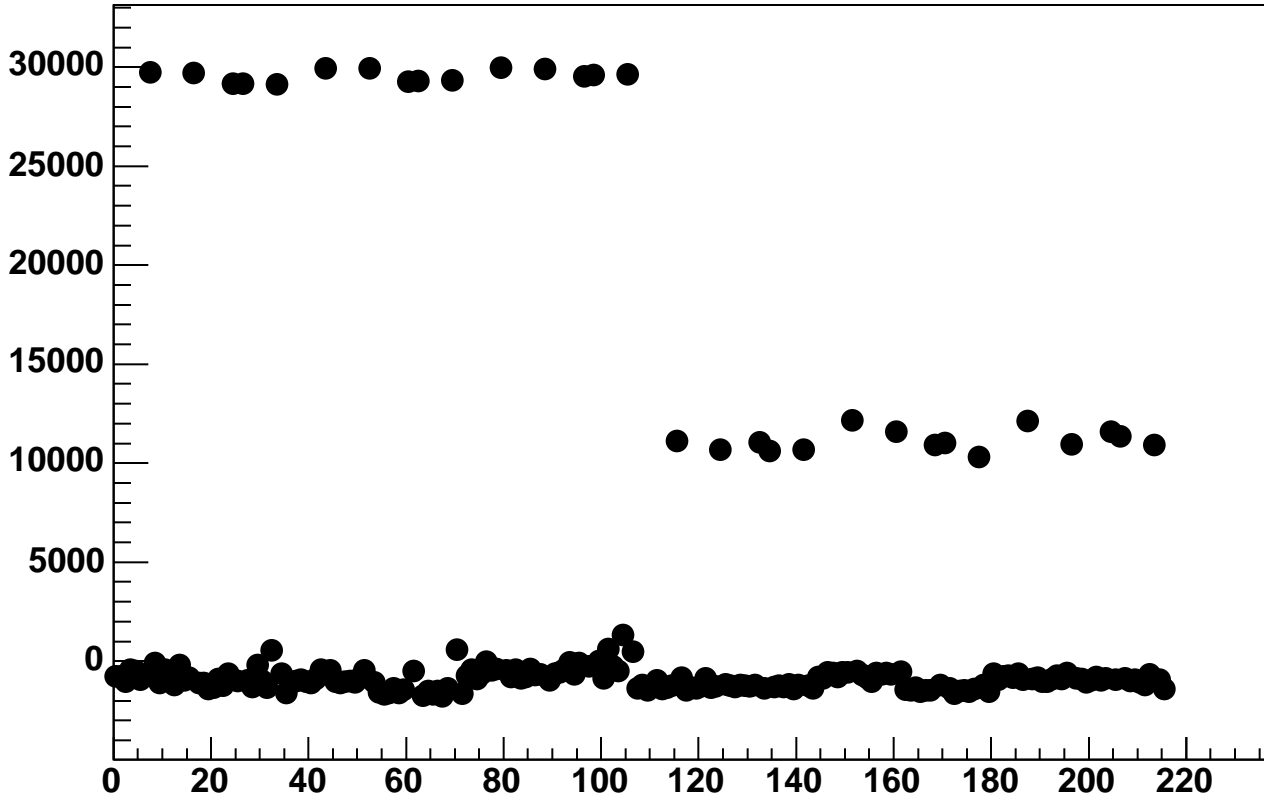
Enable 5, Hold=30, DAC=100, ADC Mean vs 18\*Chip+Chan



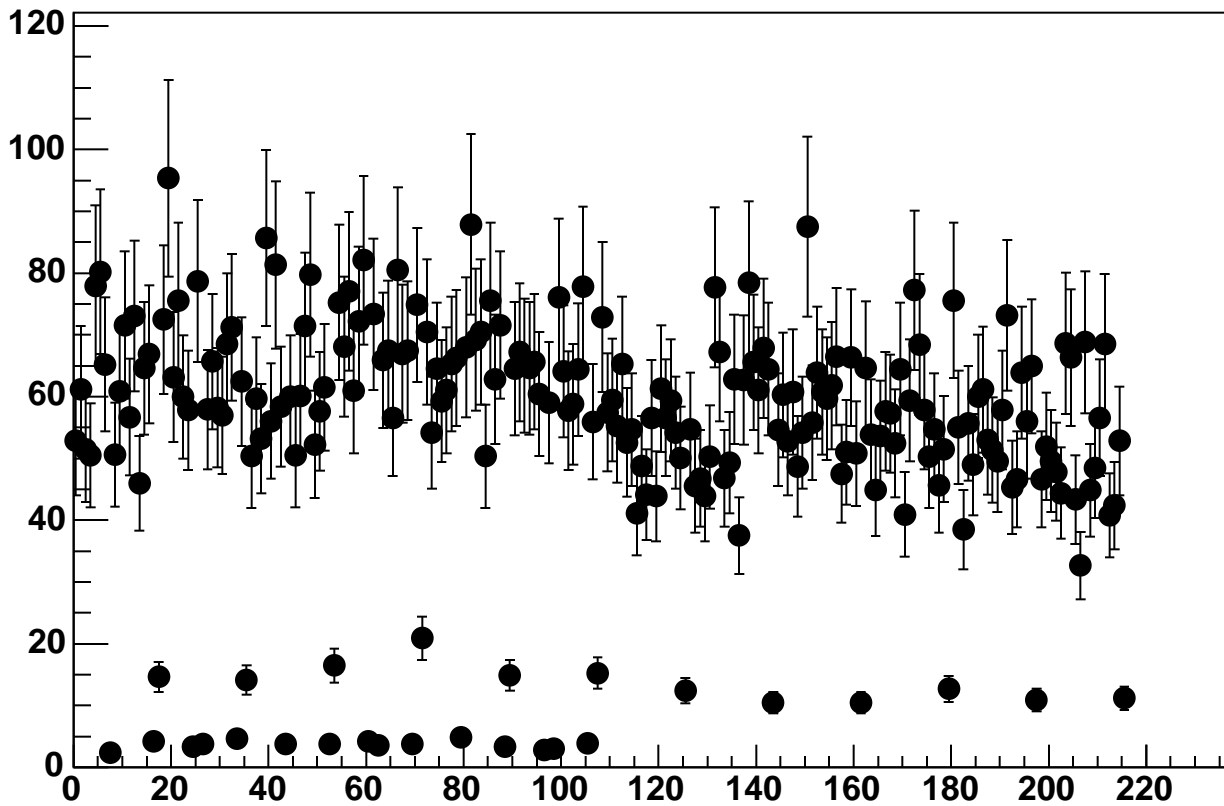
Enable 5, Hold=30, DAC=100, ADC Noise vs 18\*Chip+Chan



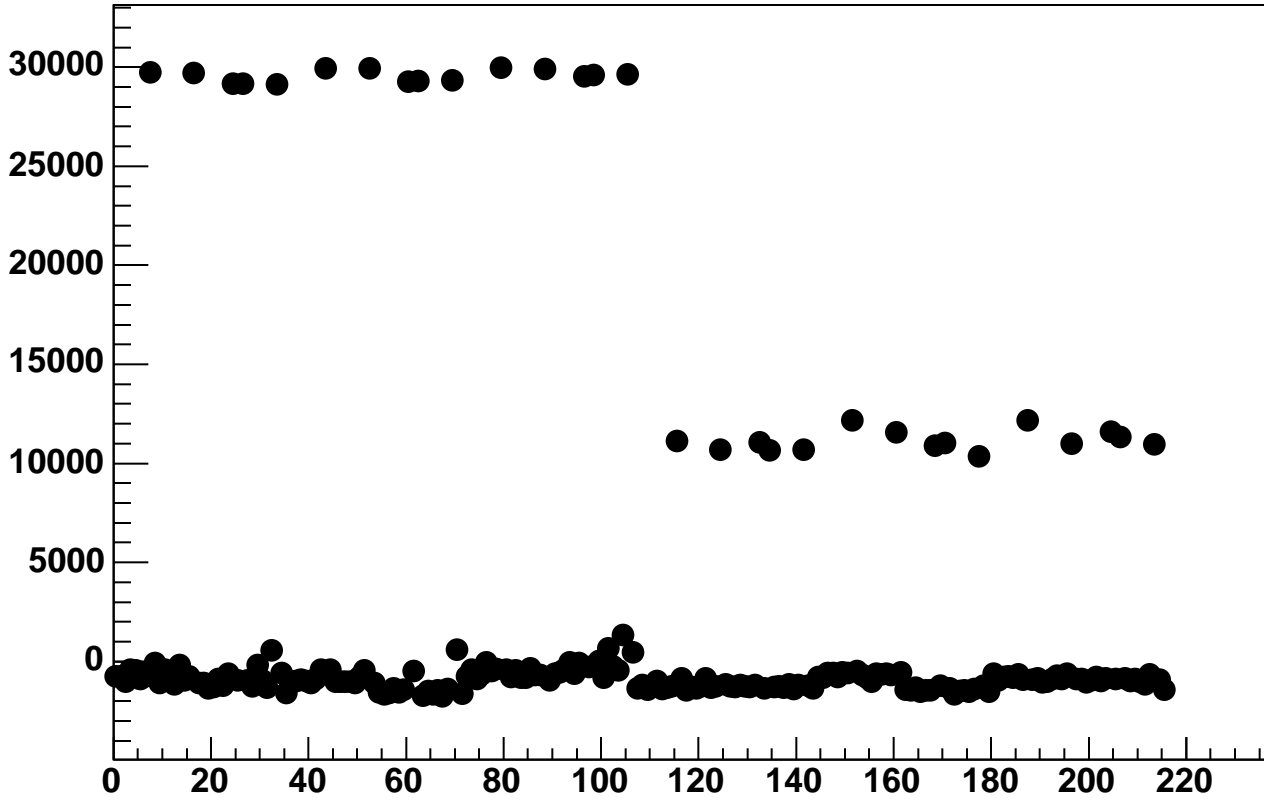
Enable 5, Hold=30, DAC=150, ADC Mean vs 18\*Chip+Chan



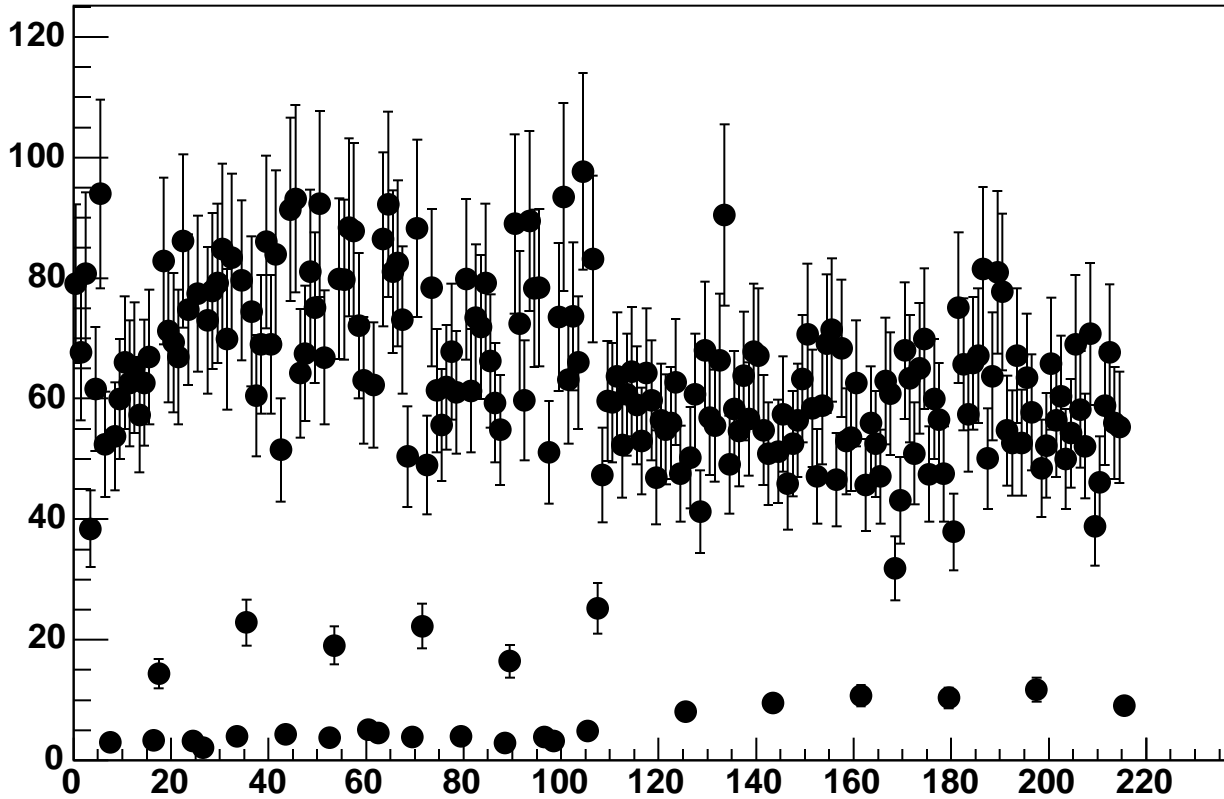
Enable 5, Hold=30, DAC=150, ADC Noise vs 18\*Chip+Chan



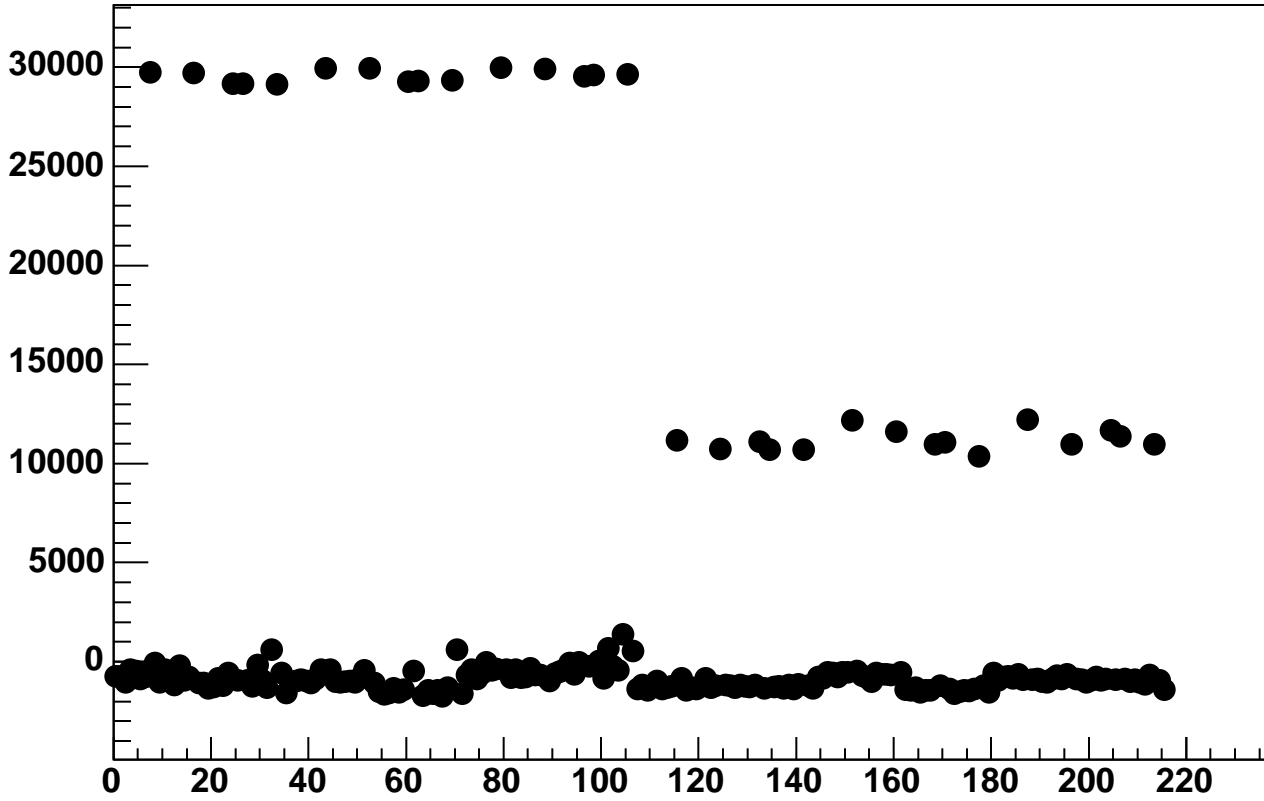
Enable 5, Hold=30, DAC=200, ADC Mean vs 18\*Chip+Chan



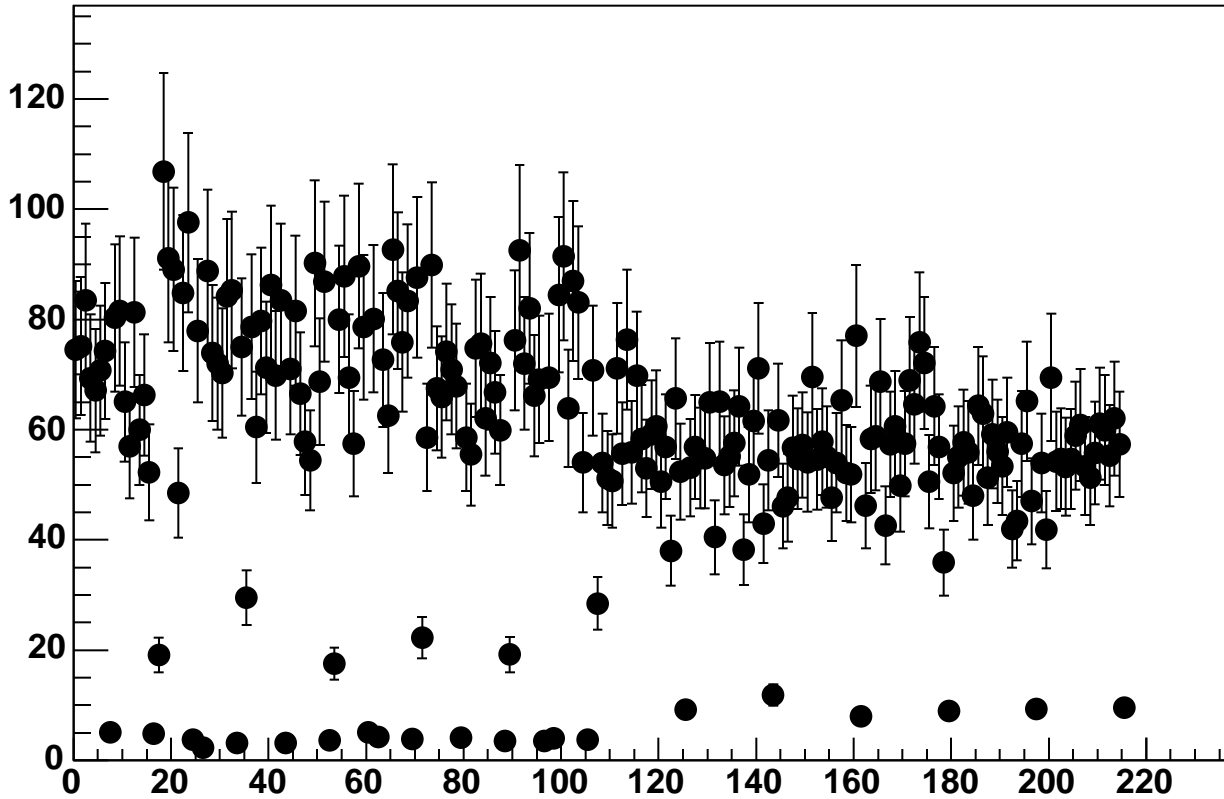
Enable 5, Hold=30, DAC=200, ADC Noise vs 18\*Chip+Chan



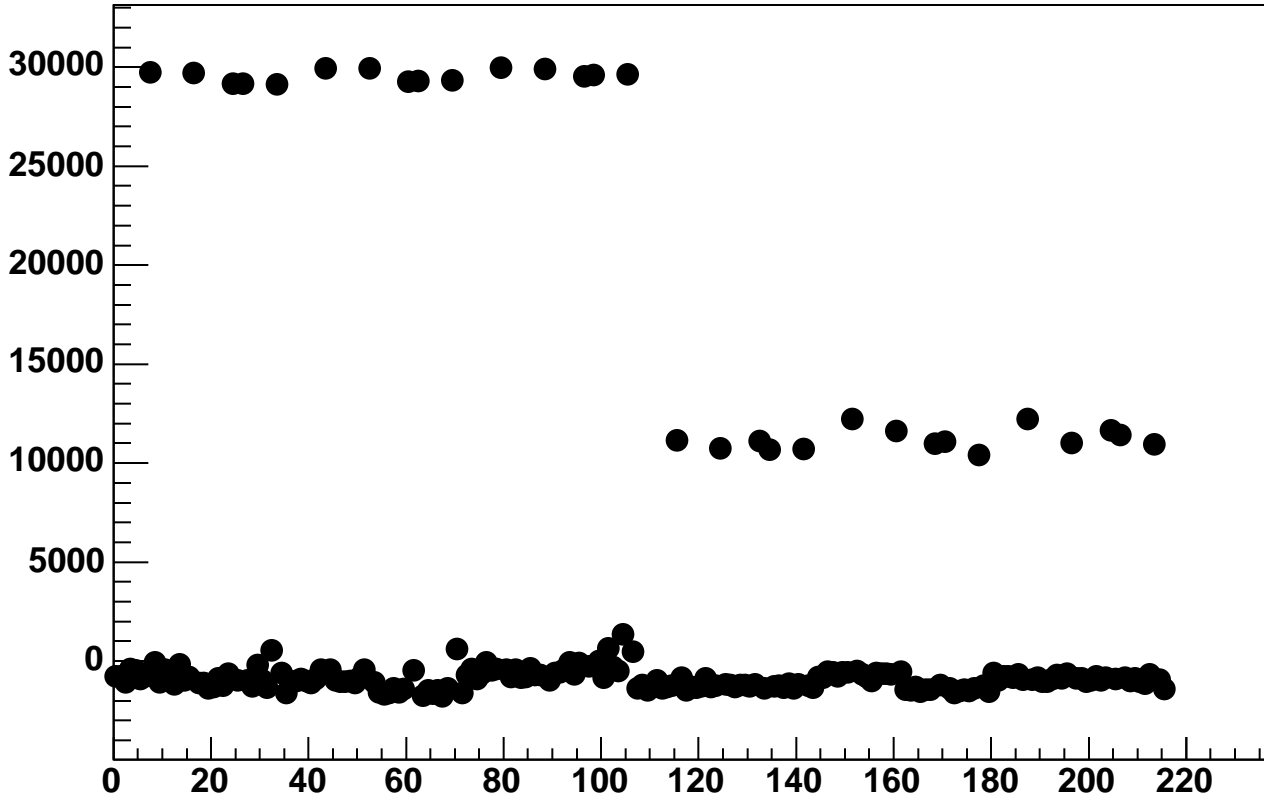
Enable 5, Hold=30, DAC=250, ADC Mean vs 18\*Chip+Chan



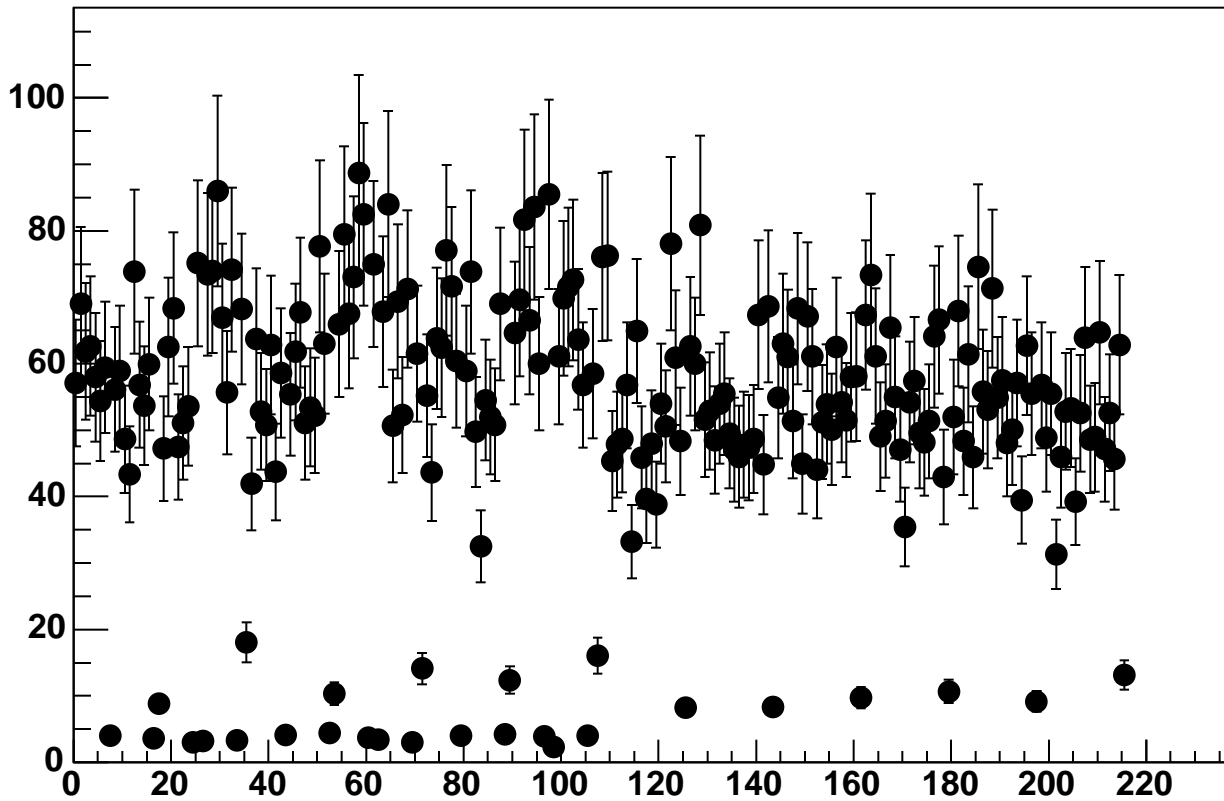
Enable 5, Hold=30, DAC=250, ADC Noise vs 18\*Chip+Chan



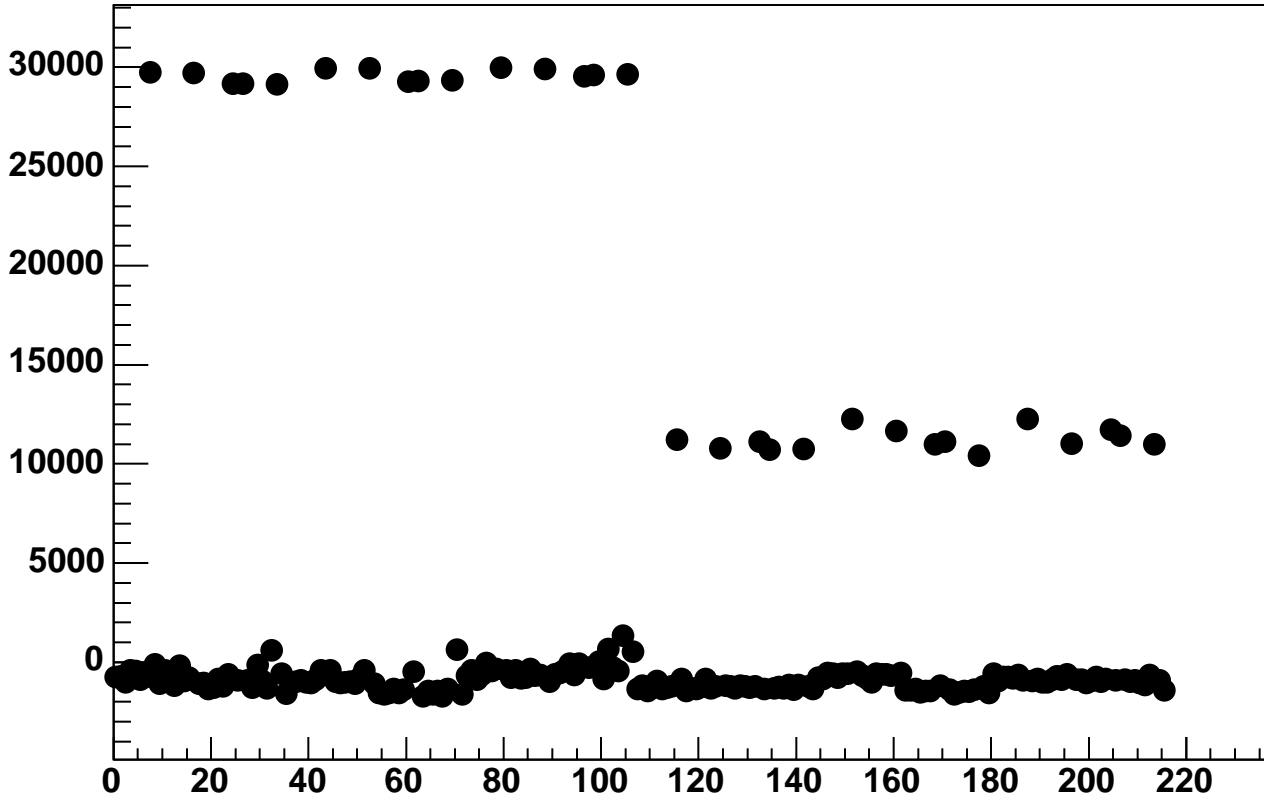
Enable 5, Hold=30, DAC=300, ADC Mean vs 18\*Chip+Chan



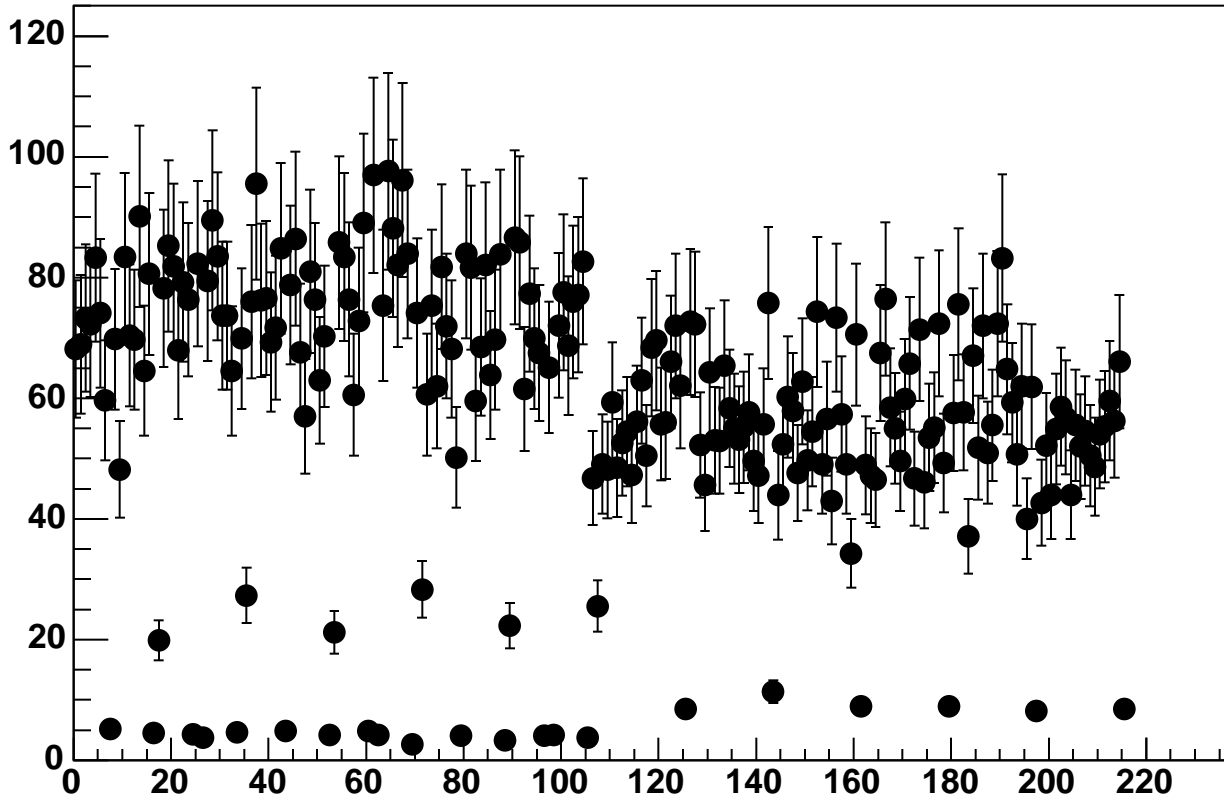
Enable 5, Hold=30, DAC=300, ADC Noise vs 18\*Chip+Chan



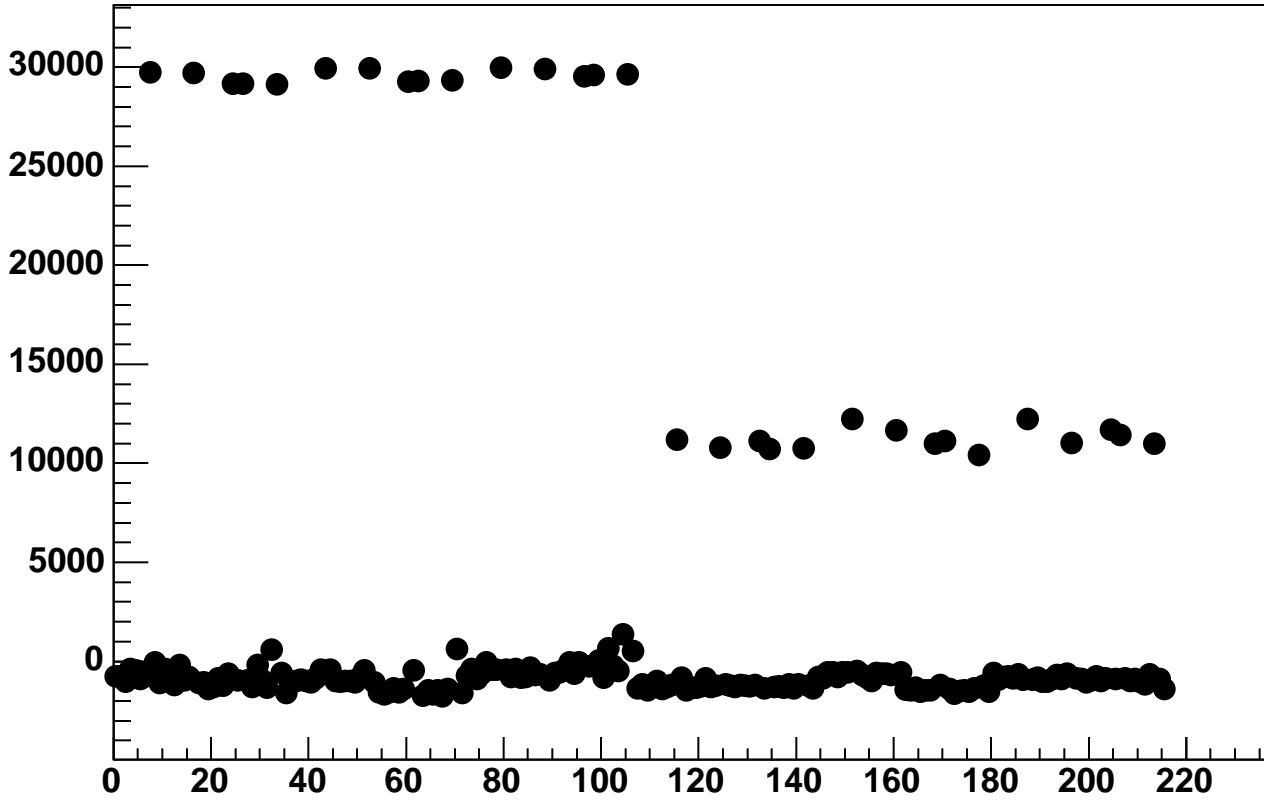
Enable 5, Hold=30, DAC=350, ADC Mean vs 18\*Chip+Chan



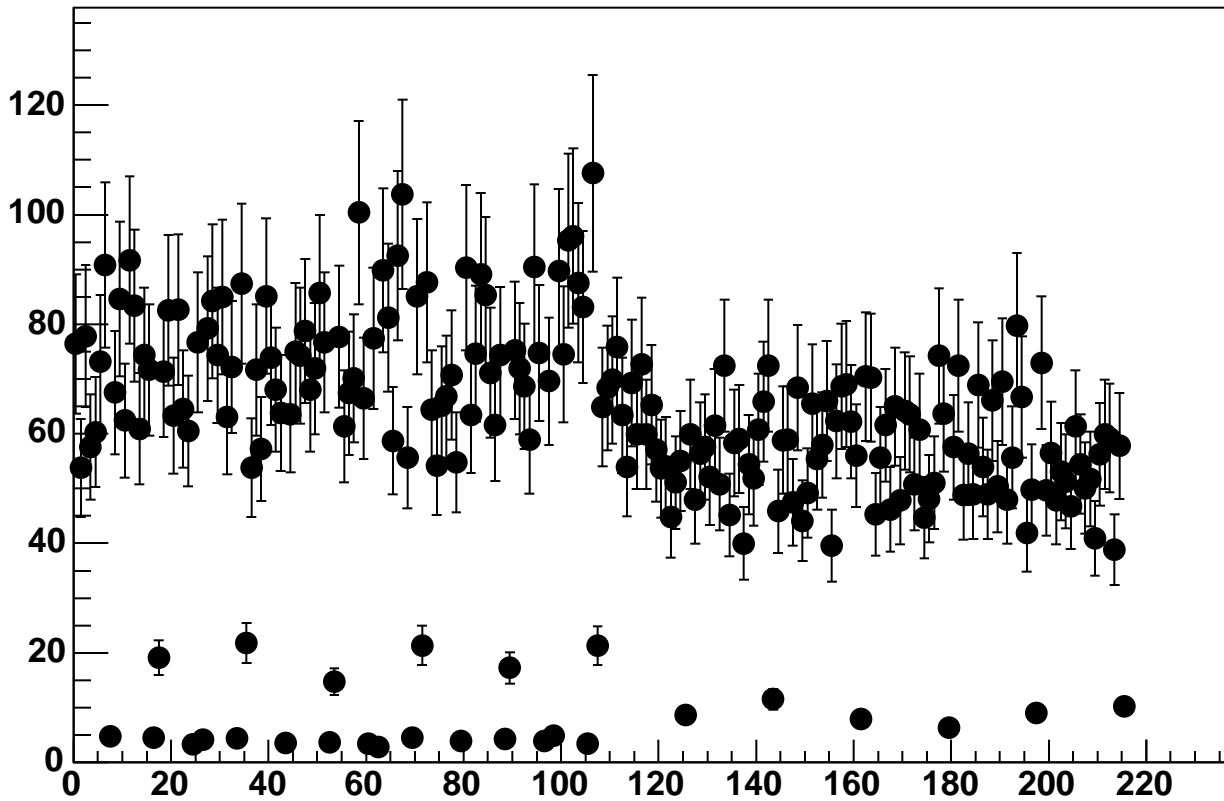
Enable 5, Hold=30, DAC=350, ADC Noise vs 18\*Chip+Chan



Enable 5, Hold=30, DAC=400, ADC Mean vs 18\*Chip+Chan

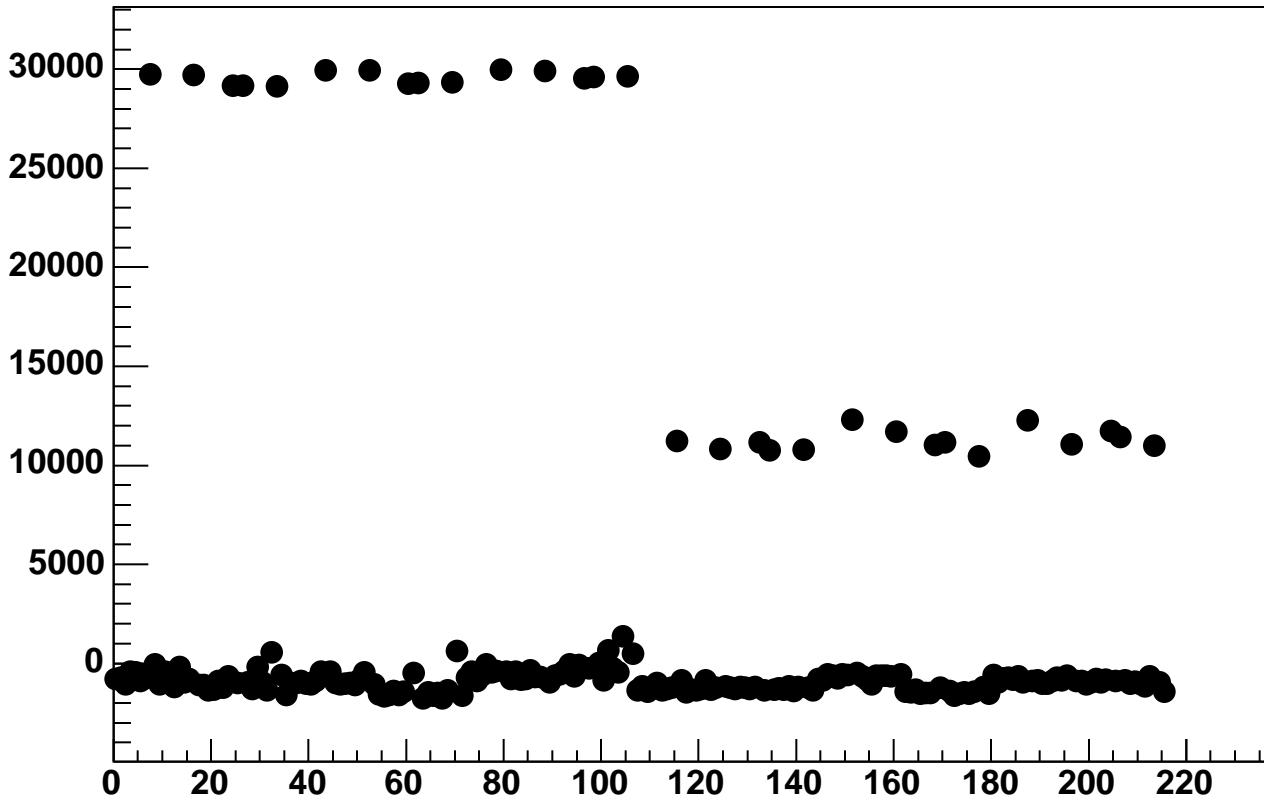


Enable 5, Hold=30, DAC=400, ADC Noise vs 18\*Chip+Chan

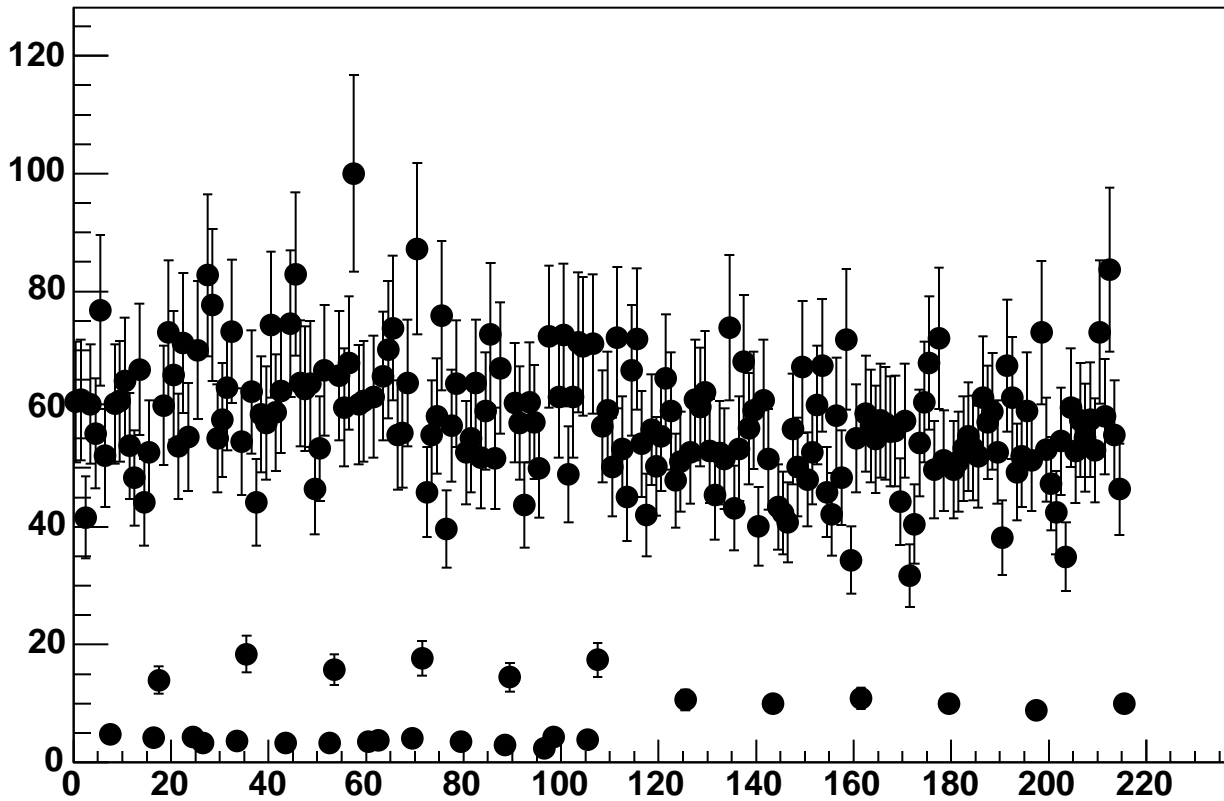




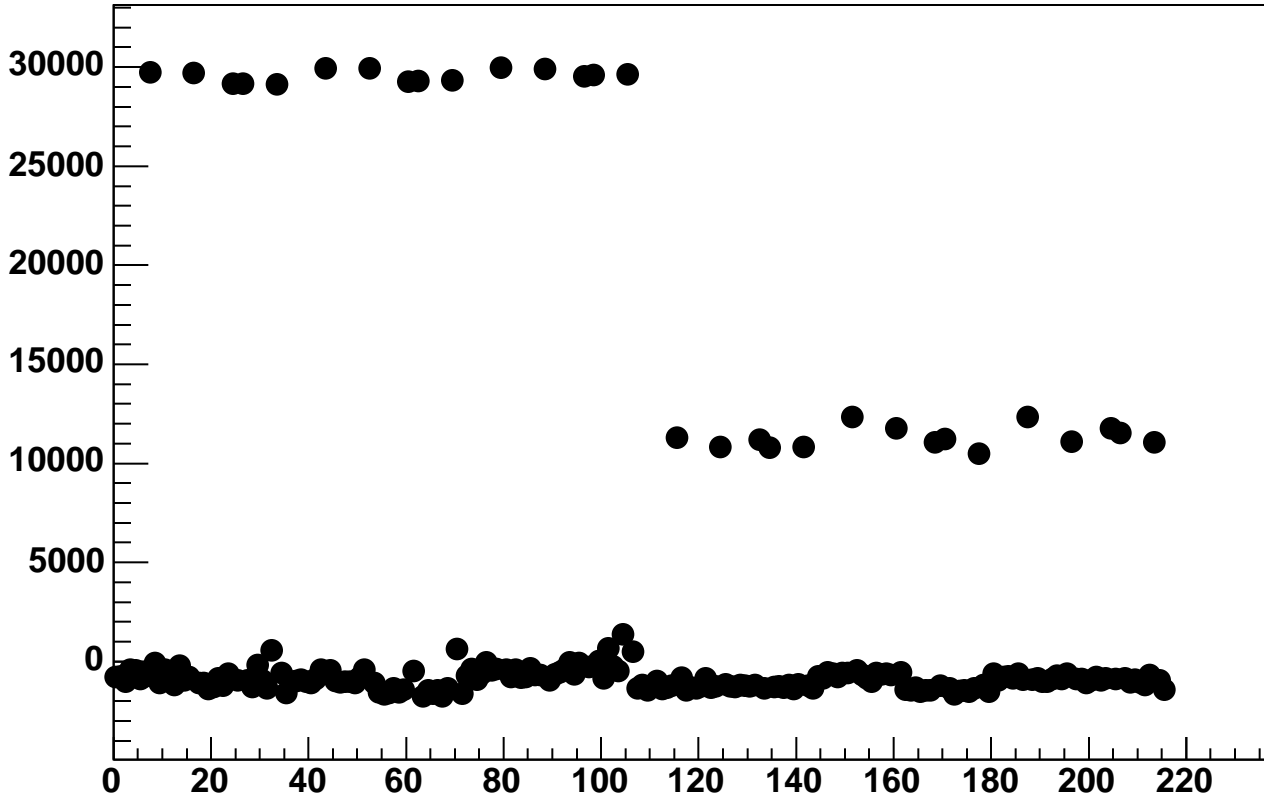
Enable 5, Hold=30, DAC=450, ADC Mean vs 18\*Chip+Chan



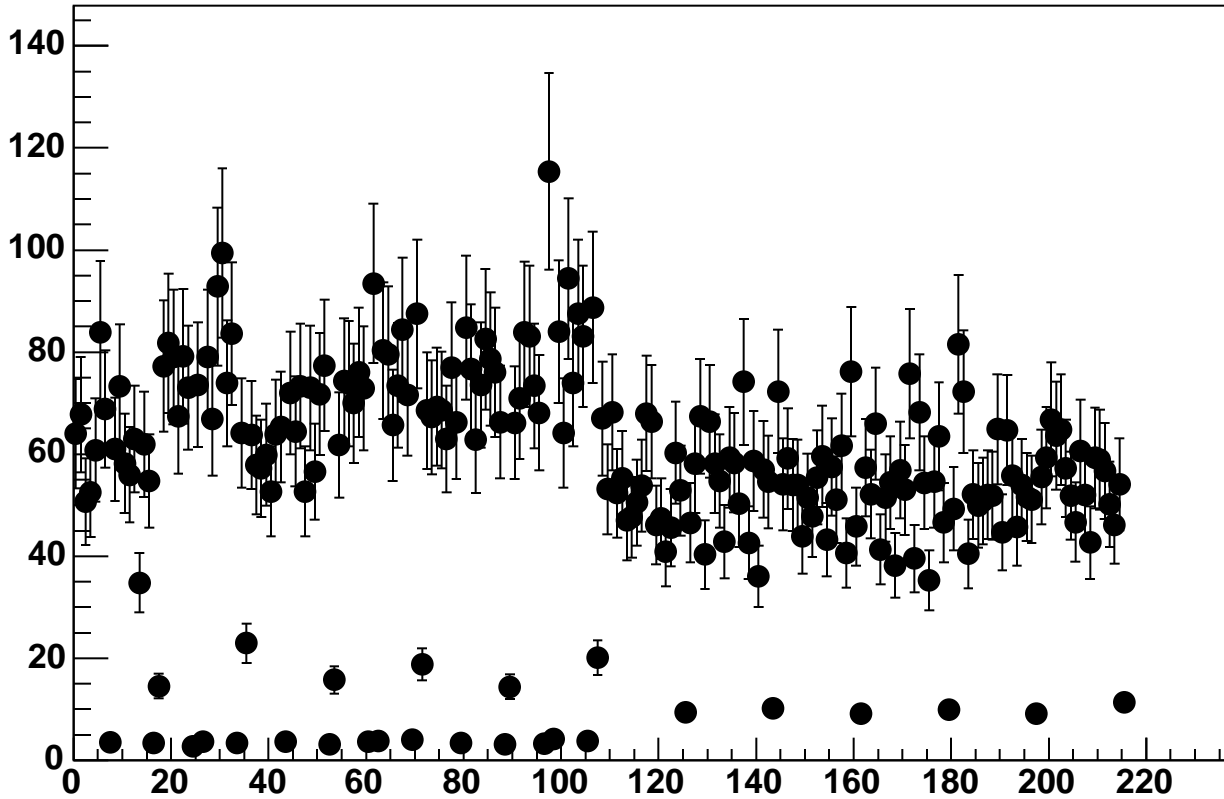
Enable 5, Hold=30, DAC=450, ADC Noise vs 18\*Chip+Chan



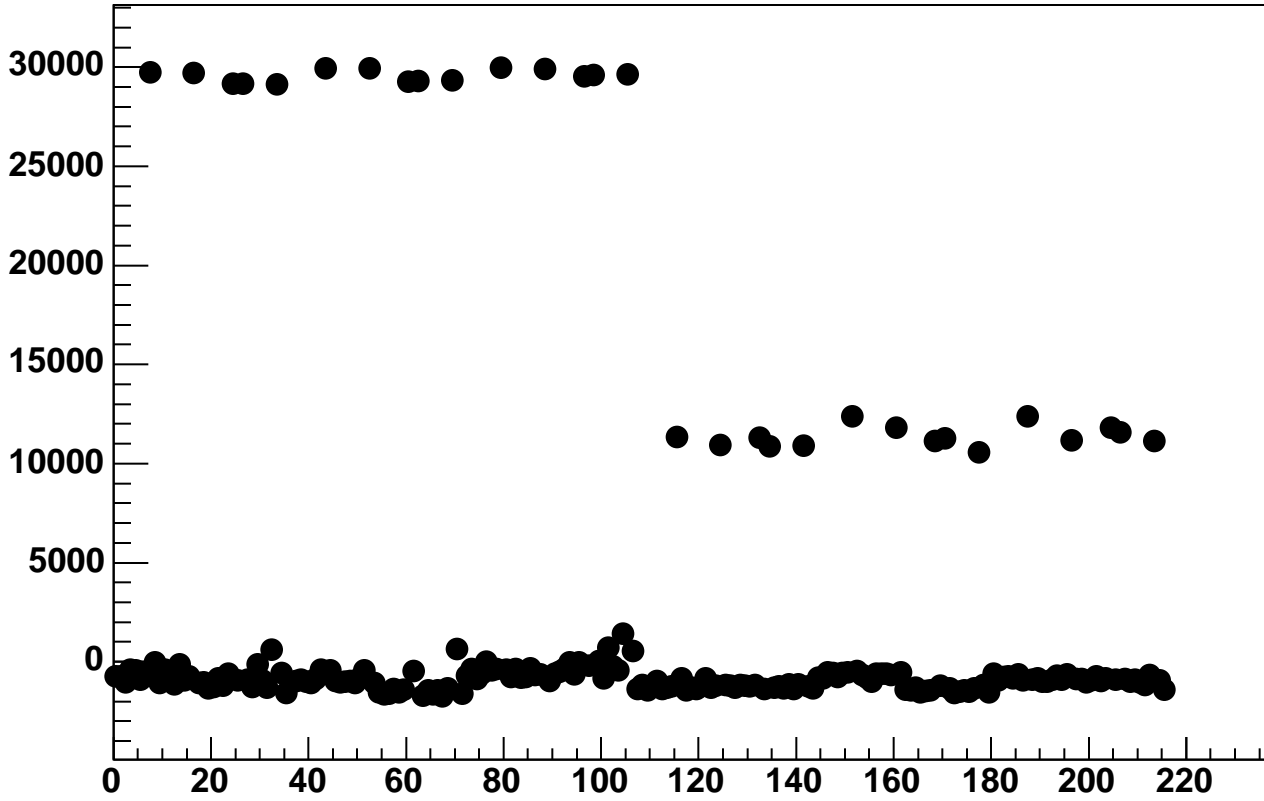
Enable 5, Hold=30, DAC=500, ADC Mean vs 18\*Chip+Chan



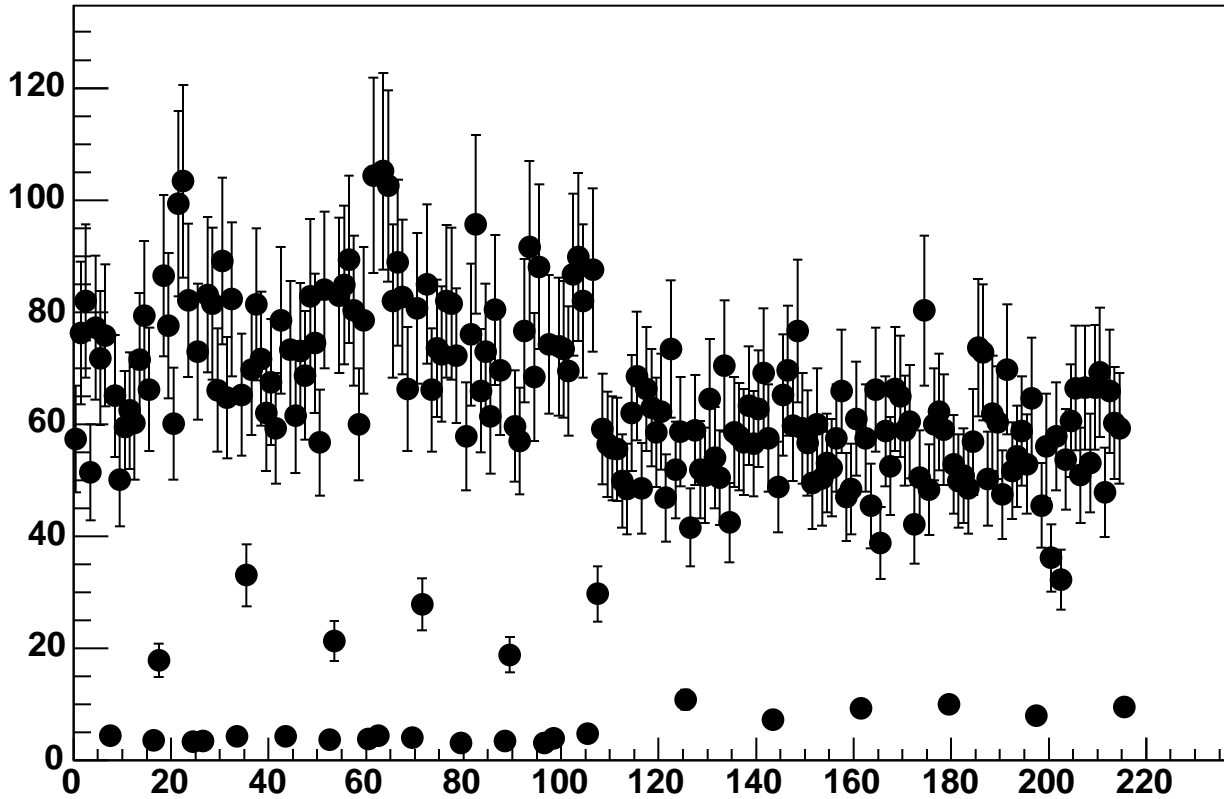
Enable 5, Hold=30, DAC=500, ADC Noise vs 18\*Chip+Chan



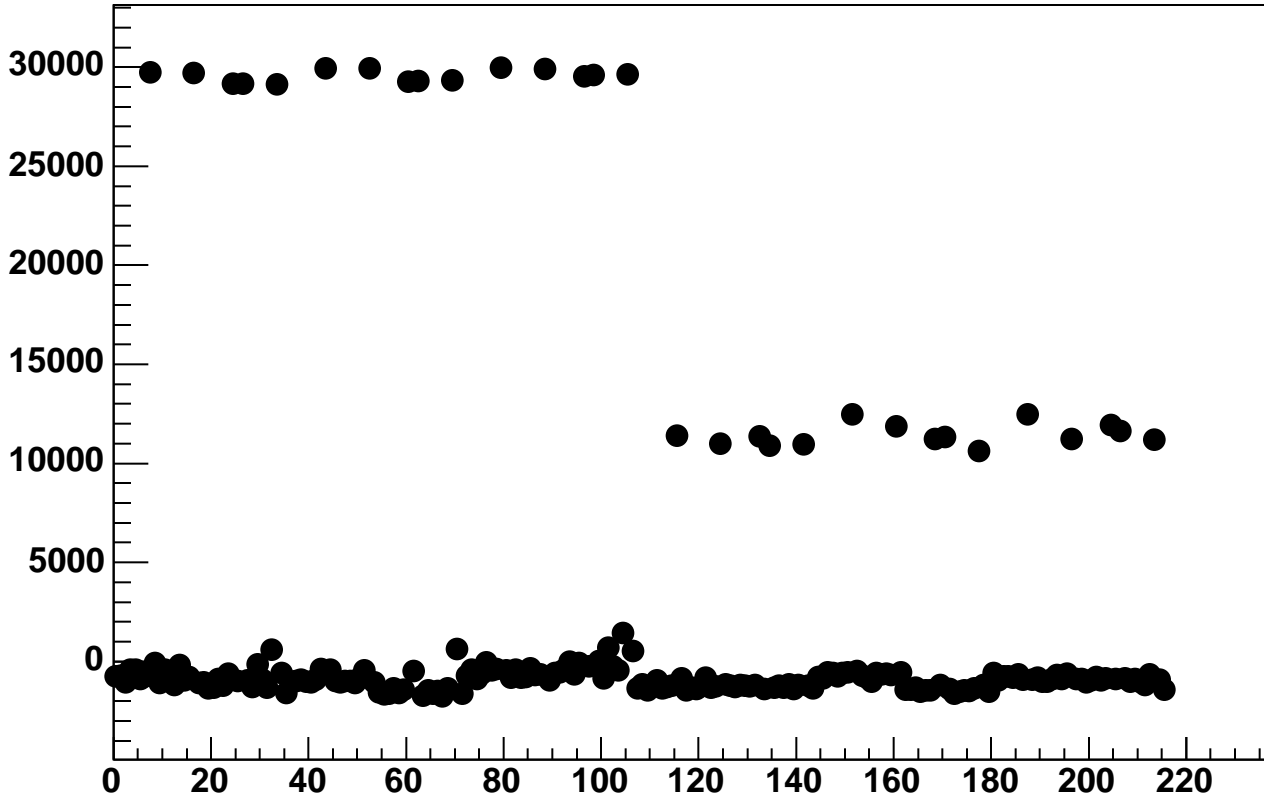
Enable 5, Hold=30, DAC=550, ADC Mean vs 18\*Chip+Chan



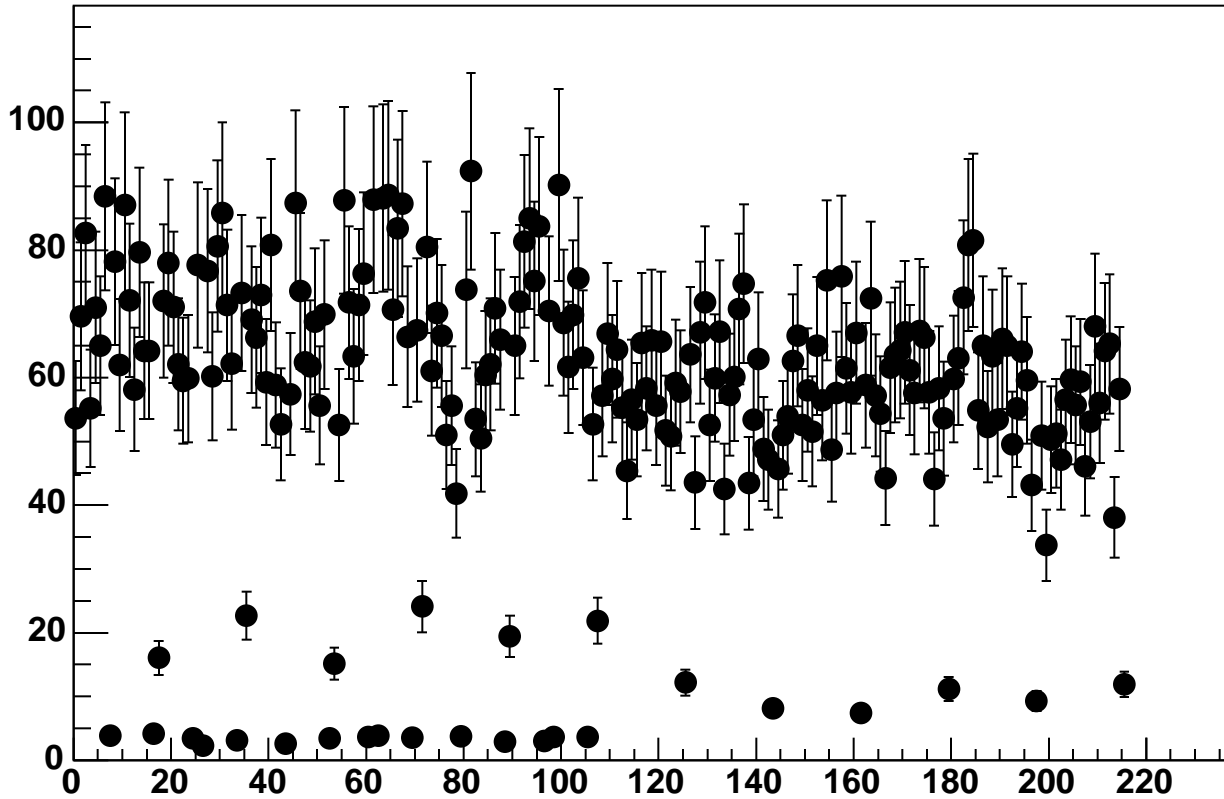
Enable 5, Hold=30, DAC=550, ADC Noise vs 18\*Chip+Chan



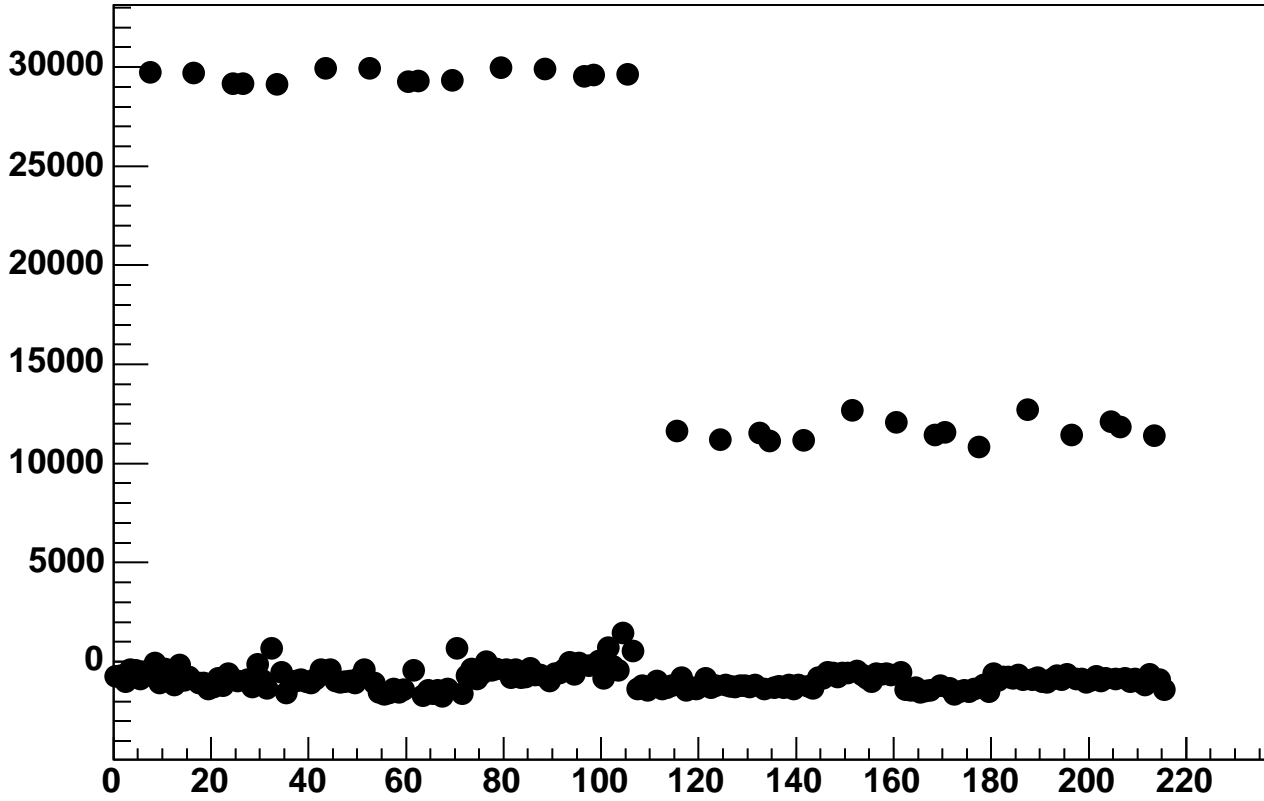
Enable 5, Hold=30, DAC=600, ADC Mean vs 18\*Chip+Chan



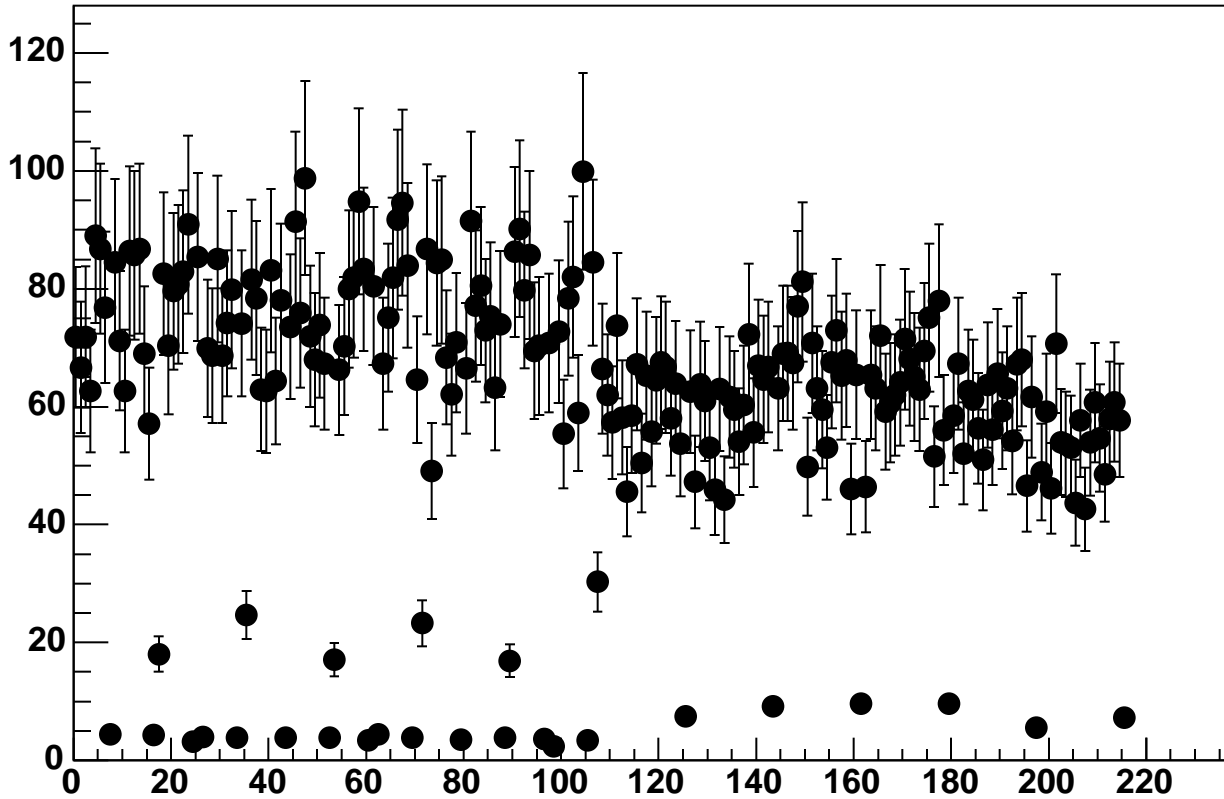
Enable 5, Hold=30, DAC=600, ADC Noise vs 18\*Chip+Chan



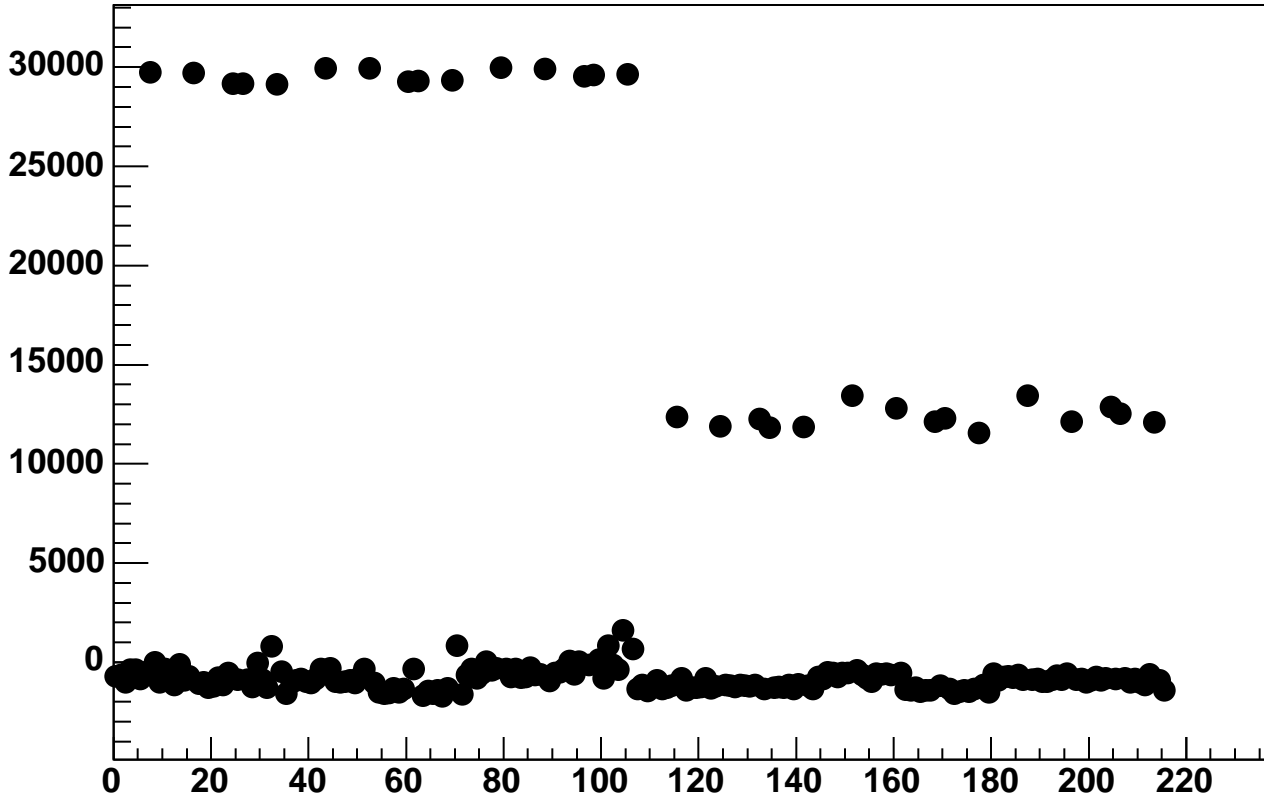
Enable 5, Hold=30, DAC=650, ADC Mean vs 18\*Chip+Chan



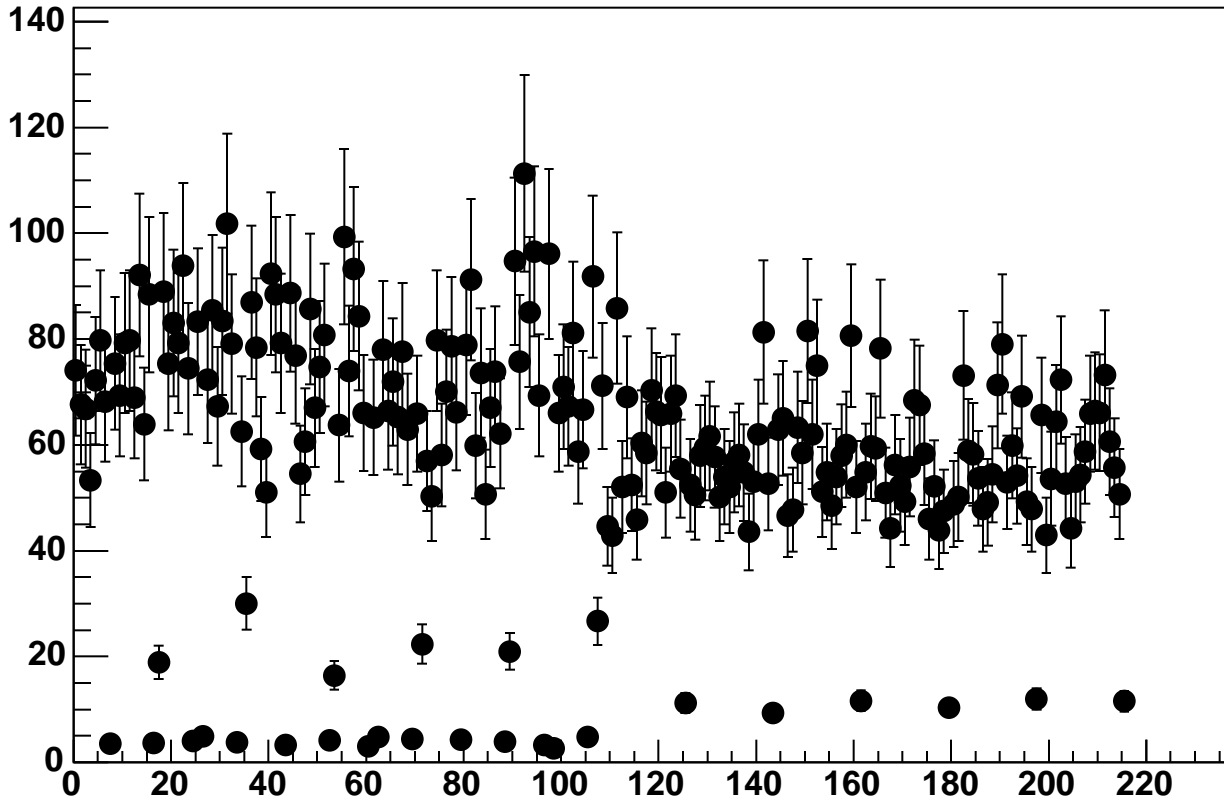
Enable 5, Hold=30, DAC=650, ADC Noise vs 18\*Chip+Chan



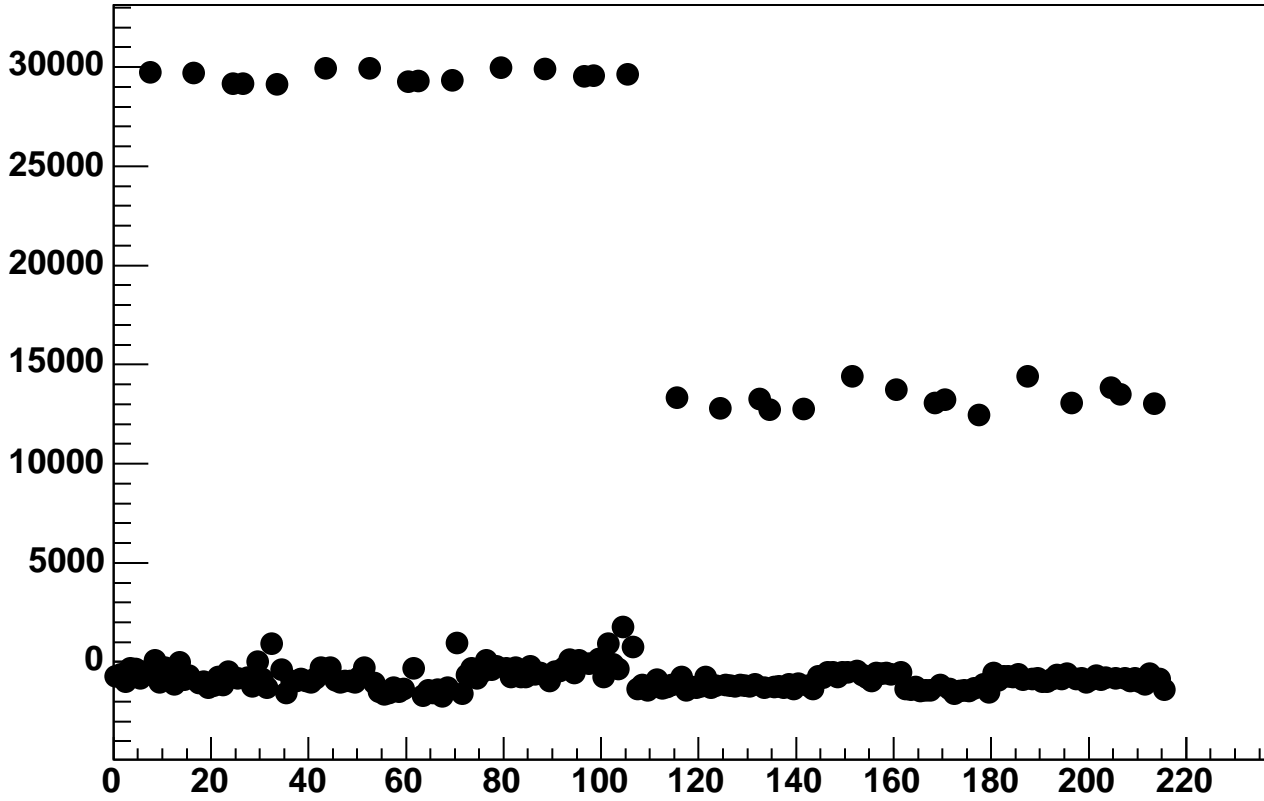
Enable 5, Hold=30, DAC=700, ADC Mean vs 18\*Chip+Chan



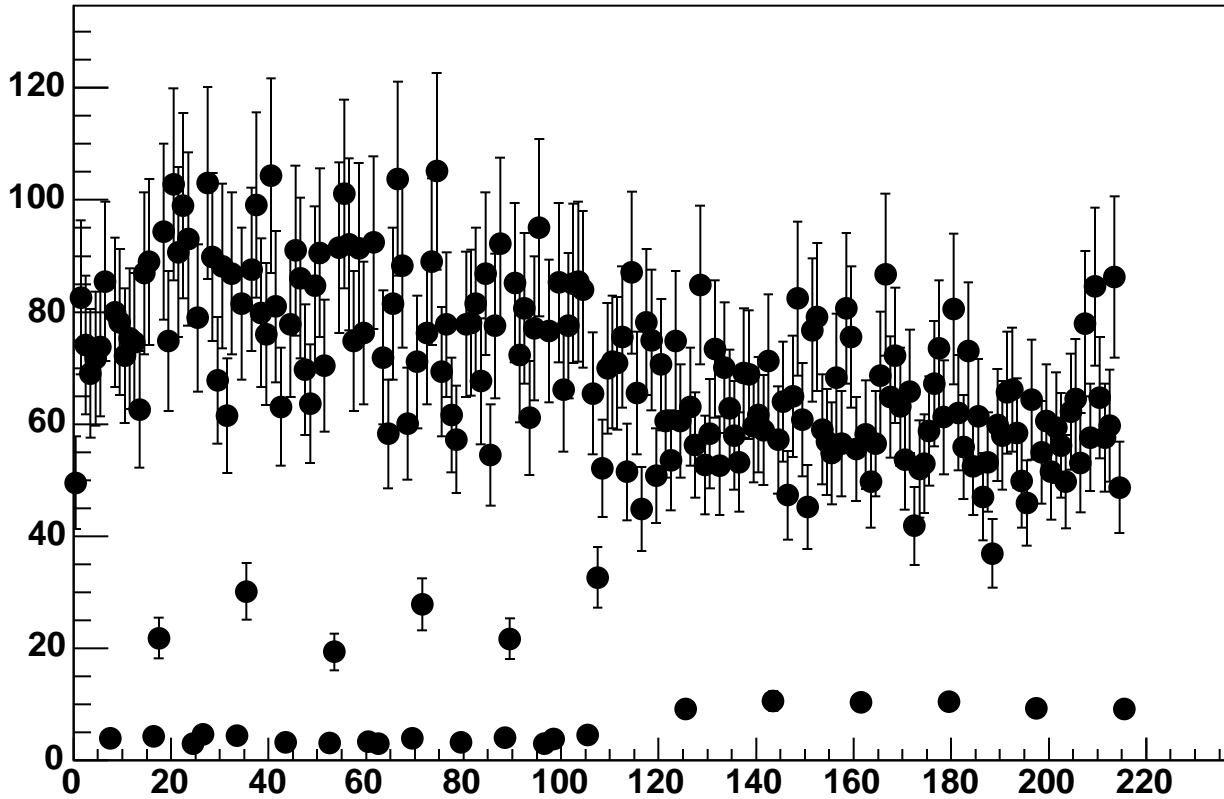
Enable 5, Hold=30, DAC=700, ADC Noise vs 18\*Chip+Chan



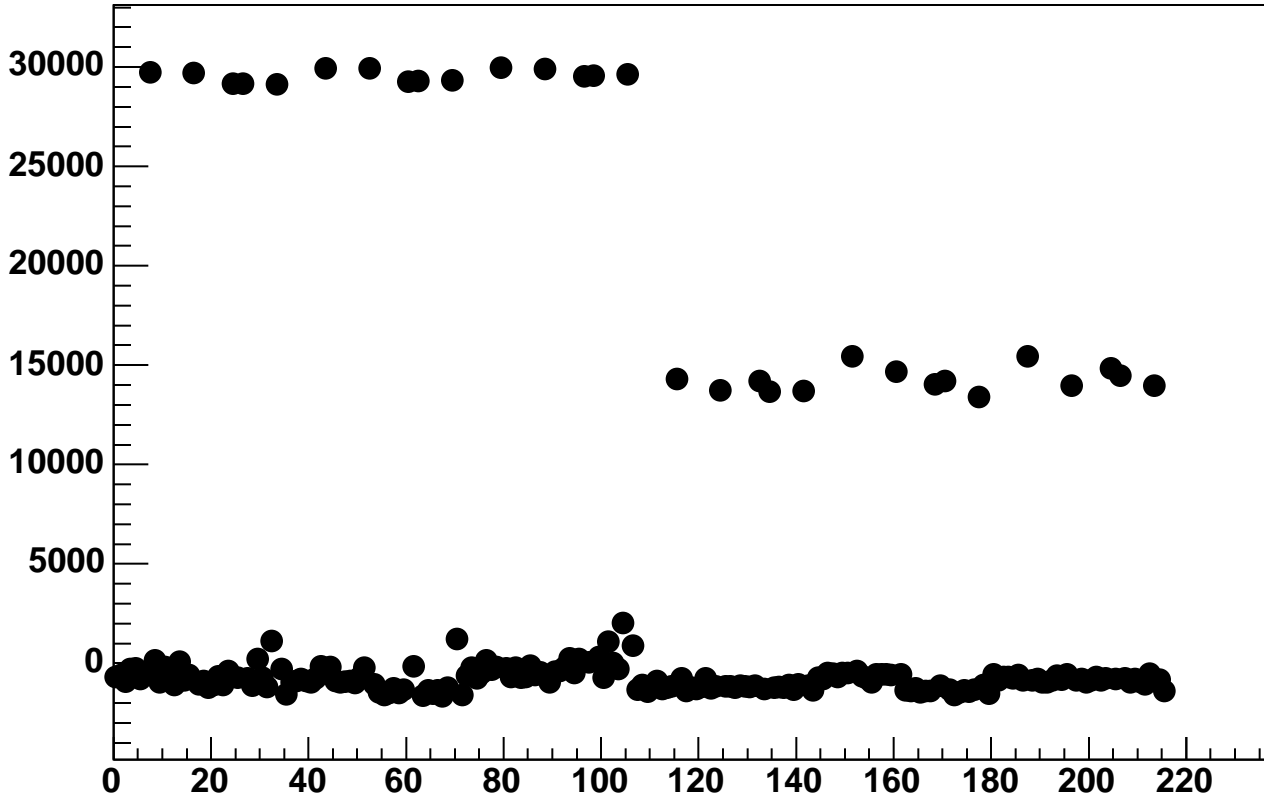
Enable 5, Hold=30, DAC=750, ADC Mean vs 18\*Chip+Chan



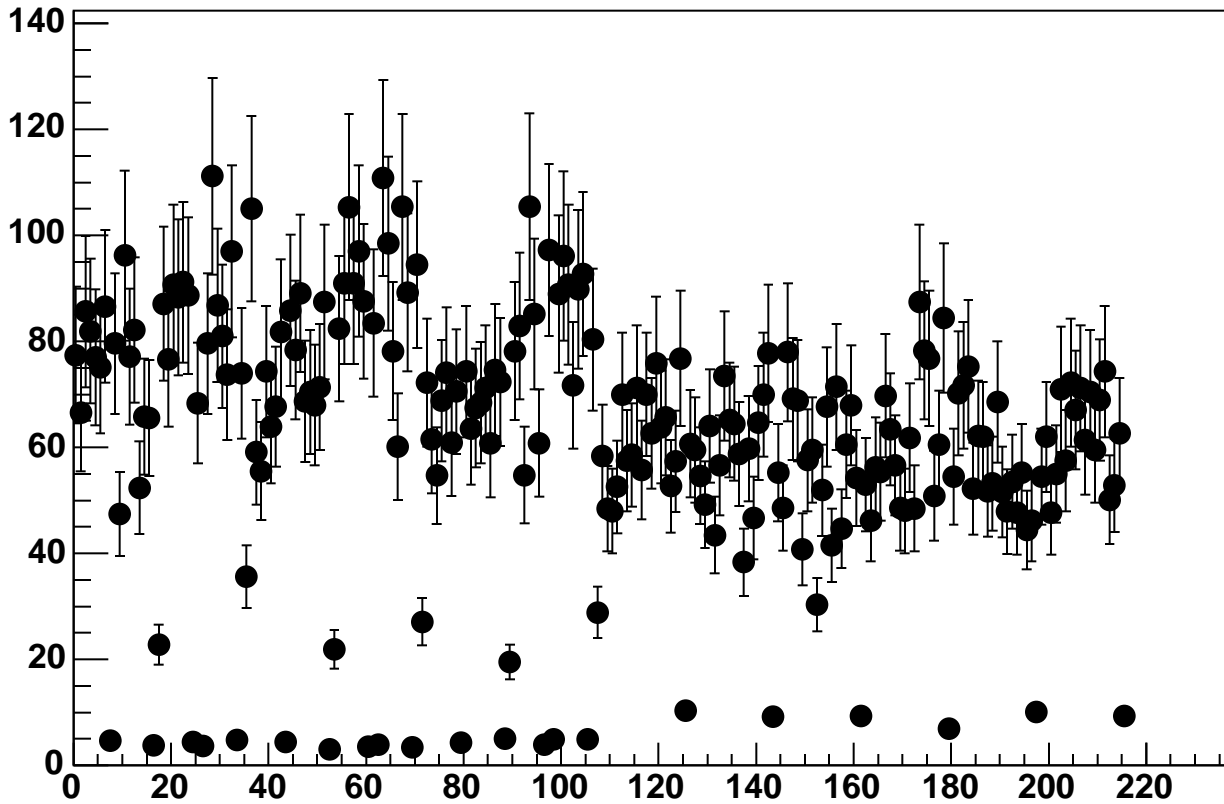
Enable 5, Hold=30, DAC=750, ADC Noise vs 18\*Chip+Chan



Enable 5, Hold=30, DAC=800, ADC Mean vs 18\*Chip+Chan

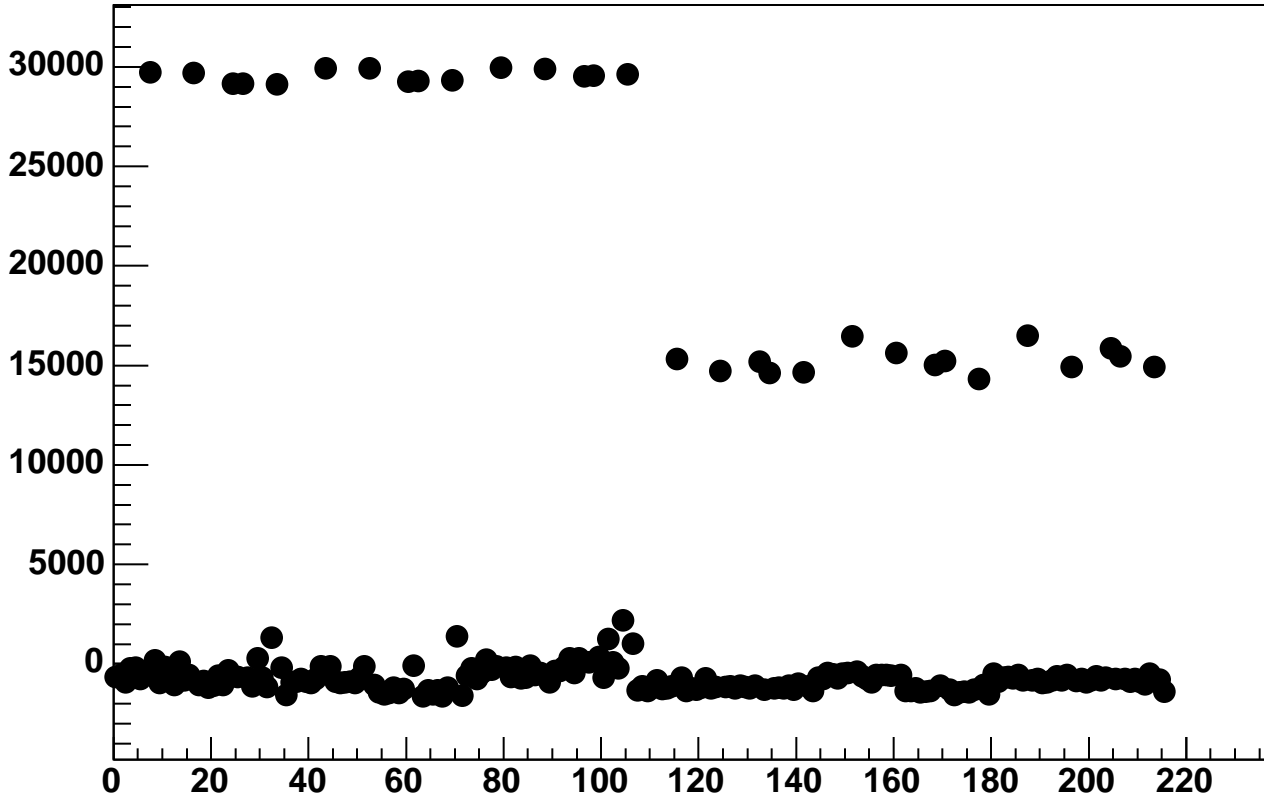


Enable 5, Hold=30, DAC=800, ADC Noise vs 18\*Chip+Chan

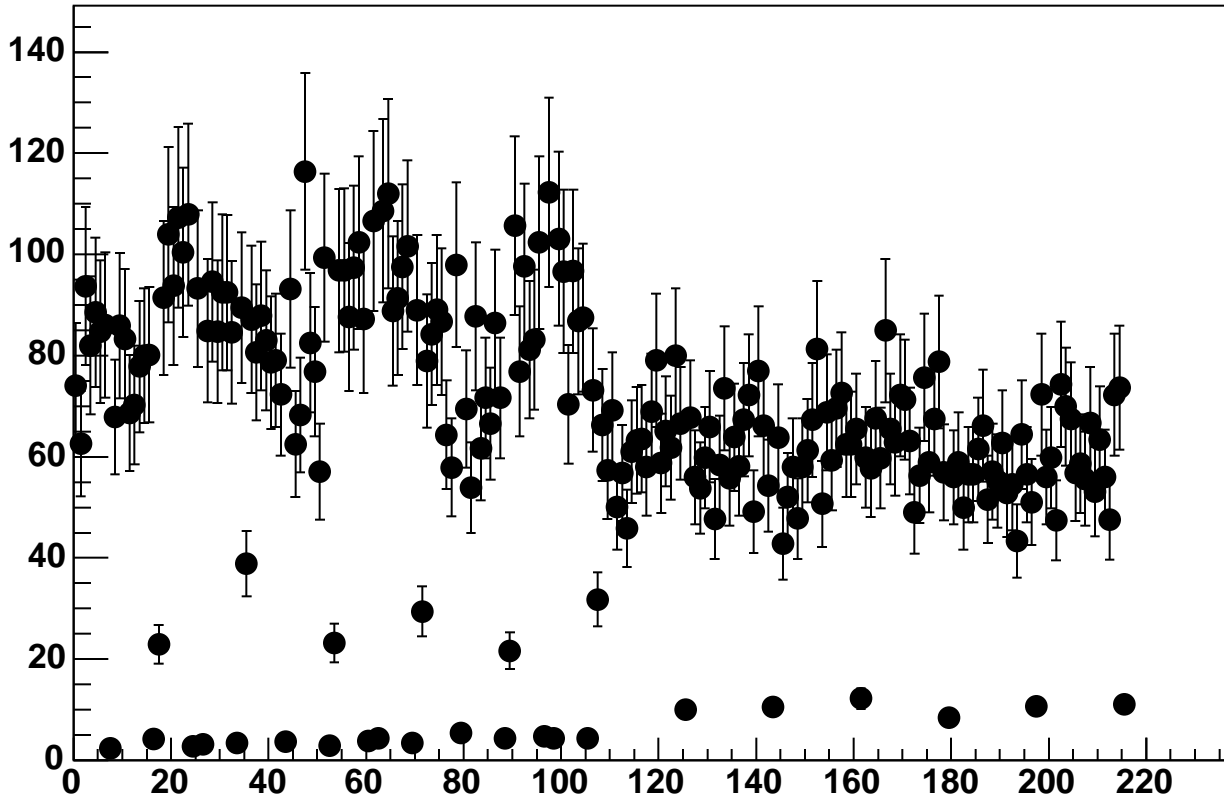




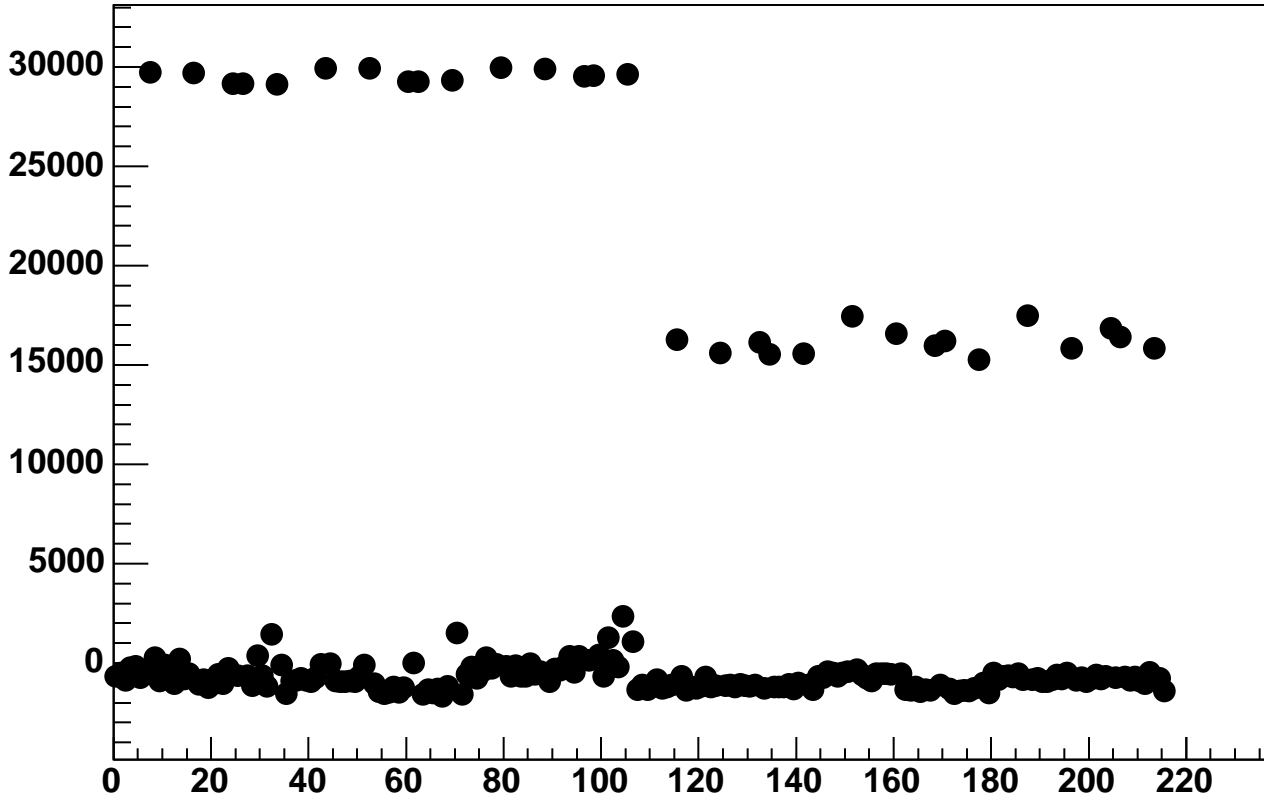
Enable 5, Hold=30, DAC=850, ADC Mean vs 18\*Chip+Chan



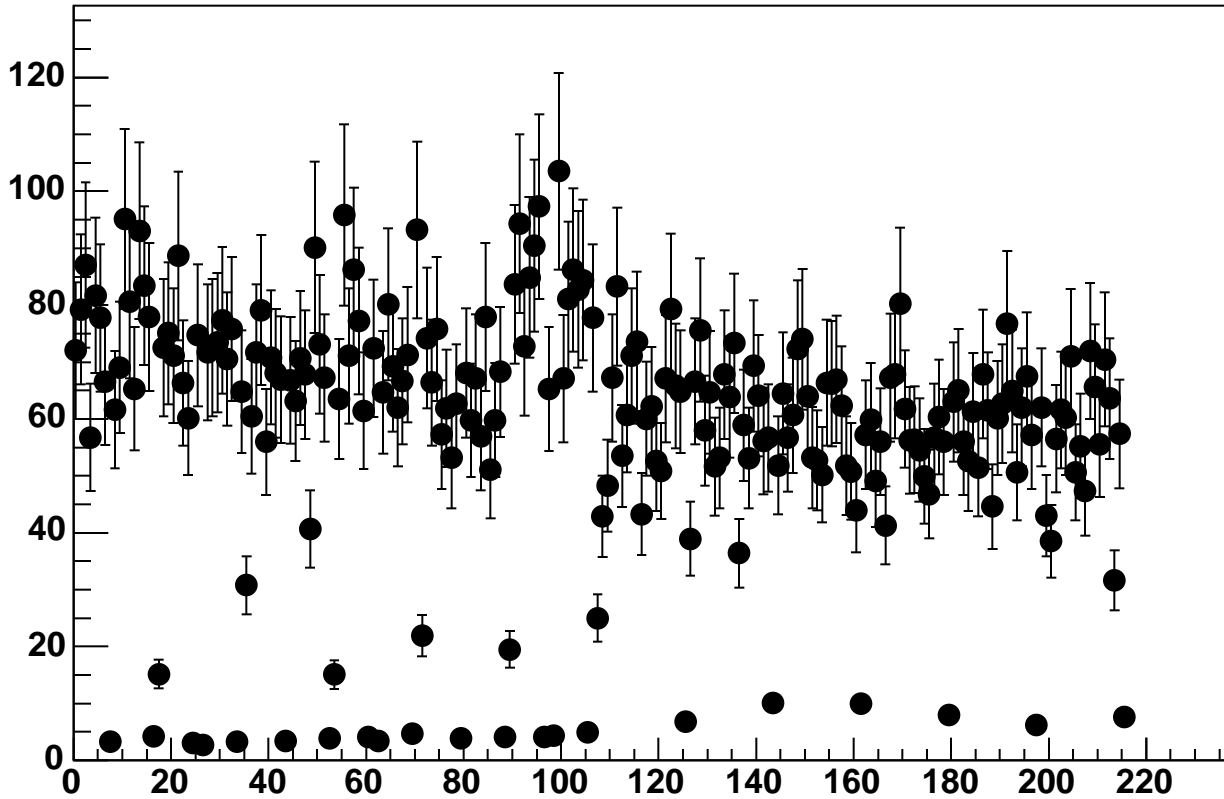
Enable 5, Hold=30, DAC=850, ADC Noise vs 18\*Chip+Chan



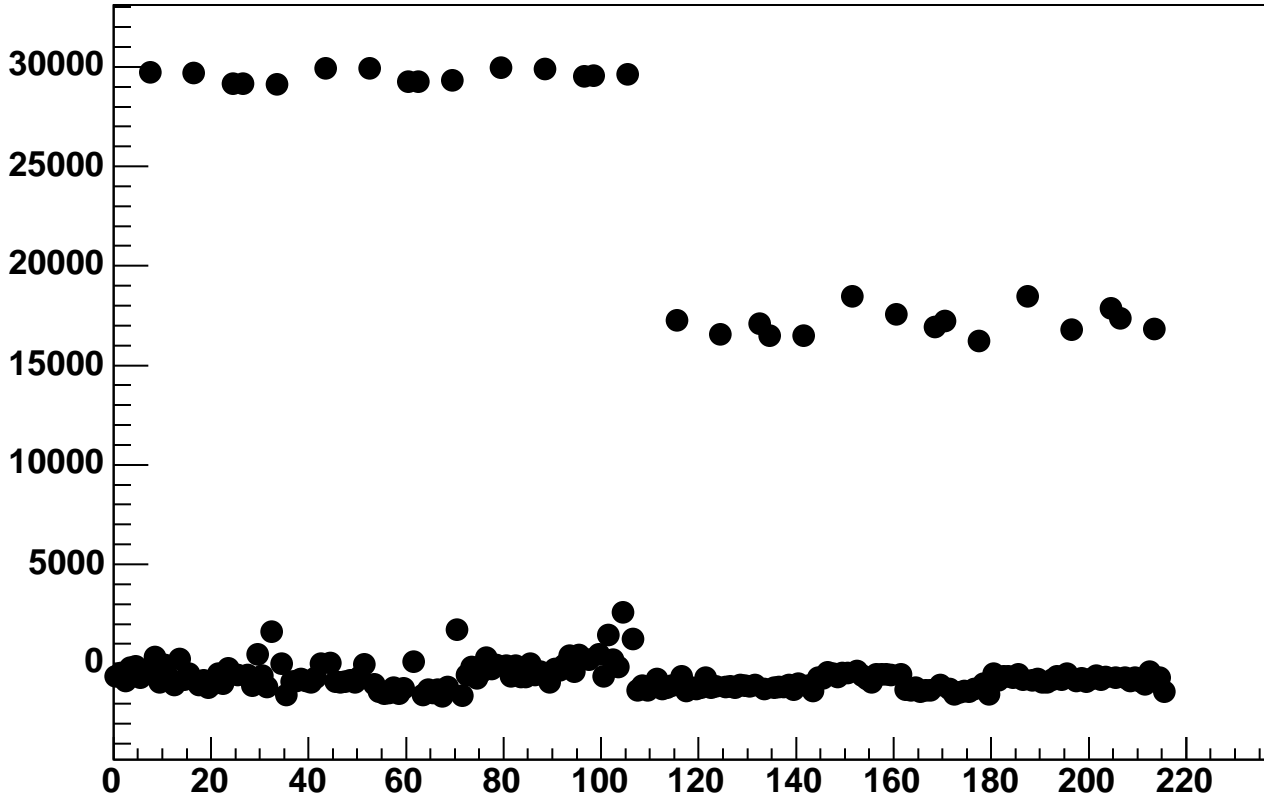
Enable 5, Hold=30, DAC=900, ADC Mean vs 18\*Chip+Chan



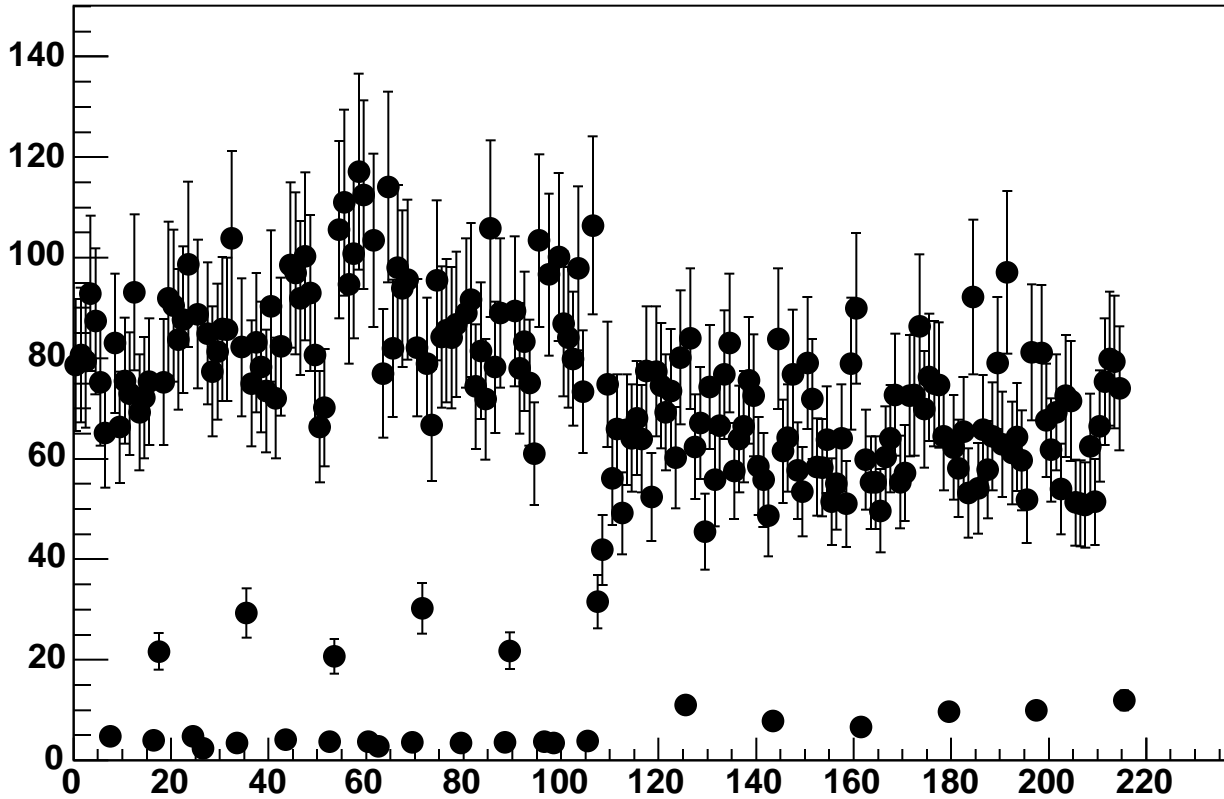
Enable 5, Hold=30, DAC=900, ADC Noise vs 18\*Chip+Chan



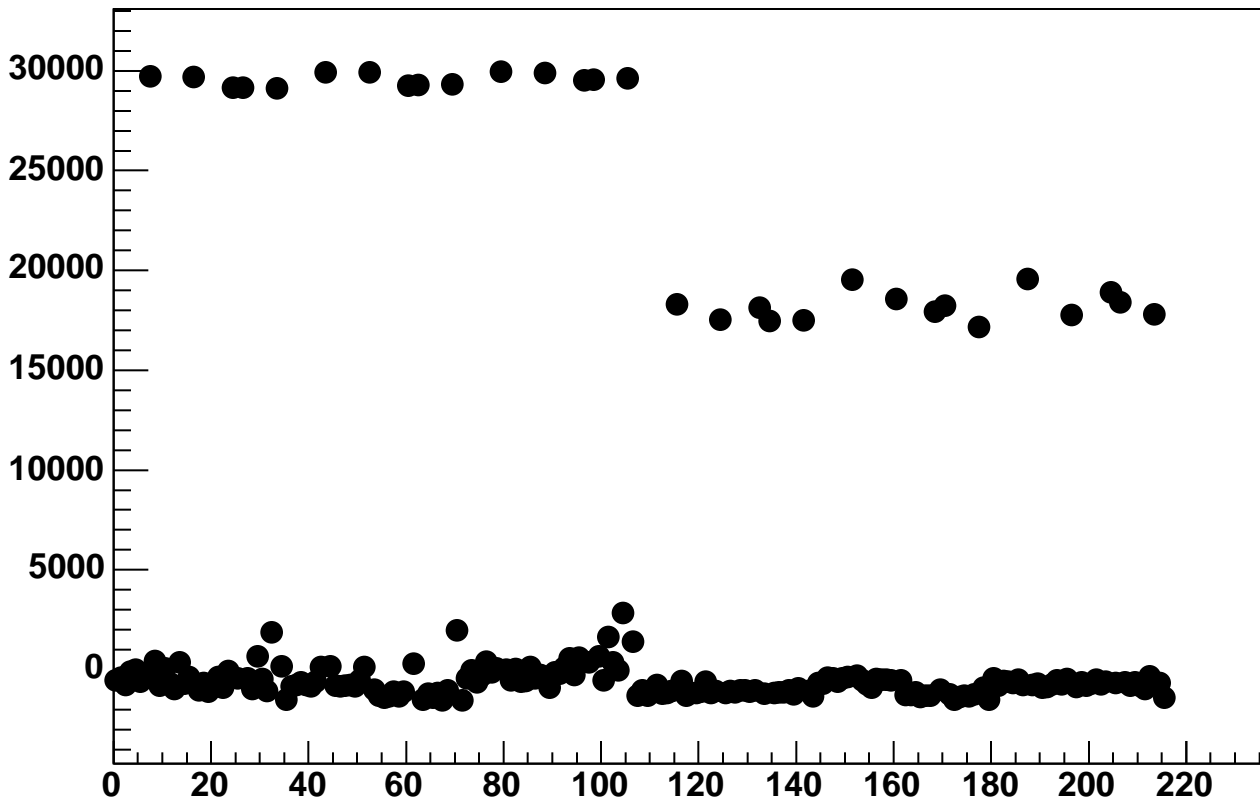
Enable 5, Hold=30, DAC=950, ADC Mean vs 18\*Chip+Chan



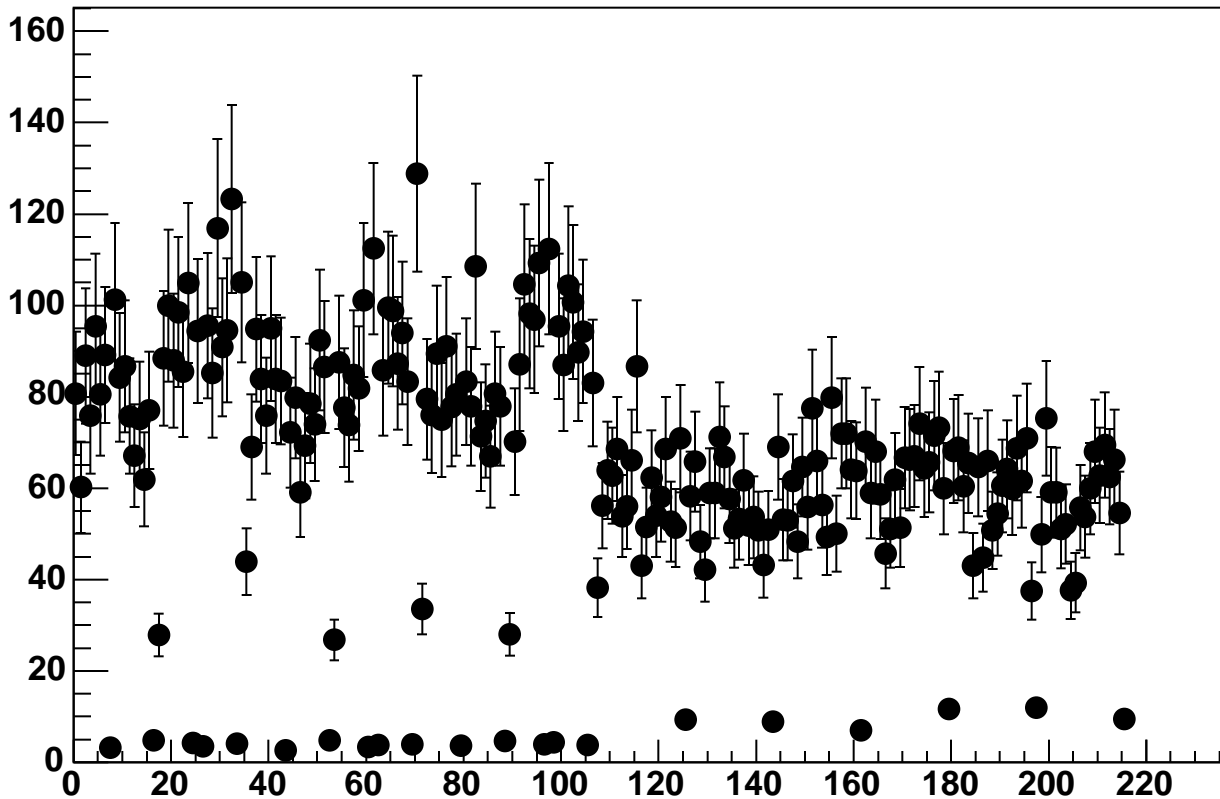
Enable 5, Hold=30, DAC=950, ADC Noise vs 18\*Chip+Chan



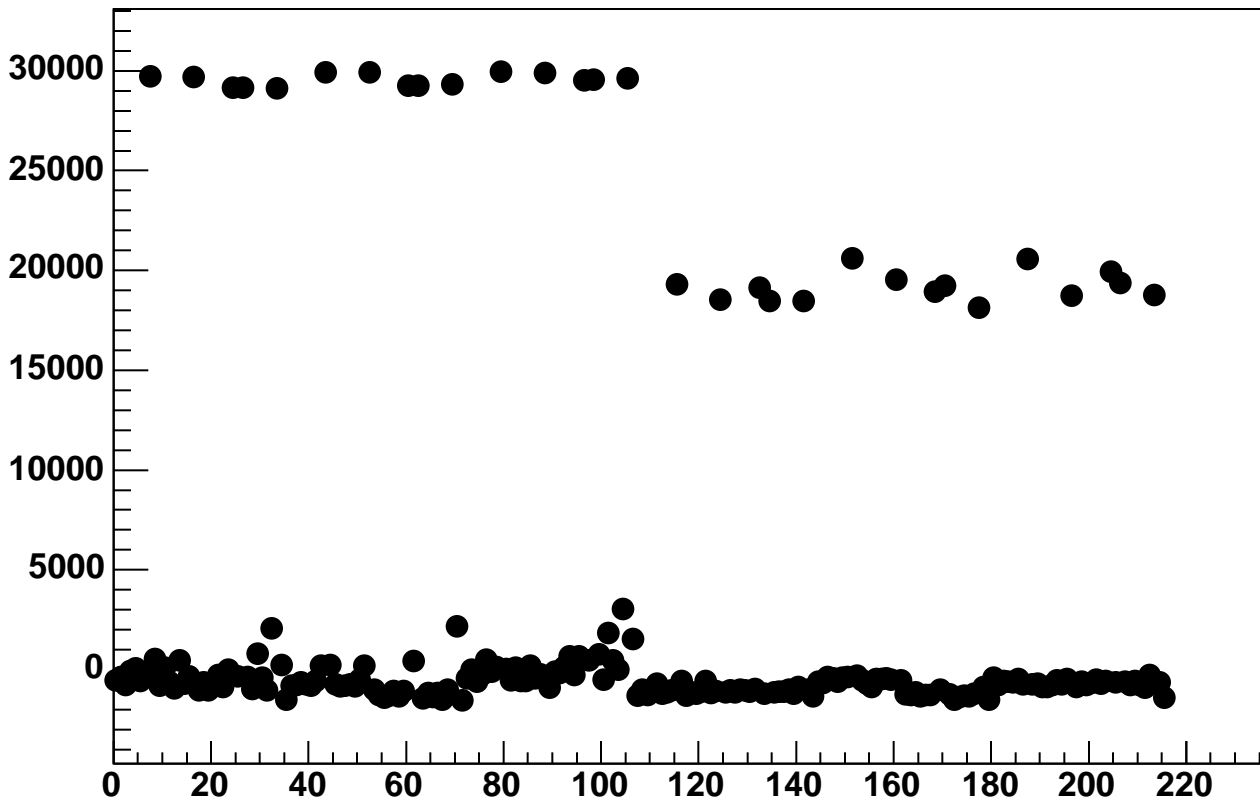
Enable 5, Hold=30, DAC=1000, ADC Mean vs 18\*Chip+Chan



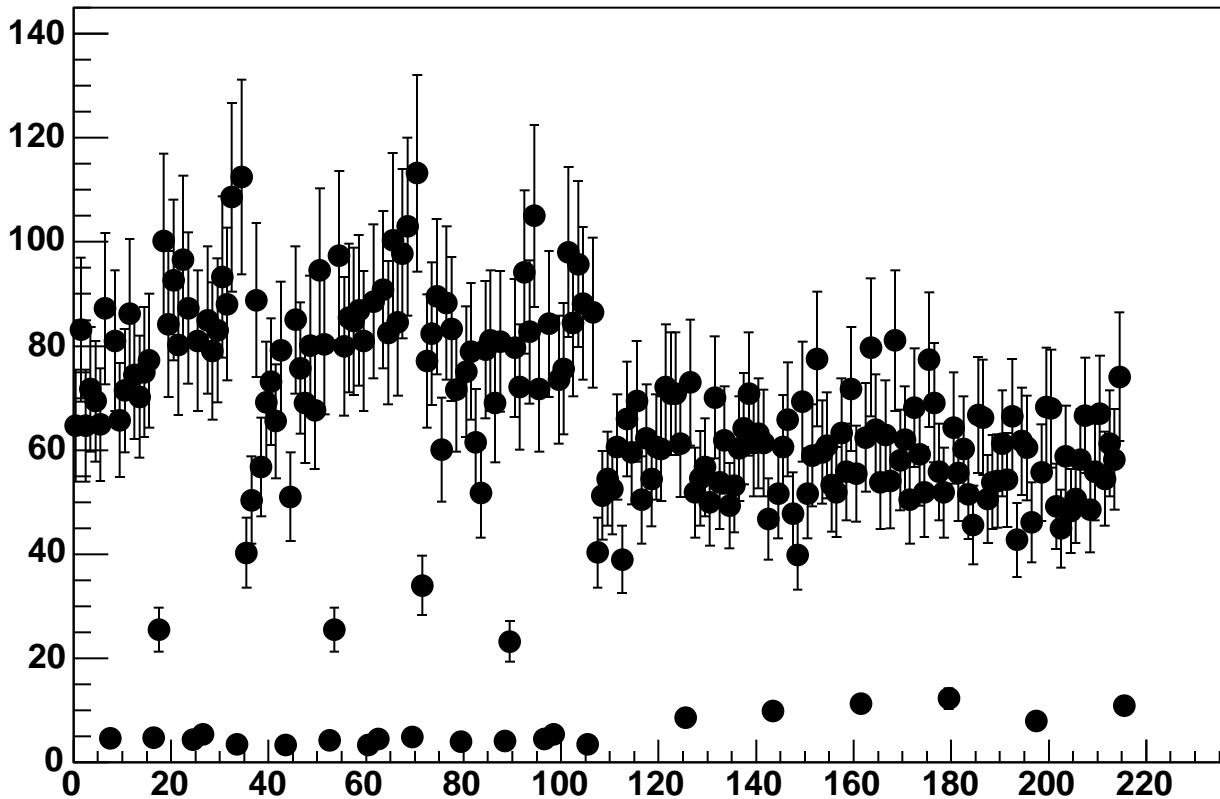
Enable 5, Hold=30, DAC=1000, ADC Noise vs 18\*Chip+Chan



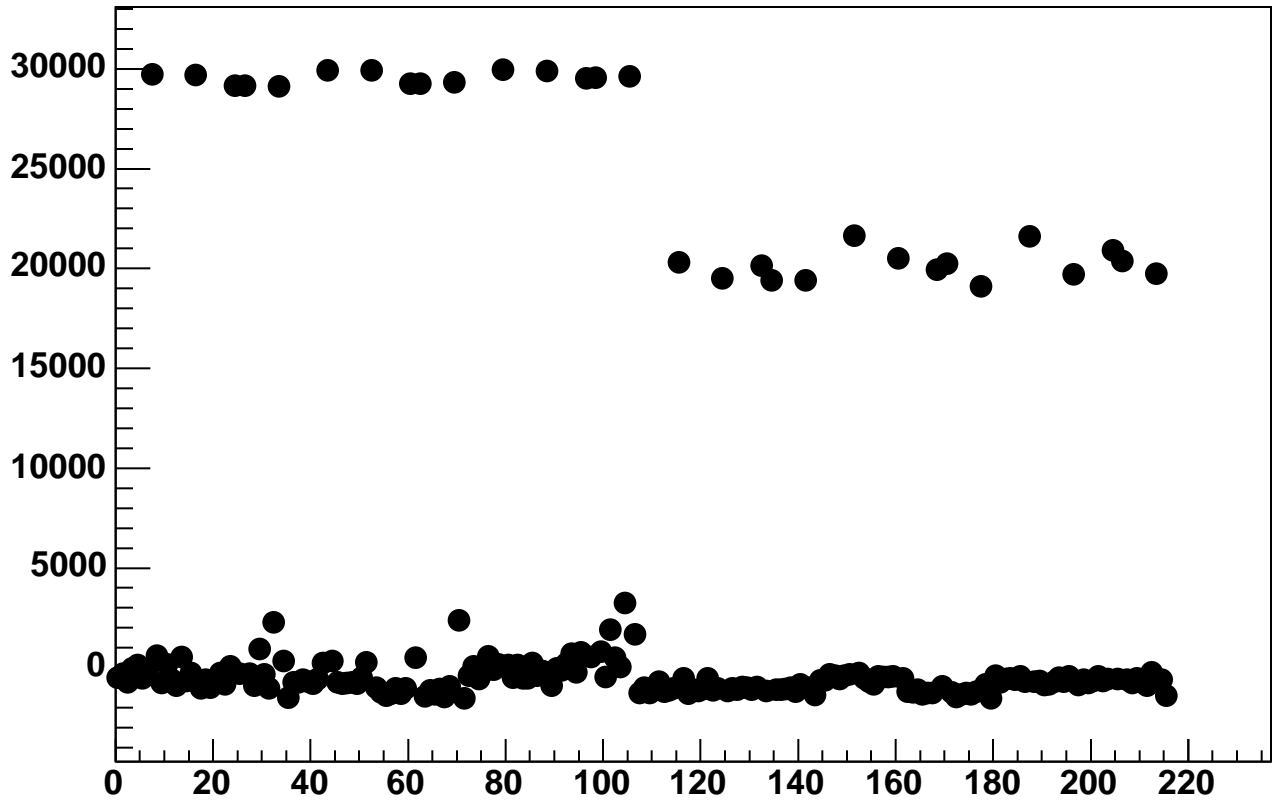
Enable 5, Hold=30, DAC=1050, ADC Mean vs 18\*Chip+Chan



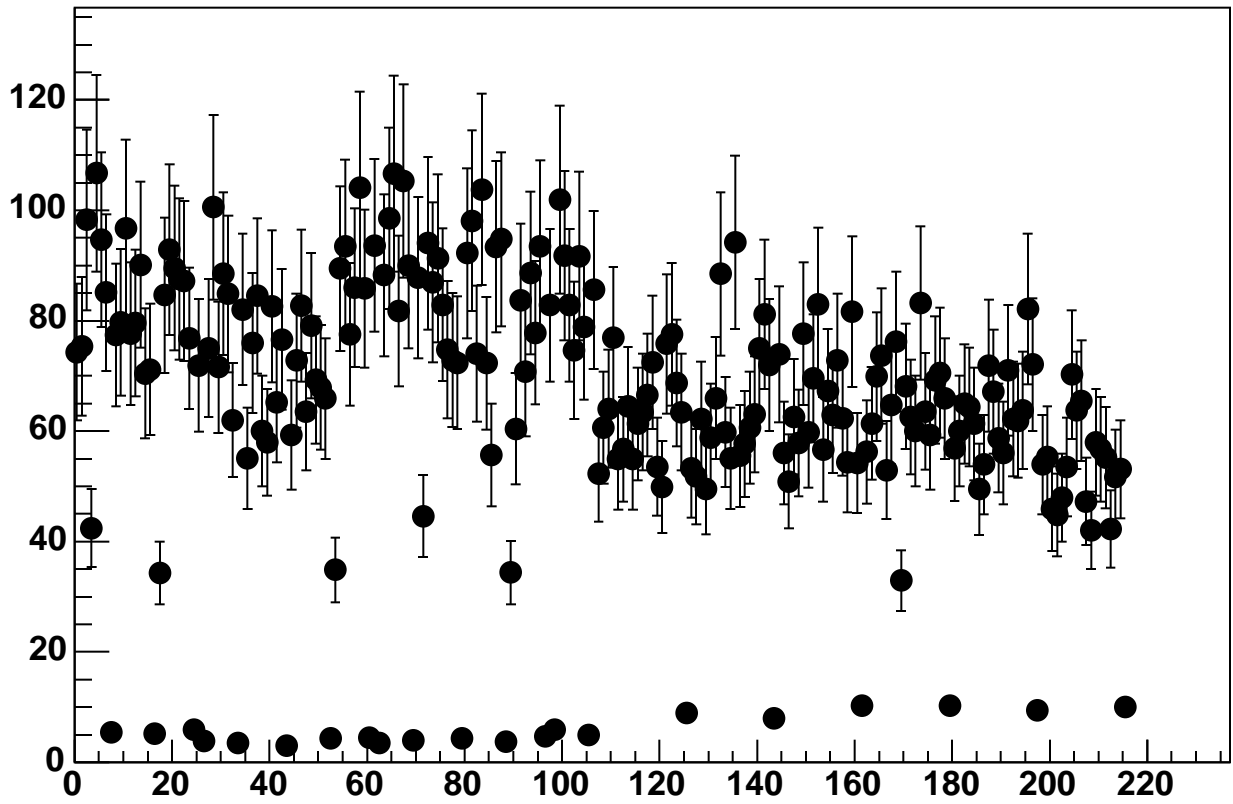
Enable 5, Hold=30, DAC=1050, ADC Noise vs 18\*Chip+Chan



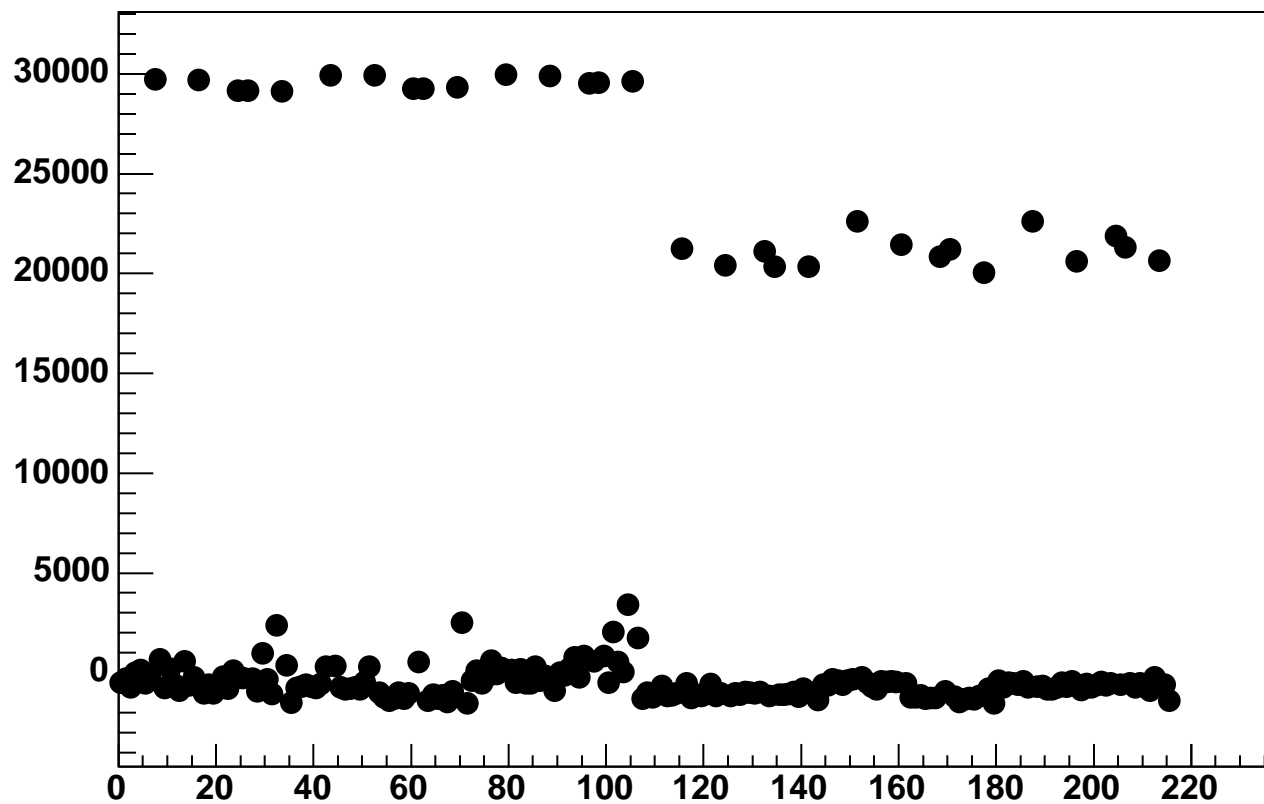
Enable 5, Hold=30, DAC=1100, ADC Mean vs 18\*Chip+Chan



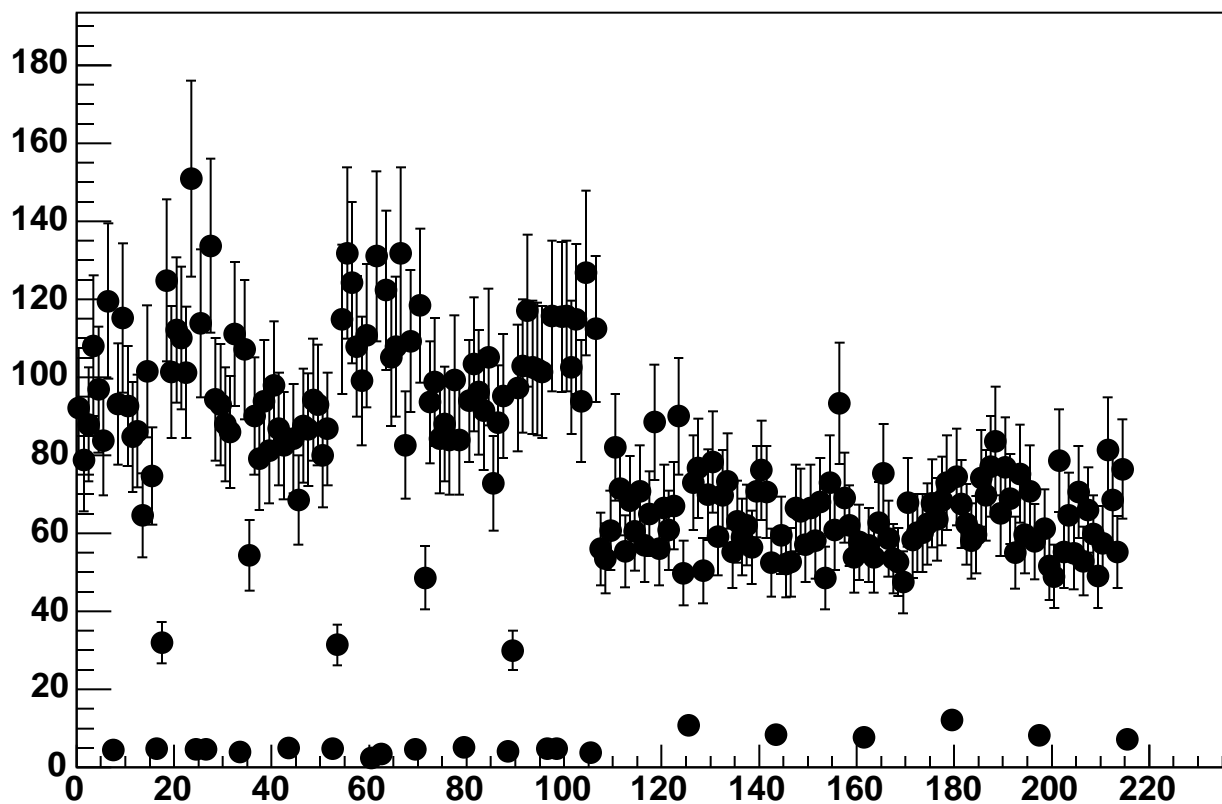
Enable 5, Hold=30, DAC=1100, ADC Noise vs 18\*Chip+Chan



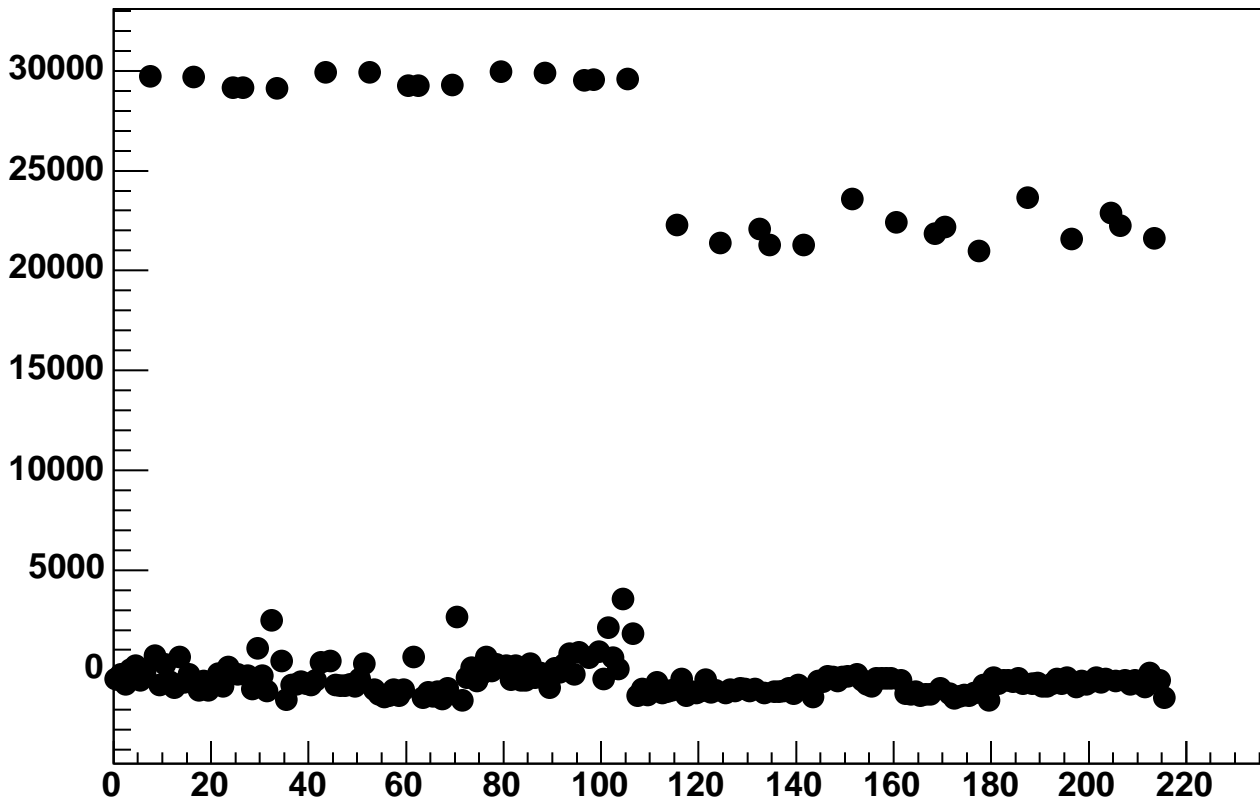
Enable 5, Hold=30, DAC=1150, ADC Mean vs 18\*Chip+Chan



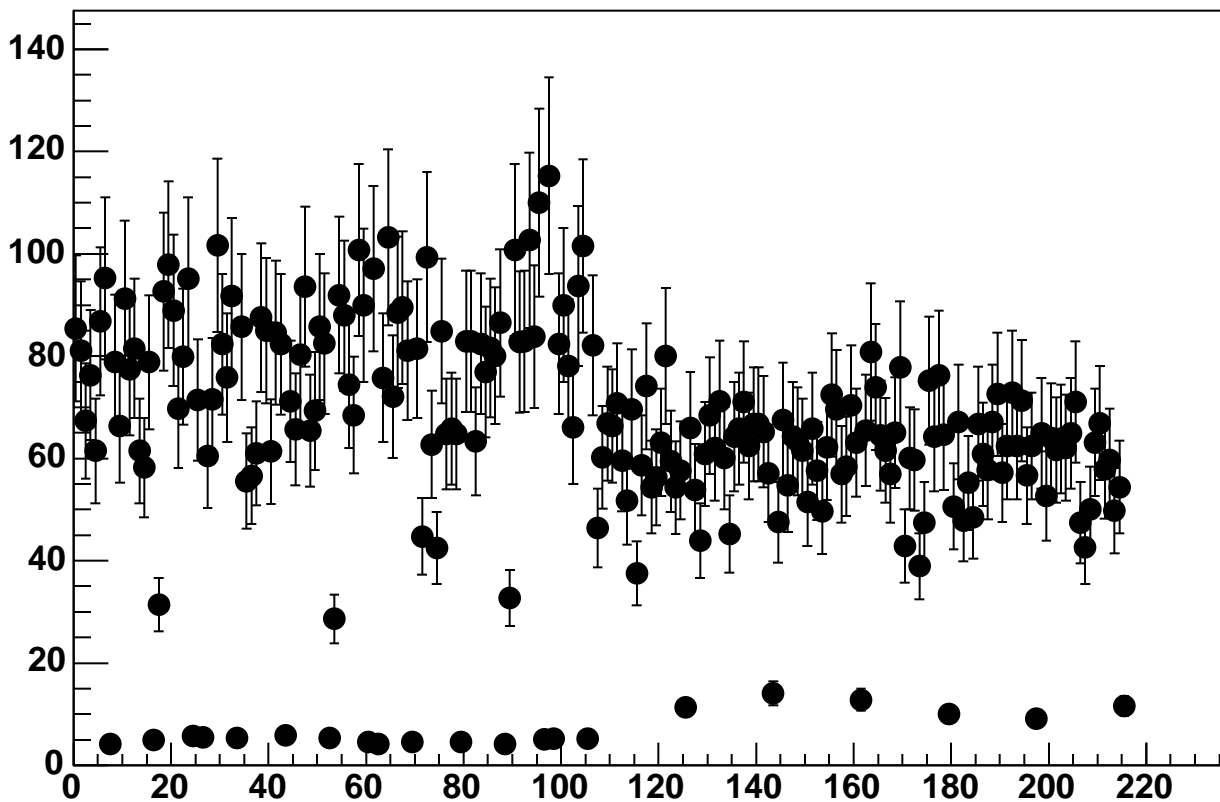
Enable 5, Hold=30, DAC=1150, ADC Noise vs 18\*Chip+Chan



Enable 5, Hold=30, DAC=1200, ADC Mean vs 18\*Chip+Chan

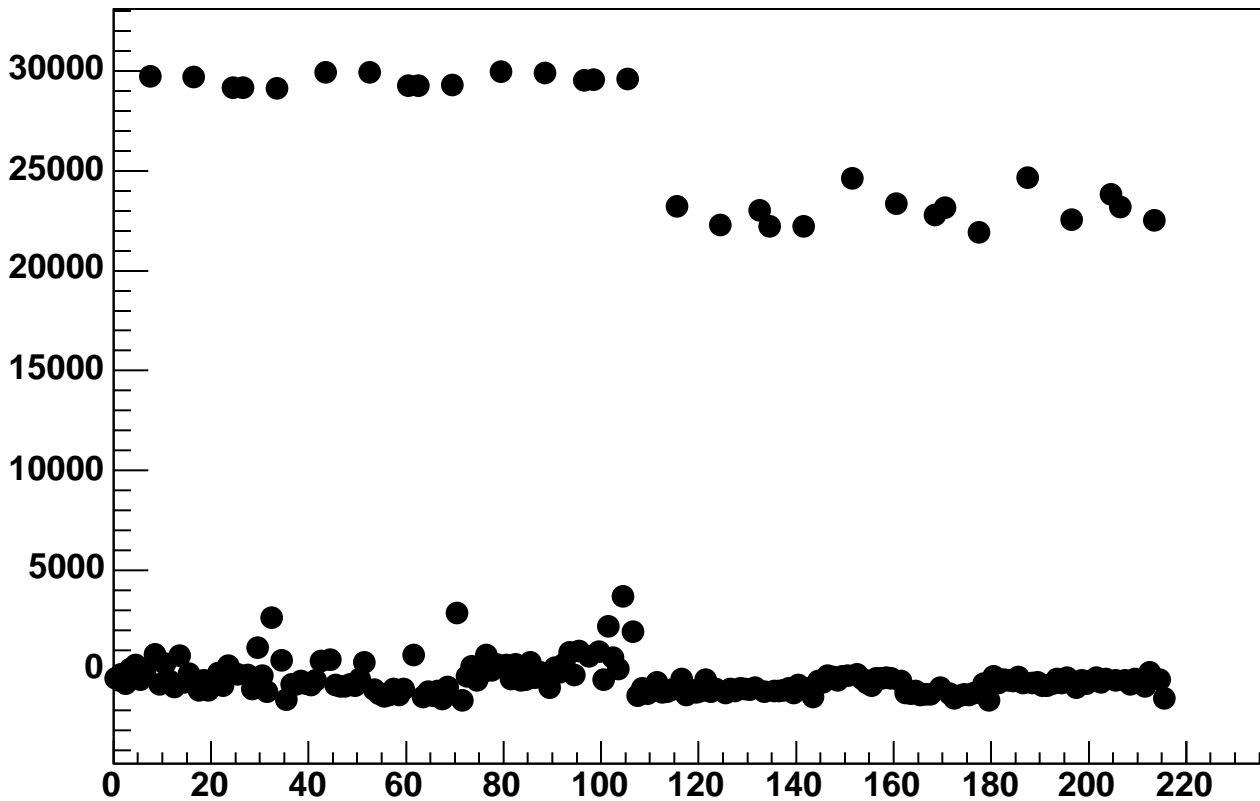


Enable 5, Hold=30, DAC=1200, ADC Noise vs 18\*Chip+Chan

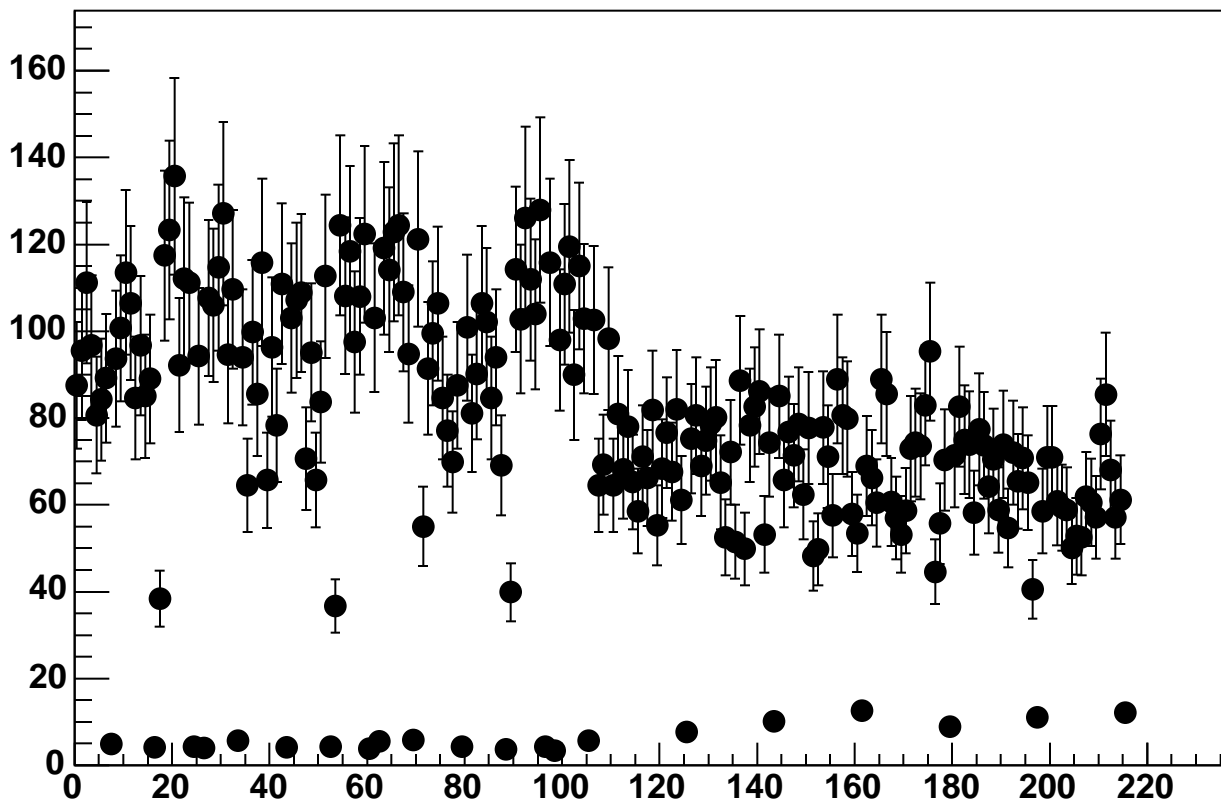




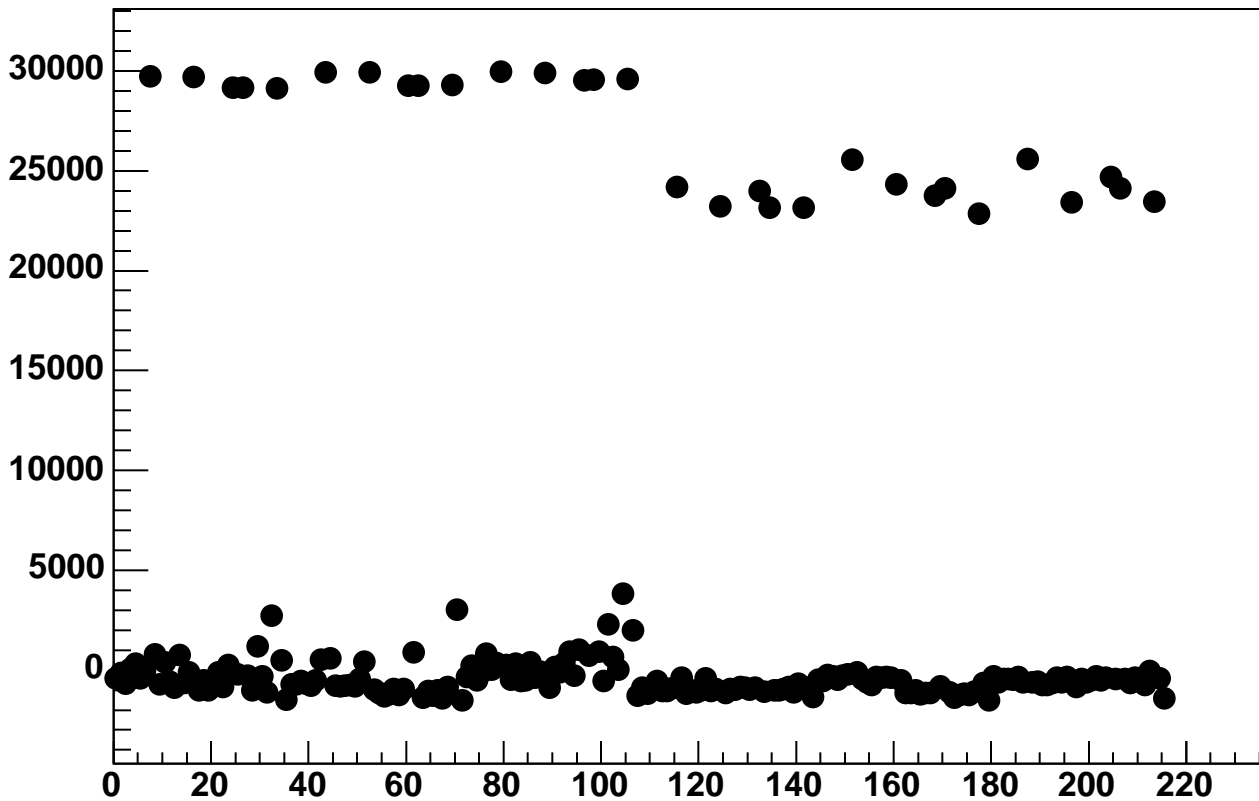
Enable 5, Hold=30, DAC=1250, ADC Mean vs 18\*Chip+Chan



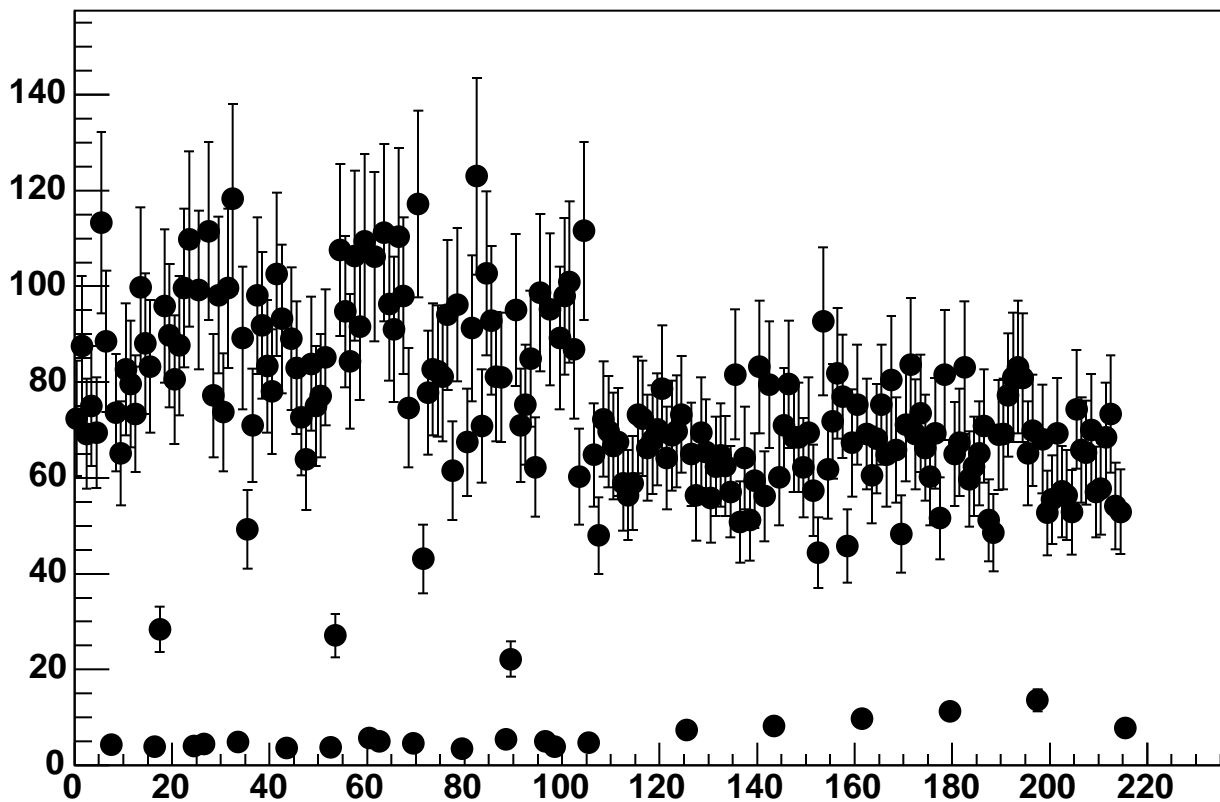
Enable 5, Hold=30, DAC=1250, ADC Noise vs 18\*Chip+Chan



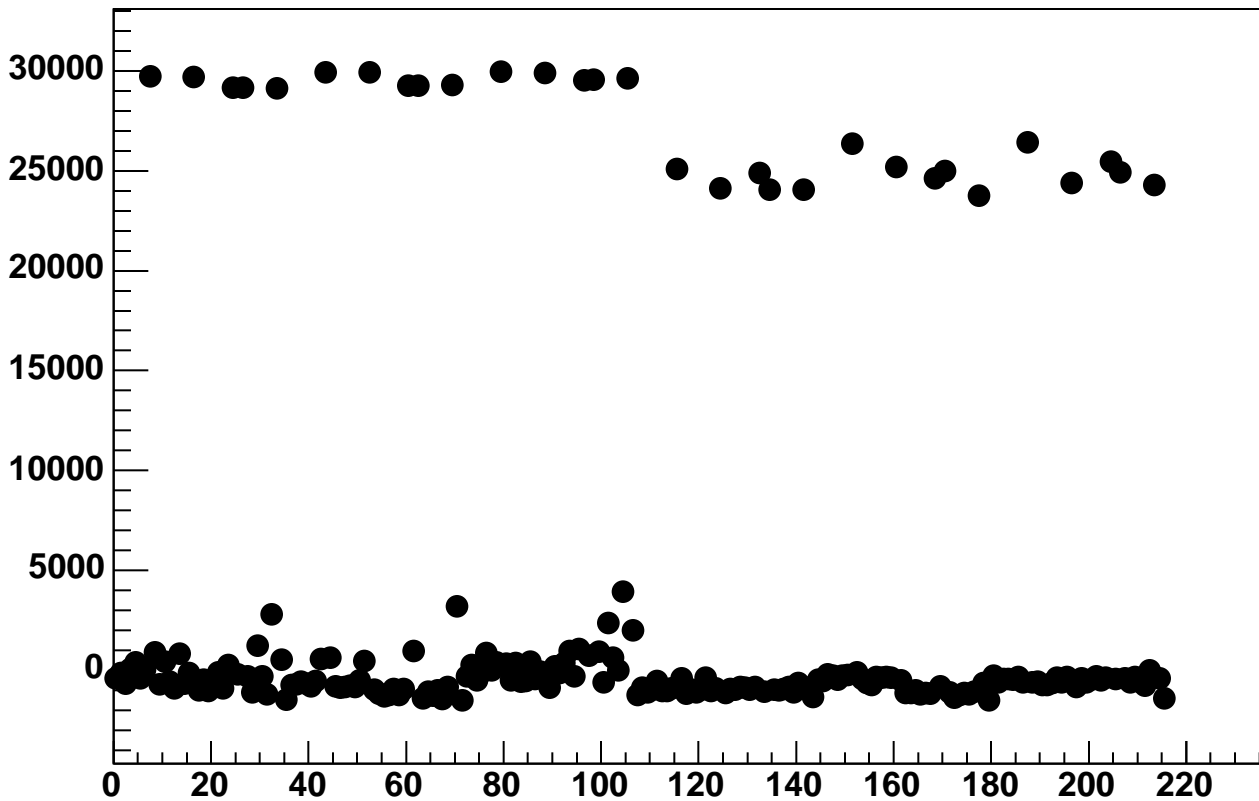
Enable 5, Hold=30, DAC=1300, ADC Mean vs 18\*Chip+Chan



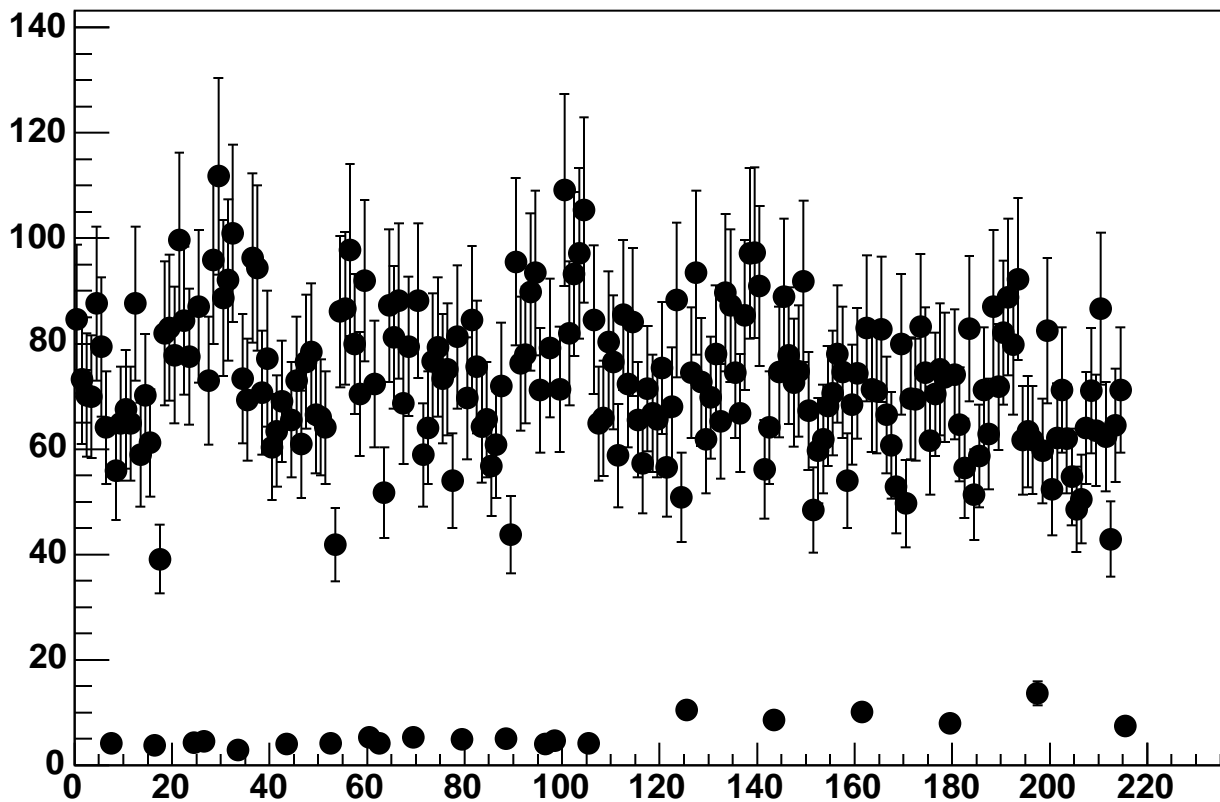
Enable 5, Hold=30, DAC=1300, ADC Noise vs 18\*Chip+Chan



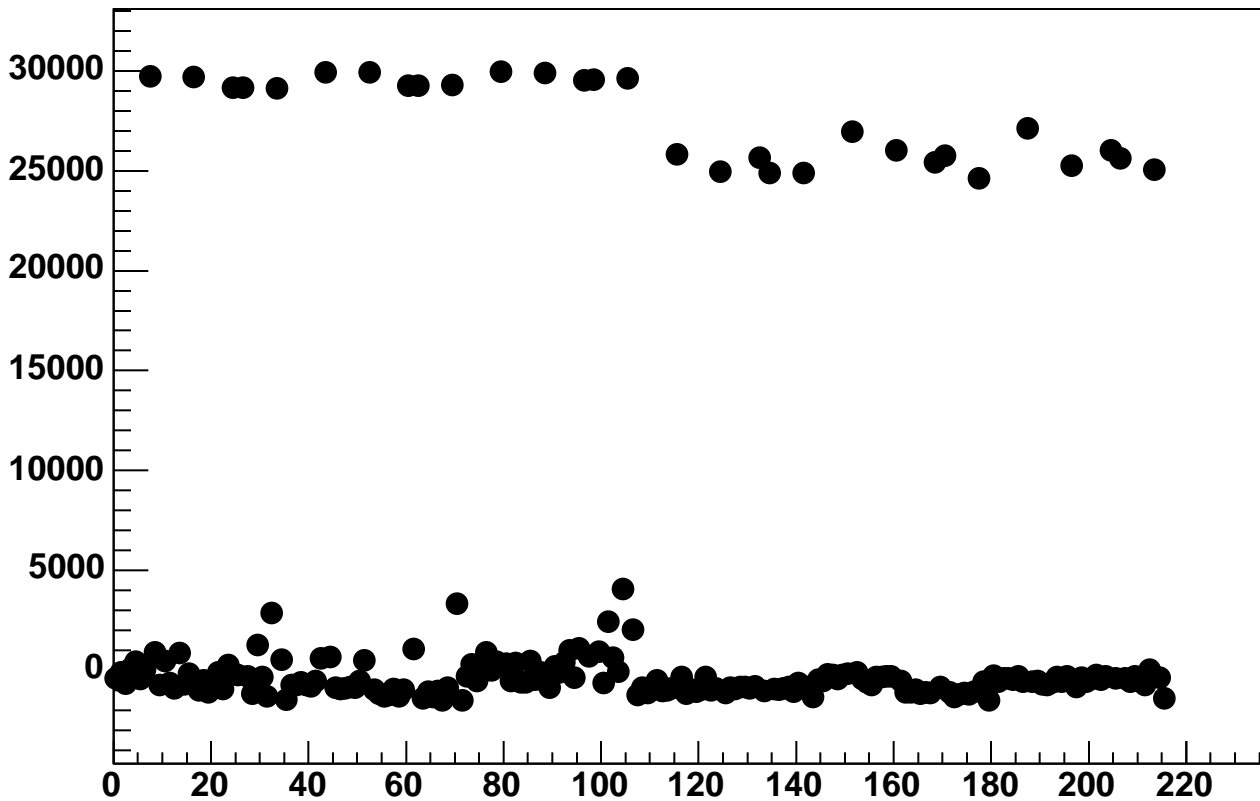
Enable 5, Hold=30, DAC=1350, ADC Mean vs 18\*Chip+Chan



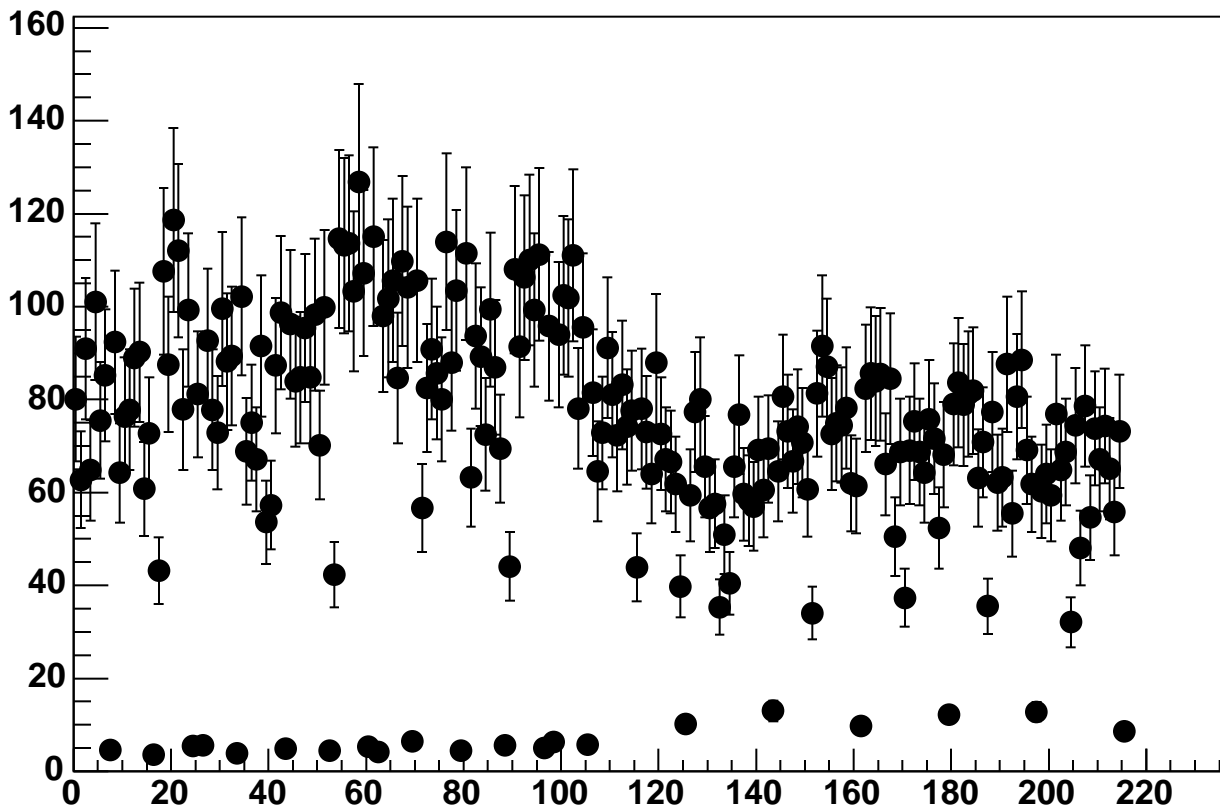
Enable 5, Hold=30, DAC=1350, ADC Noise vs 18\*Chip+Chan



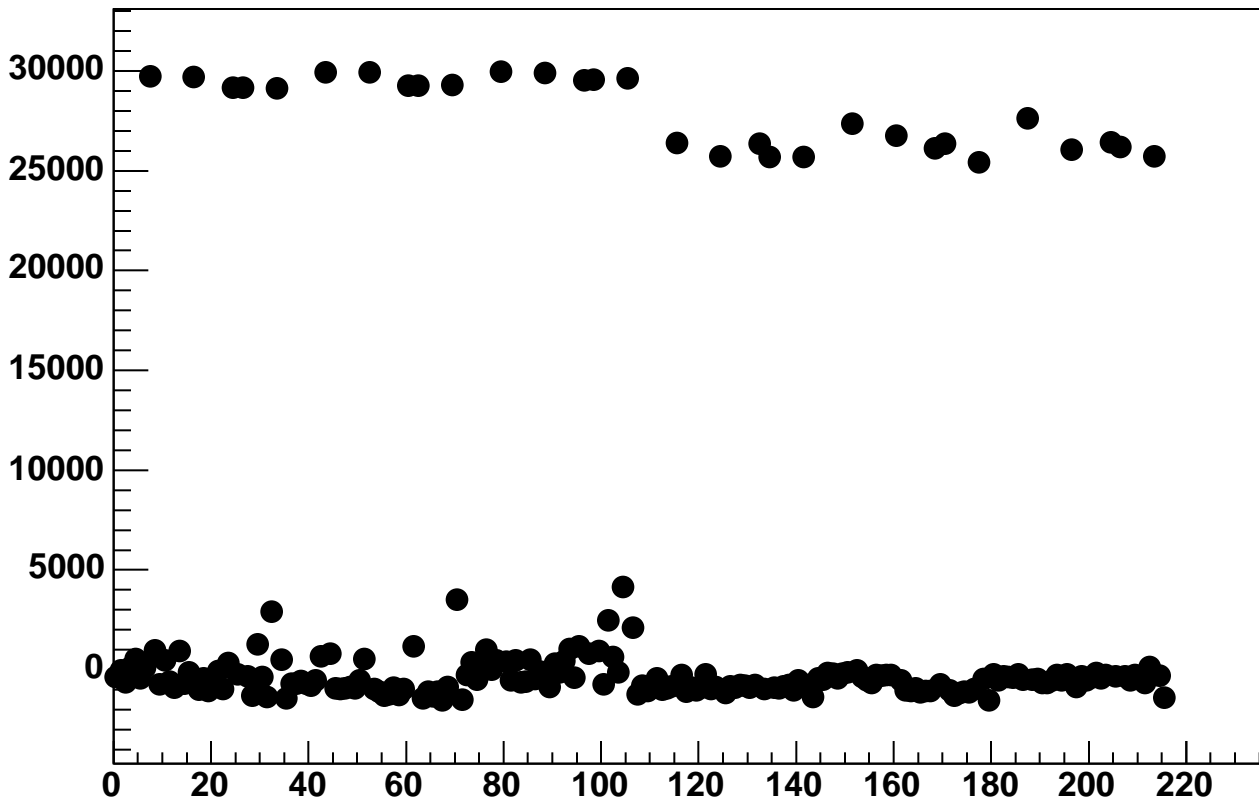
Enable 5, Hold=30, DAC=1400, ADC Mean vs 18\*Chip+Chan



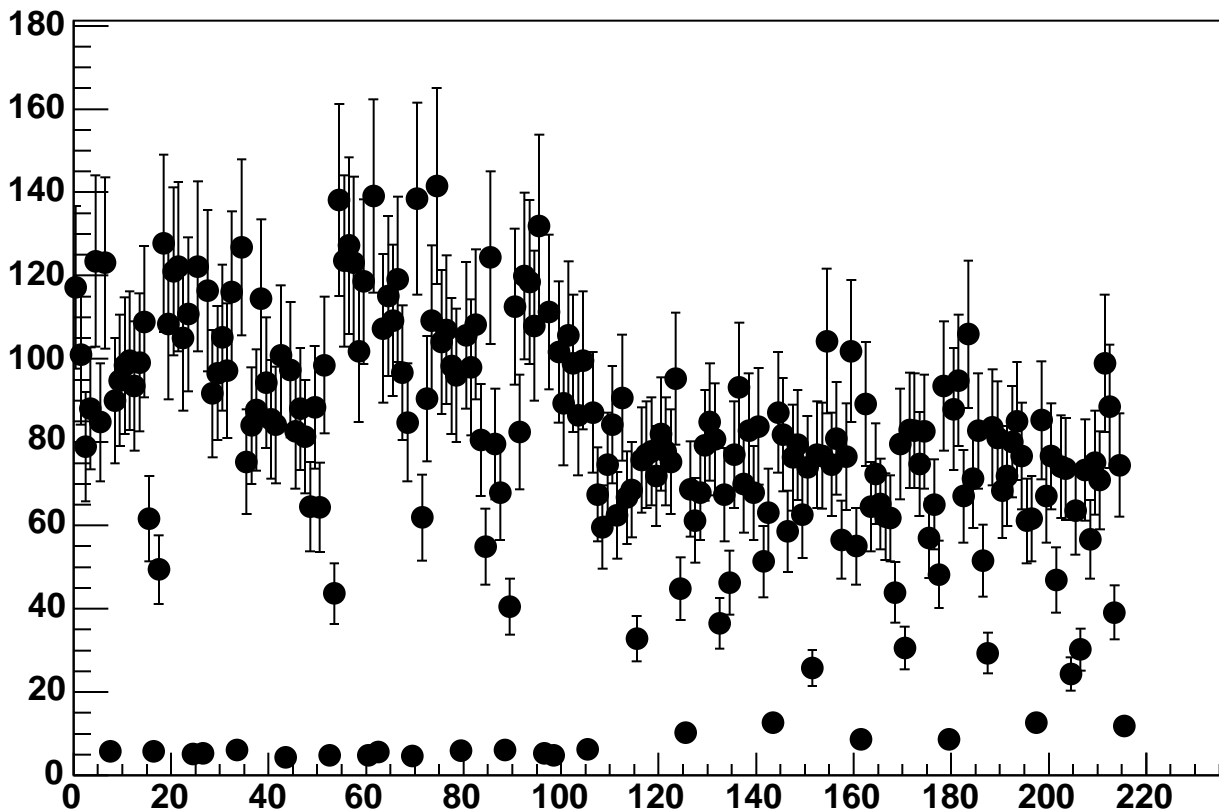
Enable 5, Hold=30, DAC=1400, ADC Noise vs 18\*Chip+Chan



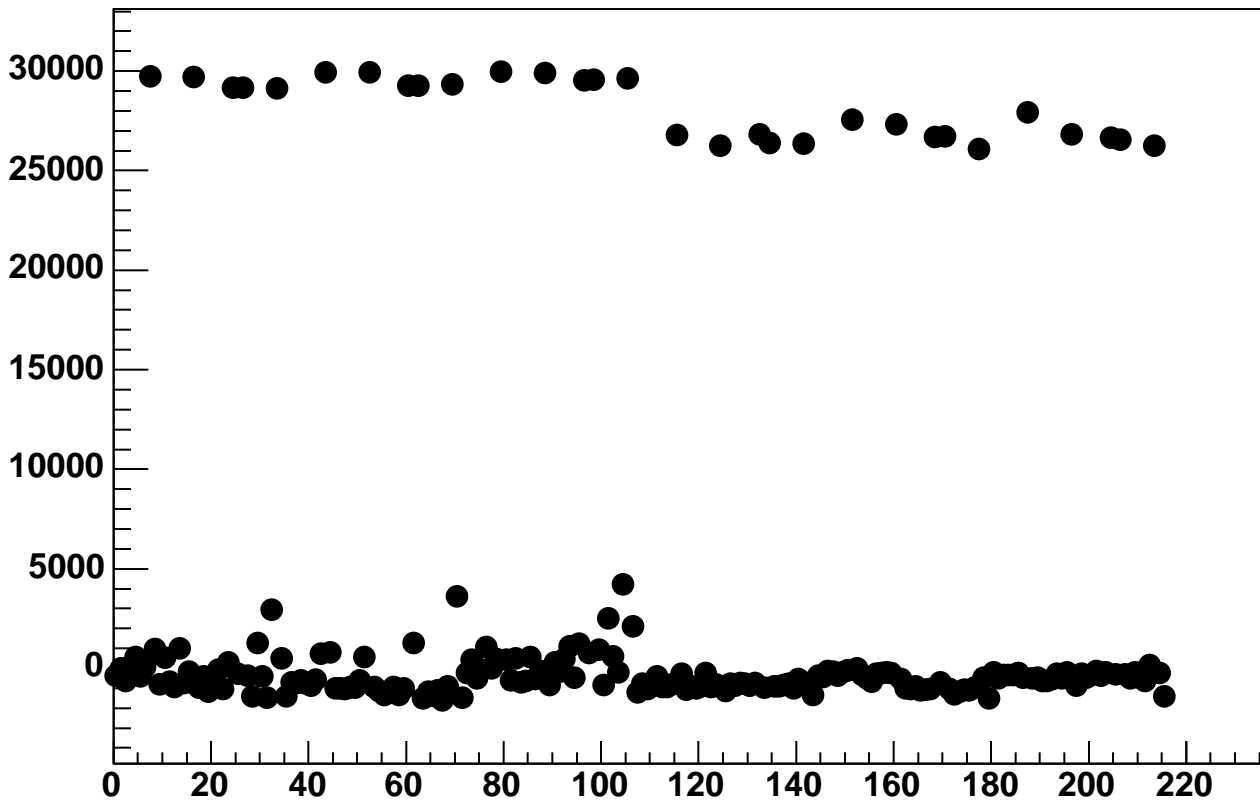
Enable 5, Hold=30, DAC=1450, ADC Mean vs 18\*Chip+Chan



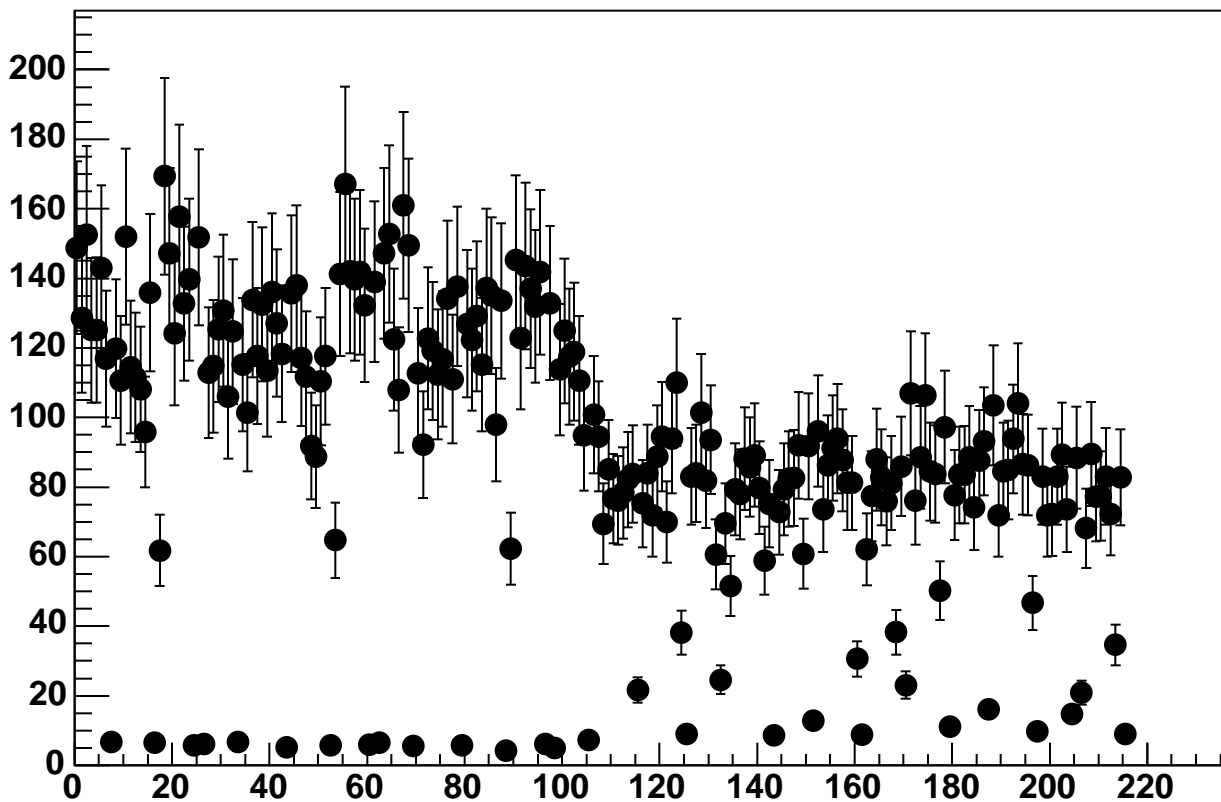
Enable 5, Hold=30, DAC=1450, ADC Noise vs 18\*Chip+Chan



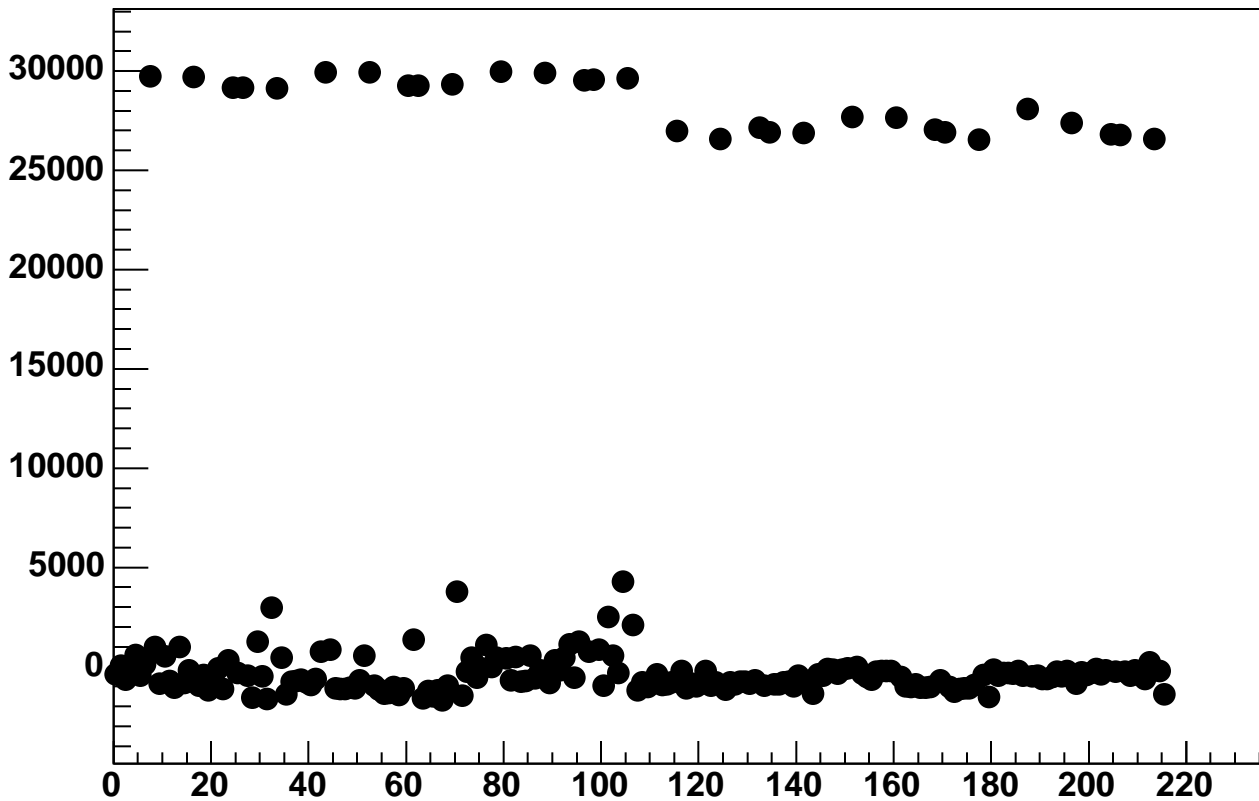
Enable 5, Hold=30, DAC=1500, ADC Mean vs 18\*Chip+Chan



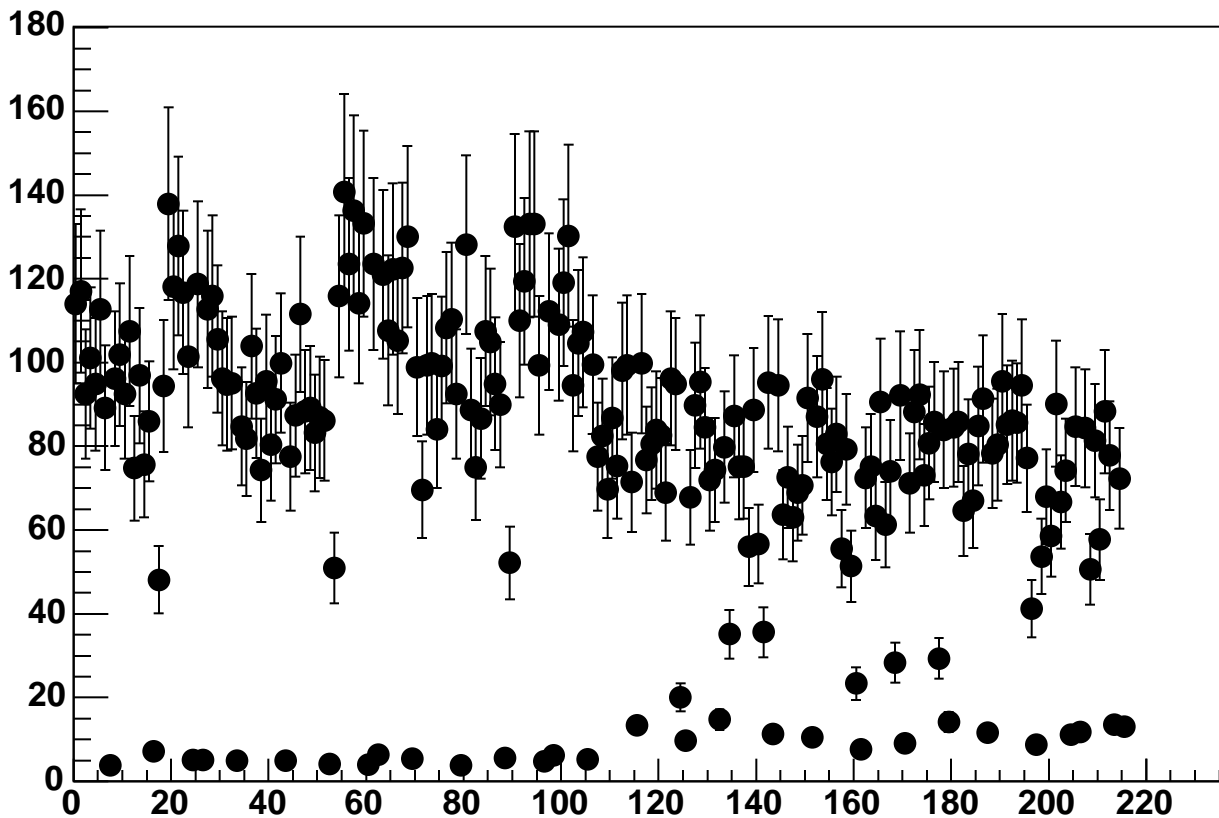
Enable 5, Hold=30, DAC=1500, ADC Noise vs 18\*Chip+Chan



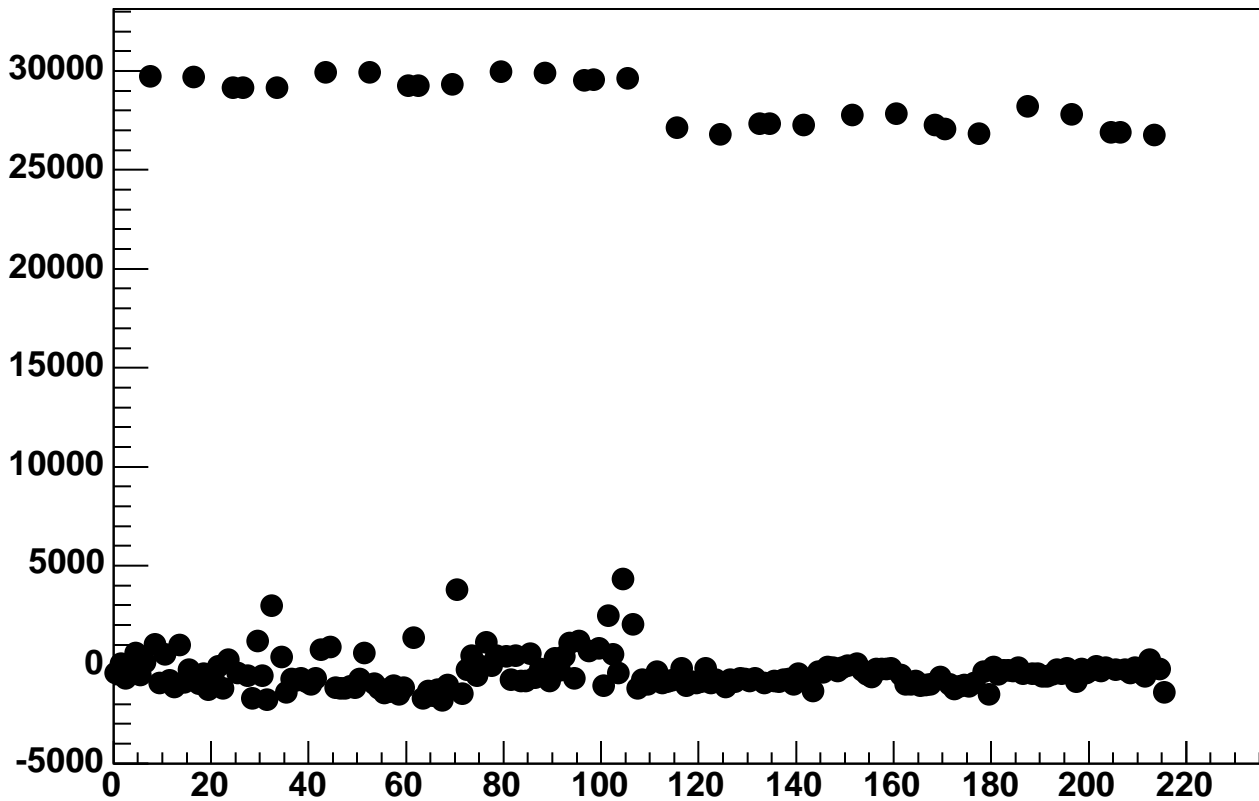
Enable 5, Hold=30, DAC=1550, ADC Mean vs 18\*Chip+Chan



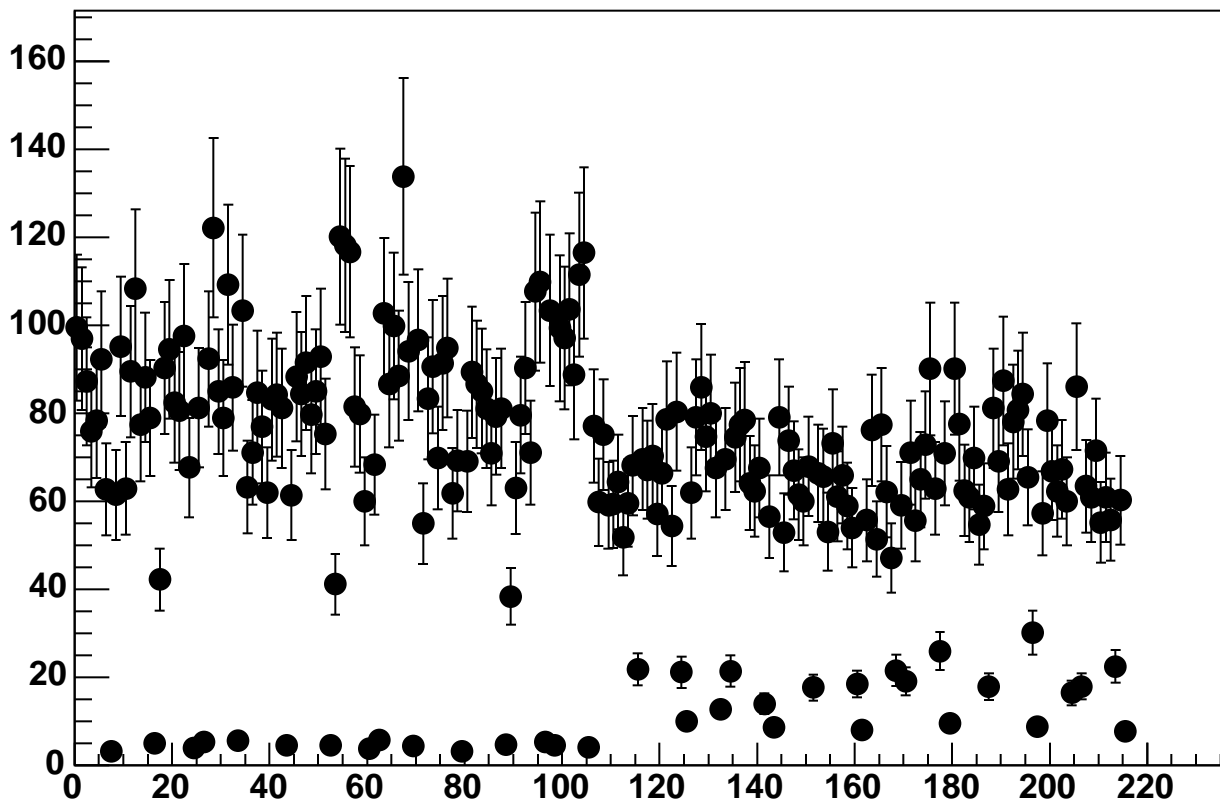
Enable 5, Hold=30, DAC=1550, ADC Noise vs 18\*Chip+Chan



Enable 5, Hold=30, DAC=1600, ADC Mean vs 18\*Chip+Chan

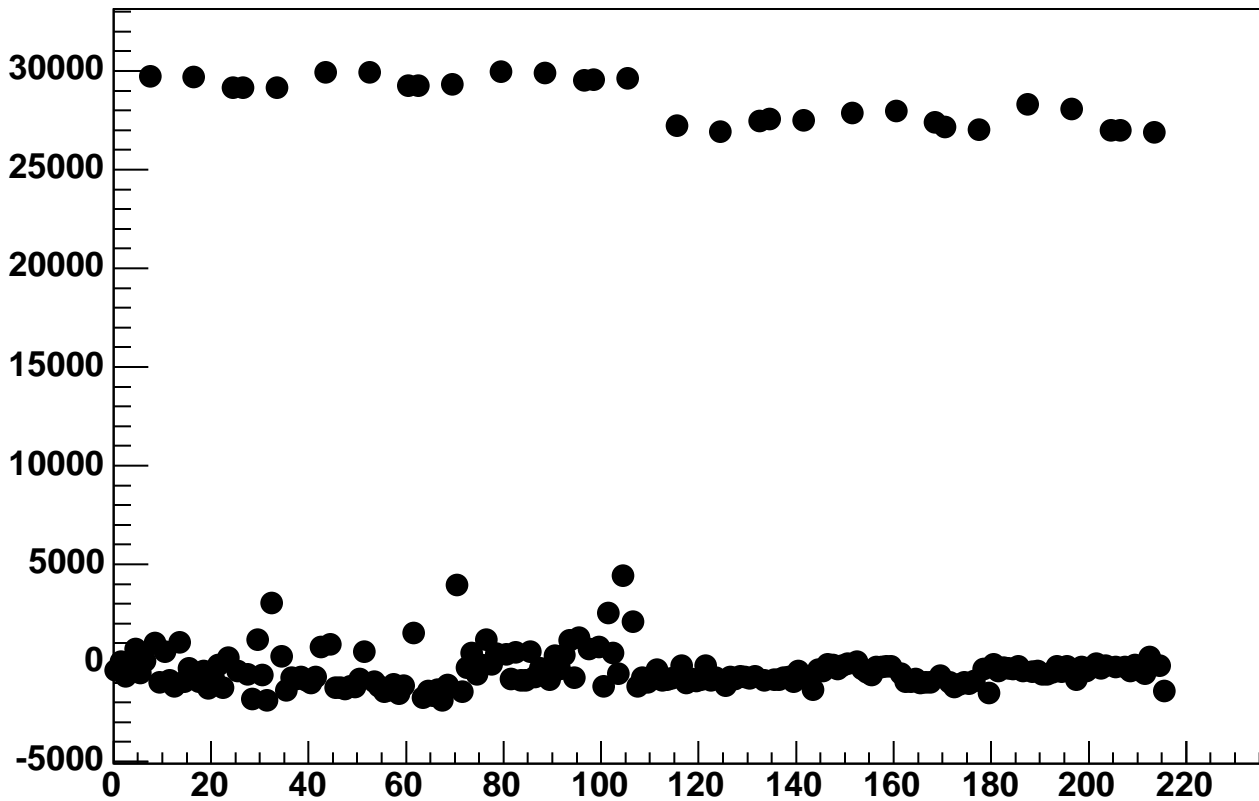


Enable 5, Hold=30, DAC=1600, ADC Noise vs 18\*Chip+Chan

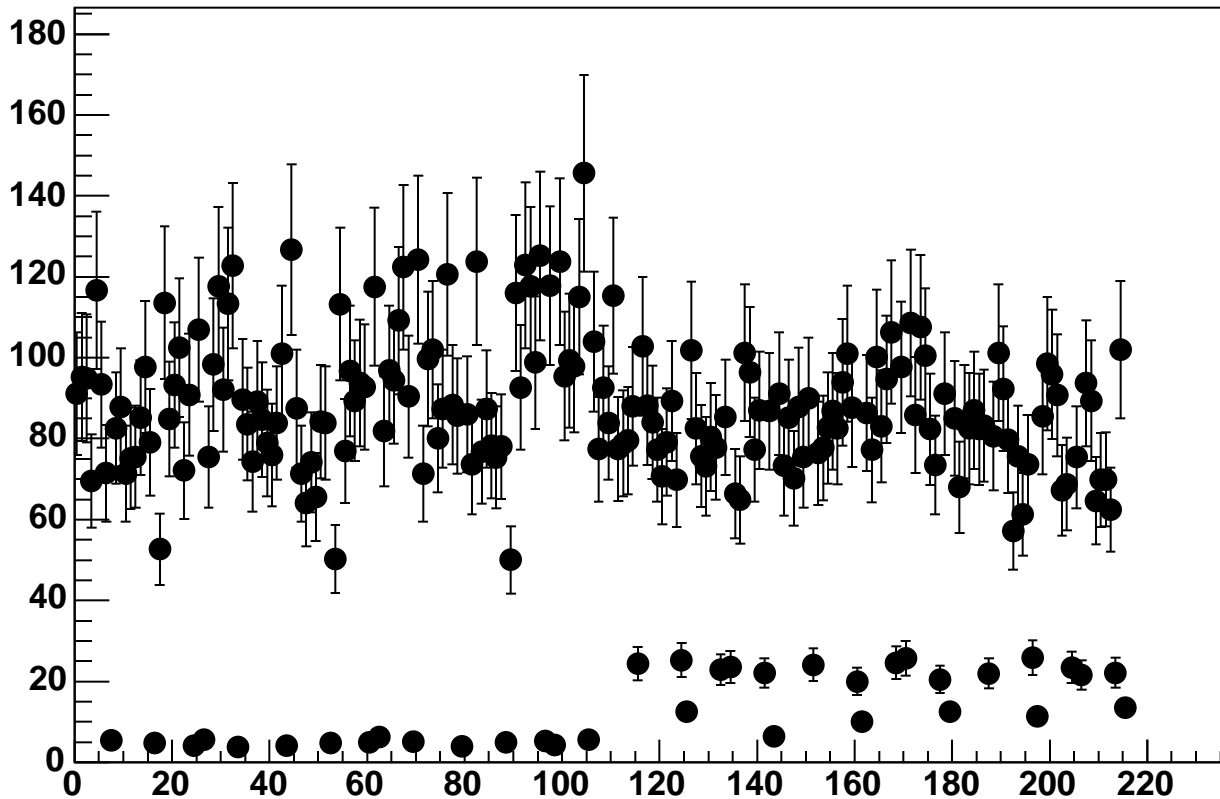




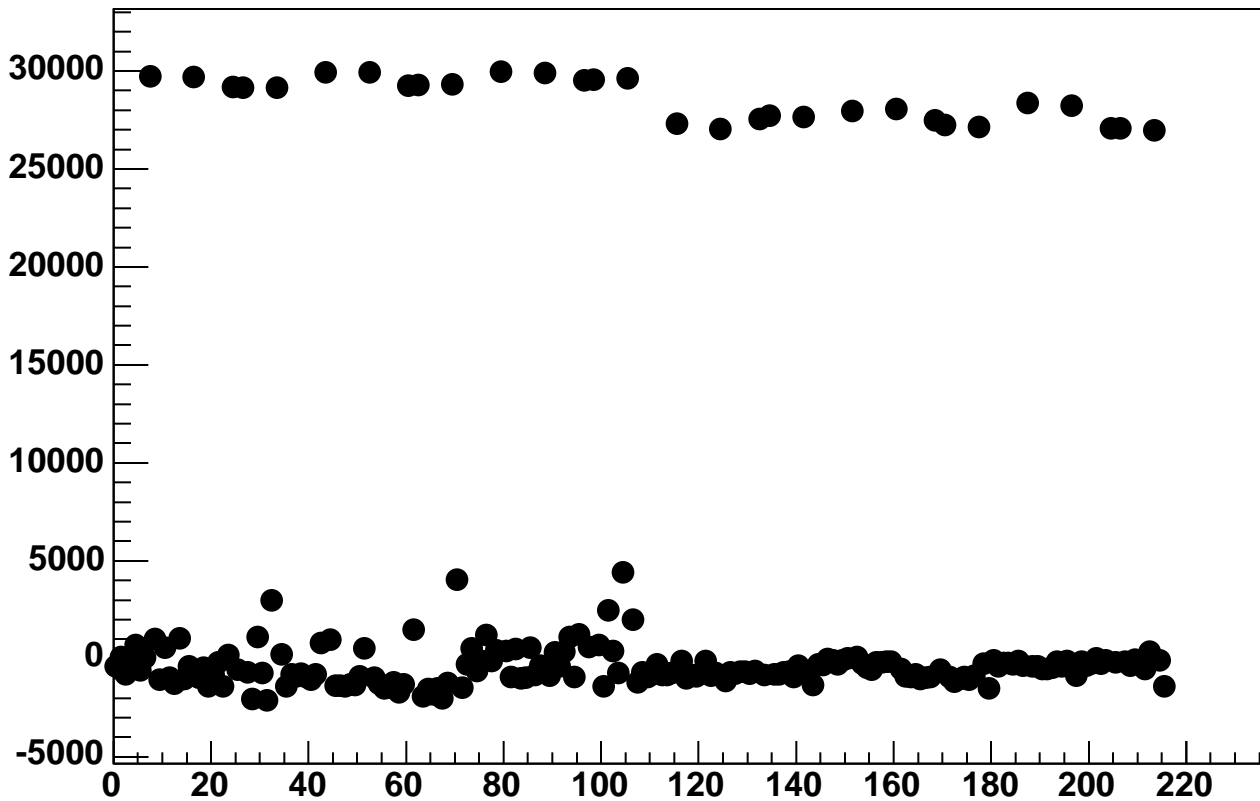
Enable 5, Hold=30, DAC=1650, ADC Mean vs 18\*Chip+Chan



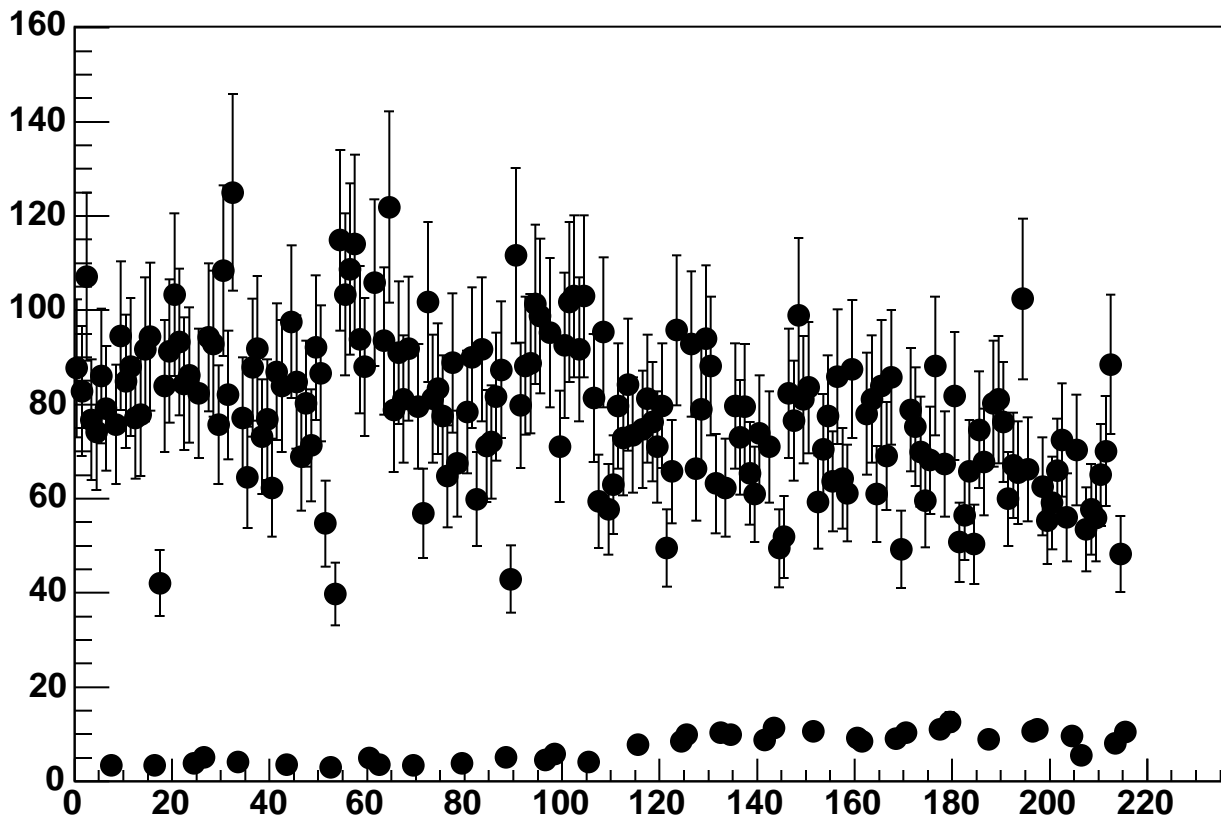
Enable 5, Hold=30, DAC=1650, ADC Noise vs 18\*Chip+Chan



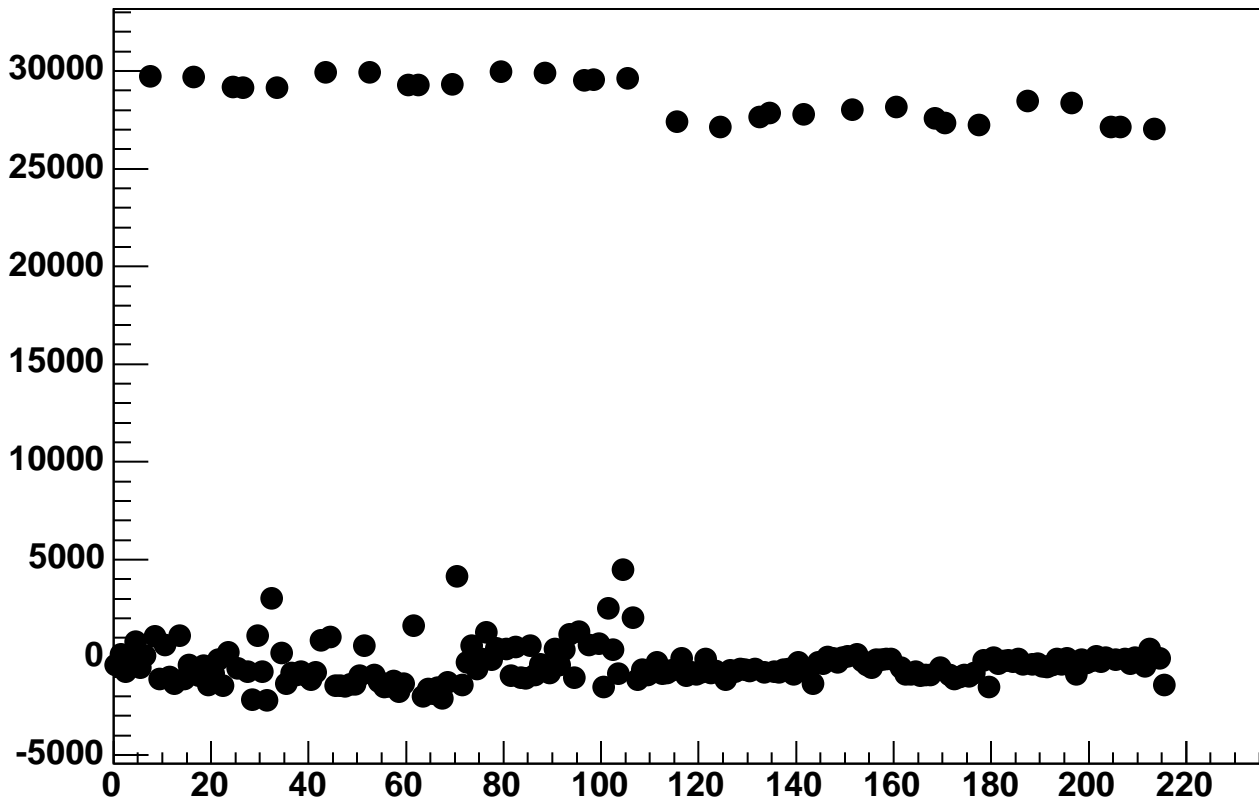
Enable 5, Hold=30, DAC=1700, ADC Mean vs 18\*Chip+Chan



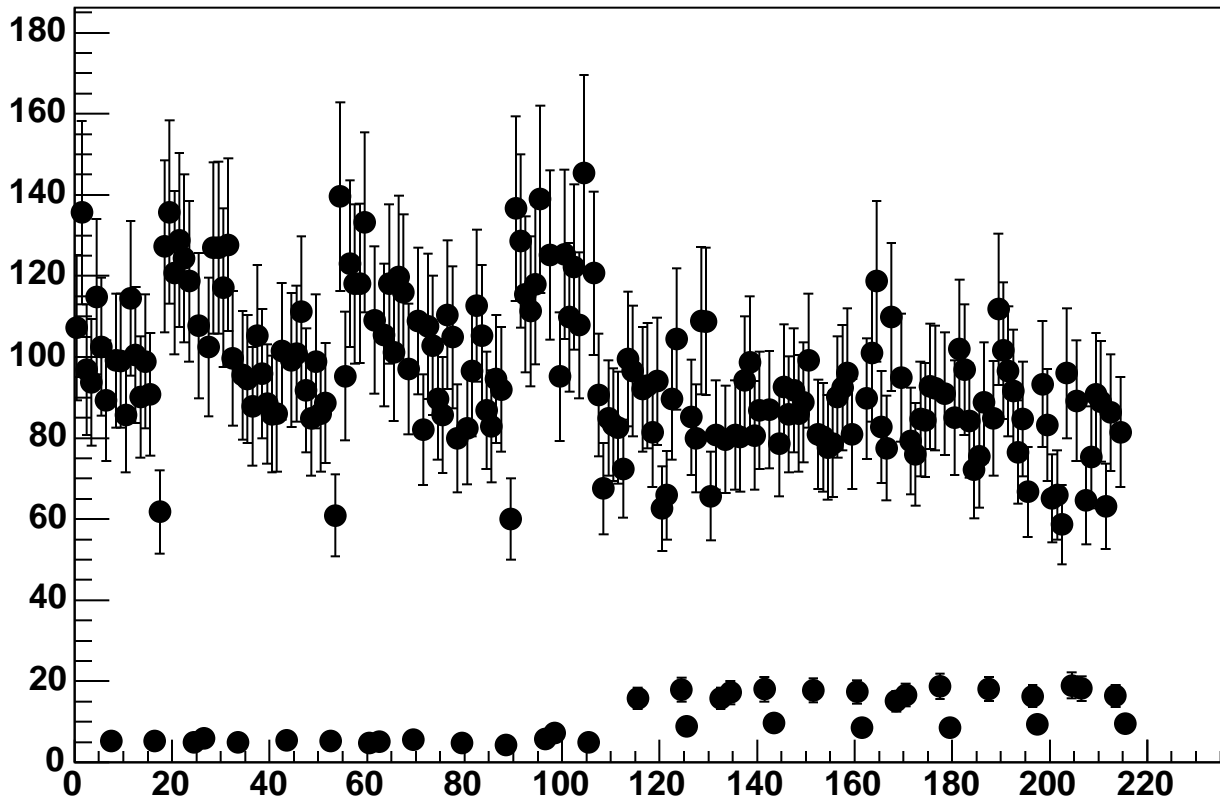
Enable 5, Hold=30, DAC=1700, ADC Noise vs 18\*Chip+Chan



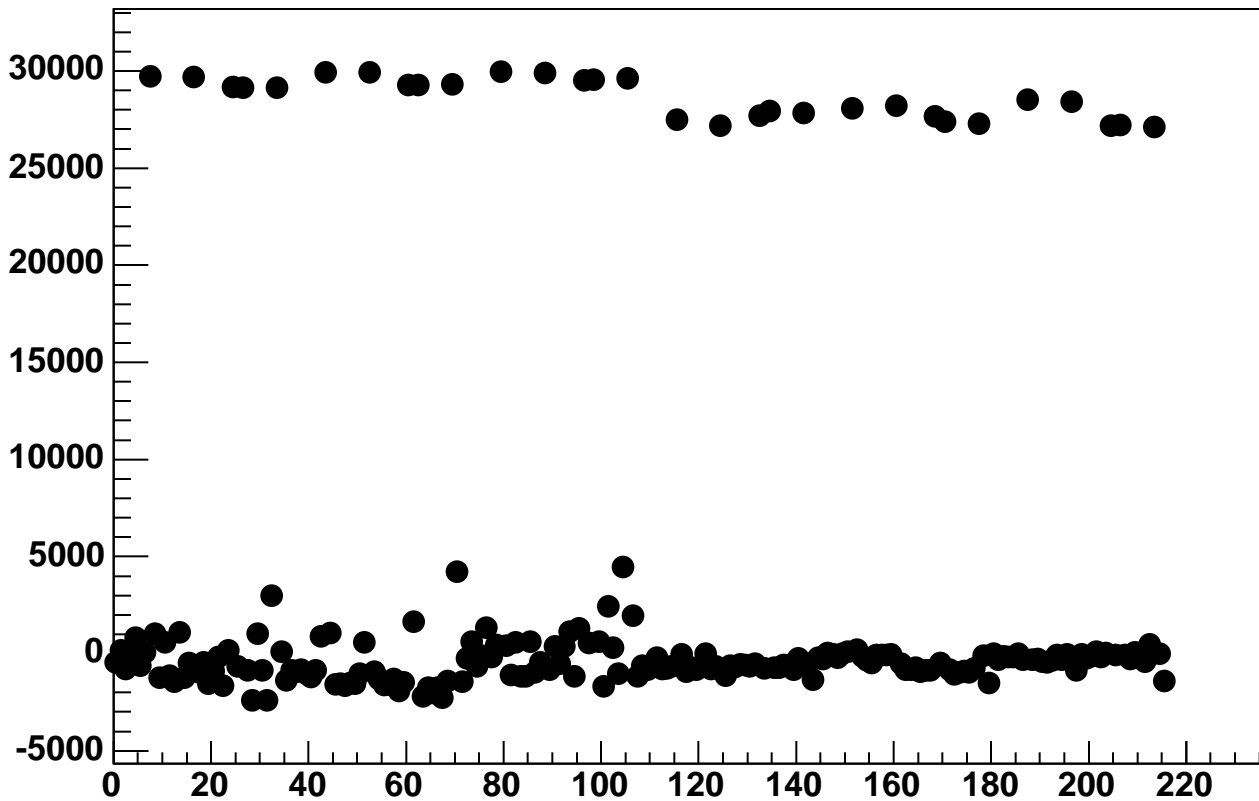
Enable 5, Hold=30, DAC=1750, ADC Mean vs 18\*Chip+Chan



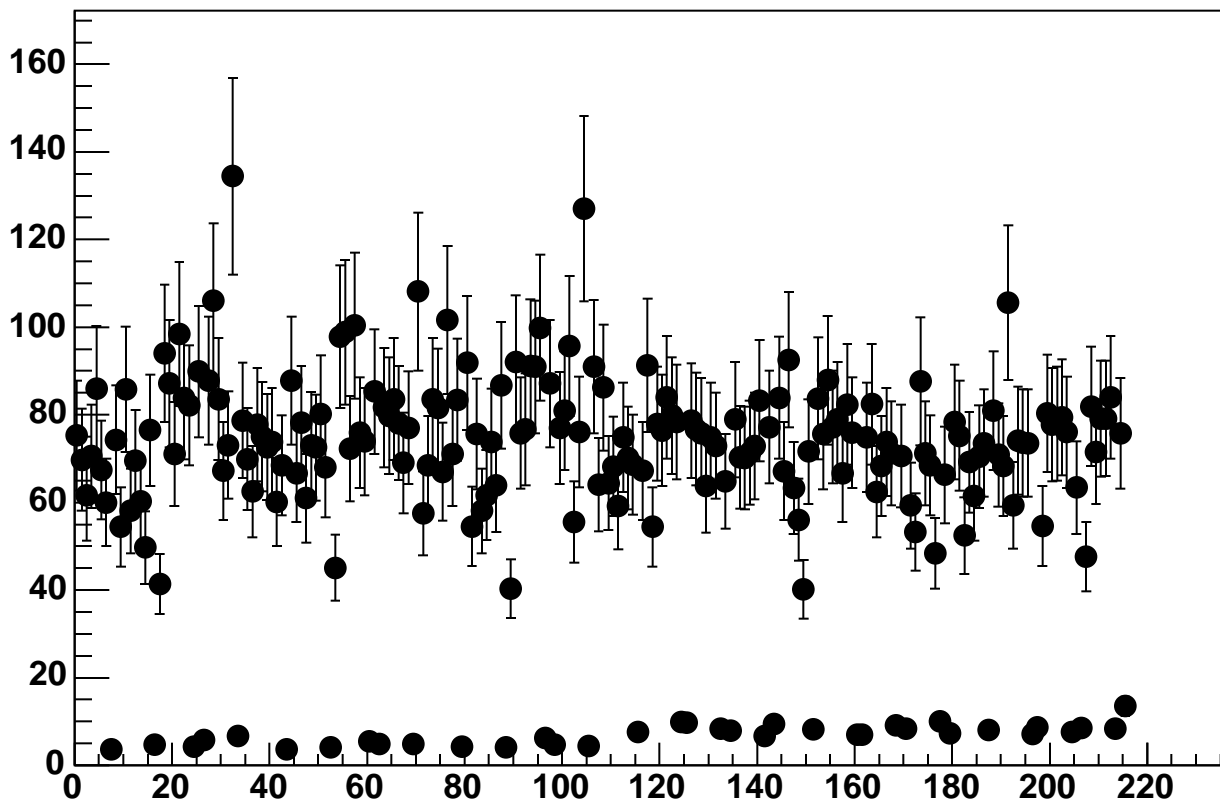
Enable 5, Hold=30, DAC=1750, ADC Noise vs 18\*Chip+Chan



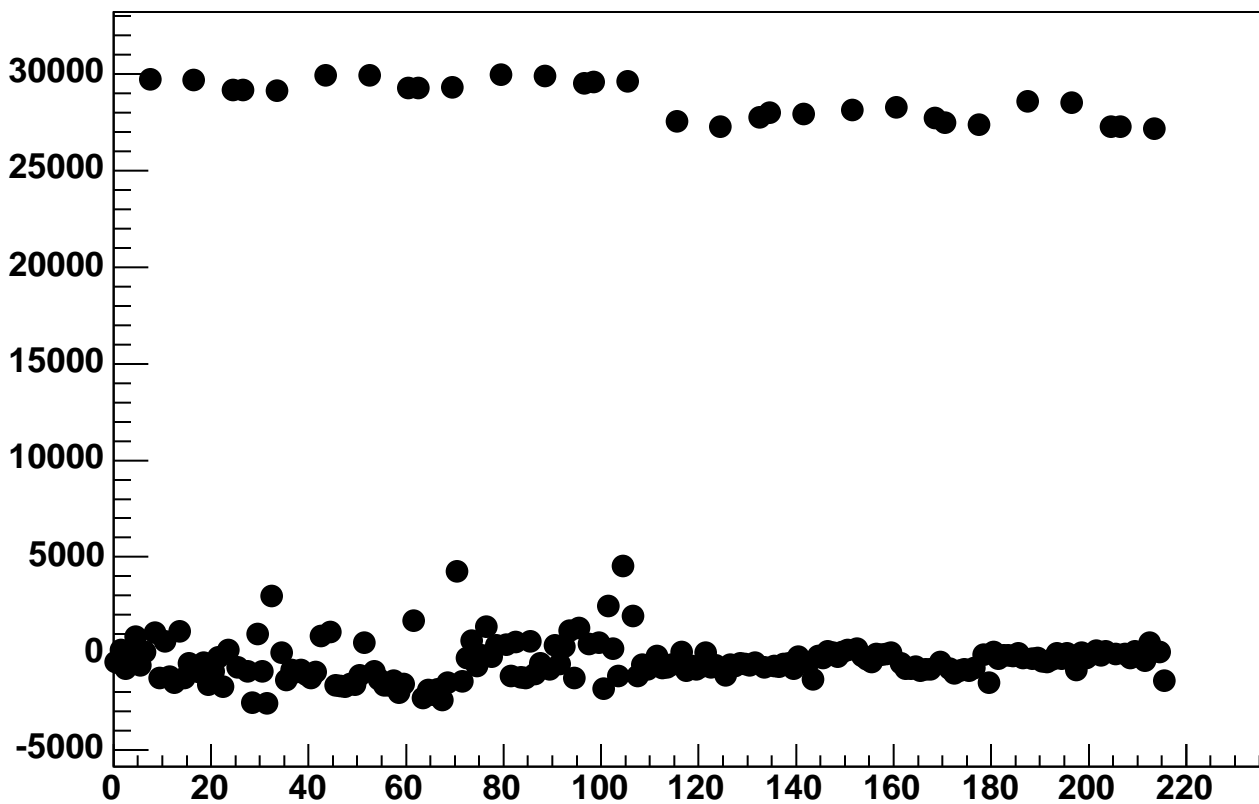
Enable 5, Hold=30, DAC=1800, ADC Mean vs 18\*Chip+Chan



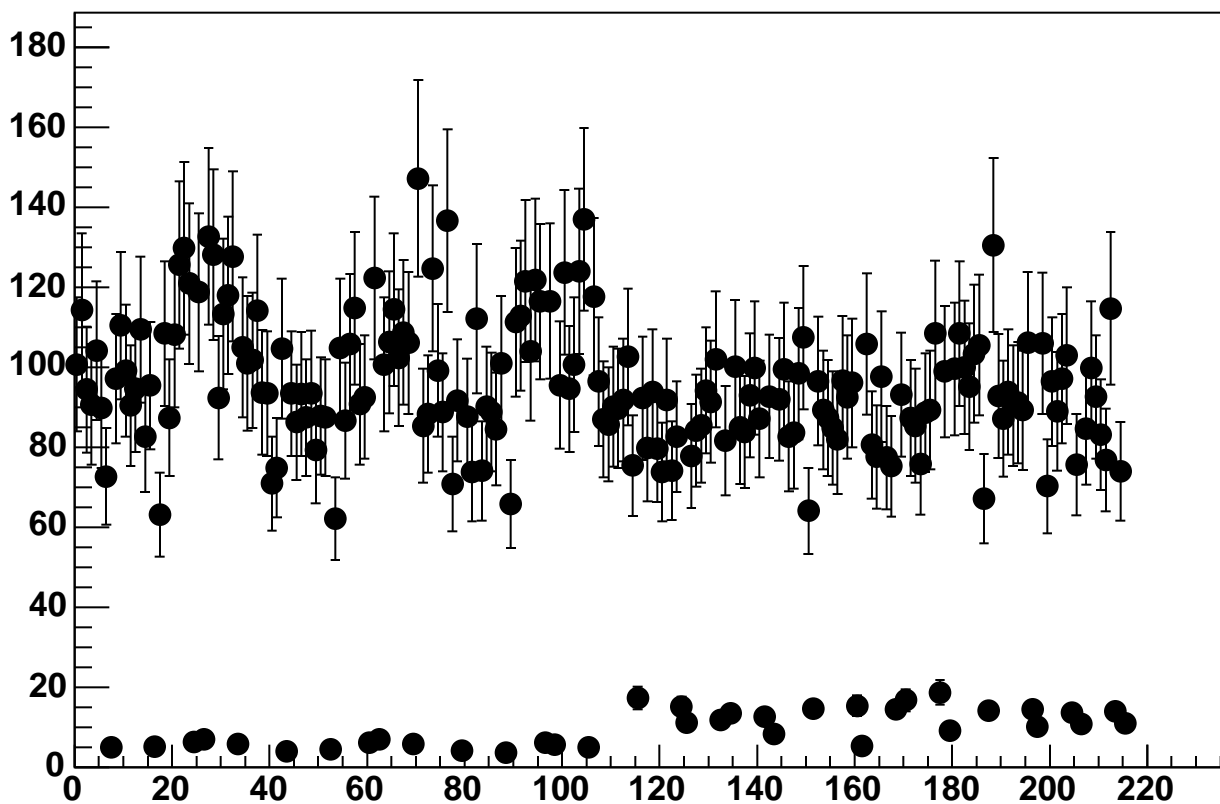
Enable 5, Hold=30, DAC=1800, ADC Noise vs 18\*Chip+Chan



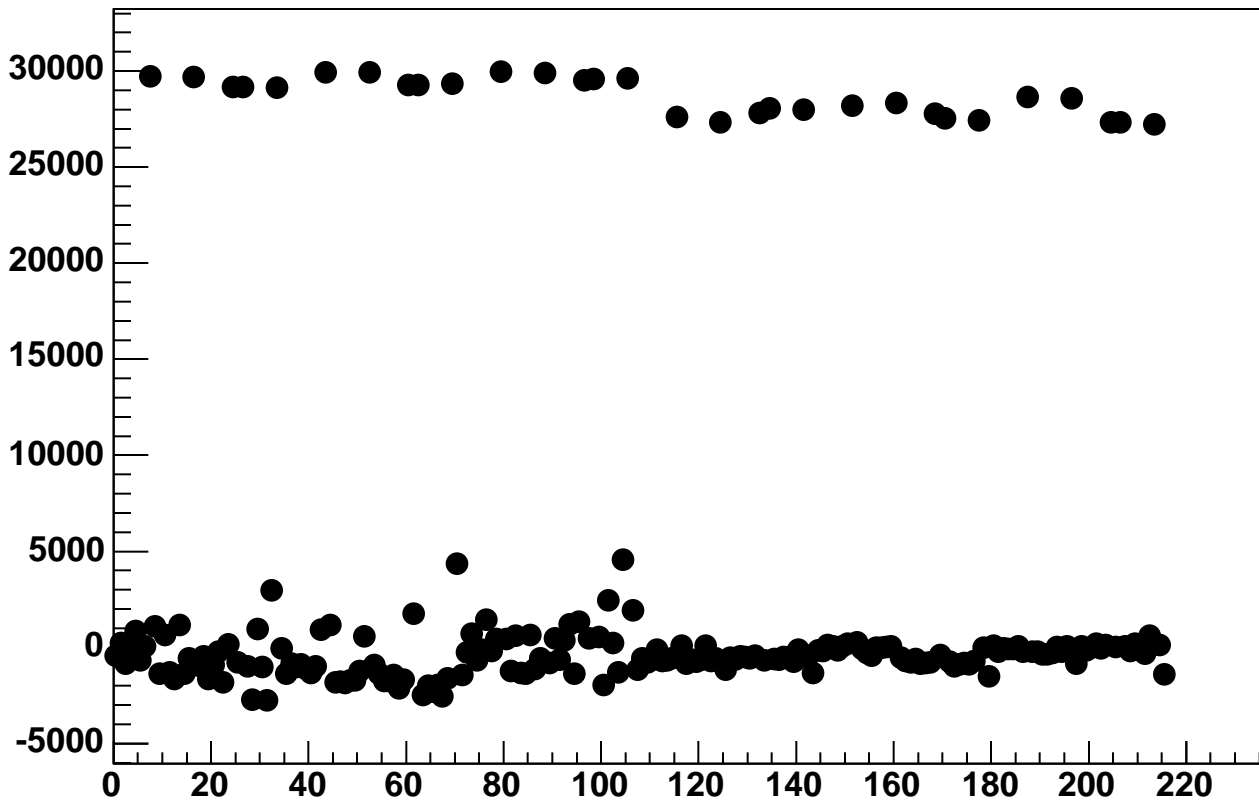
Enable 5, Hold=30, DAC=1850, ADC Mean vs 18\*Chip+Chan



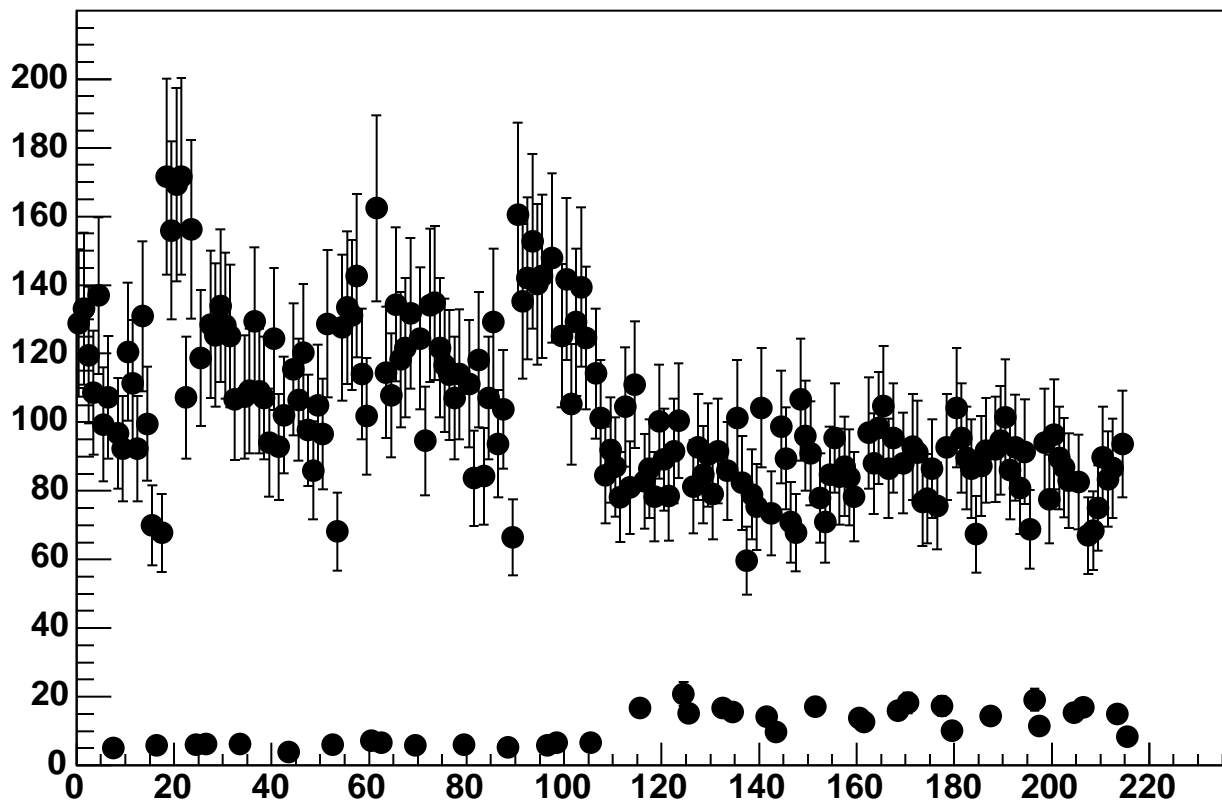
Enable 5, Hold=30, DAC=1850, ADC Noise vs 18\*Chip+Chan



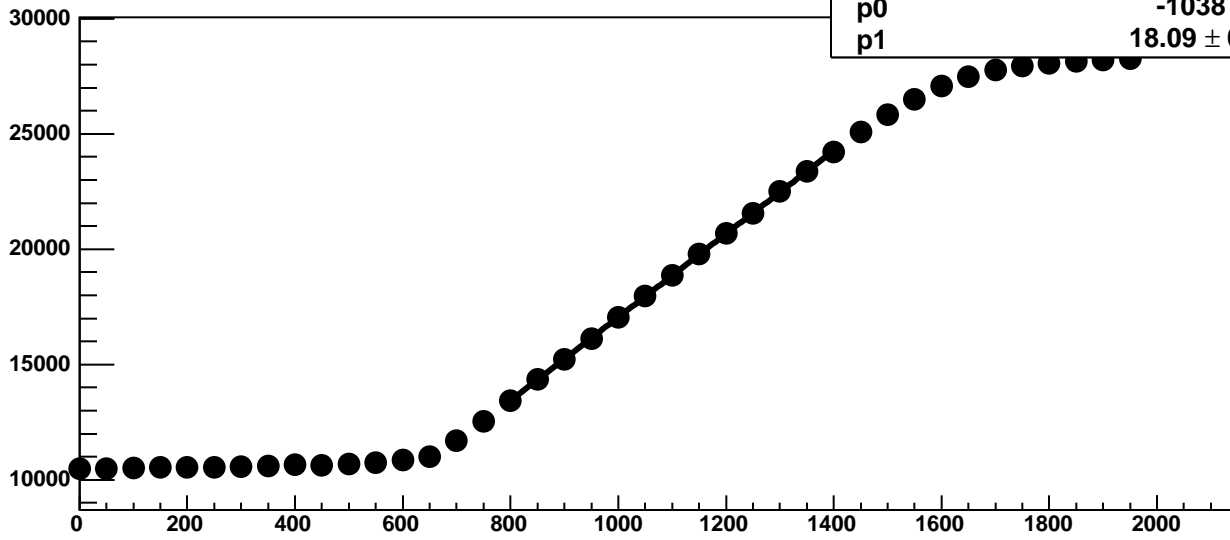
Enable 5, Hold=30, DAC=1900, ADC Mean vs 18\*Chip+Chan



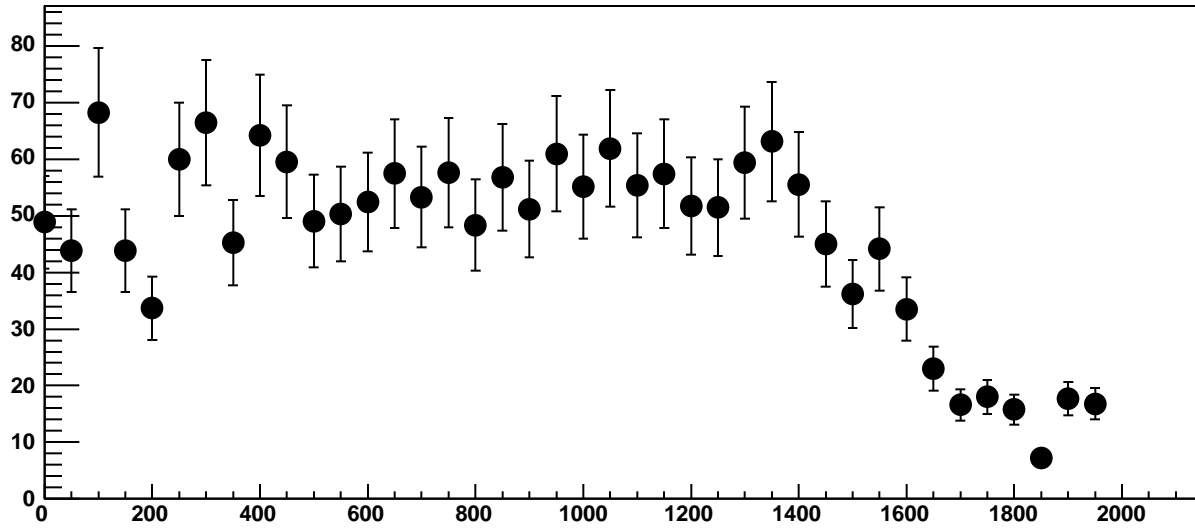
Enable 5, Hold=30, DAC=1900, ADC Noise vs 18\*Chip+Chan



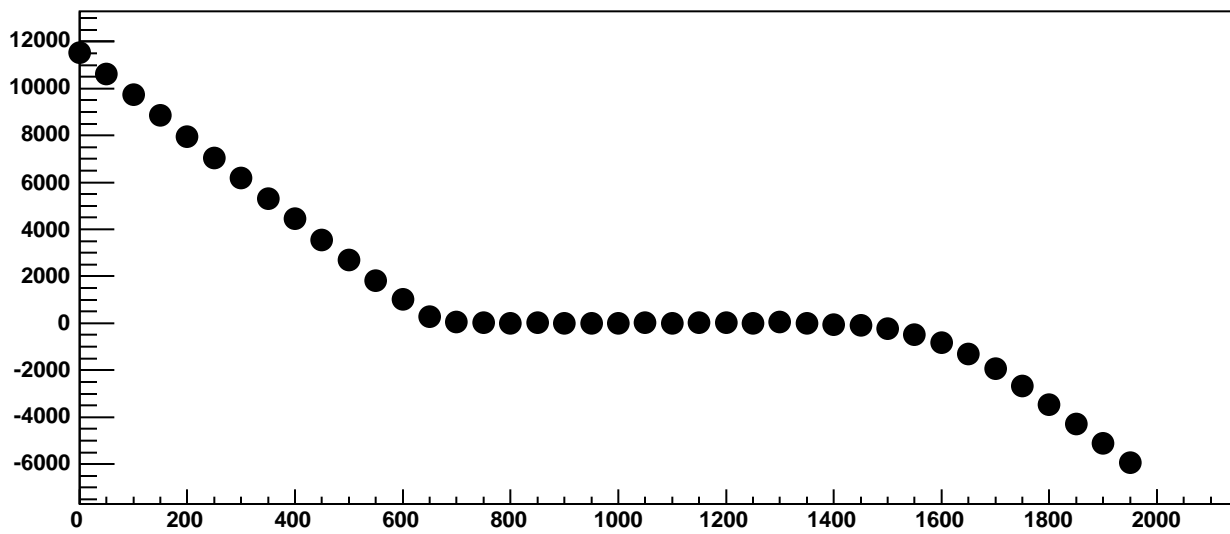
Chip 0, Channel 0, Enable 0!, Hold=30, ADC Mean vs DAC



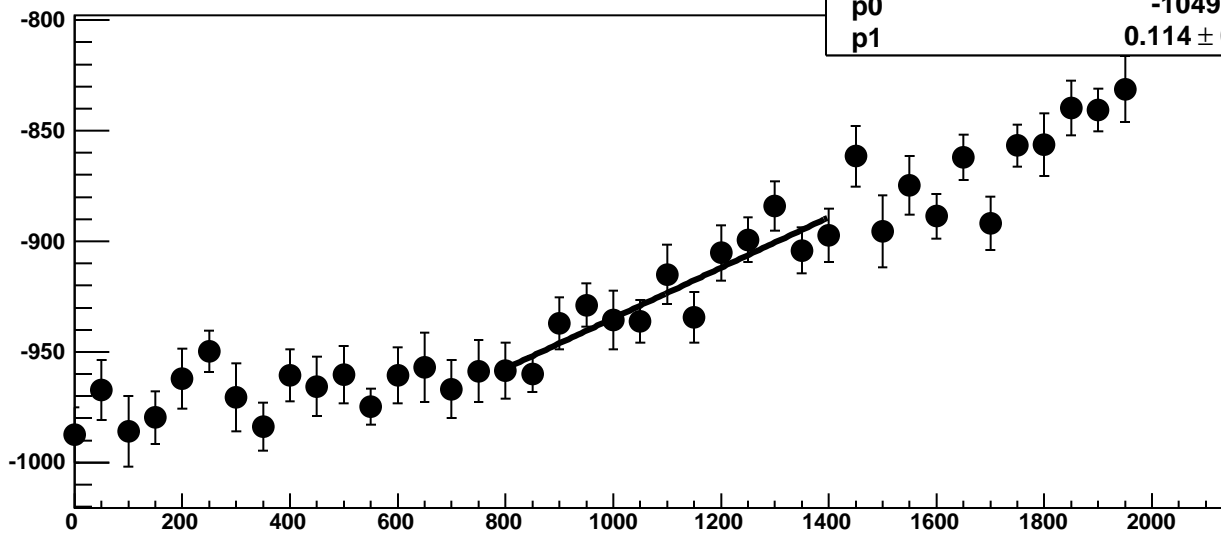
Chip 0, Channel 0, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 0, Channel 0, Enable 0!, Hold=30, ADC Residuals vs DAC



Chip 0, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

10.34 / 11

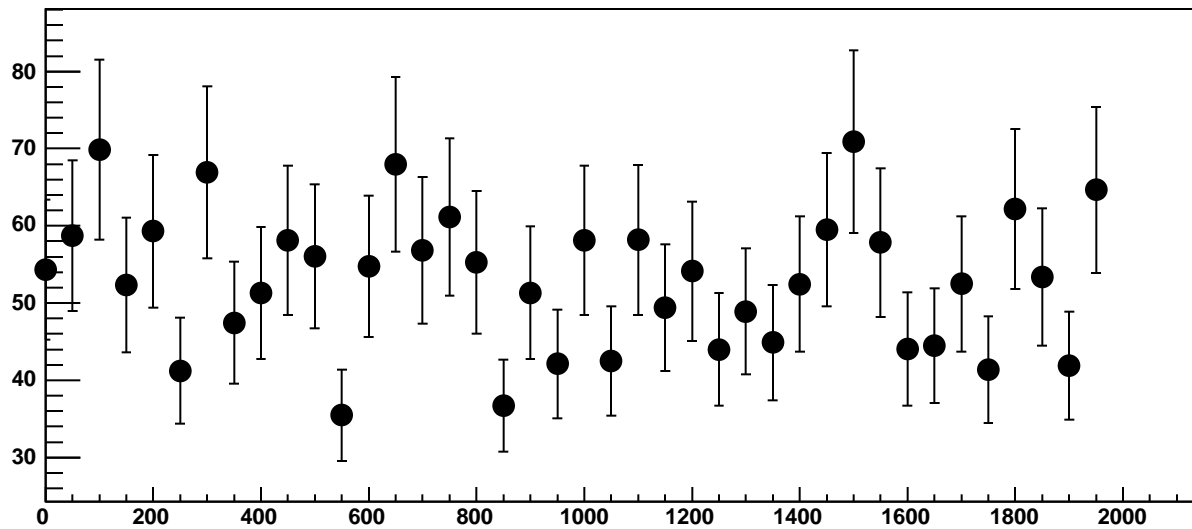
p0

$-1049 \pm 17.66$

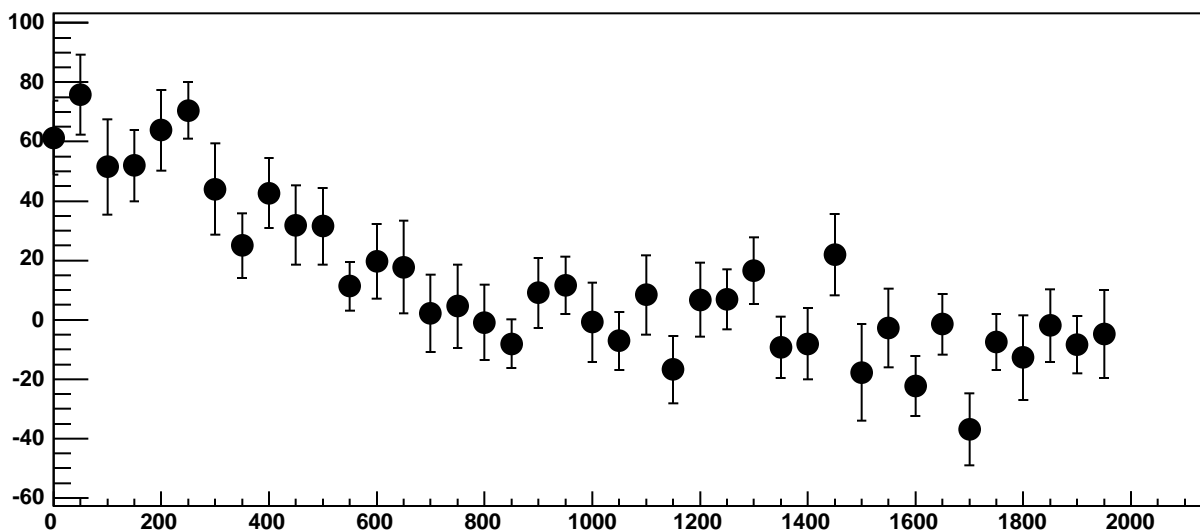
p1

$0.114 \pm 0.01597$

Chip 0, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC

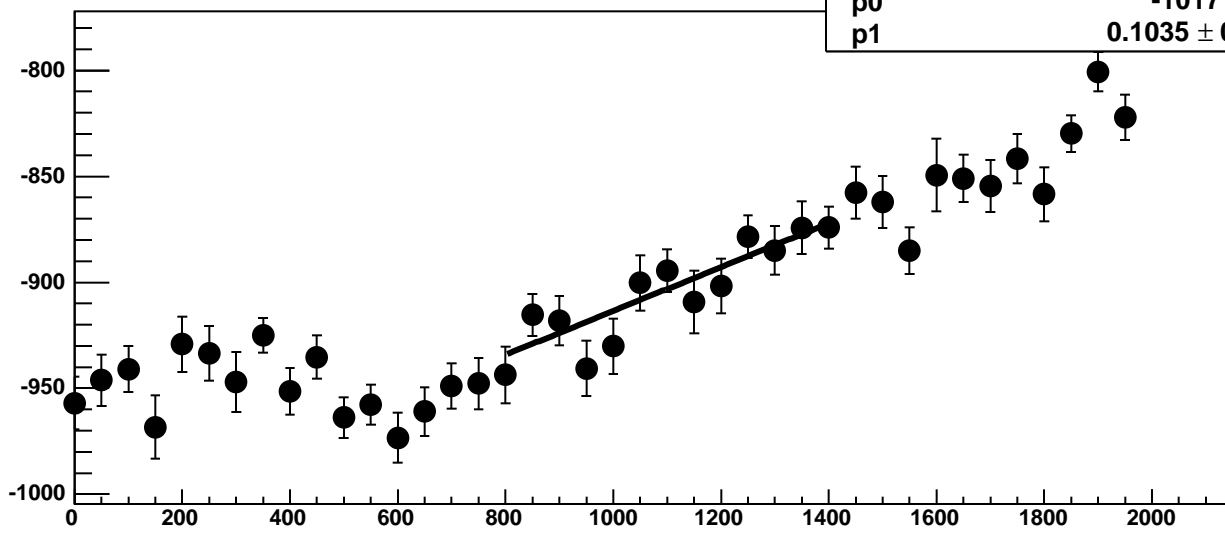


Chip 0, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC



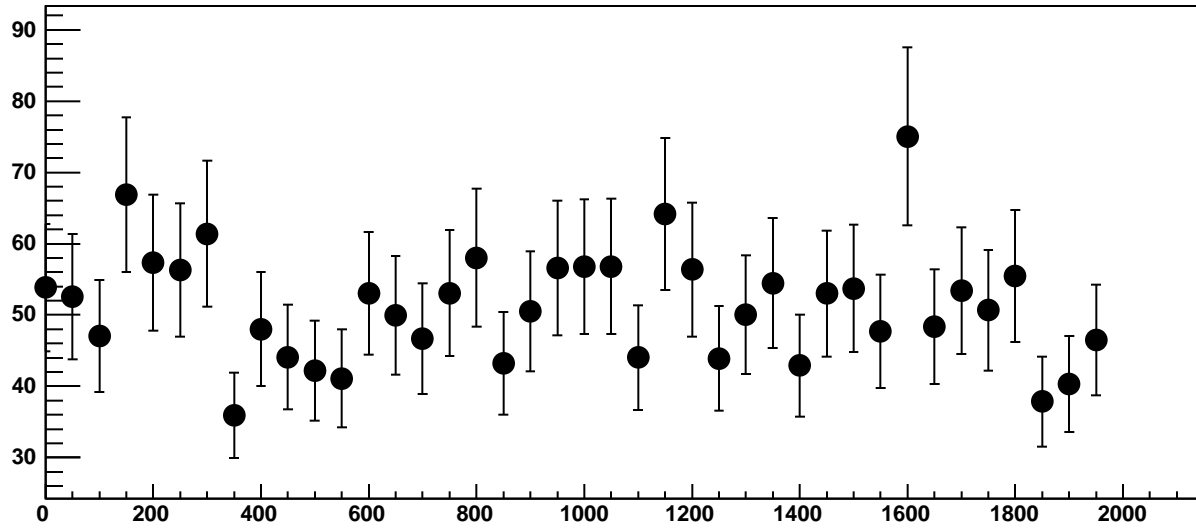


Chip 0, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC

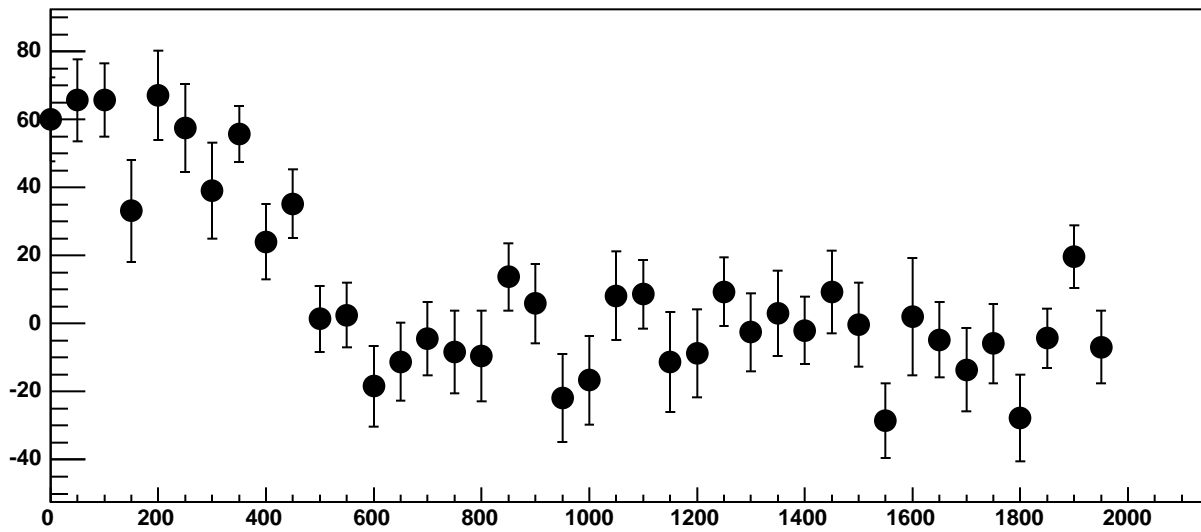


$\chi^2 / \text{ndf}$  10.32 / 11  
p0  $-1017 \pm 18.89$   
p1  $0.1035 \pm 0.01676$

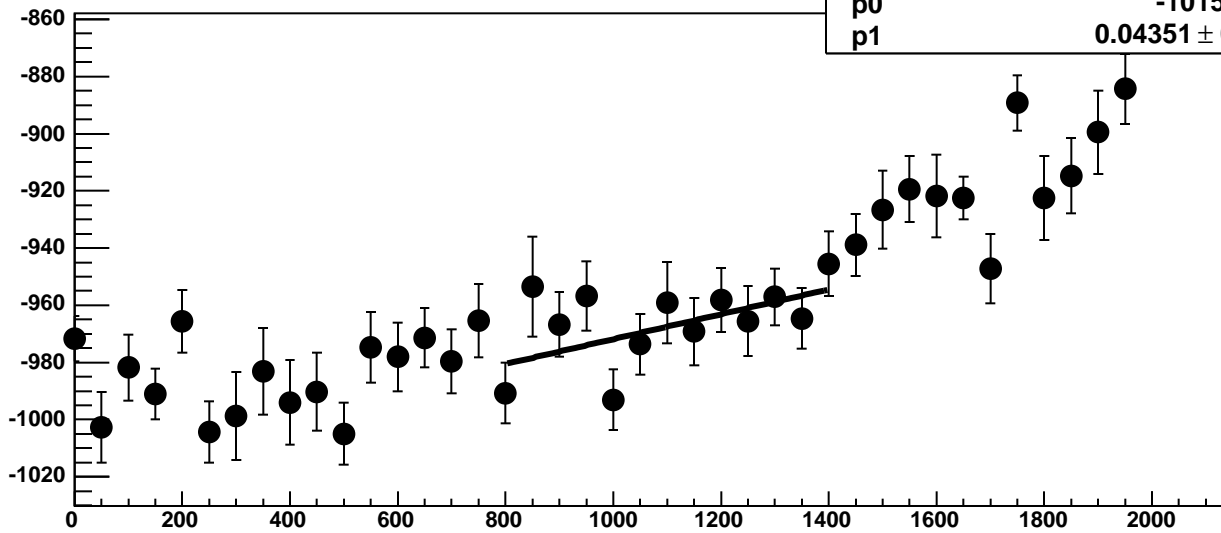
Chip 0, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC



Chip 0, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC

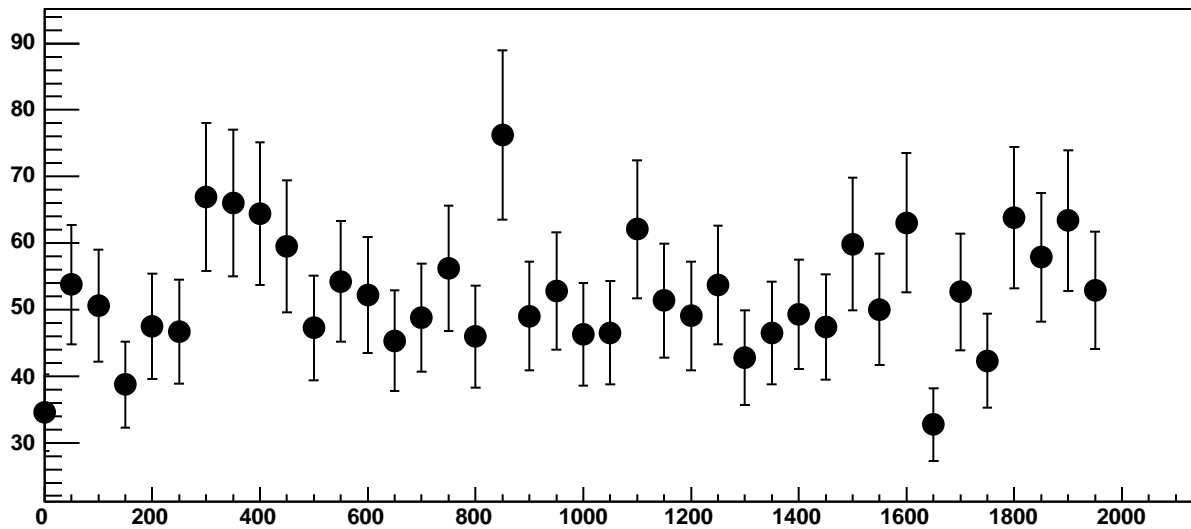


Chip 0, Channel 0, Enable 3, Hold=30, ADC Mean vs DAC

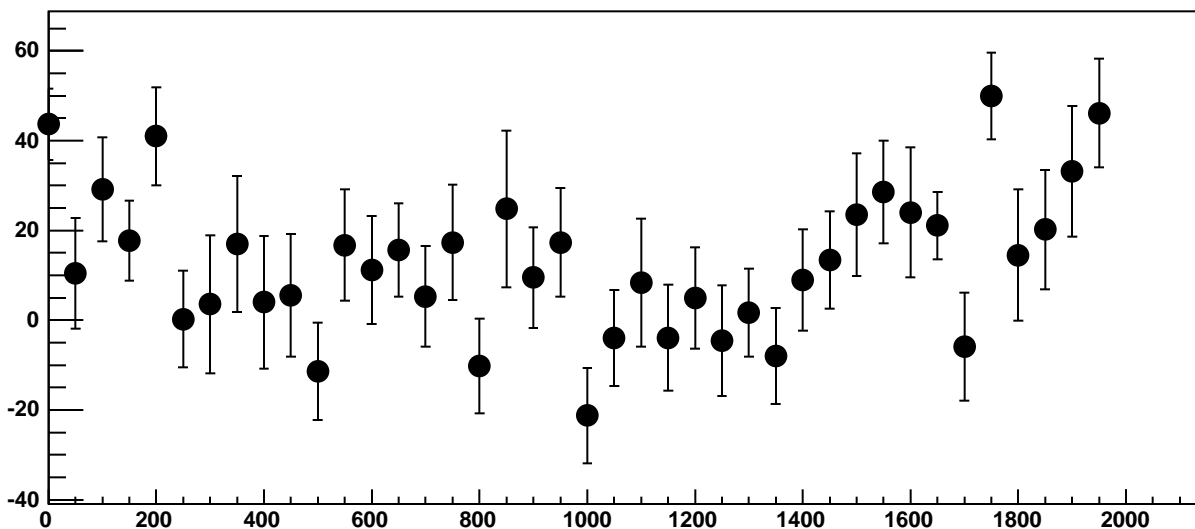


$\chi^2 / \text{ndf}$  11.82 / 11  
p0  $-1015 \pm 19.11$   
p1  $0.04351 \pm 0.01693$

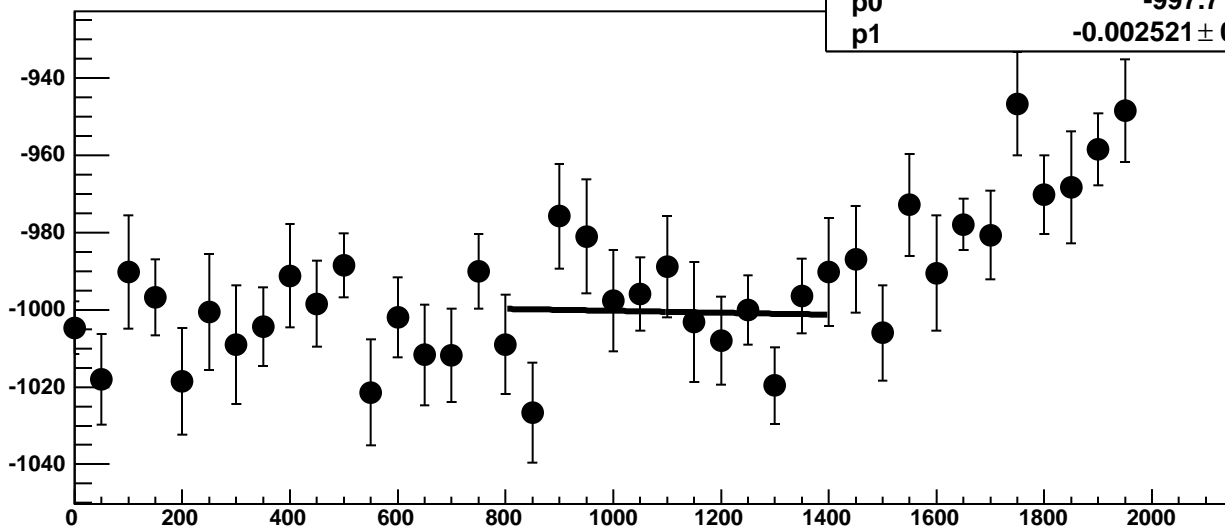
Chip 0, Channel 0, Enable 3, Hold=30, ADC Noise vs DAC



Chip 0, Channel 0, Enable 3, Hold=30, ADC Residuals vs DAC

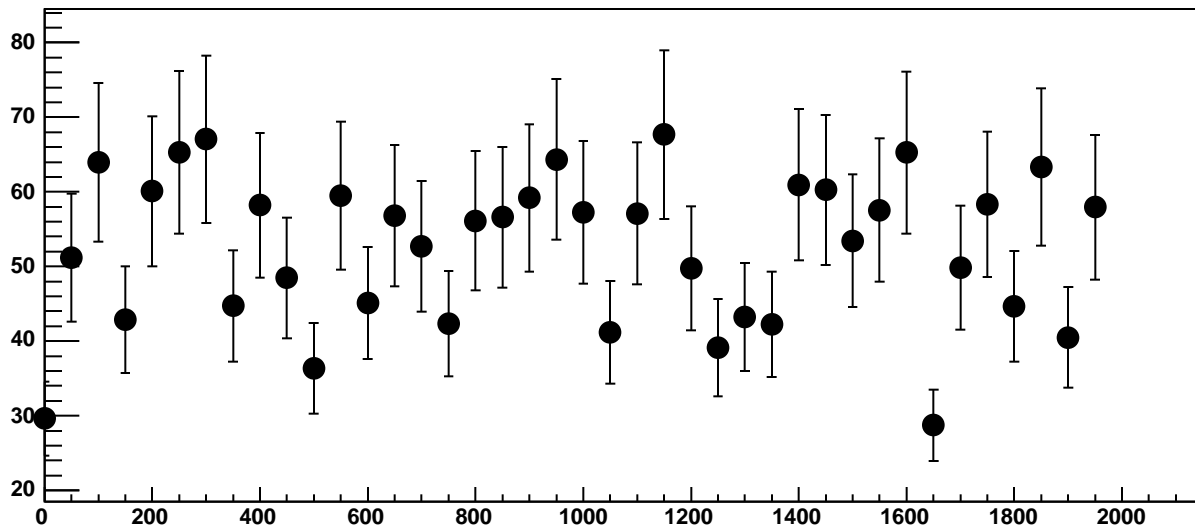


Chip 0, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC

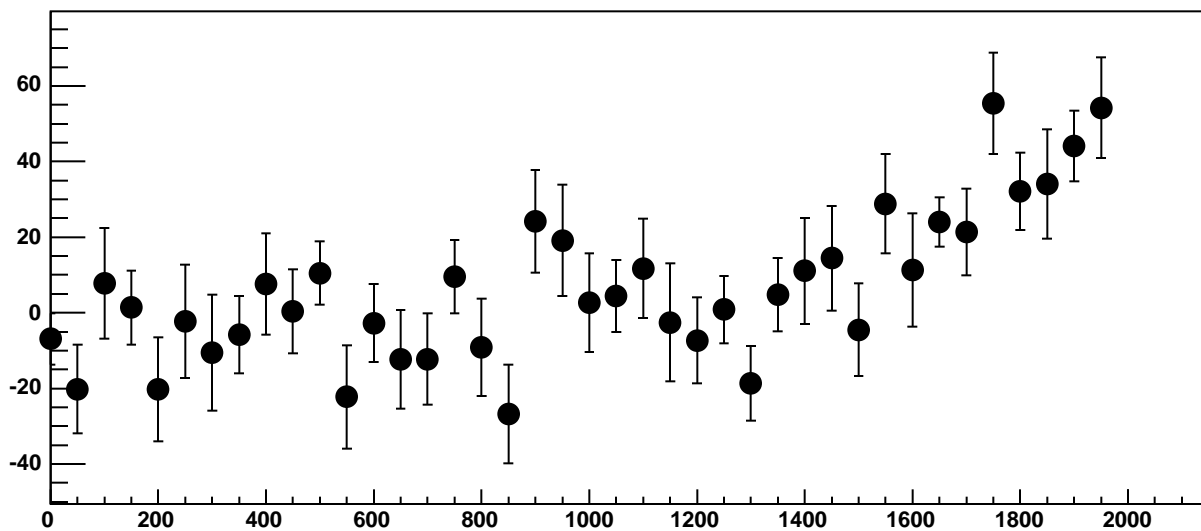


$\chi^2 / \text{ndf}$  15.51 / 11  
p0  $-997.7 \pm 20.46$   
p1  $-0.002521 \pm 0.01787$

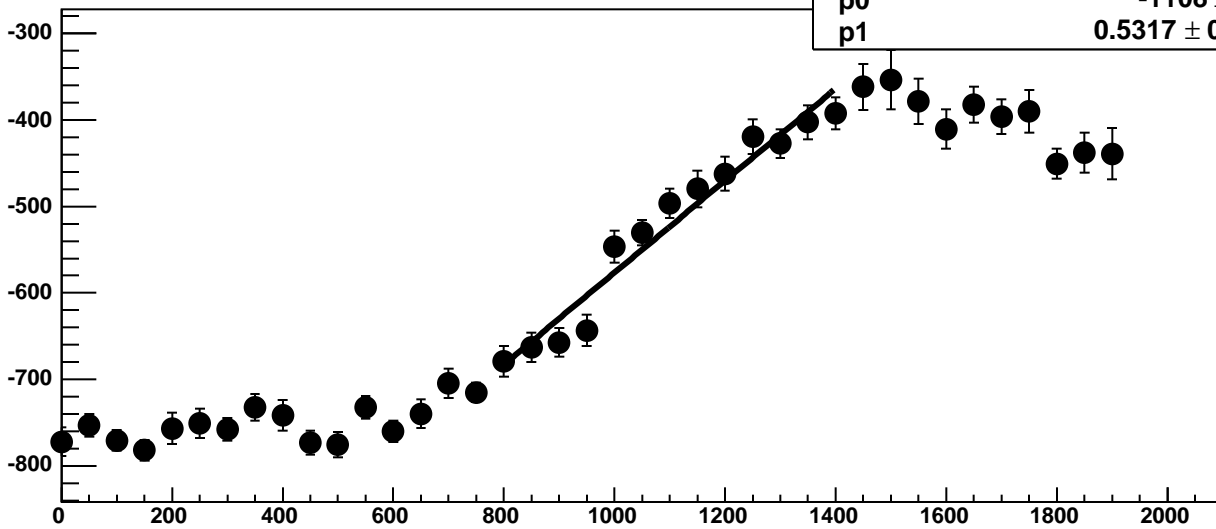
Chip 0, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC



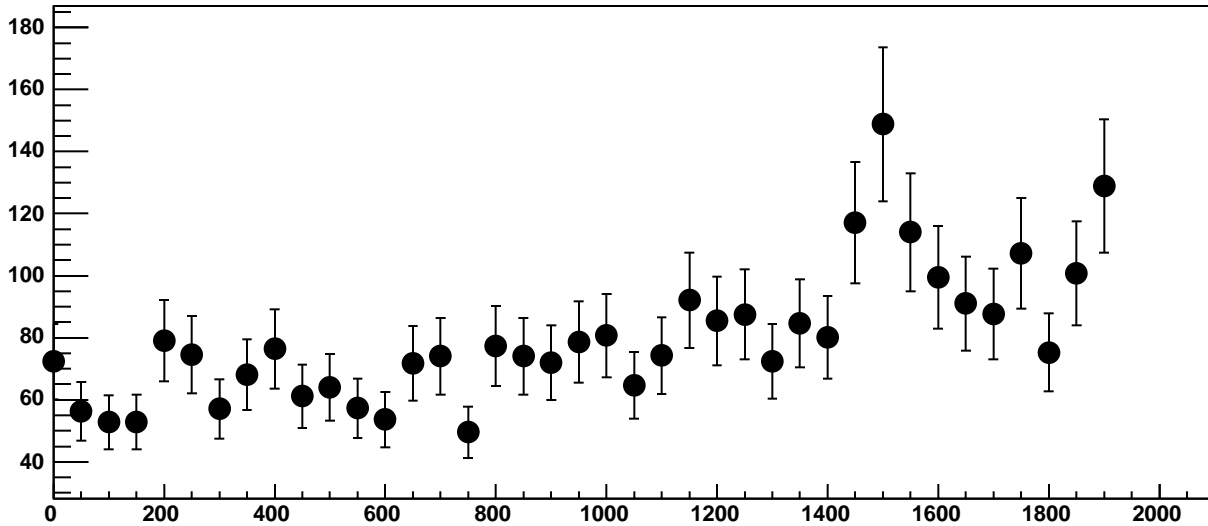
Chip 0, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC



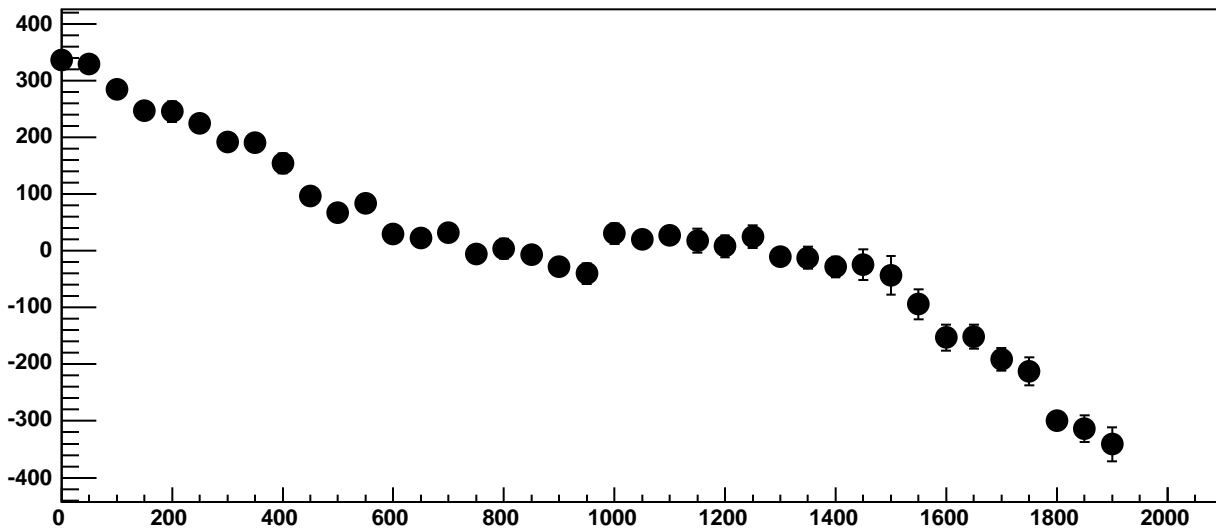
Chip 0, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC



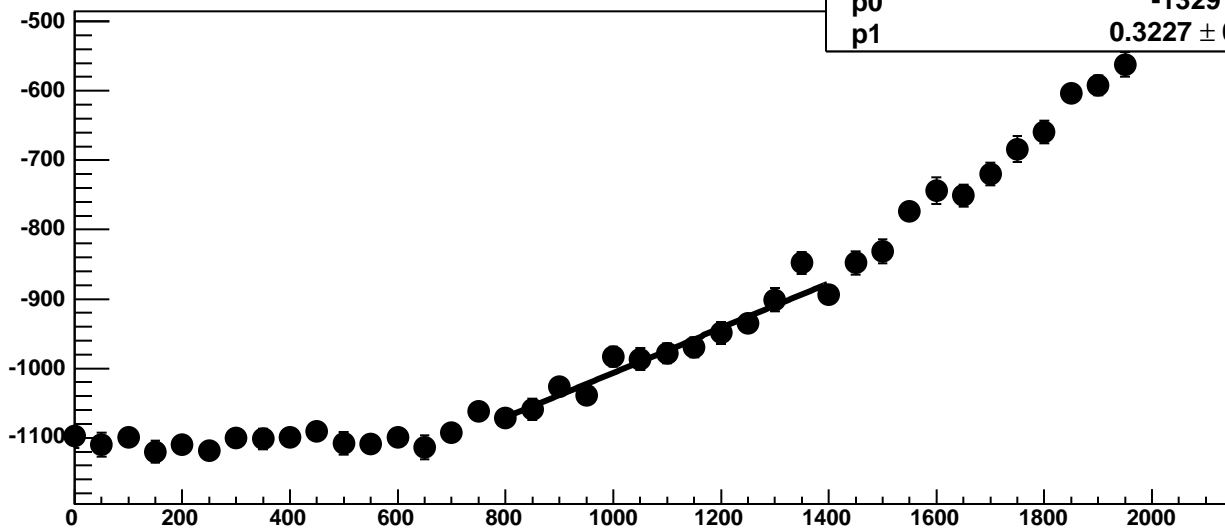
Chip 0, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC



Chip 0, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC

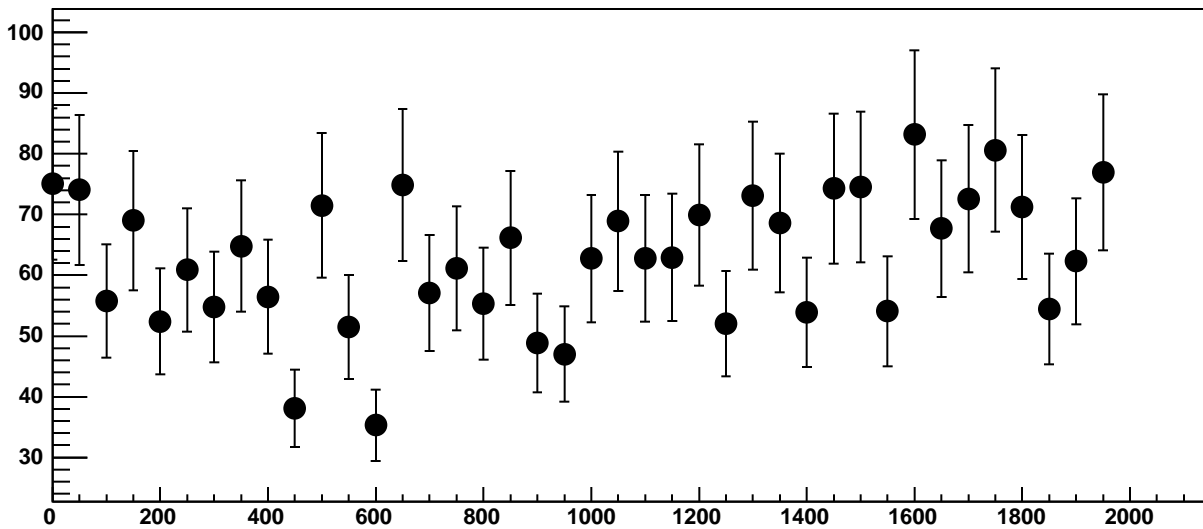


Chip 0, Channel 1, Enable 0, Hold=30, ADC Mean vs DAC

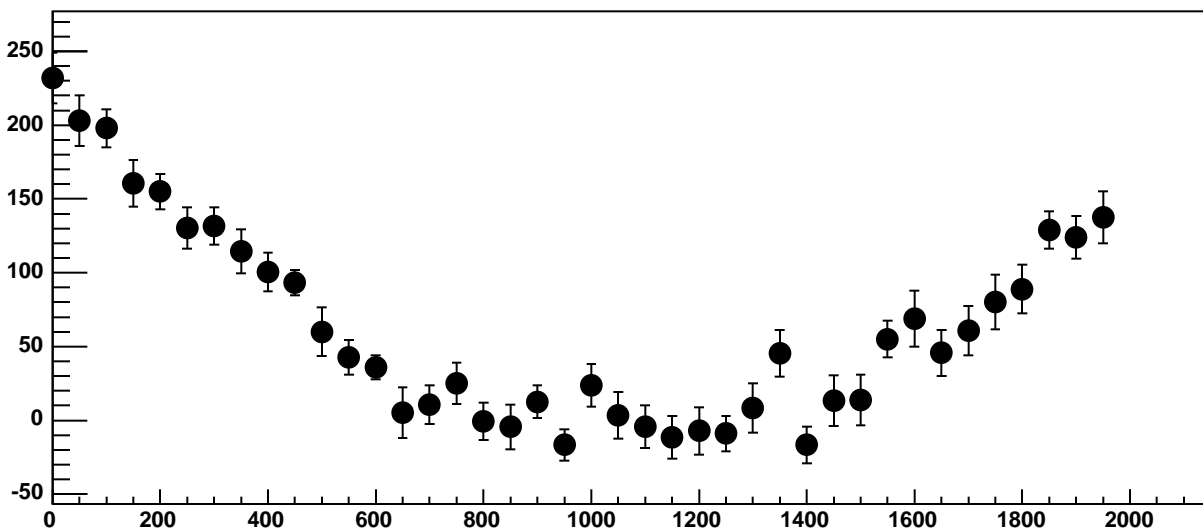


$\chi^2 / \text{ndf}$  18.33 / 11  
p0  $-1329 \pm 21.69$   
p1  $0.3227 \pm 0.01971$

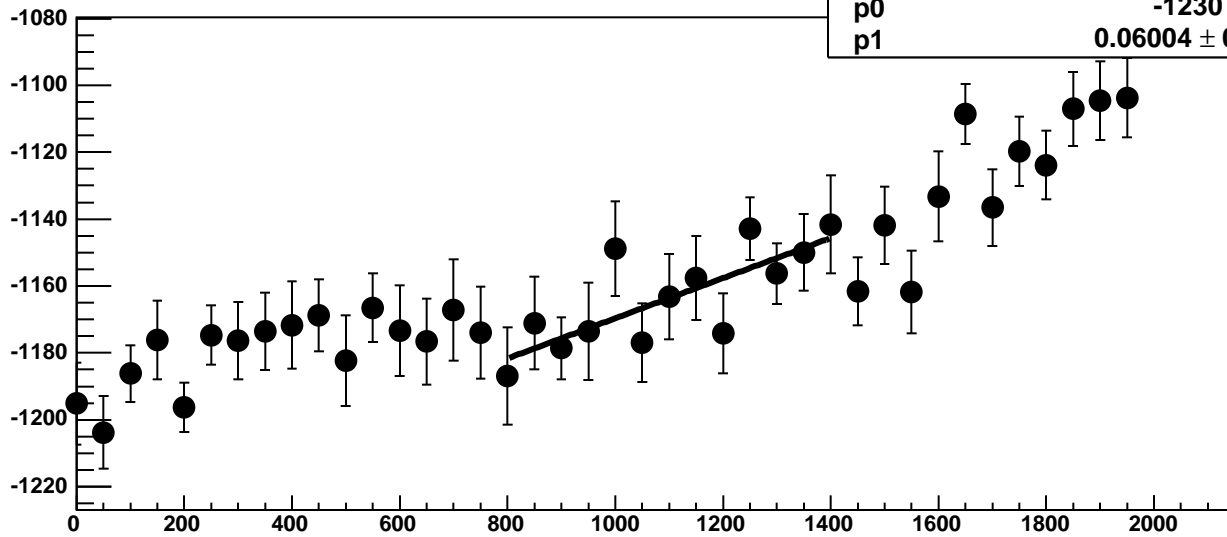
Chip 0, Channel 1, Enable 0, Hold=30, ADC Noise vs DAC



Chip 0, Channel 1, Enable 0, Hold=30, ADC Residuals vs DAC

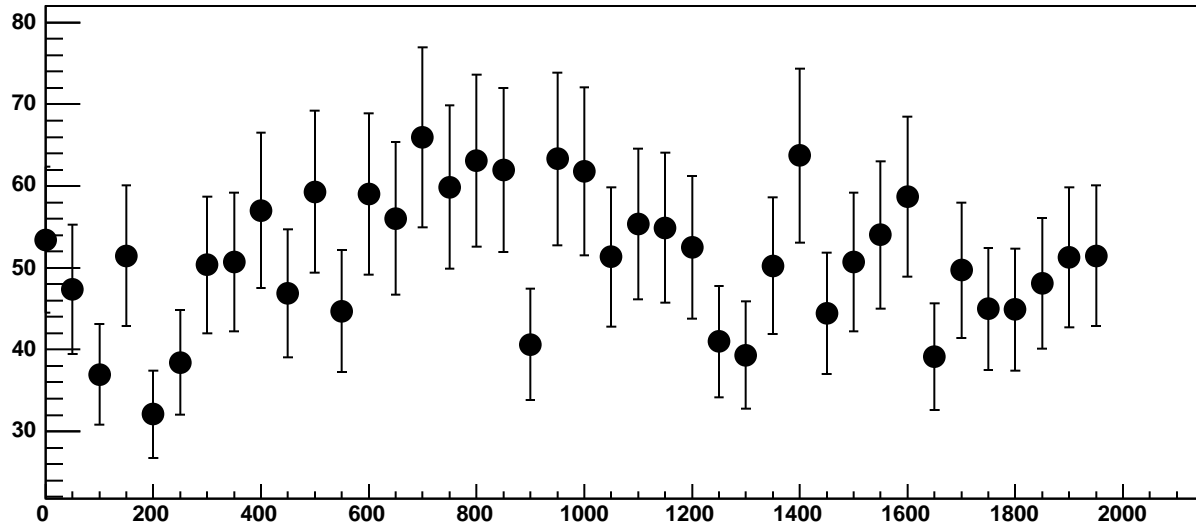


Chip 0, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC

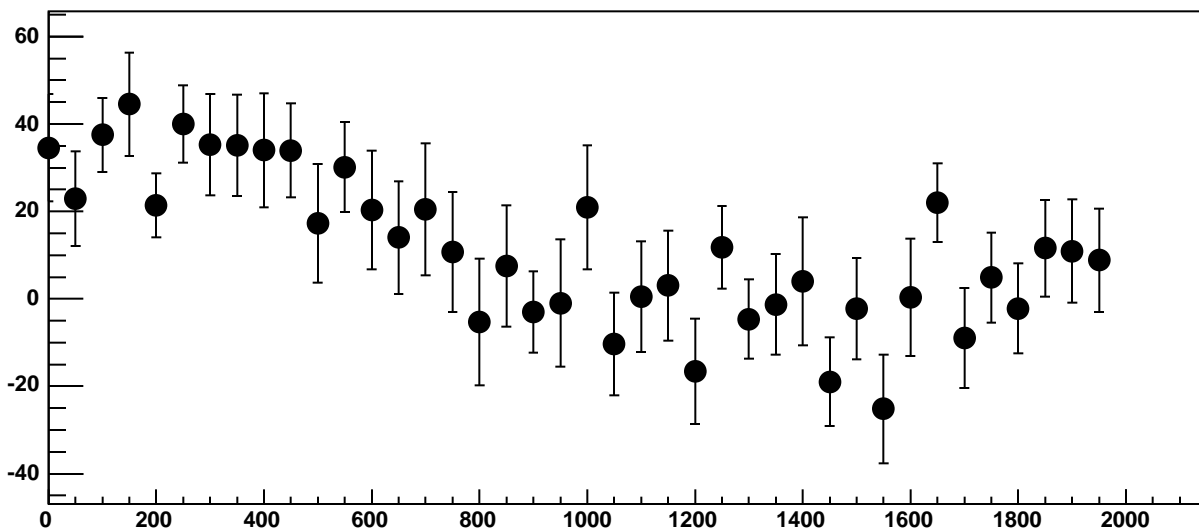


$\chi^2 / \text{ndf}$  7.332 / 11  
p0  $-1230 \pm 20.45$   
p1  $0.06004 \pm 0.01803$

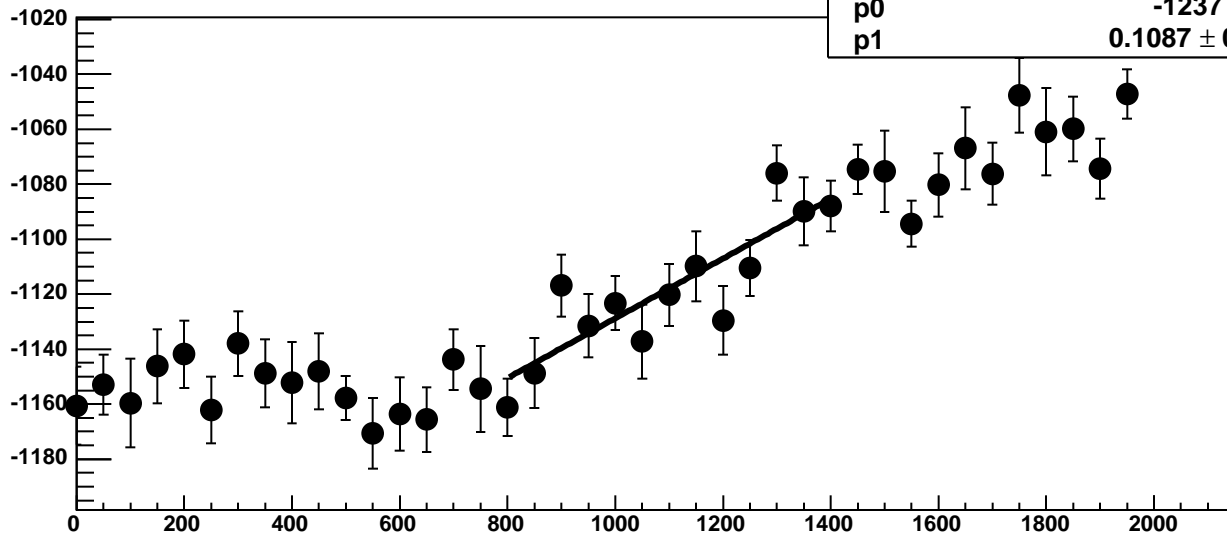
Chip 0, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC



Chip 0, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC

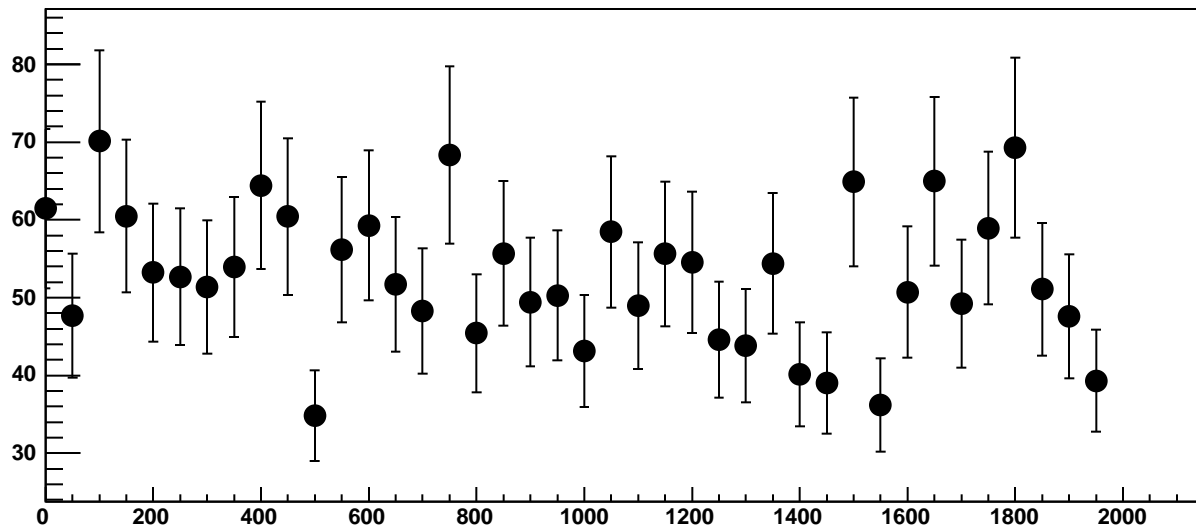


Chip 0, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC

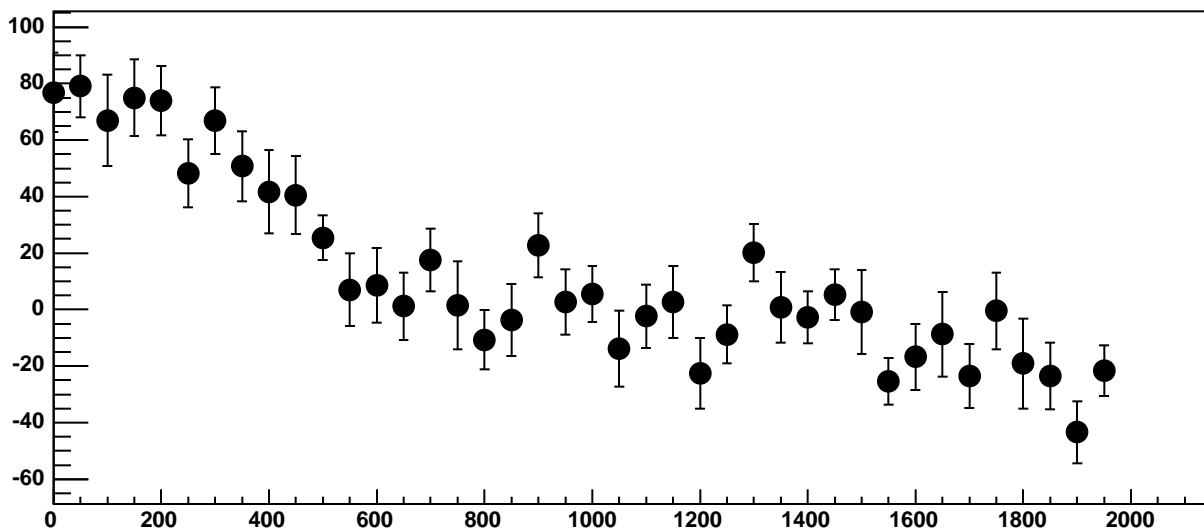


$\chi^2 / \text{ndf}$  14.77 / 11  
p0  $-1237 \pm 17.97$   
p1  $0.1087 \pm 0.01592$

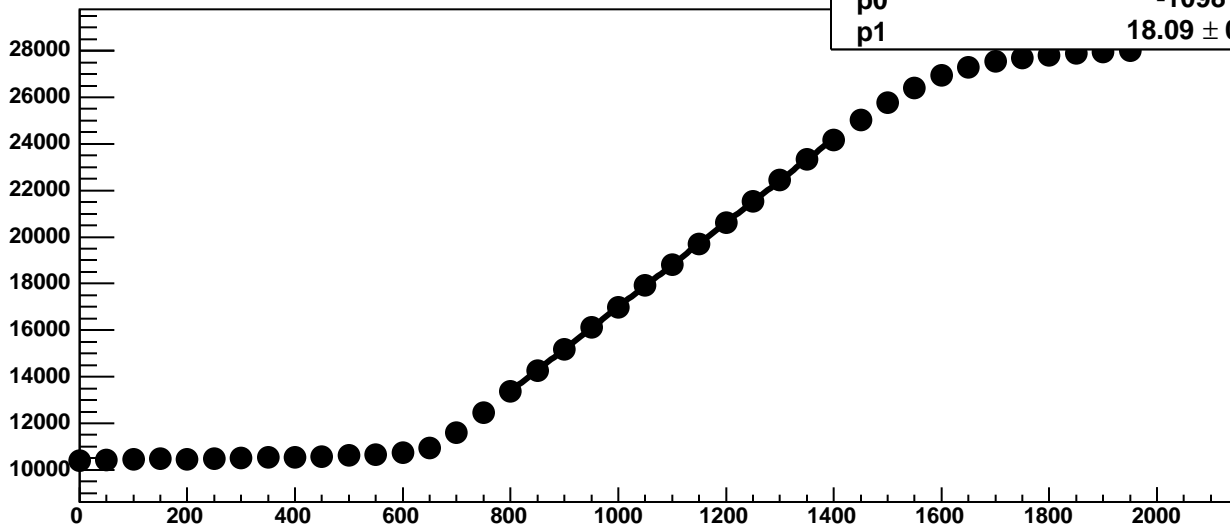
Chip 0, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC



Chip 0, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC

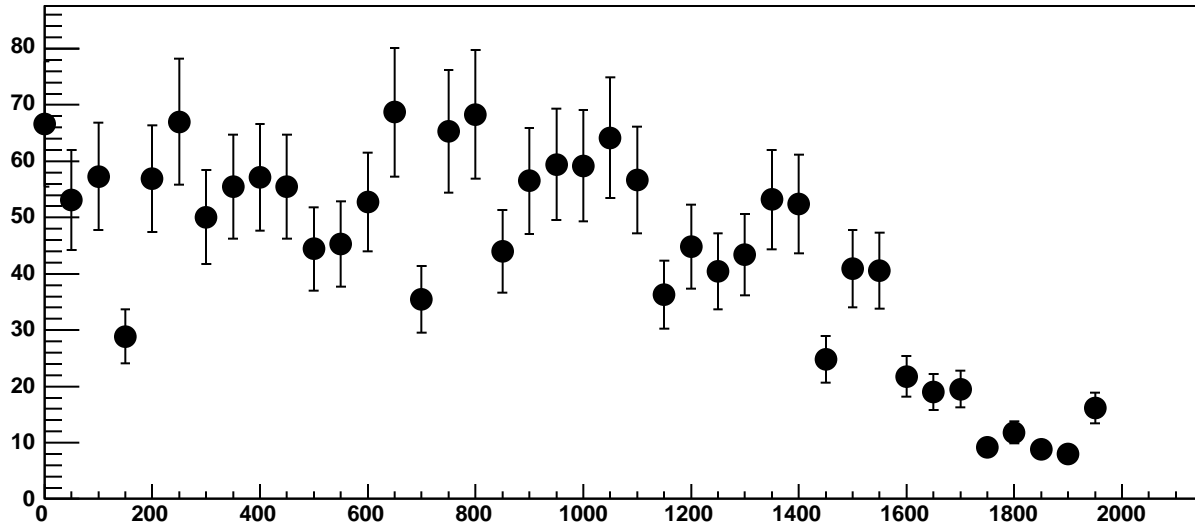


Chip 0, Channel 1, Enable 3!, Hold=30, ADC Mean vs DAC

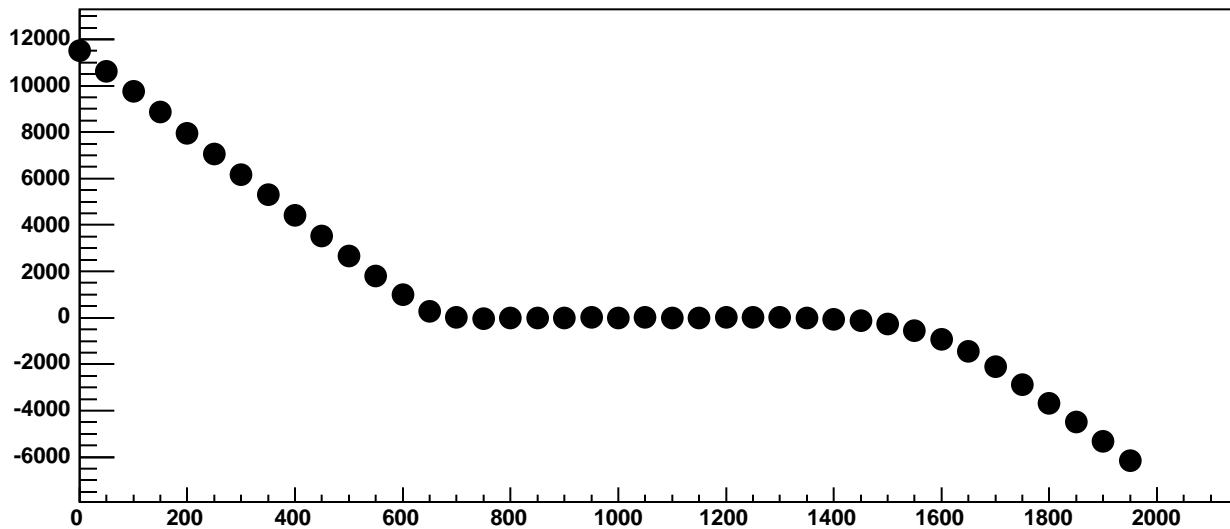


$\chi^2 / \text{ndf}$  39.36 / 11  
p0  $-1098 \pm 20.28$   
p1  $18.09 \pm 0.01777$

Chip 0, Channel 1, Enable 3!, Hold=30, ADC Noise vs DAC

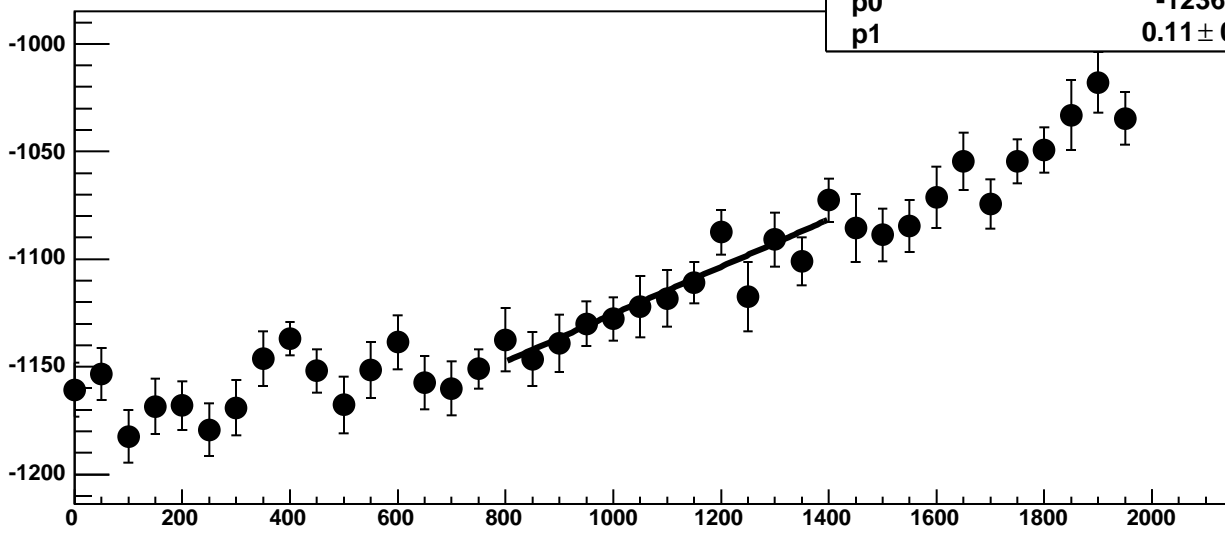


Chip 0, Channel 1, Enable 3!, Hold=30, ADC Residuals vs DAC



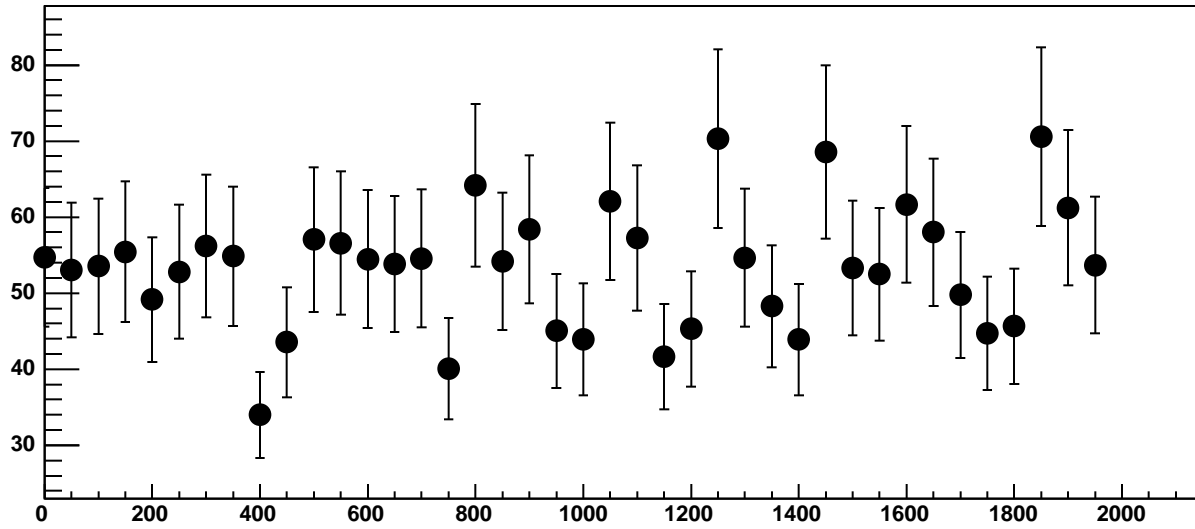


Chip 0, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC

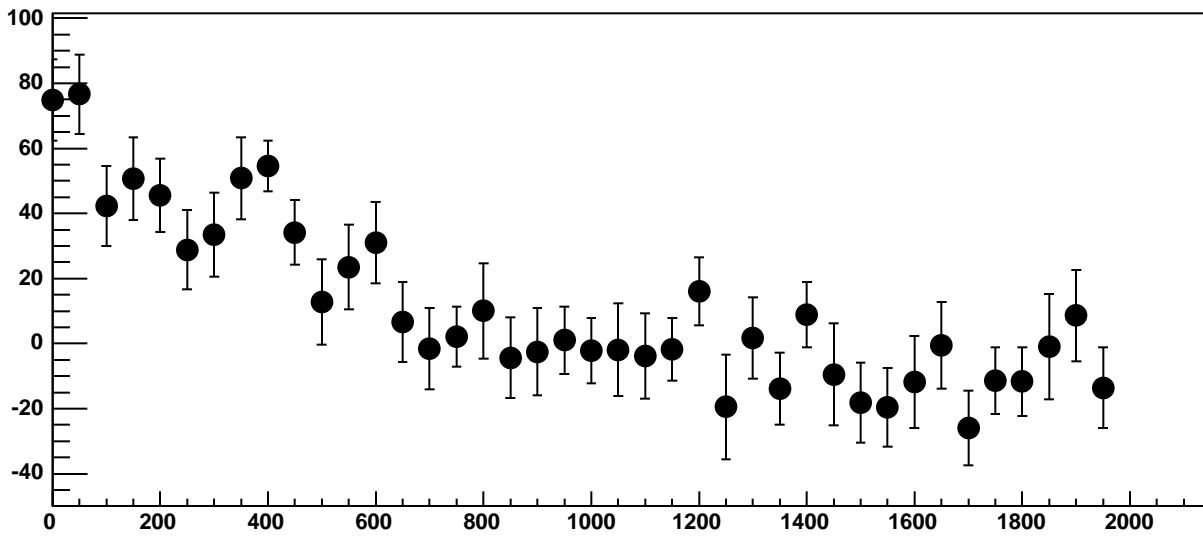


$\chi^2 / \text{ndf}$  7.058 / 11  
p0 -1236 ± 20.01  
p1 0.11 ± 0.01769

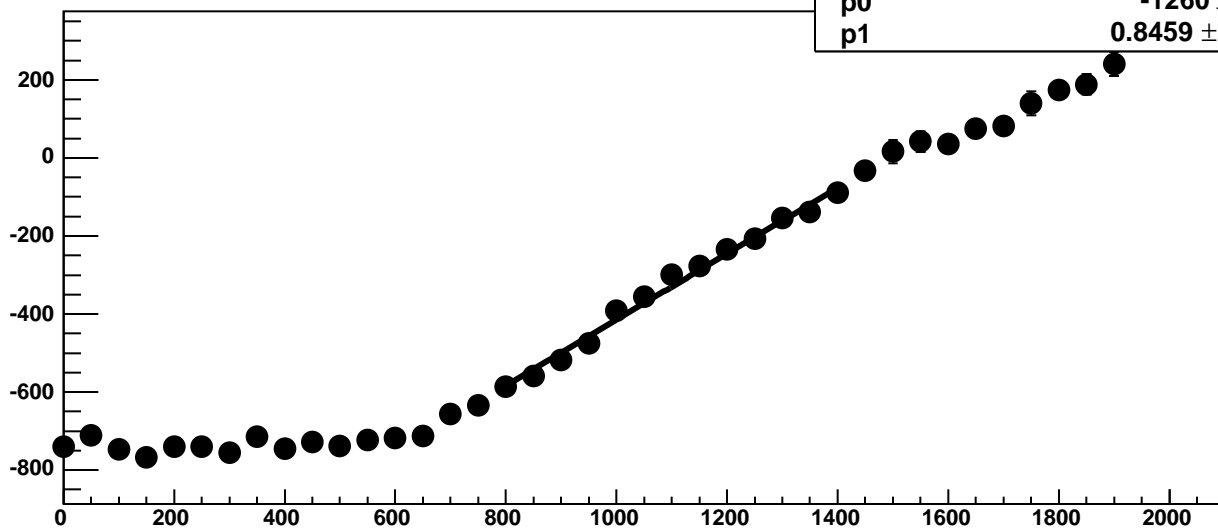
Chip 0, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC



Chip 0, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC

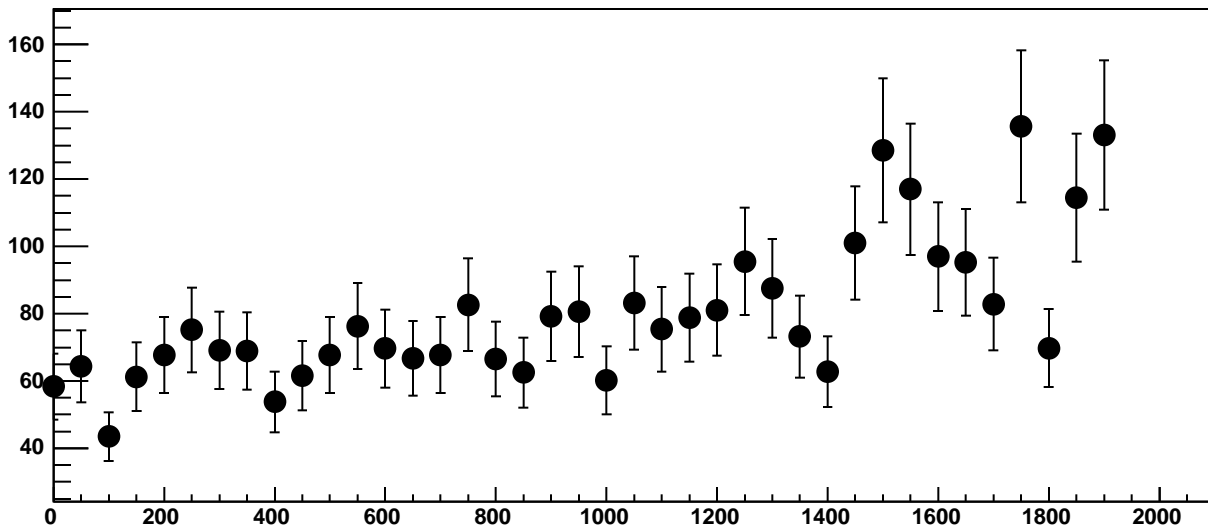


Chip 0, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC

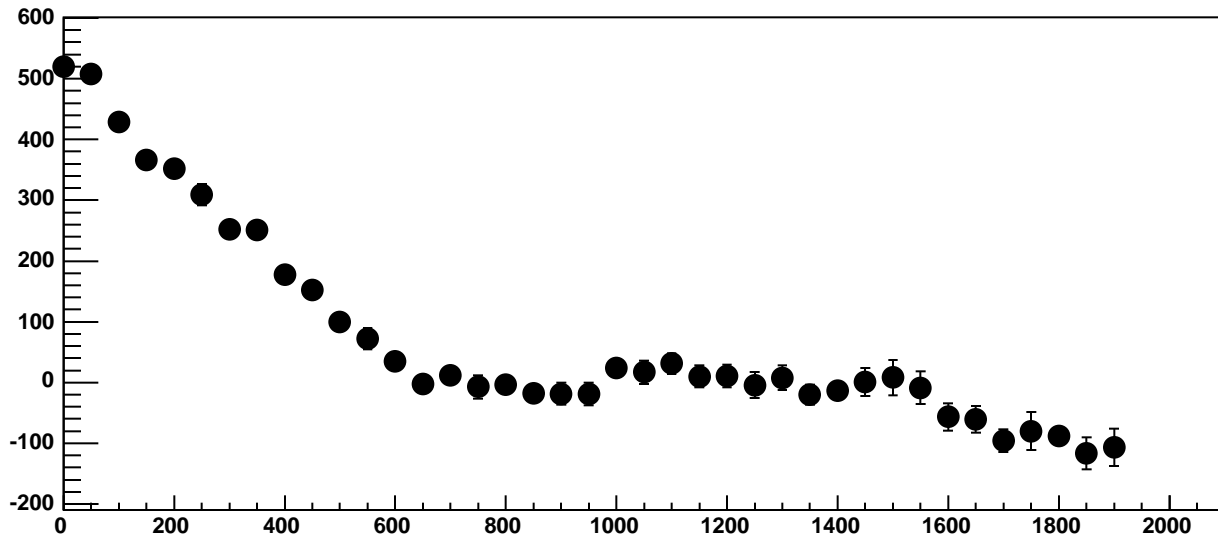


$\chi^2 / \text{ndf}$  13.63 / 11  
p0  $-1260 \pm 26.28$   
p1  $0.8459 \pm 0.0238$

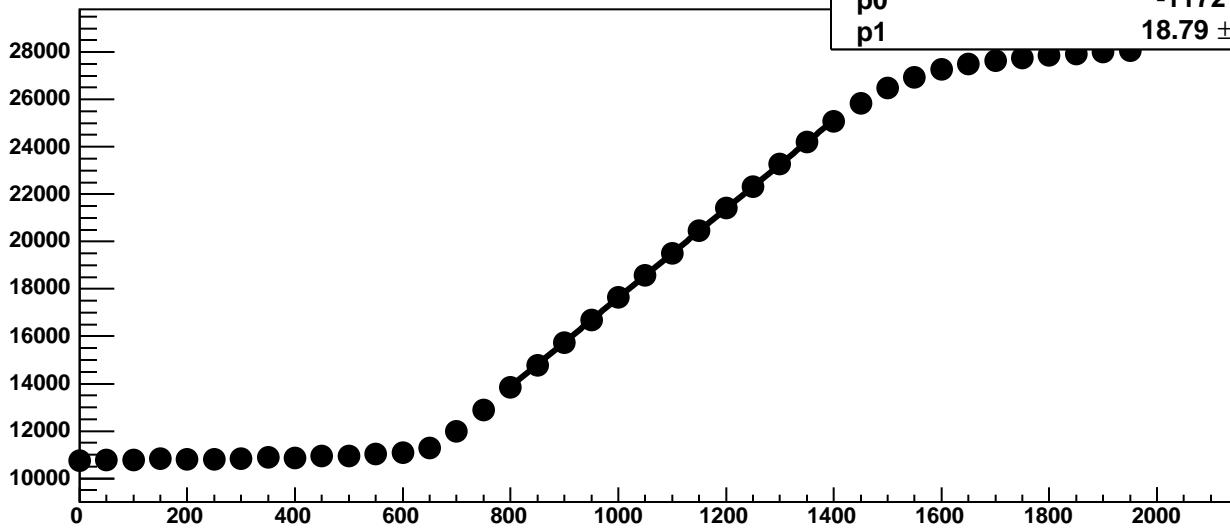
Chip 0, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC



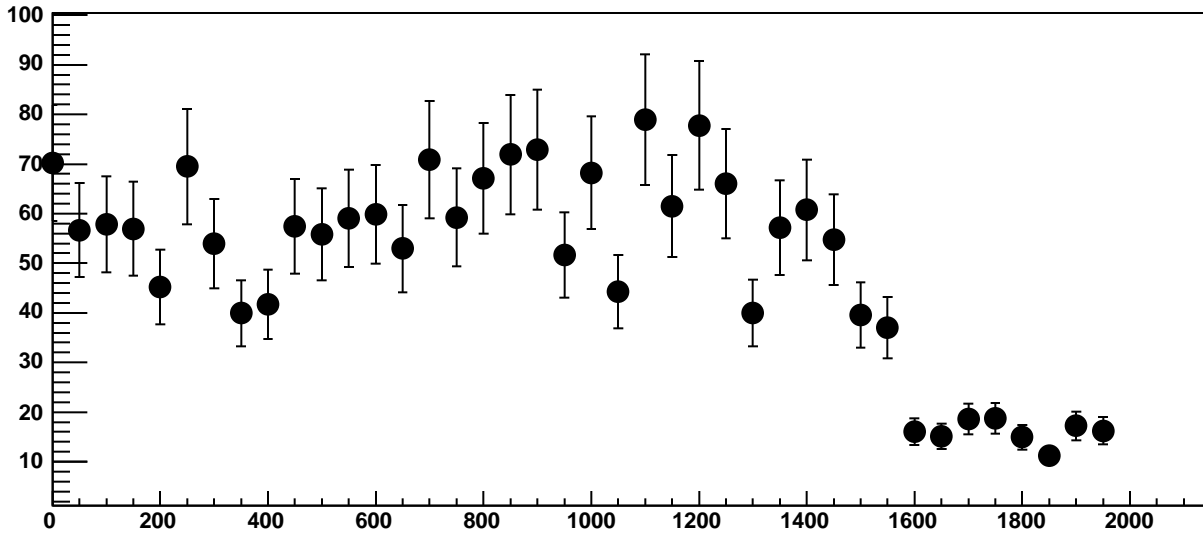
Chip 0, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC



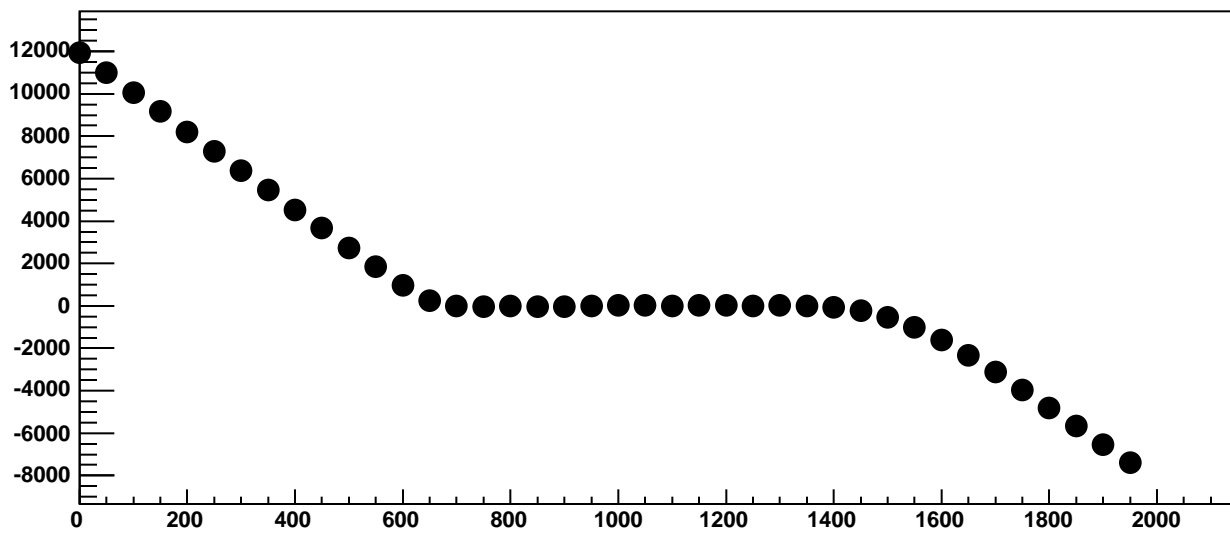
Chip 0, Channel 2, Enable 0!, Hold=30, ADC Mean vs DAC



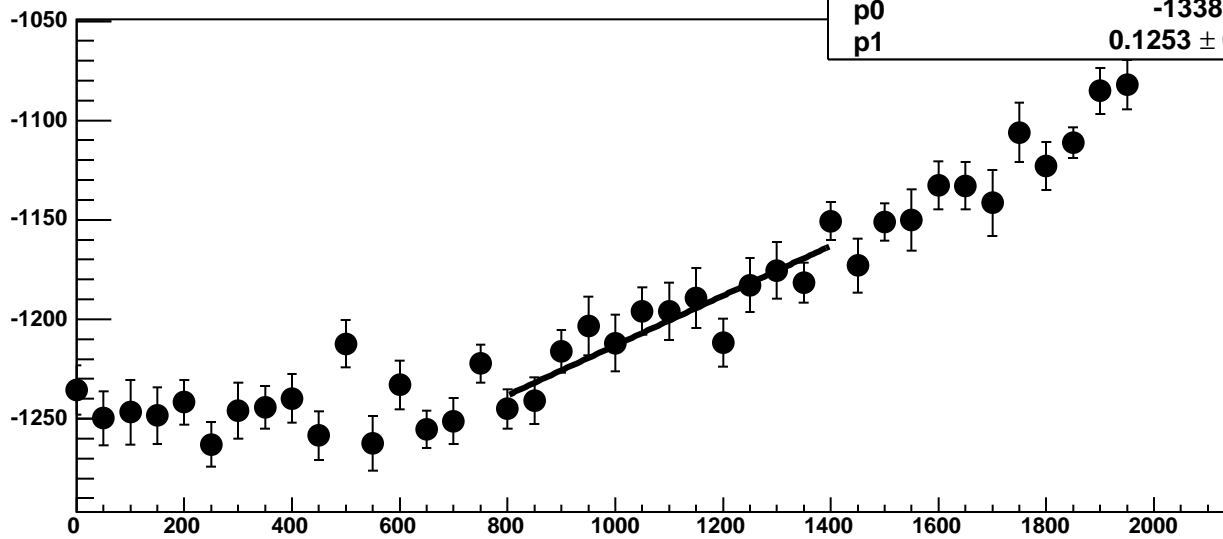
Chip 0, Channel 2, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 0, Channel 2, Enable 0!, Hold=30, ADC Residuals vs DAC



Chip 0, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

11.15 / 11

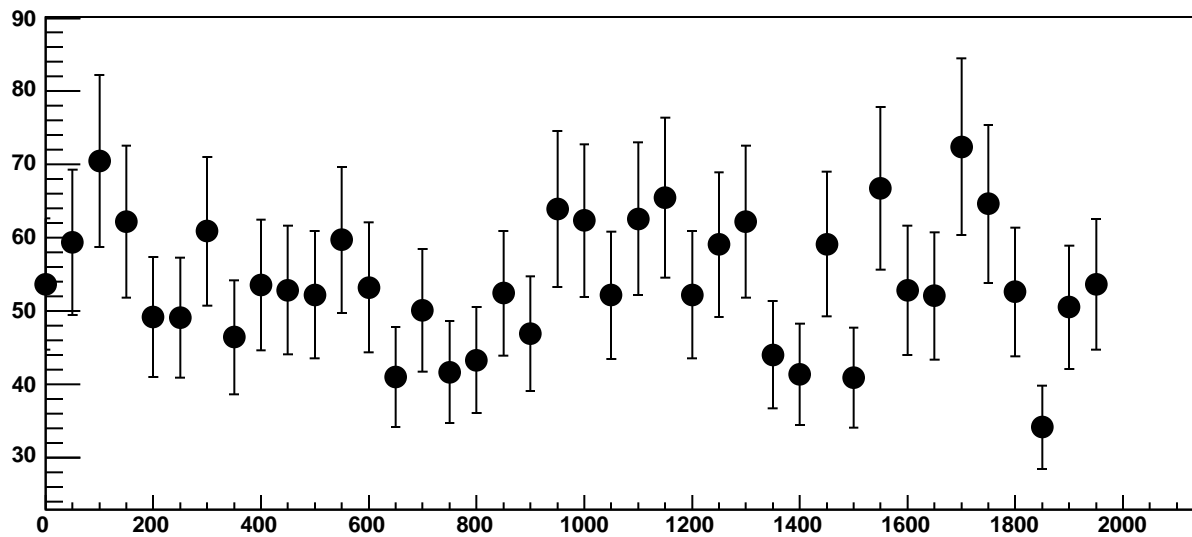
p0

$-1338 \pm 18.02$

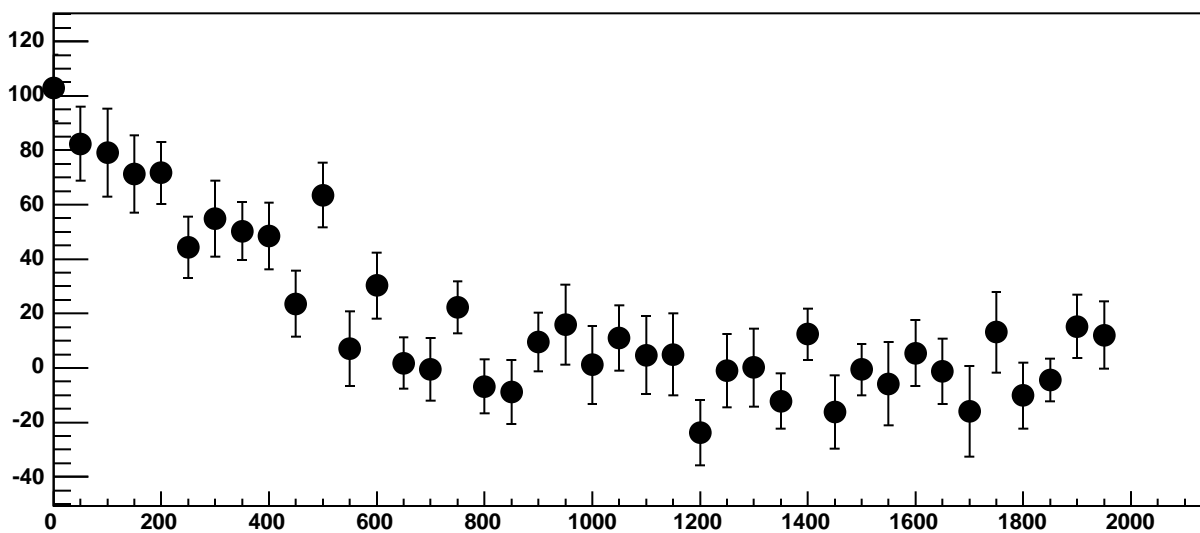
p1

$0.1253 \pm 0.01603$

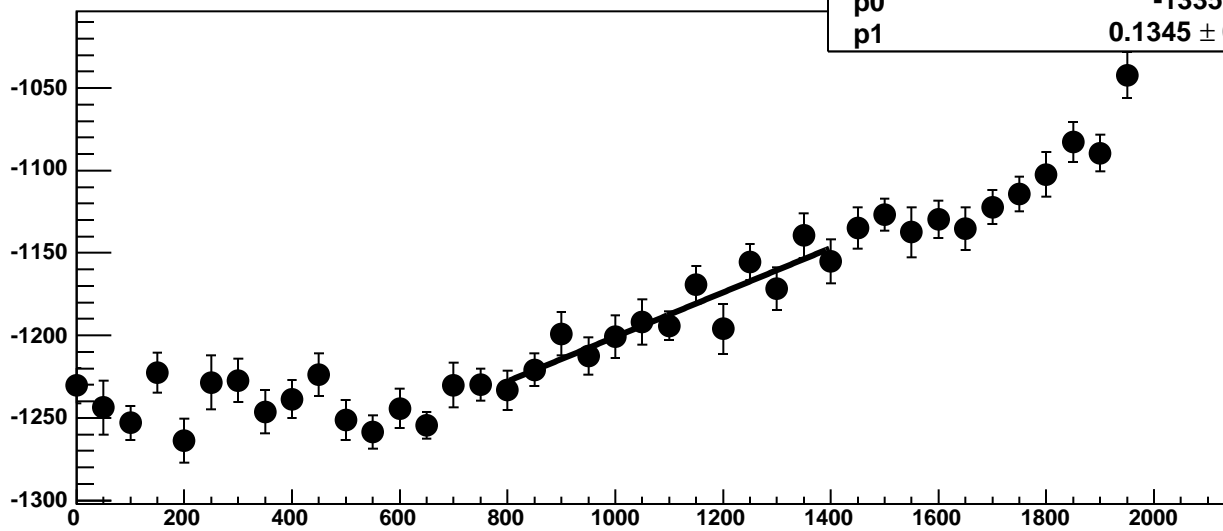
Chip 0, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC



Chip 0, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC



Chip 0, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

8.878 / 11

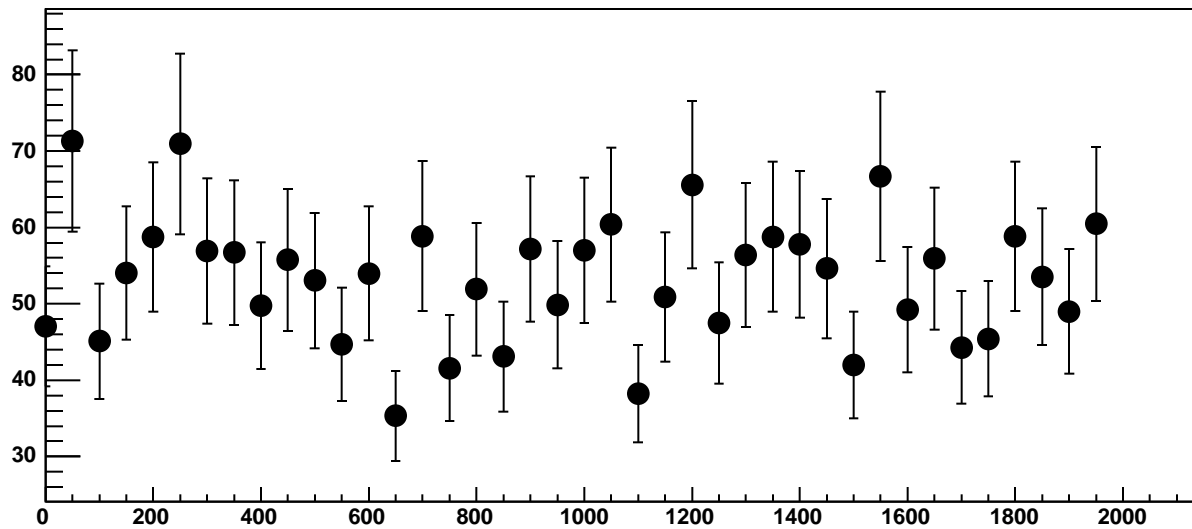
p0

$-1335 \pm 19.83$

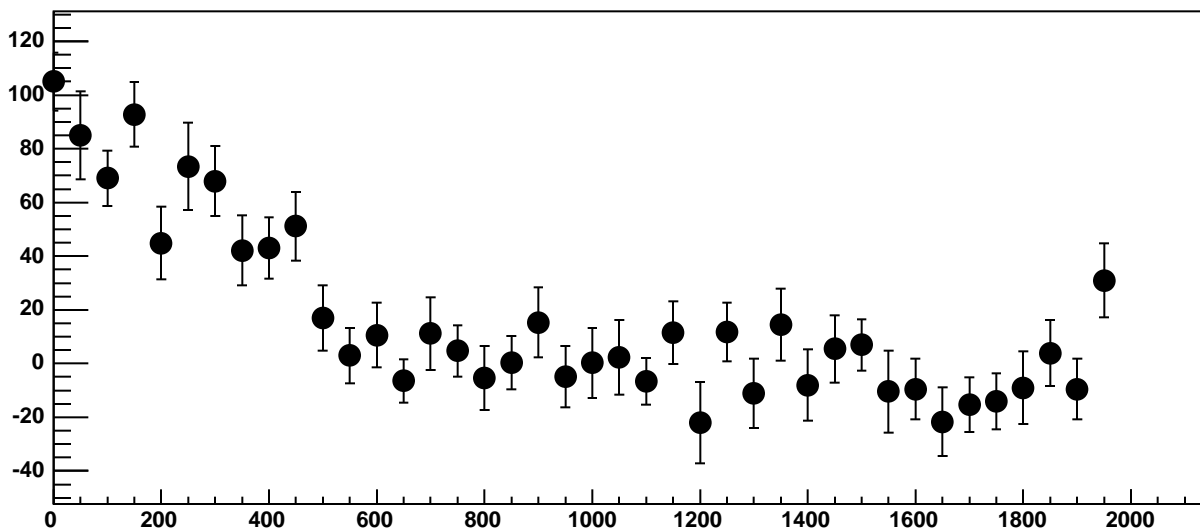
p1

$0.1345 \pm 0.01804$

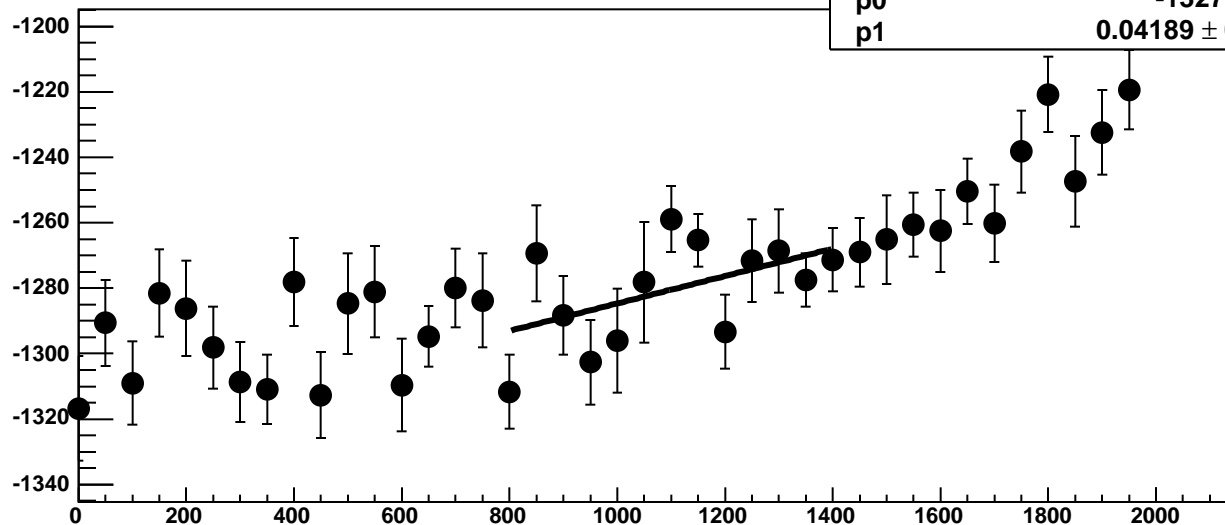
Chip 0, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC



Chip 0, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 0, Channel 2, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

17.54 / 11

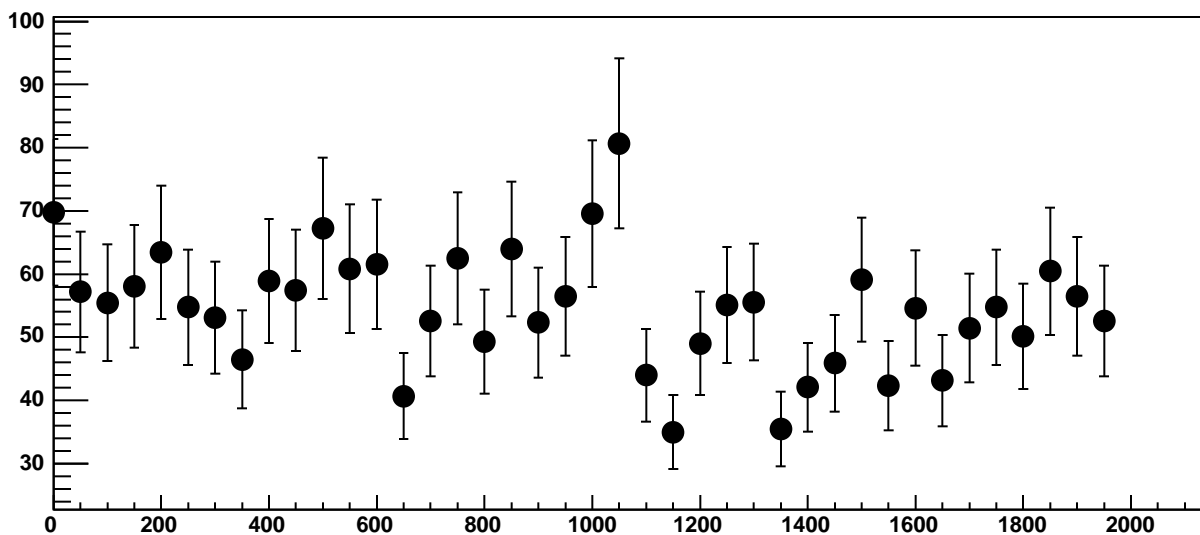
p0

$-1327 \pm 19.09$

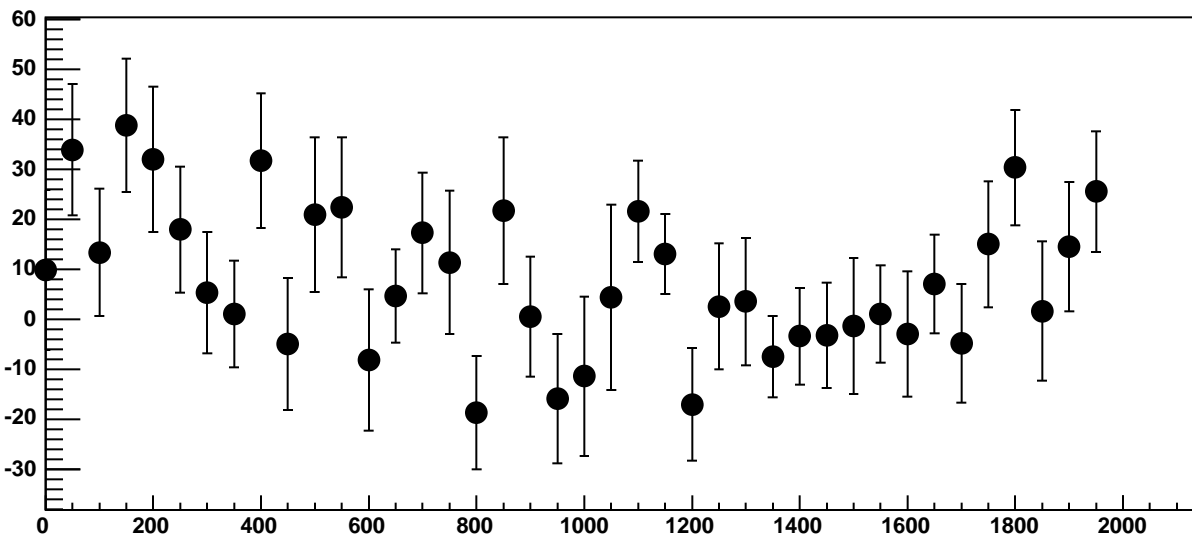
p1

$0.04189 \pm 0.01649$

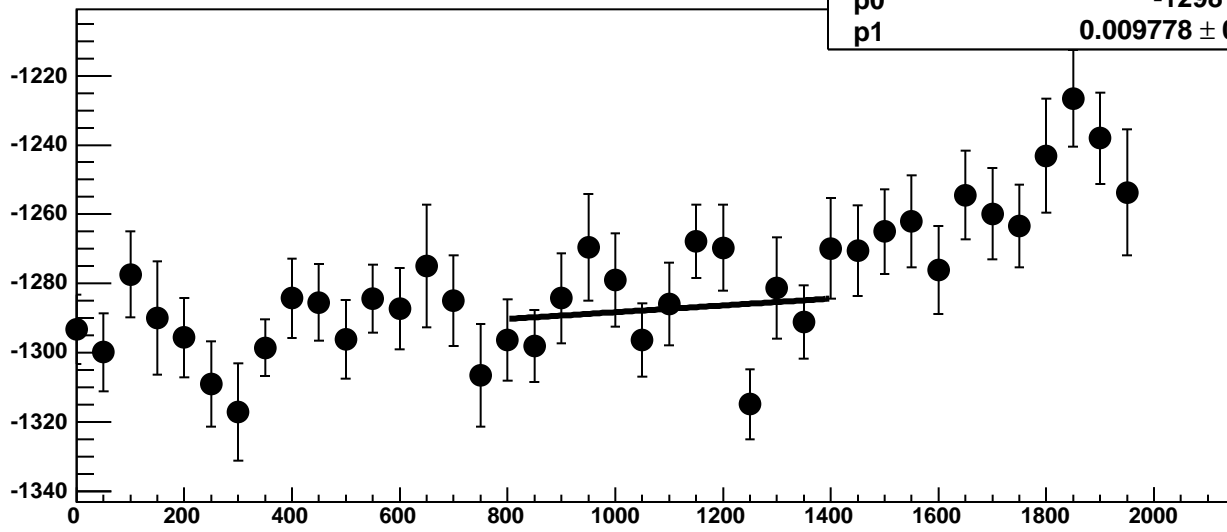
Chip 0, Channel 2, Enable 3, Hold=30, ADC Noise vs DAC



Chip 0, Channel 2, Enable 3, Hold=30, ADC Residuals vs DAC

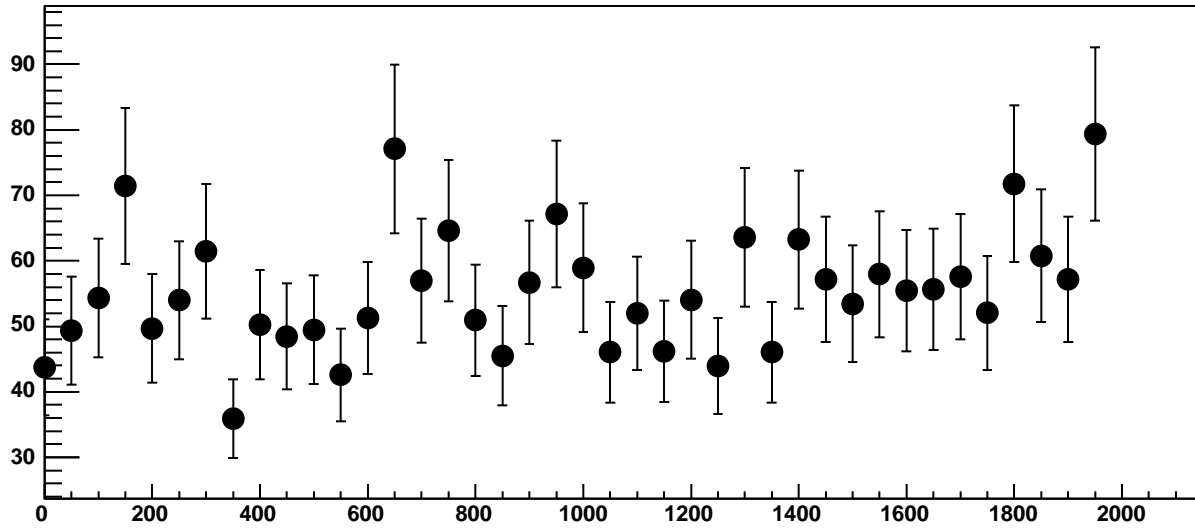


Chip 0, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC

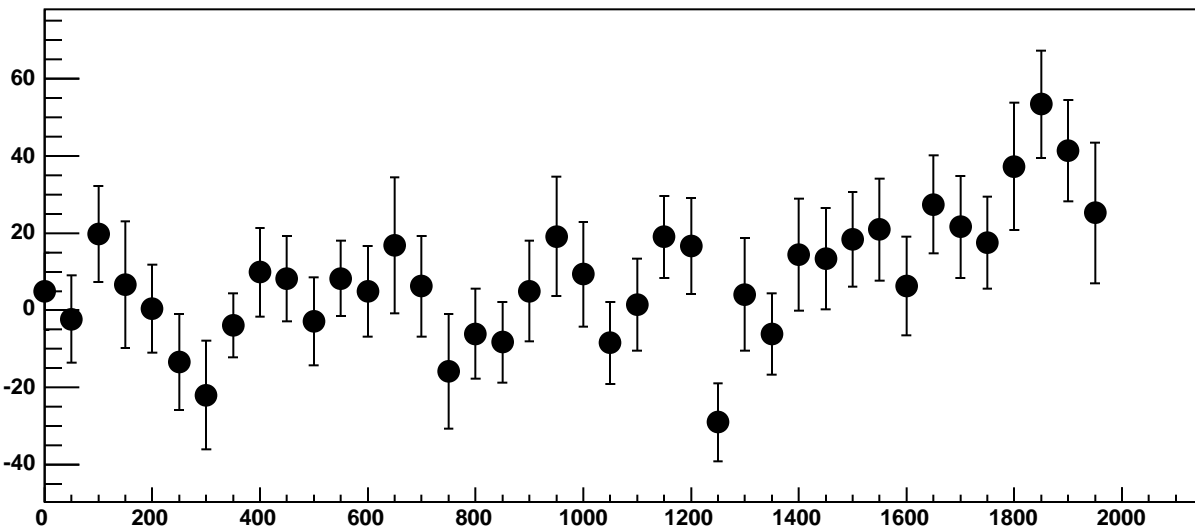


$\chi^2 / \text{ndf}$  18.45 / 11  
p0  $-1298 \pm 19.93$   
p1  $0.009778 \pm 0.01789$

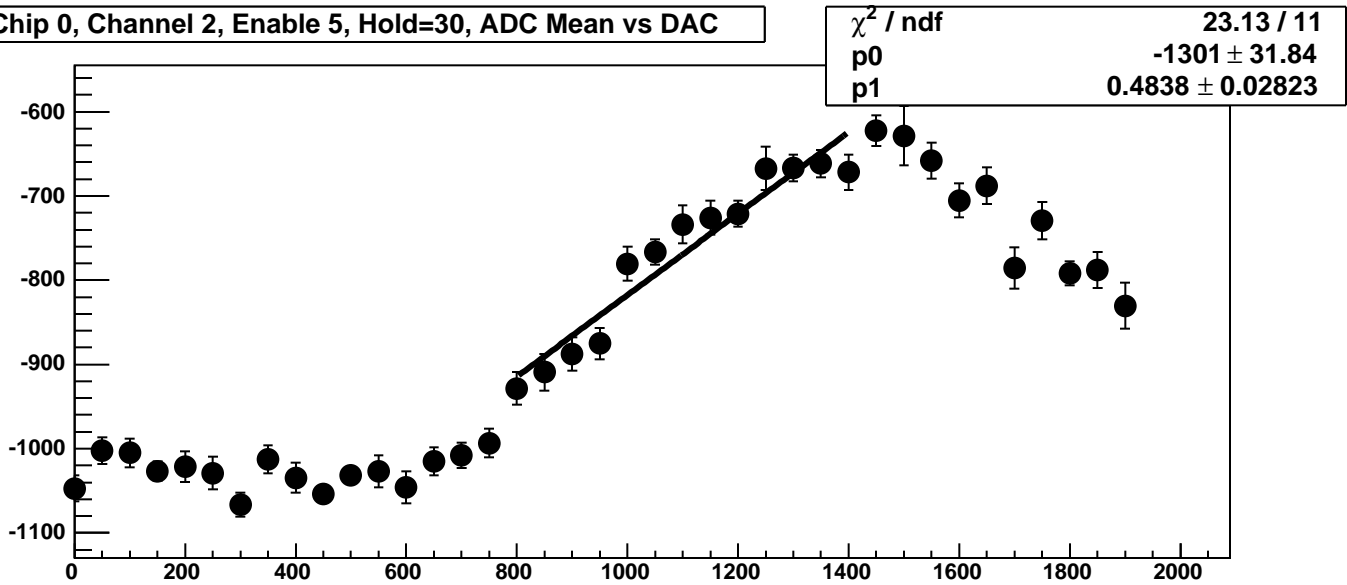
Chip 0, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC



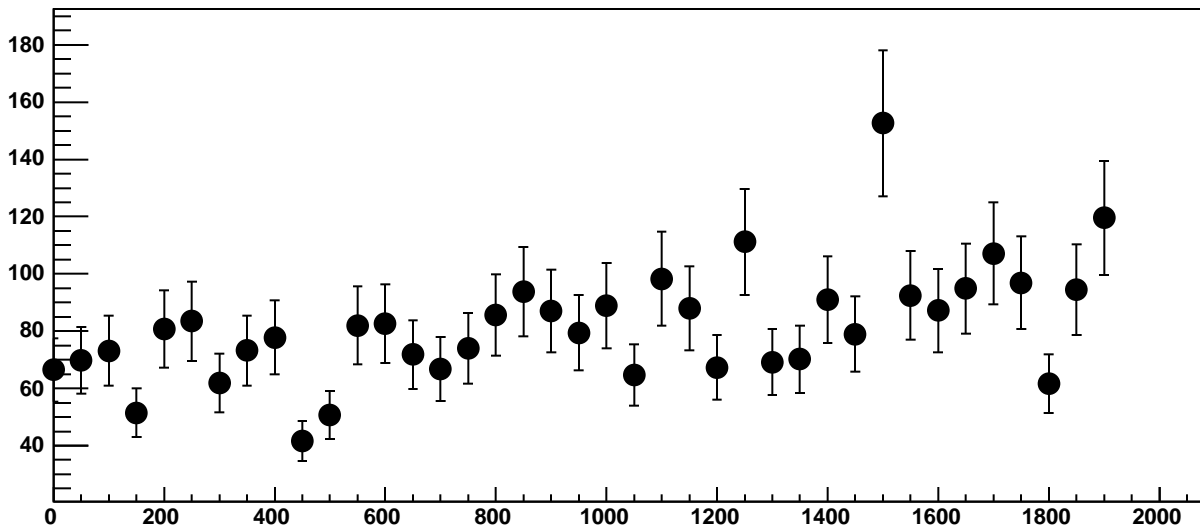
Chip 0, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC



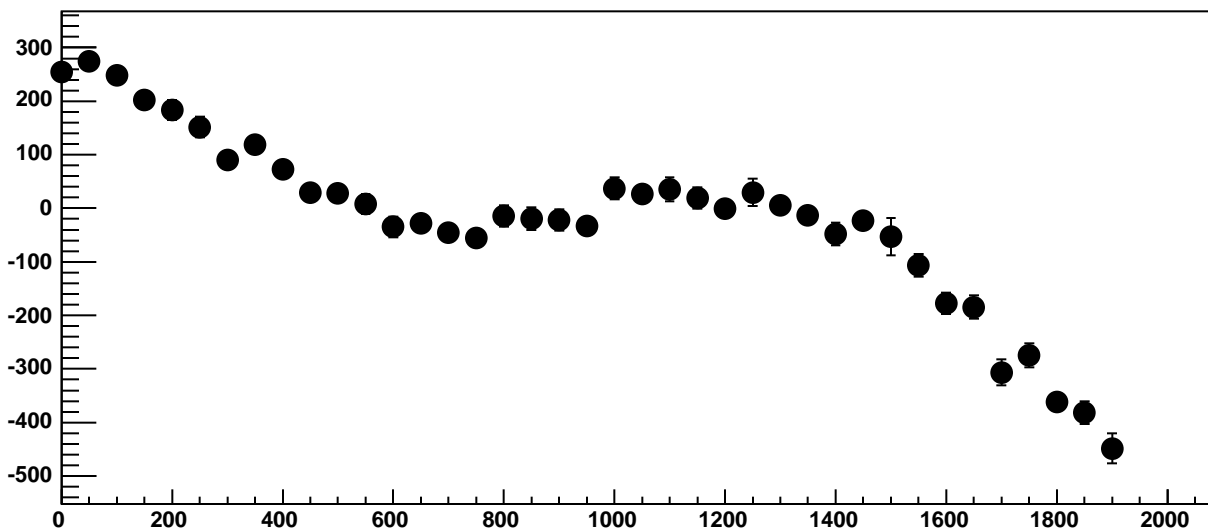
Chip 0, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC



Chip 0, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC

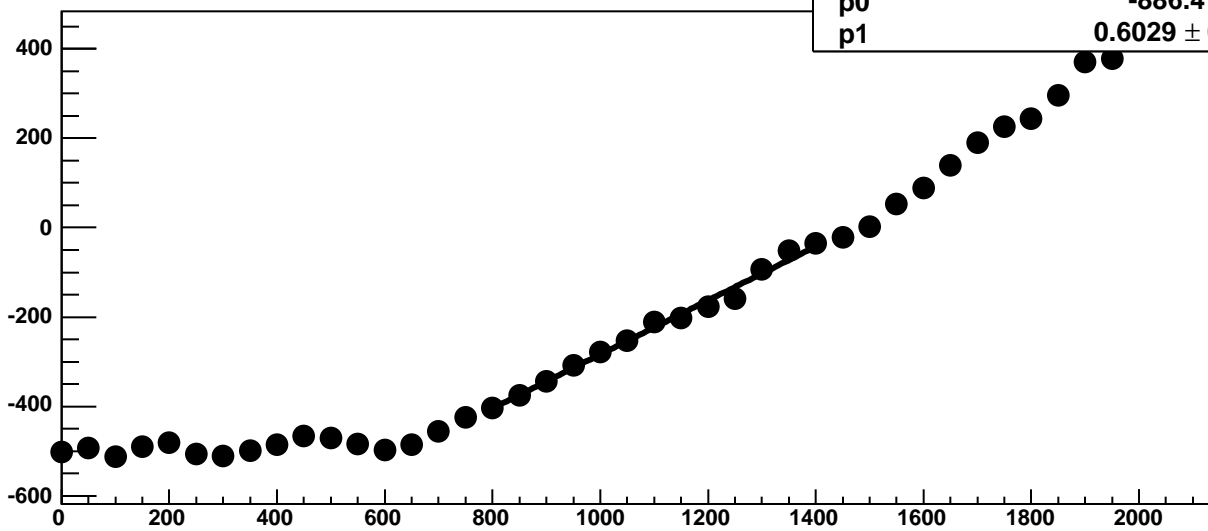


Chip 0, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC

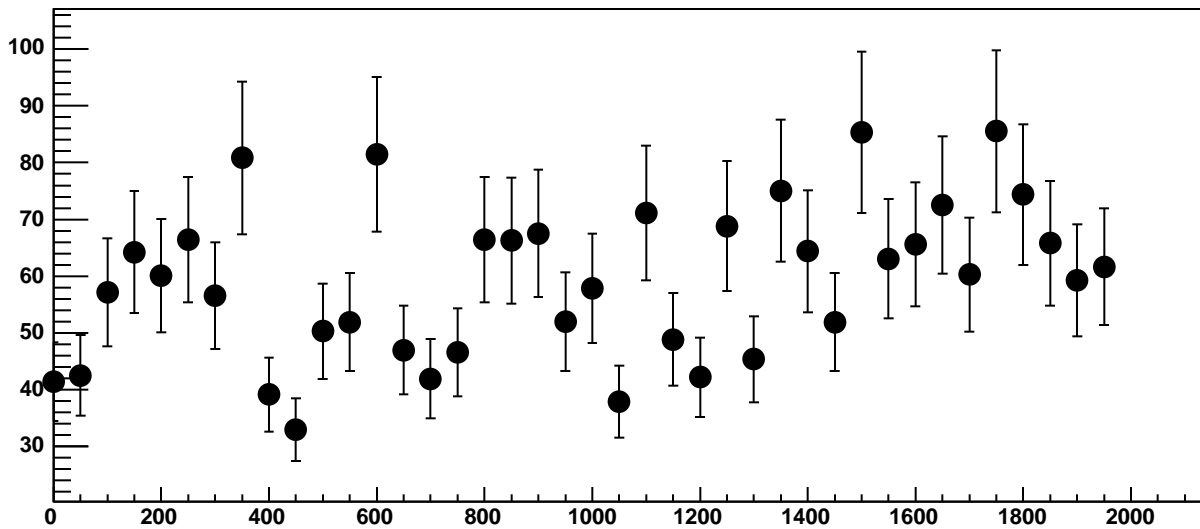




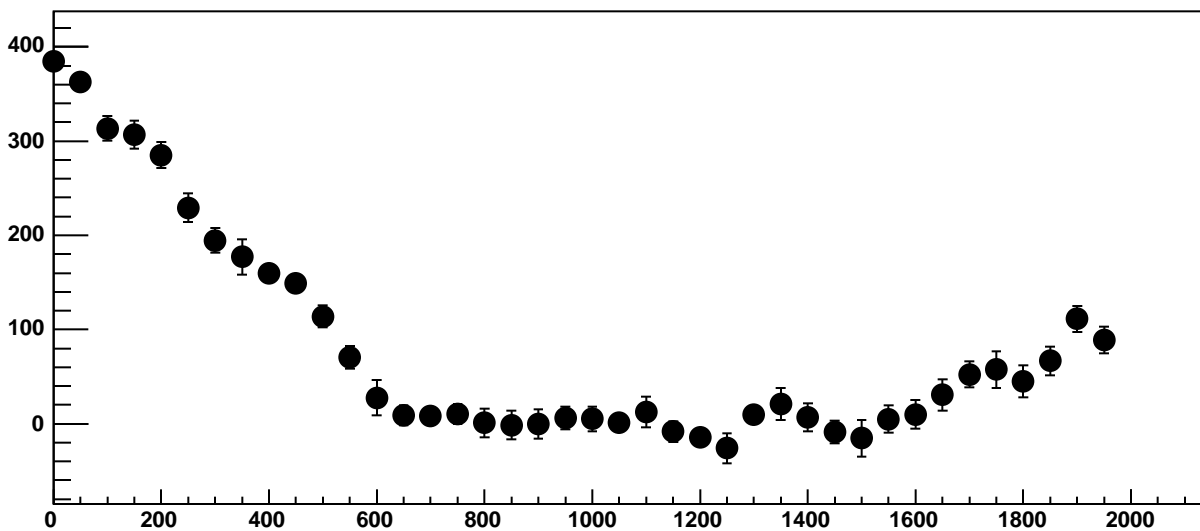
Chip 0, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC



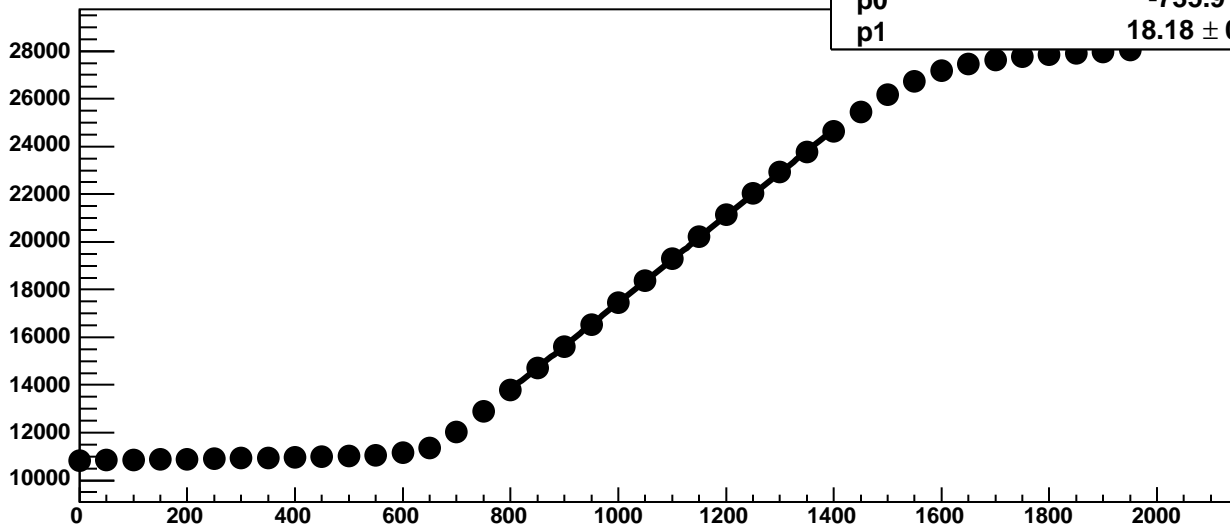
Chip 0, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC



Chip 0, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC

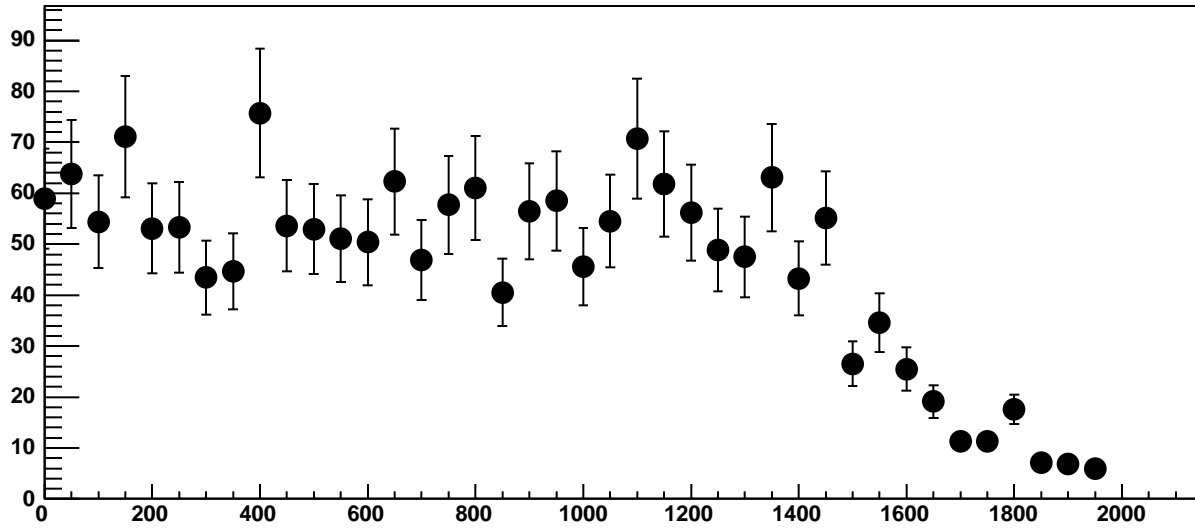


Chip 0, Channel 3, Enable 1!, Hold=30, ADC Mean vs DAC

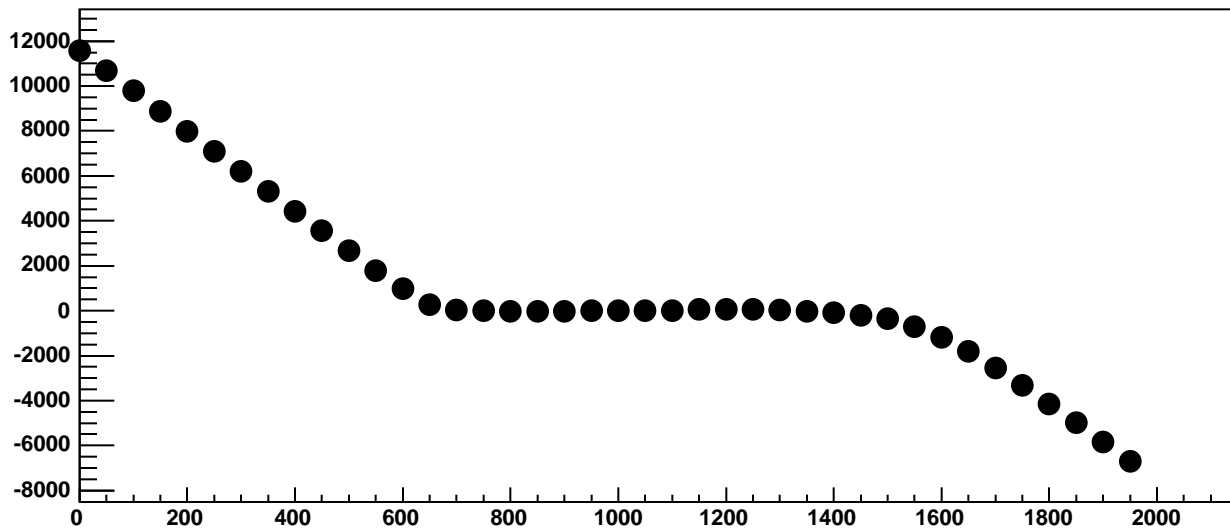


$\chi^2 / \text{ndf}$	128.9 / 11
p0	$-735.9 \pm 18.94$
p1	$18.18 \pm 0.01693$

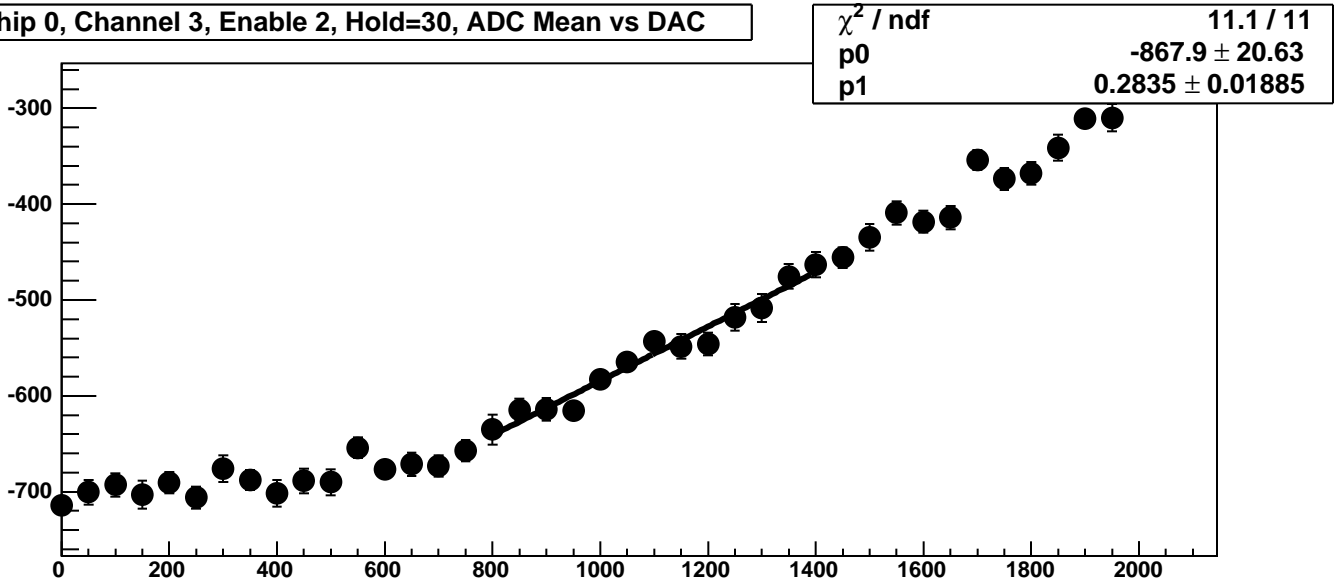
Chip 0, Channel 3, Enable 1!, Hold=30, ADC Noise vs DAC



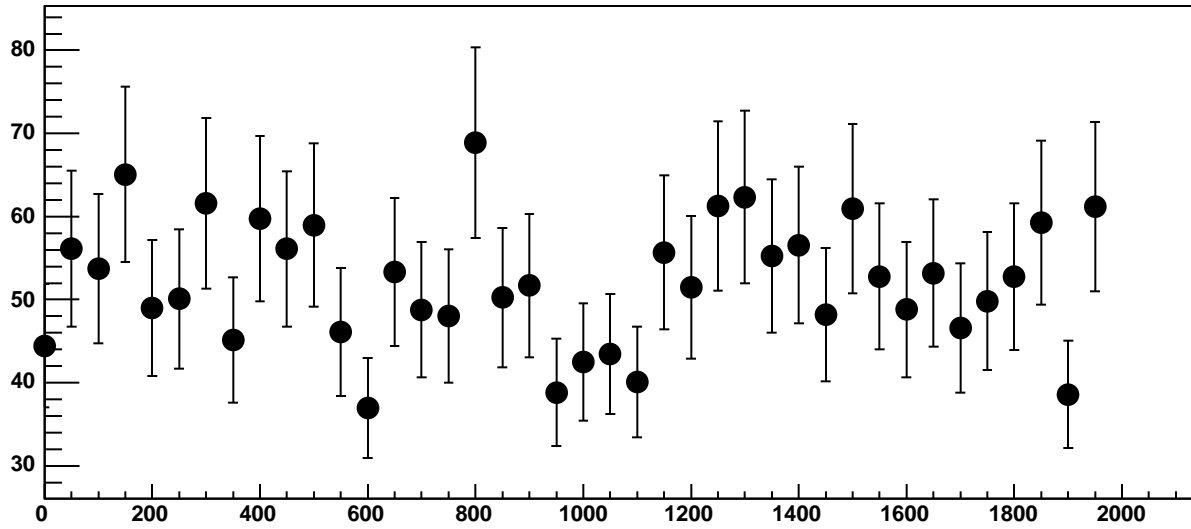
Chip 0, Channel 3, Enable 1!, Hold=30, ADC Residuals vs DAC



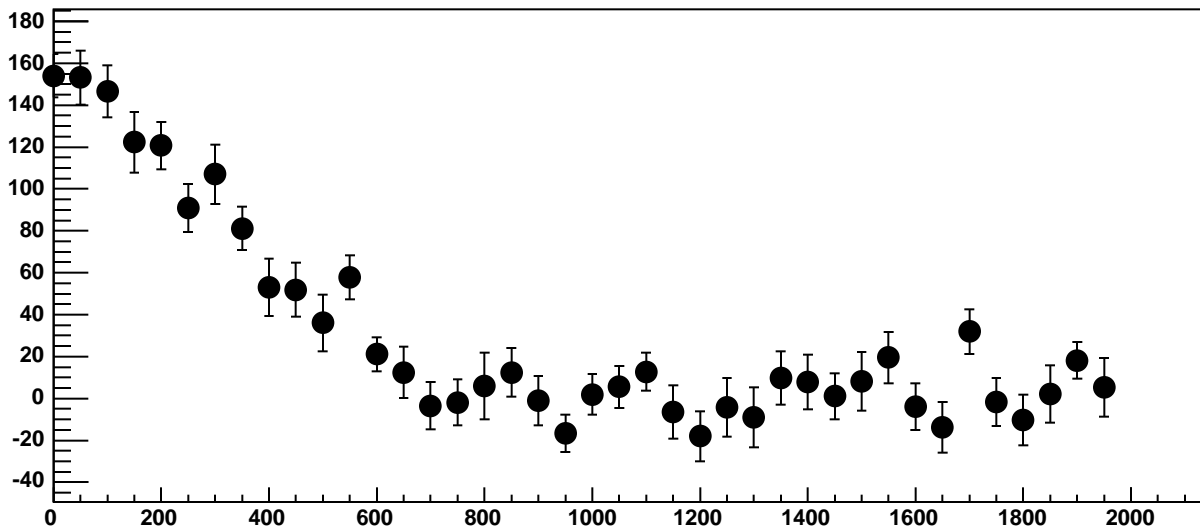
Chip 0, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC



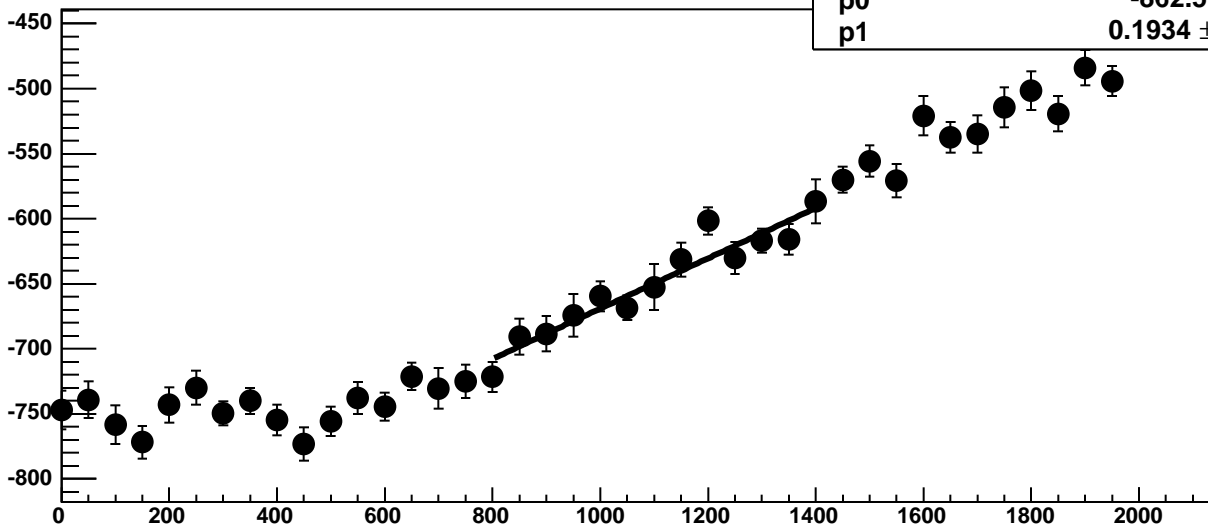
Chip 0, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC



Chip 0, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC

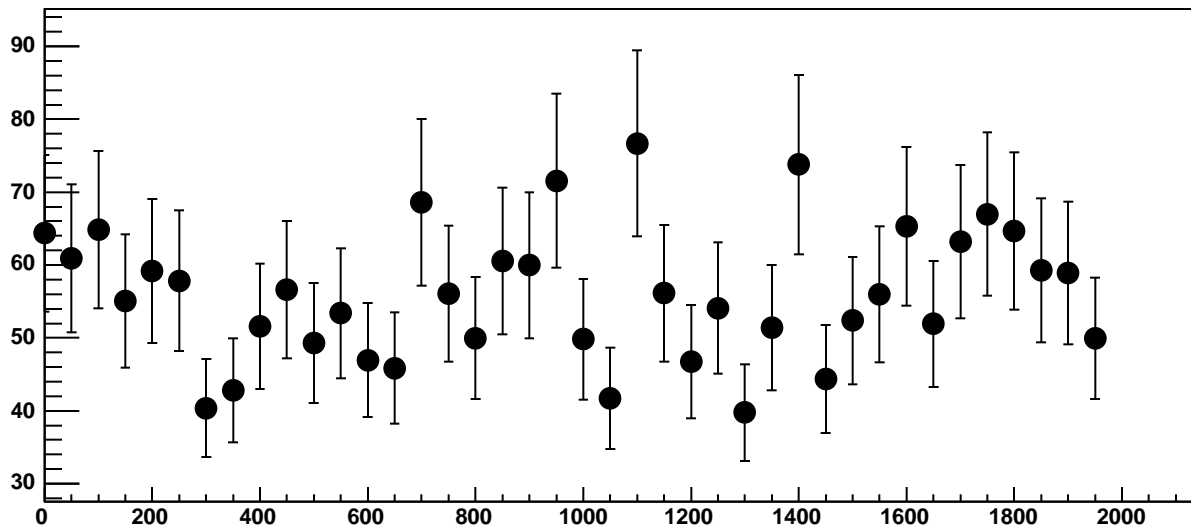


Chip 0, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC

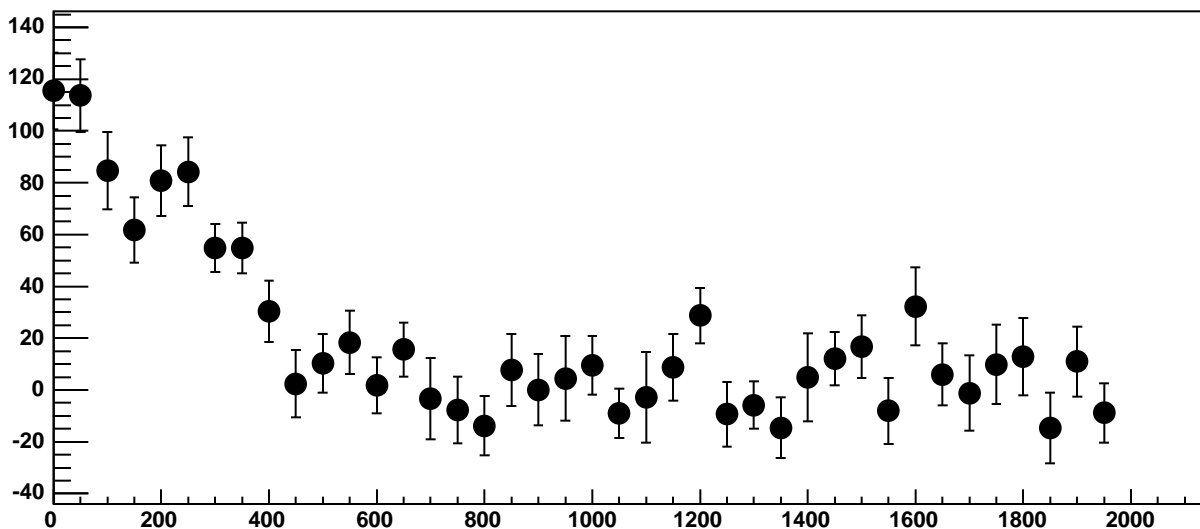


$\chi^2 / \text{ndf}$  13.64 / 11  
p0  $-862.5 \pm 20.93$   
p1  $0.1934 \pm 0.0186$

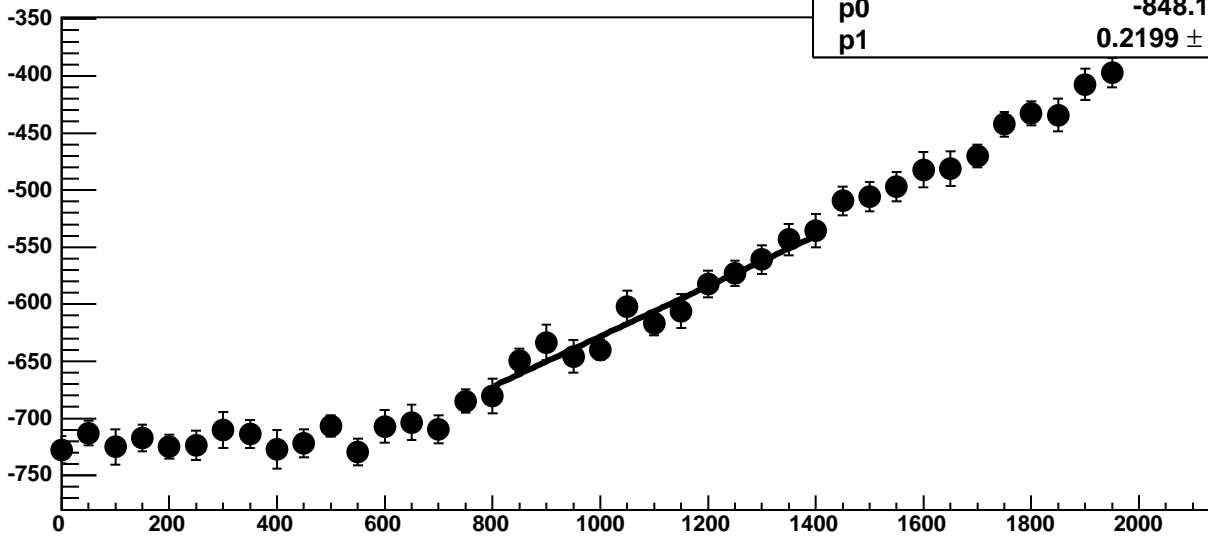
Chip 0, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC



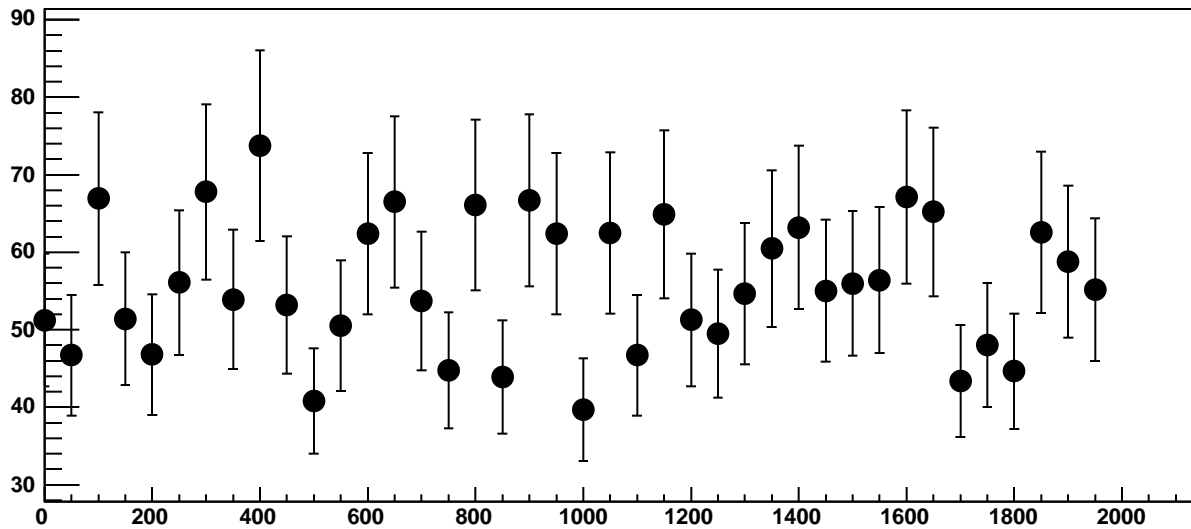
Chip 0, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC



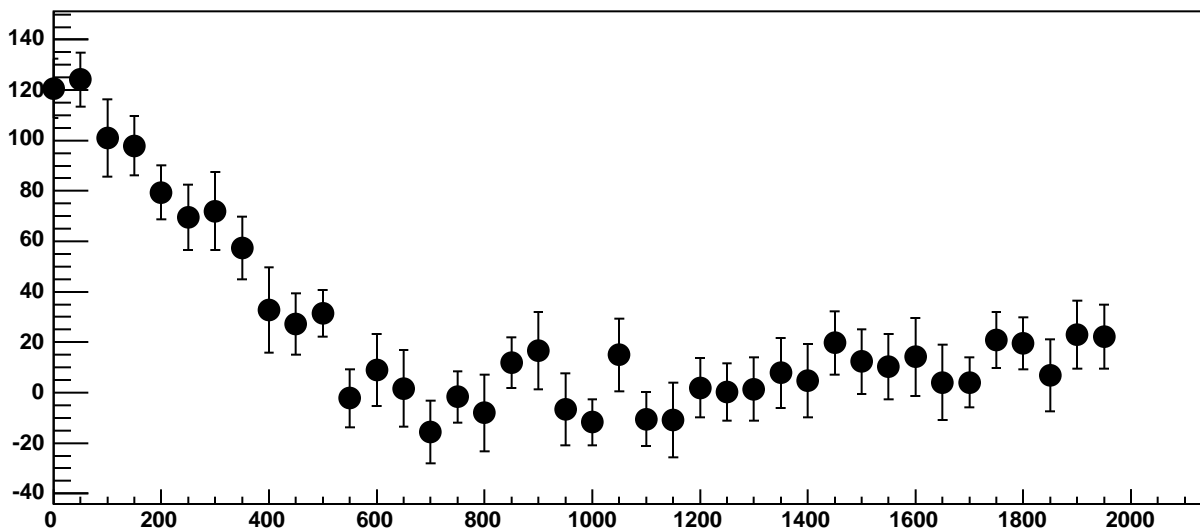
Chip 0, Channel 3, Enable 4, Hold=30, ADC Mean vs DAC



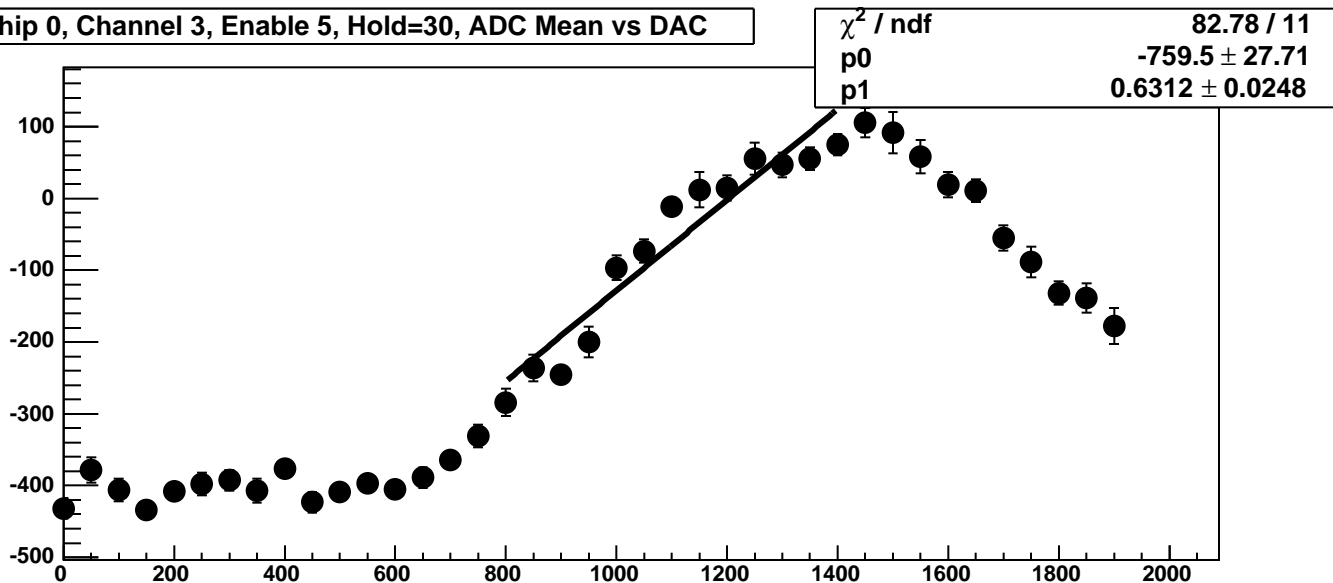
Chip 0, Channel 3, Enable 4, Hold=30, ADC Noise vs DAC



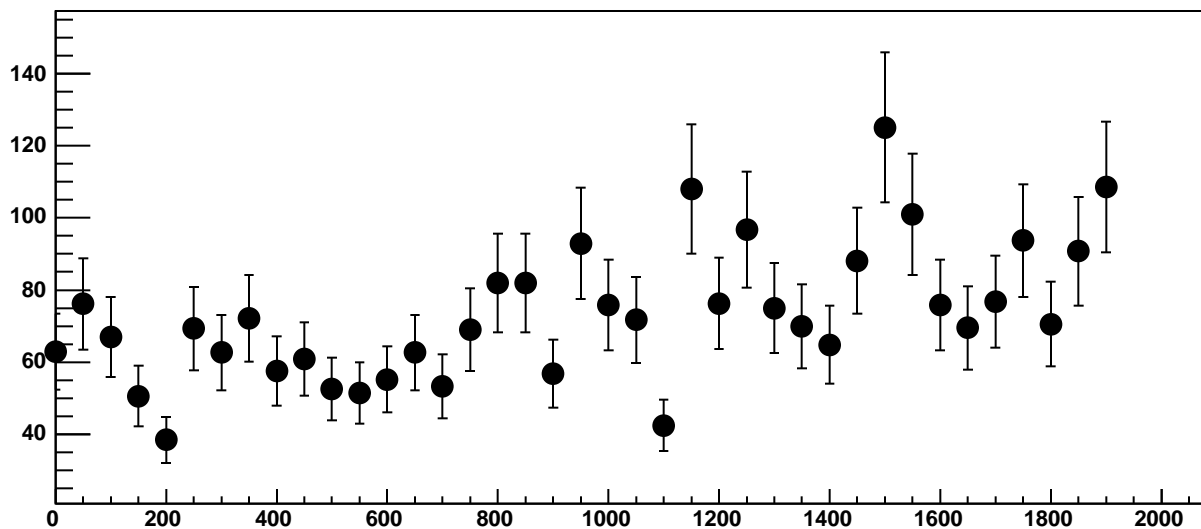
Chip 0, Channel 3, Enable 4, Hold=30, ADC Residuals vs DAC



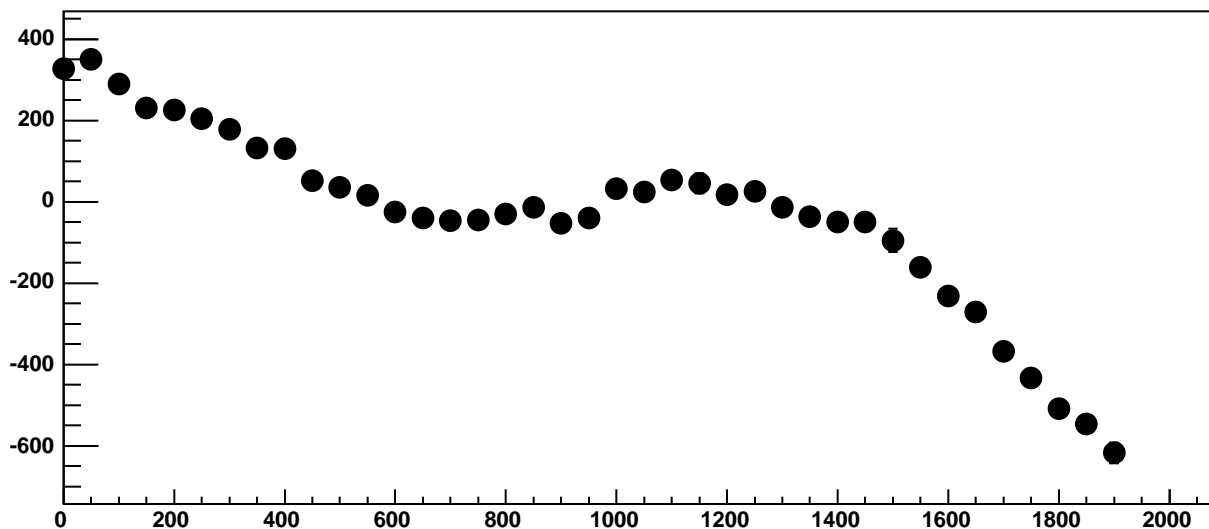
Chip 0, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC



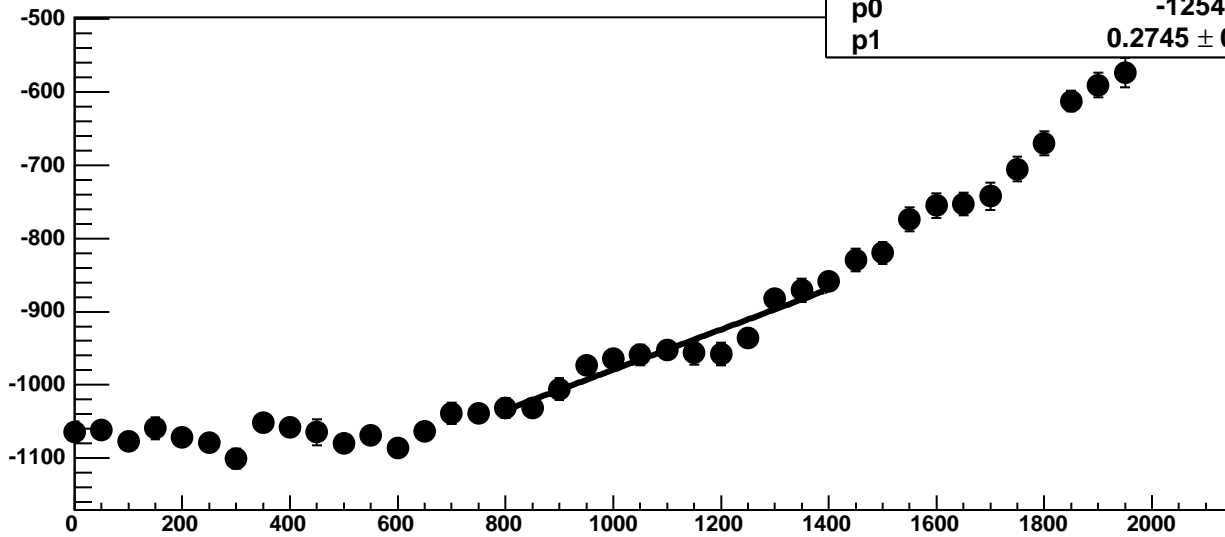
Chip 0, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC



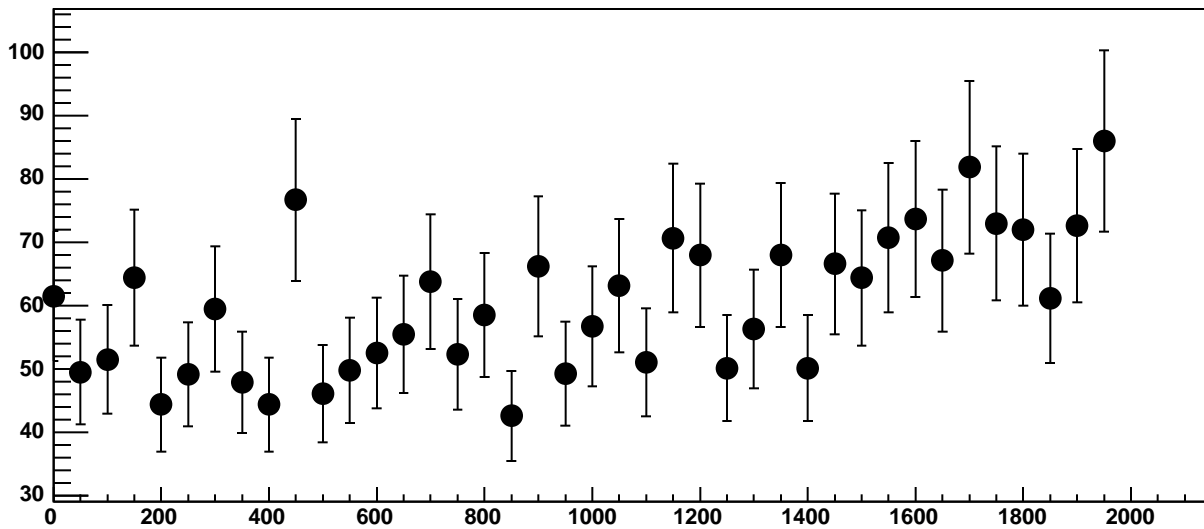
Chip 0, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC



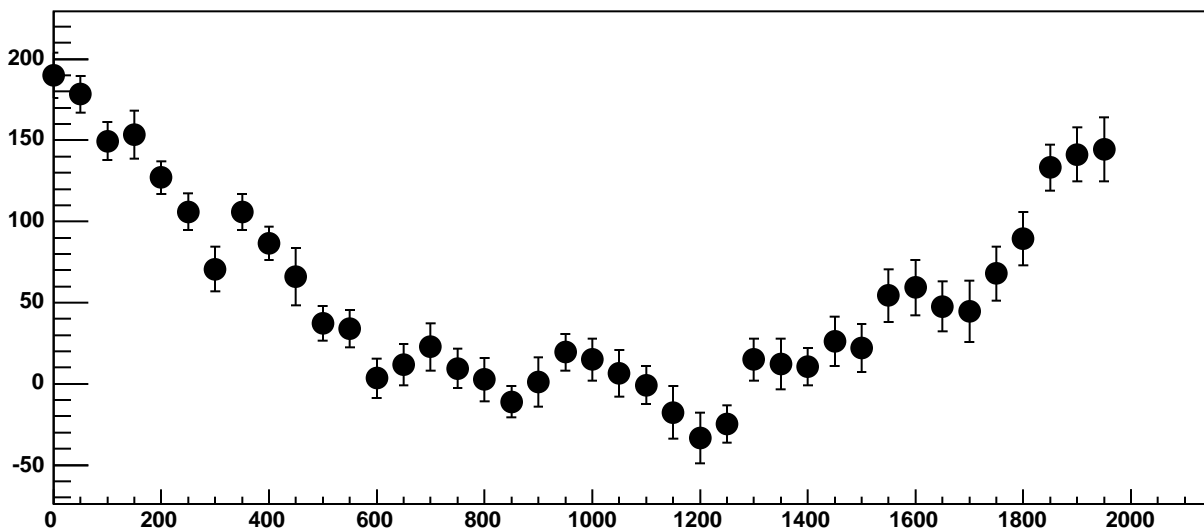
Chip 0, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC



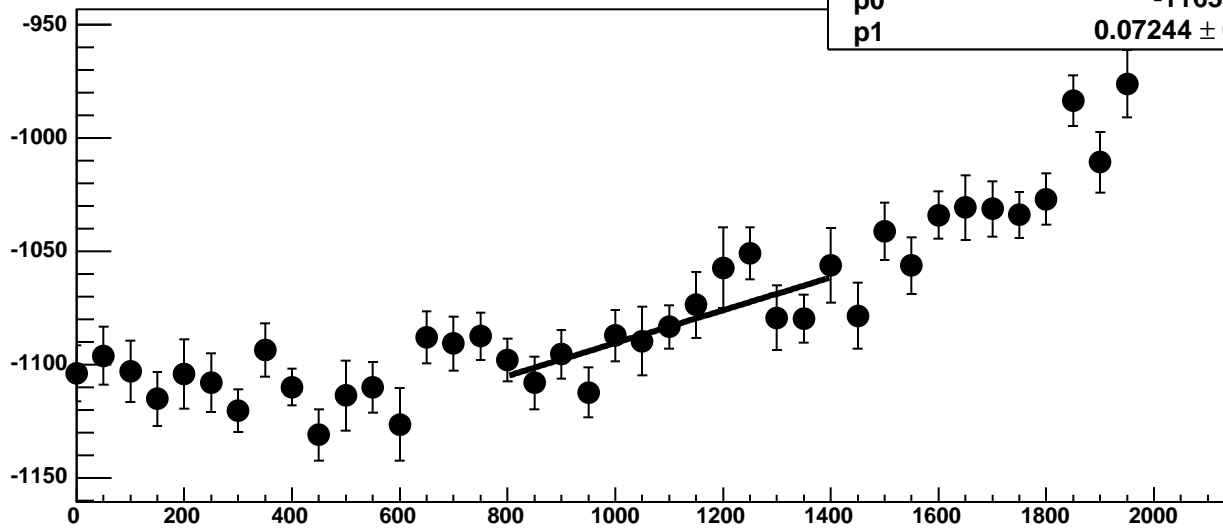
Chip 0, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC



Chip 0, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 0, Channel 4, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

11.14 / 11

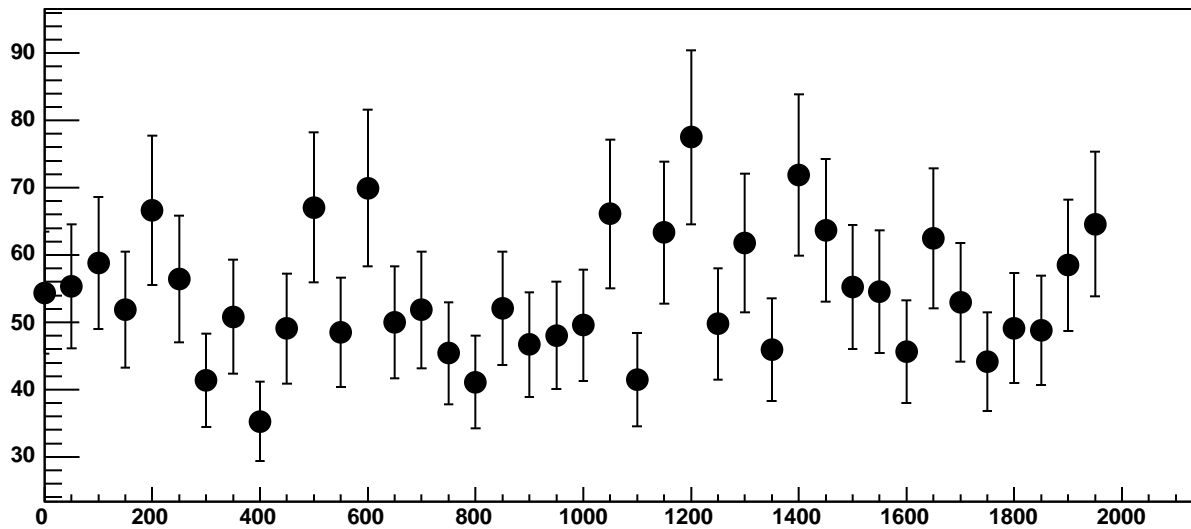
p0

$-1163 \pm 18.86$

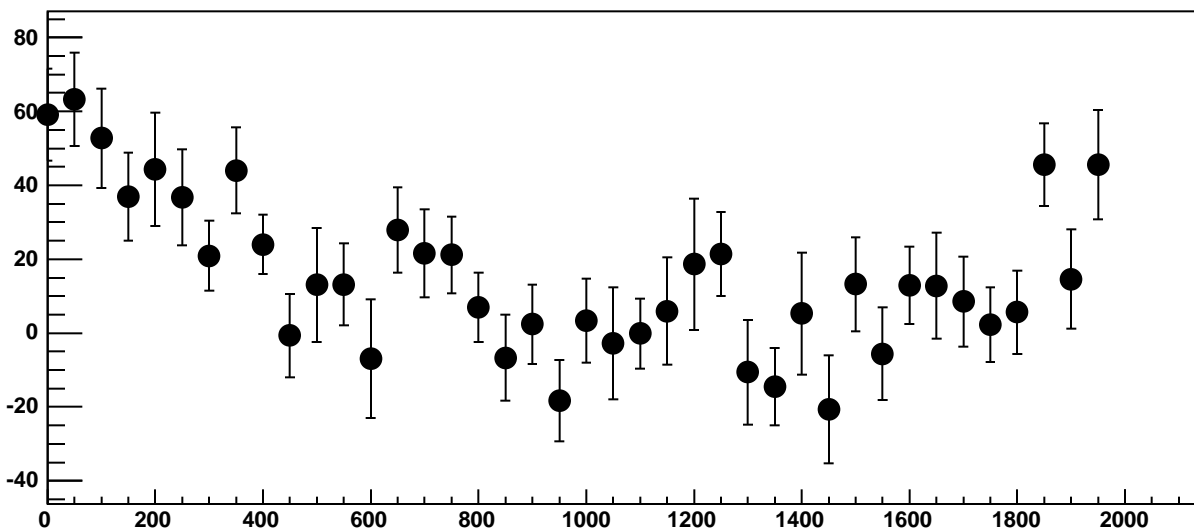
p1

$0.07244 \pm 0.01742$

Chip 0, Channel 4, Enable 1, Hold=30, ADC Noise vs DAC

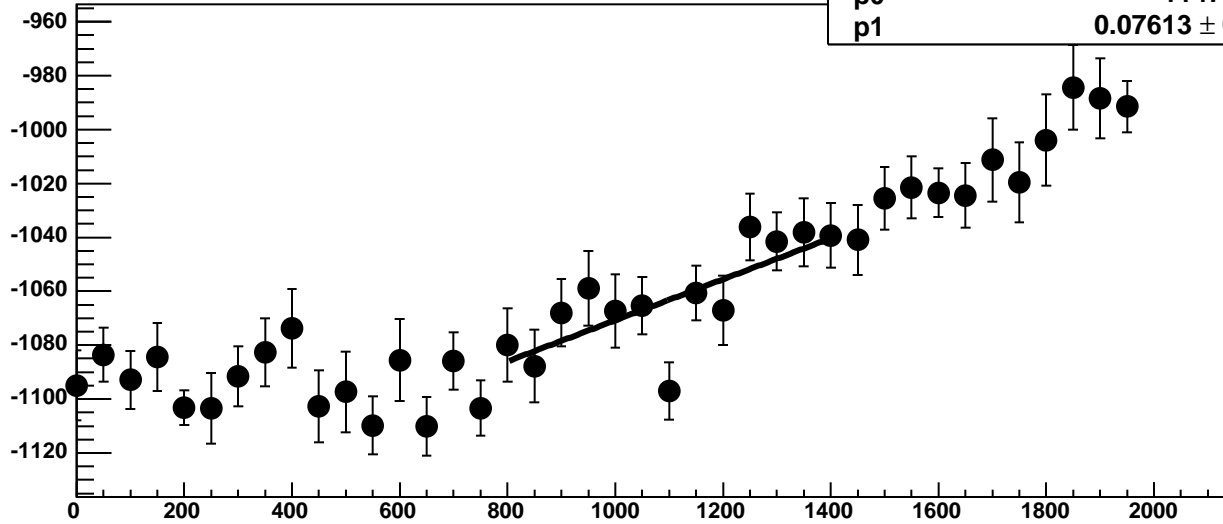


Chip 0, Channel 4, Enable 1, Hold=30, ADC Residuals vs DAC

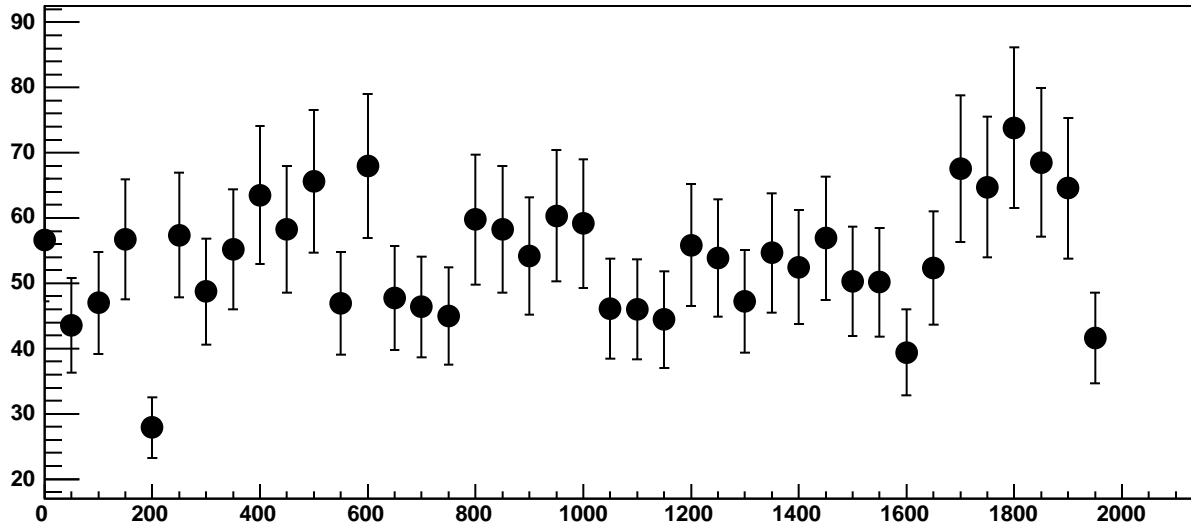




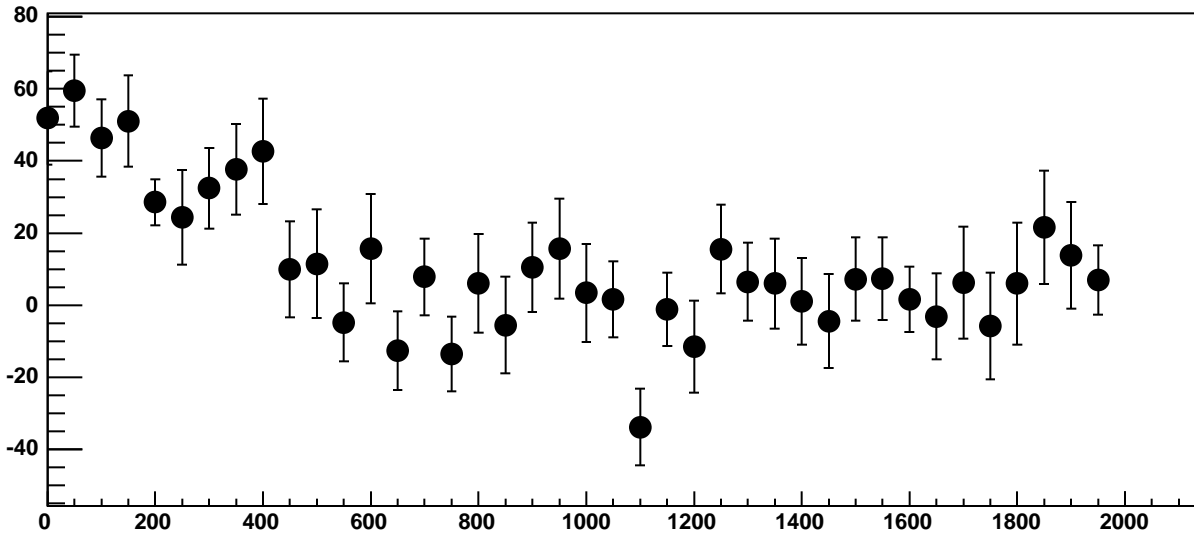
Chip 0, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC



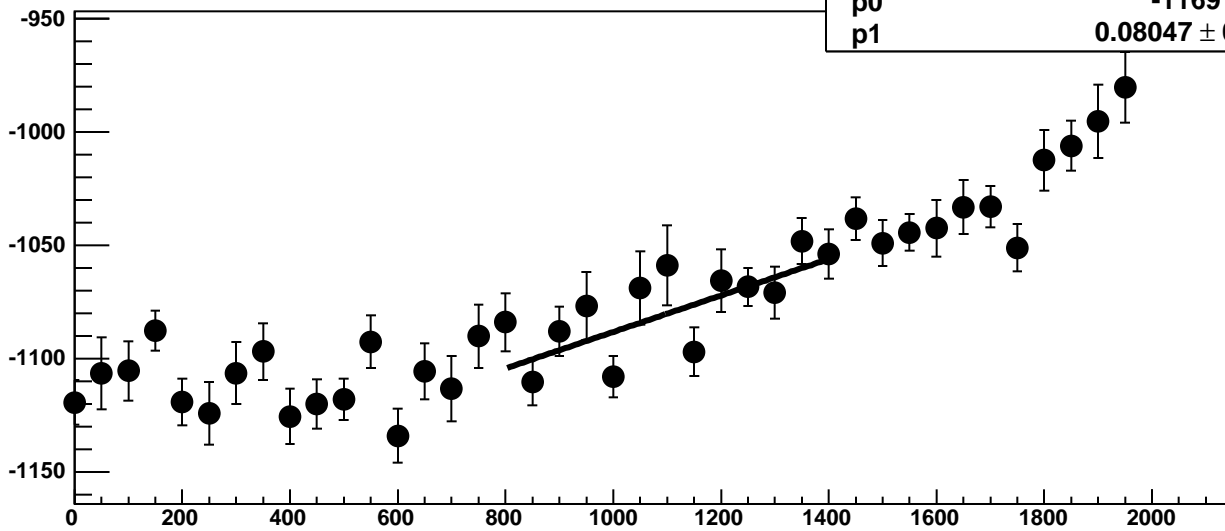
Chip 0, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC



Chip 0, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 0, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

17.87 / 11

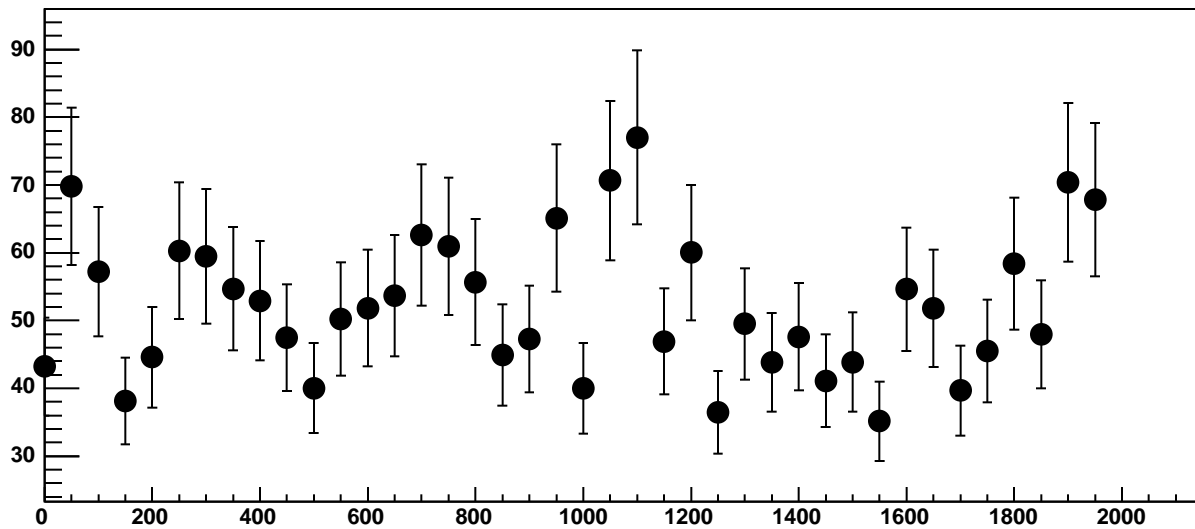
p0

$-1169 \pm 18.48$

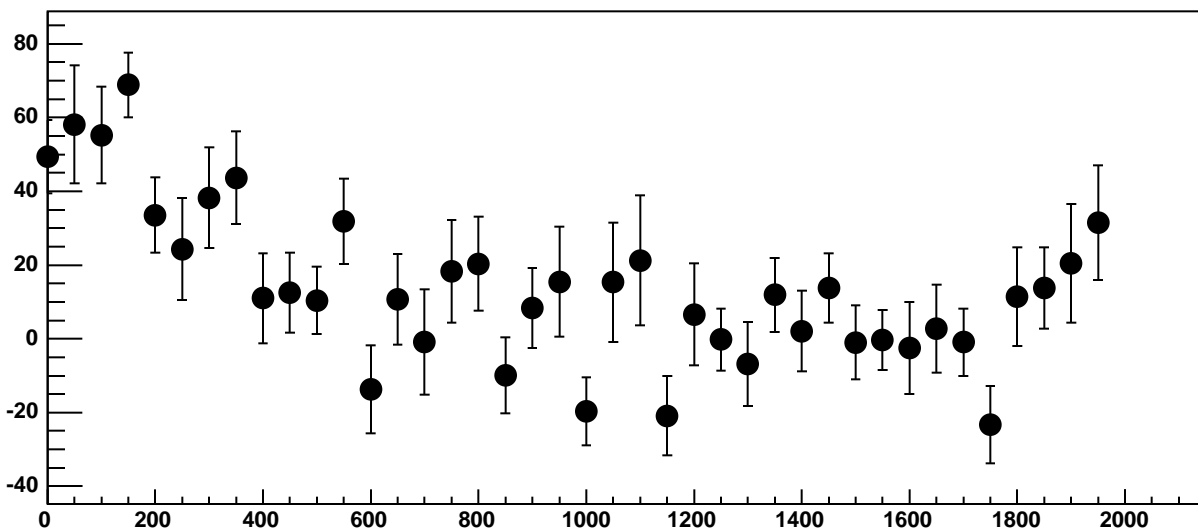
p1

$0.08047 \pm 0.01631$

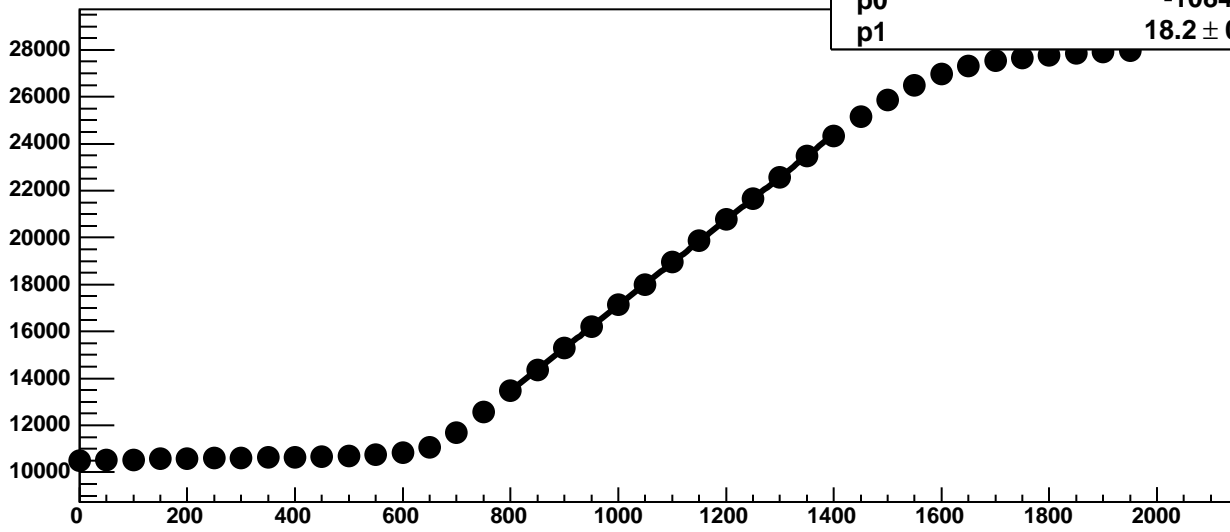
Chip 0, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC



Chip 0, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC

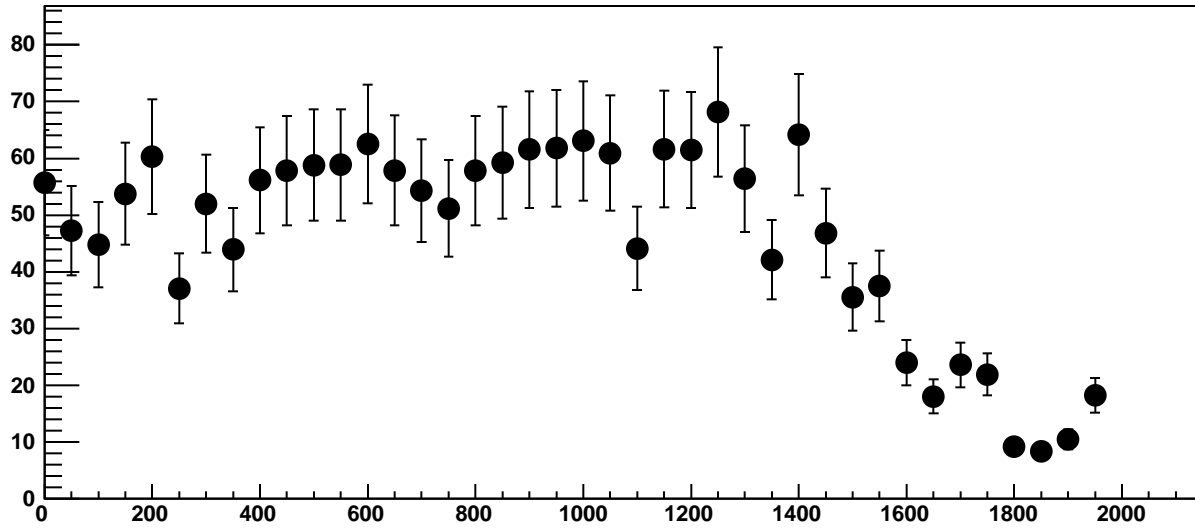


Chip 0, Channel 4, Enable 4!, Hold=30, ADC Mean vs DAC

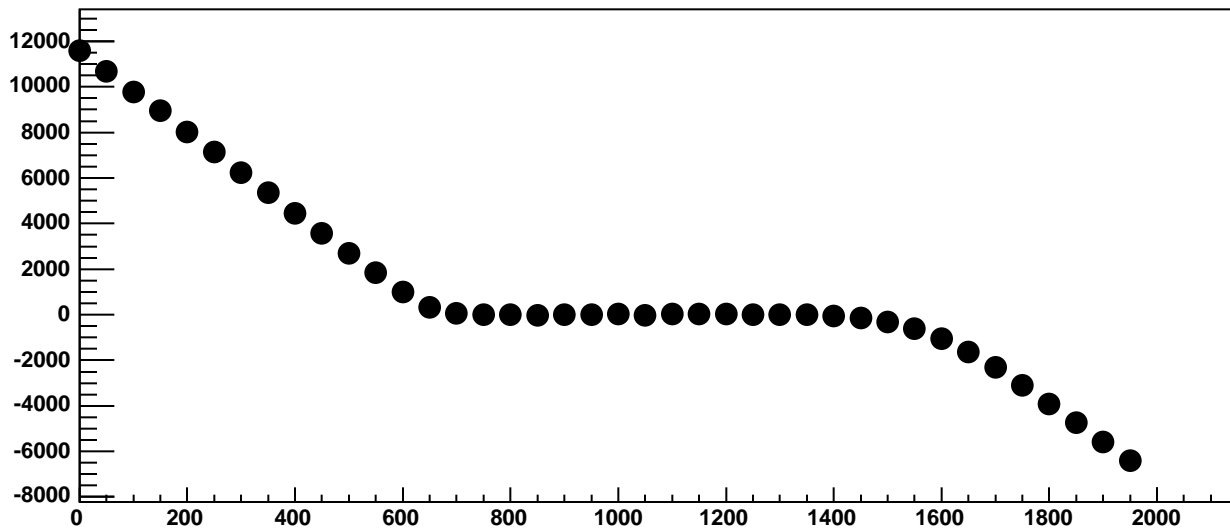


$\chi^2 / \text{ndf}$  43.13 / 11  
p0  $-1084 \pm 21.8$   
p1  $18.2 \pm 0.01929$

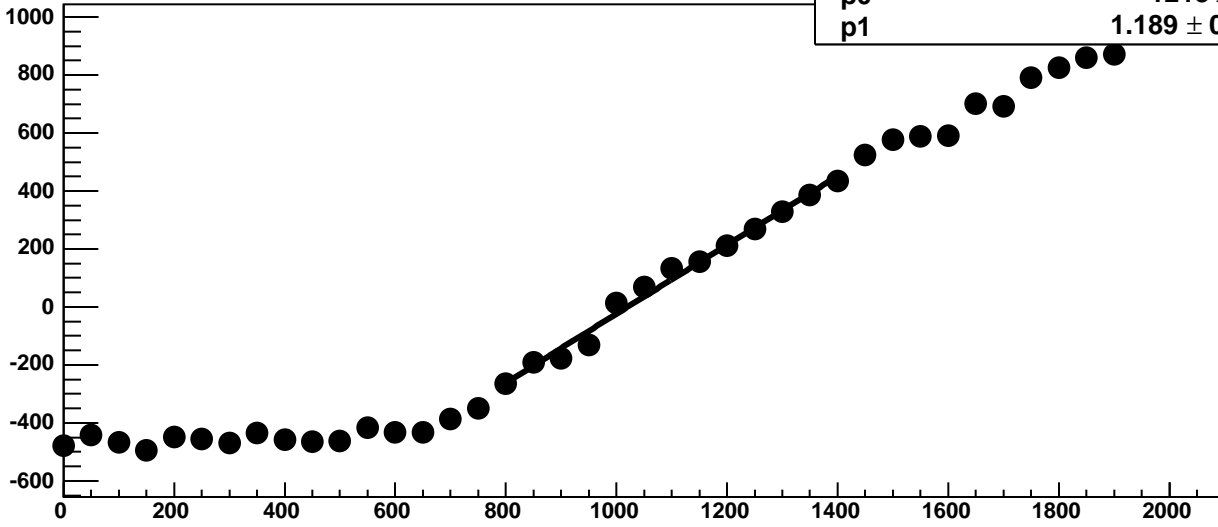
Chip 0, Channel 4, Enable 4!, Hold=30, ADC Noise vs DAC



Chip 0, Channel 4, Enable 4!, Hold=30, ADC Residuals vs DAC

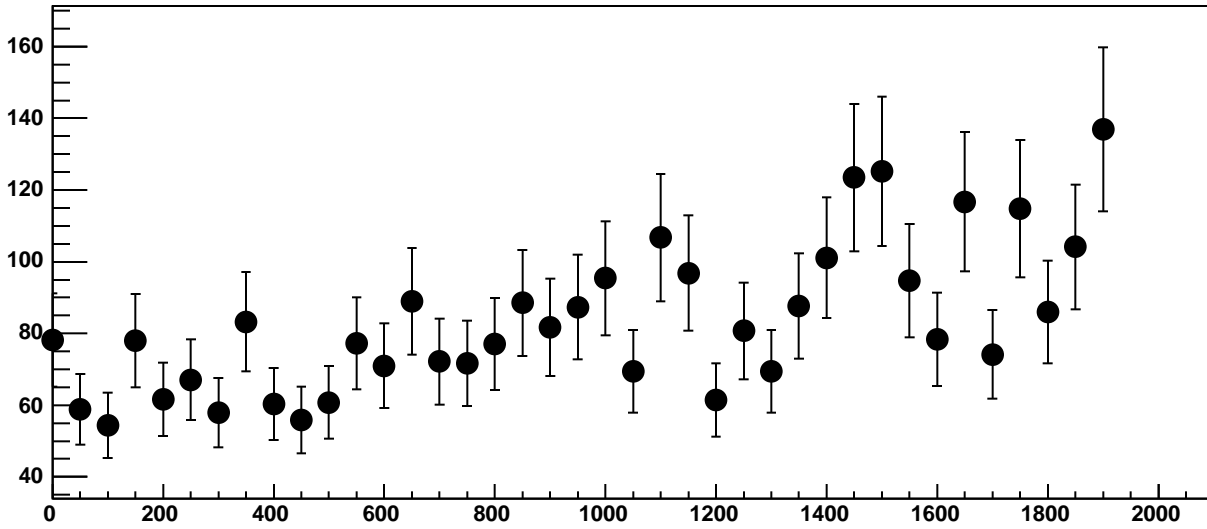


Chip 0, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC

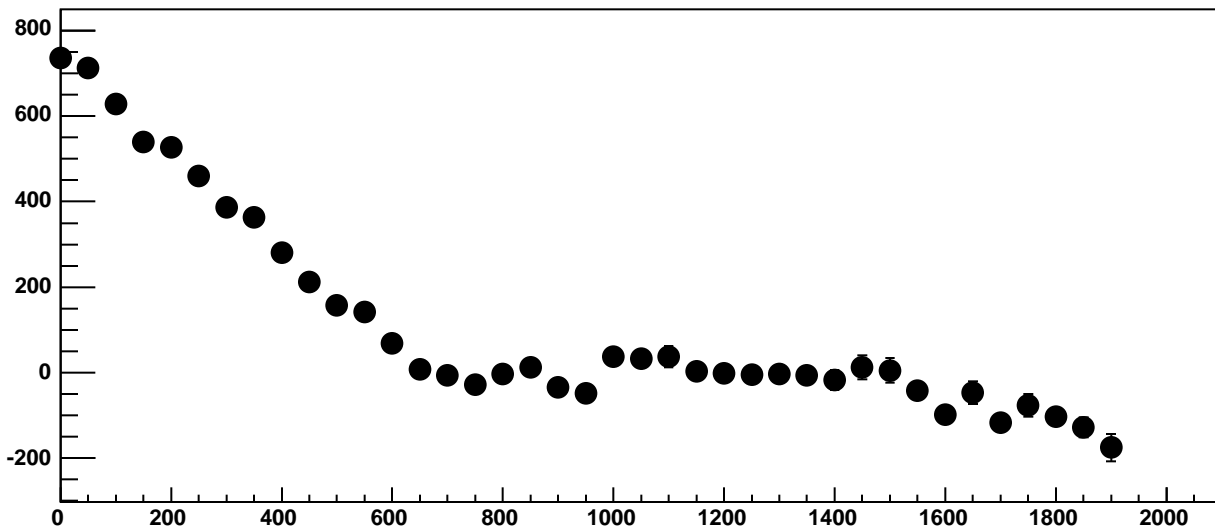


$\chi^2 / \text{ndf}$  20.08 / 11  
p0  $-1213 \pm 31.73$   
p1  $1.189 \pm 0.02839$

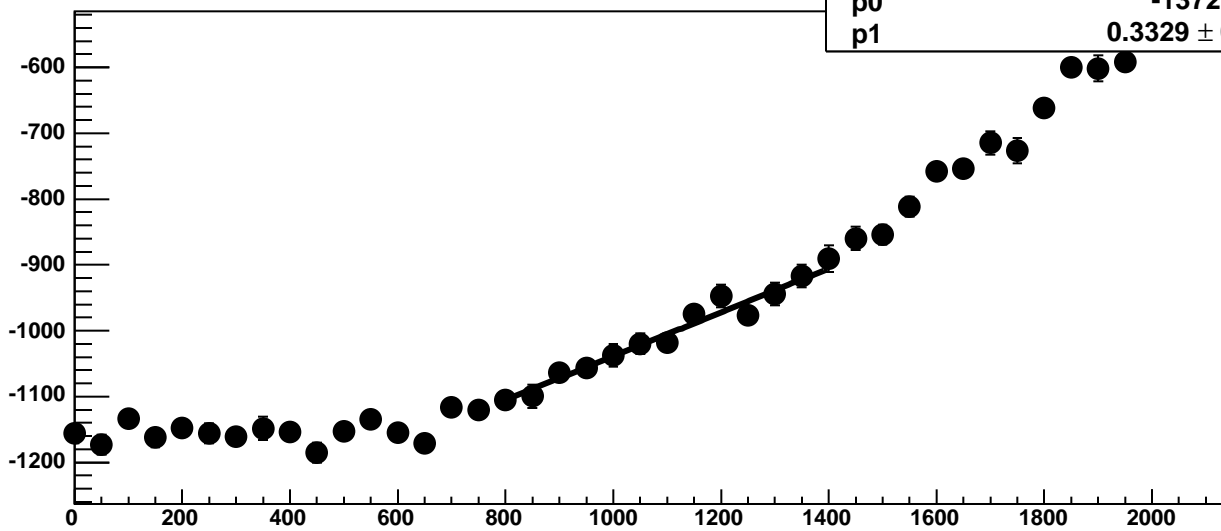
Chip 0, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC



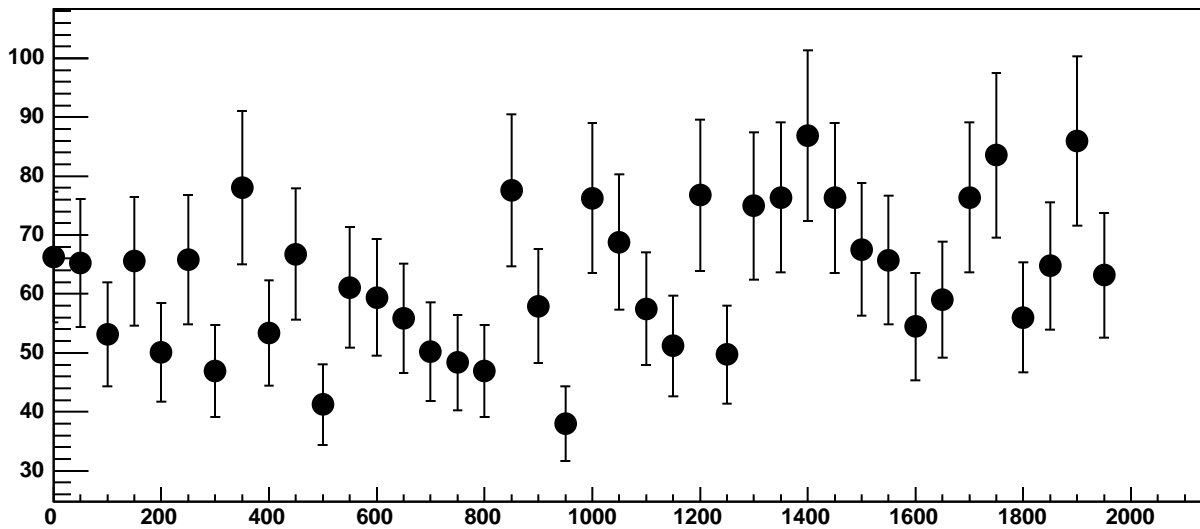
Chip 0, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC



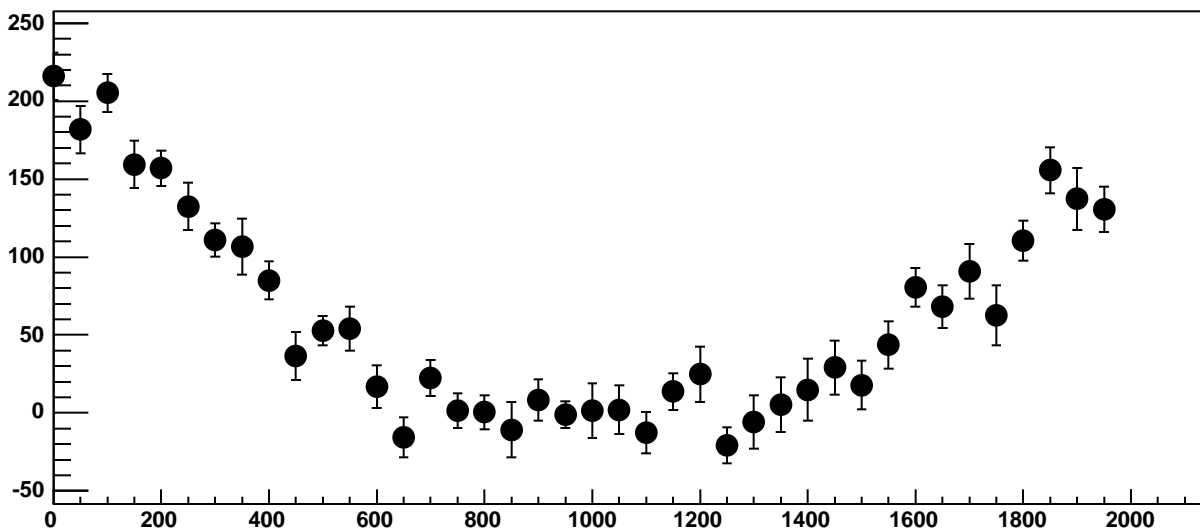
Chip 0, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC



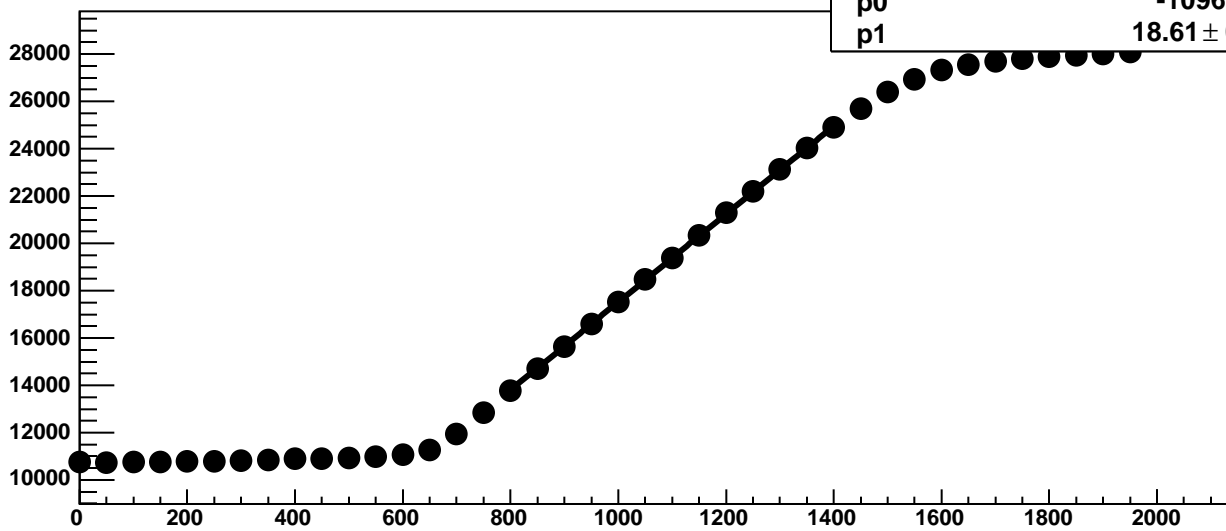
Chip 0, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC



Chip 0, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC

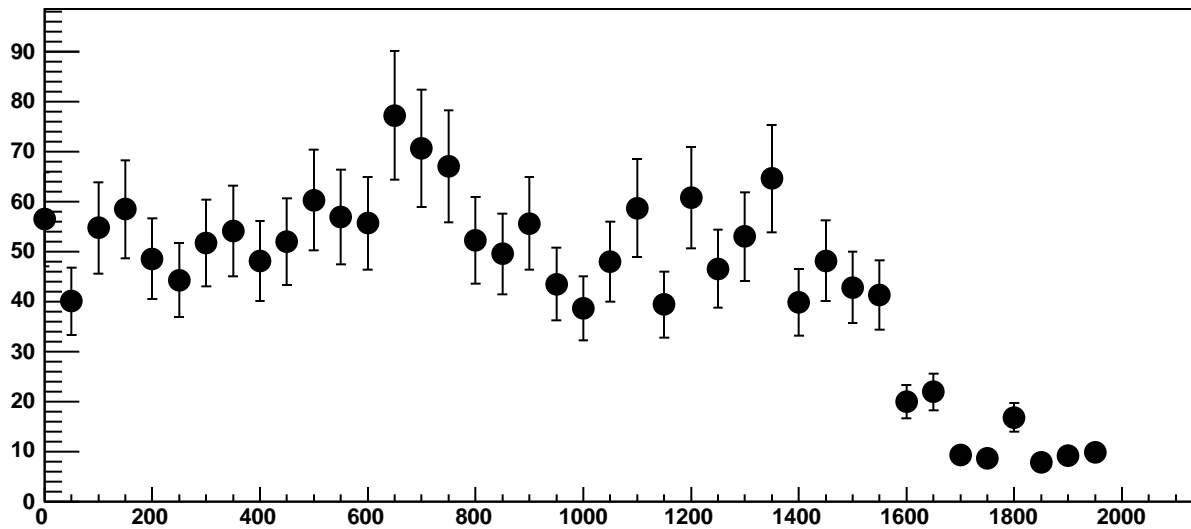


Chip 0, Channel 5, Enable 1!, Hold=30, ADC Mean vs DAC

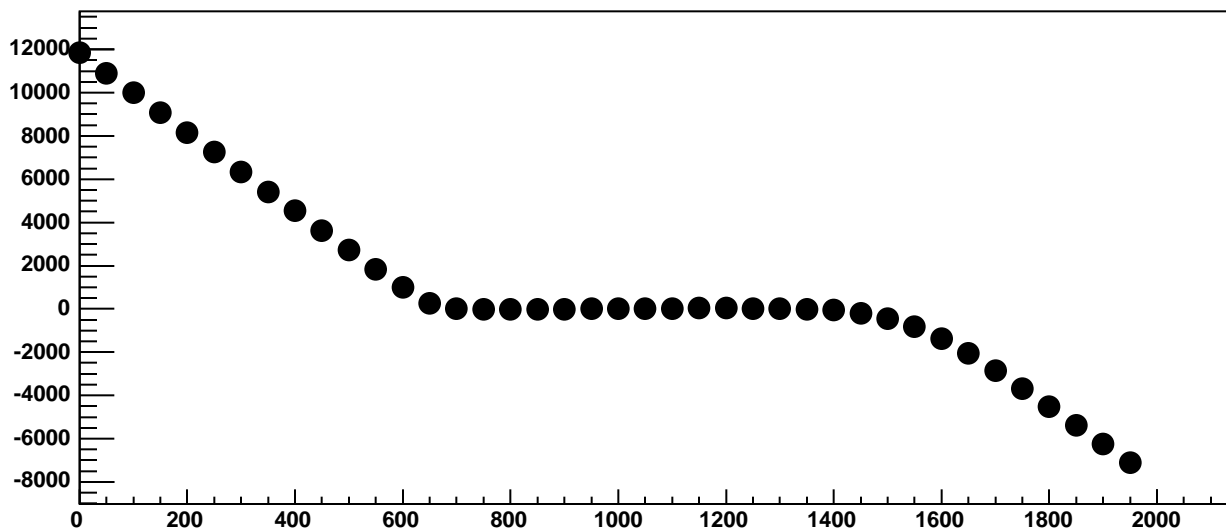


$\chi^2 / \text{ndf}$  88.83 / 11  
p0  $-1096 \pm 18.45$   
p1  $18.61 \pm 0.01654$

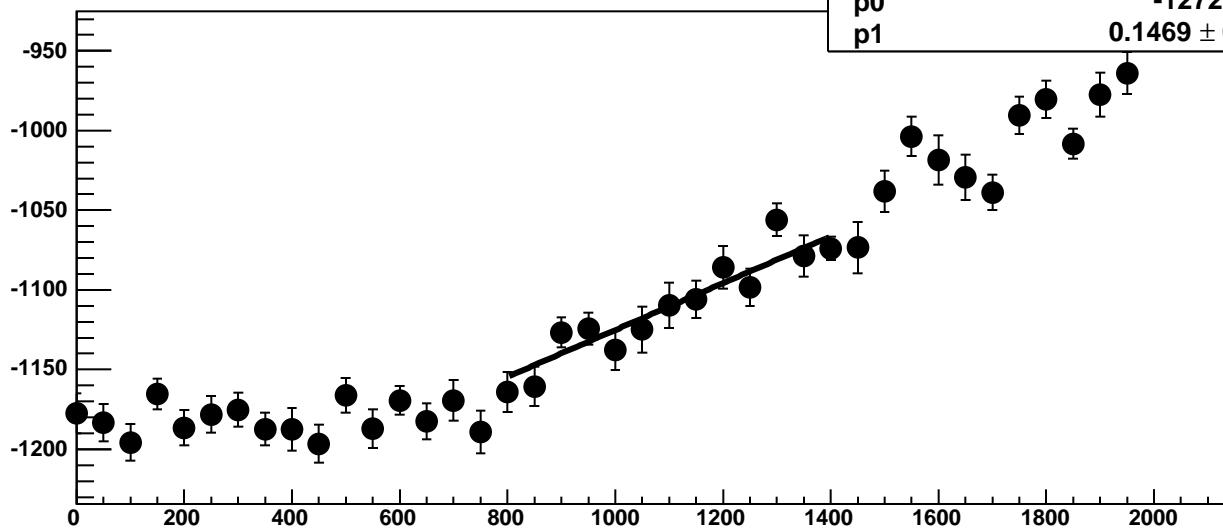
Chip 0, Channel 5, Enable 1!, Hold=30, ADC Noise vs DAC



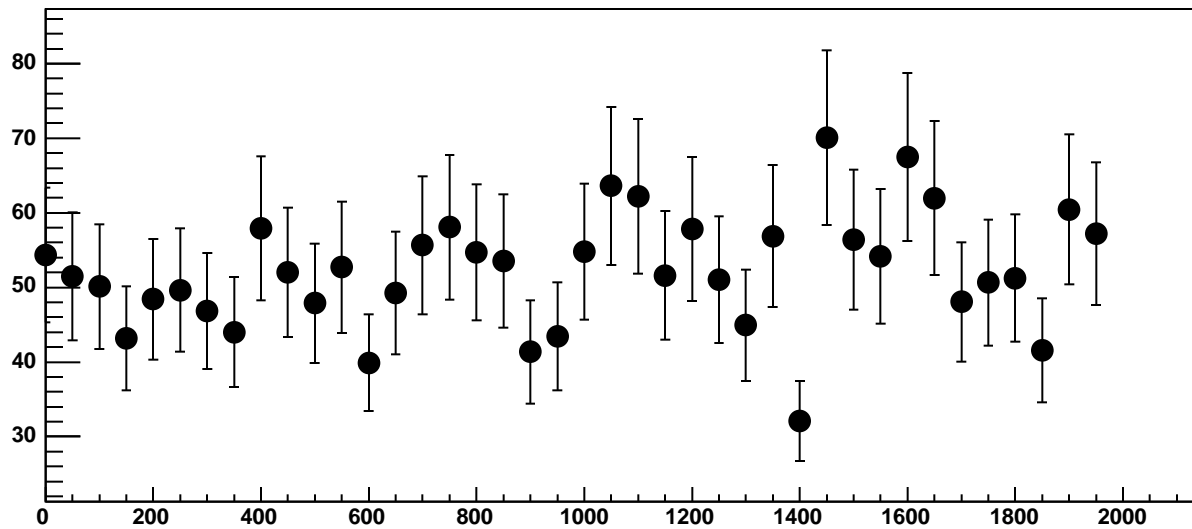
Chip 0, Channel 5, Enable 1!, Hold=30, ADC Residuals vs DAC



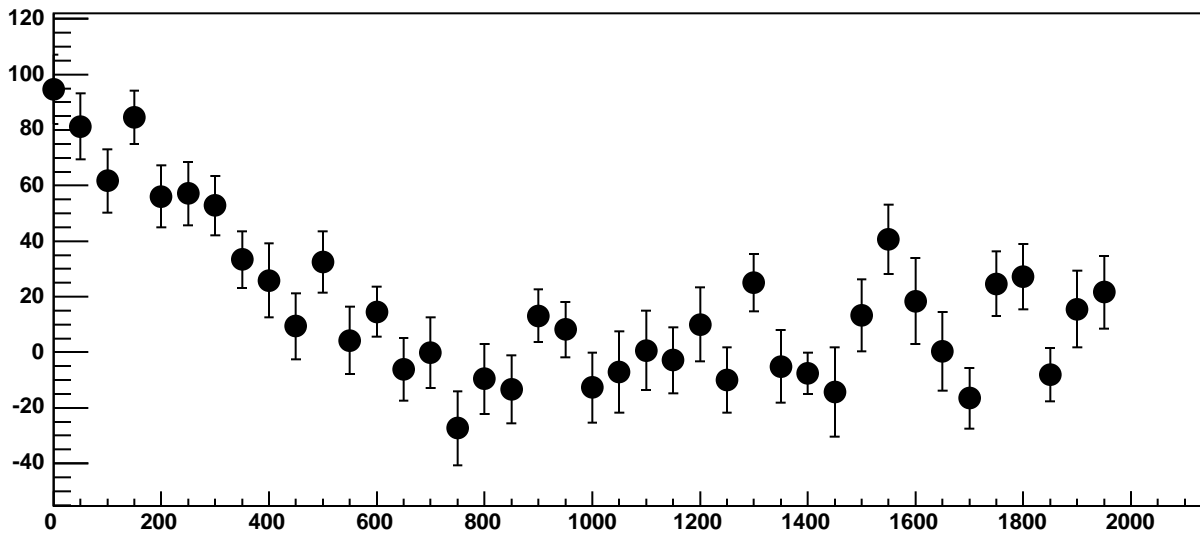
Chip 0, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC



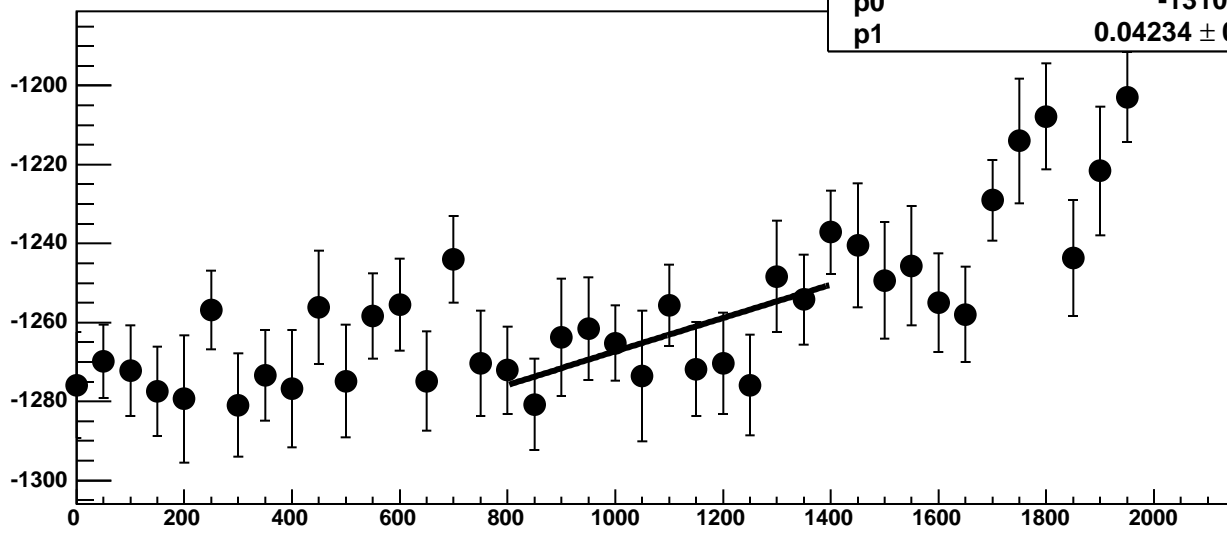
Chip 0, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC



Chip 0, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC

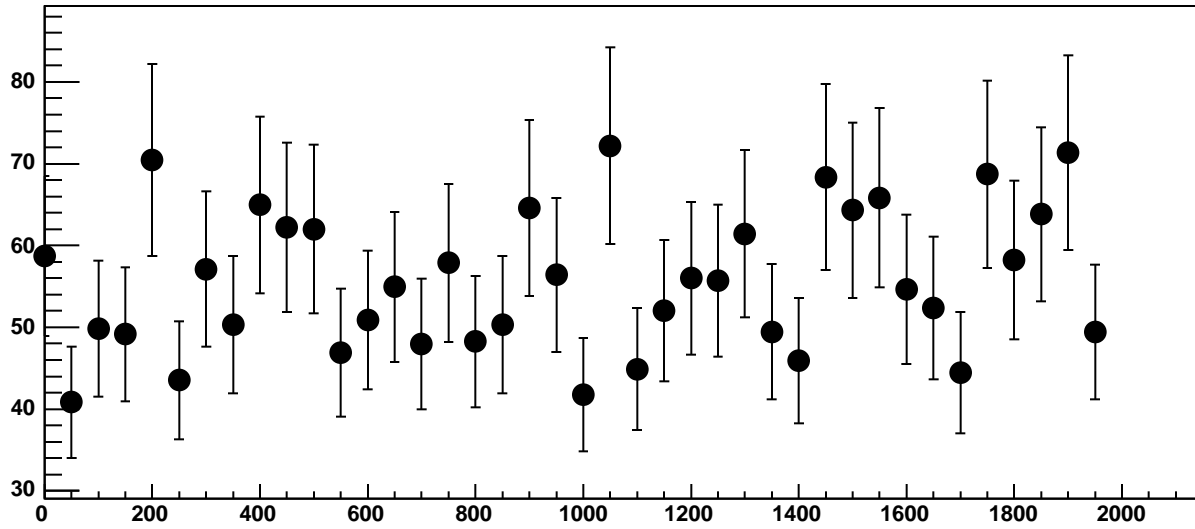


Chip 0, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC

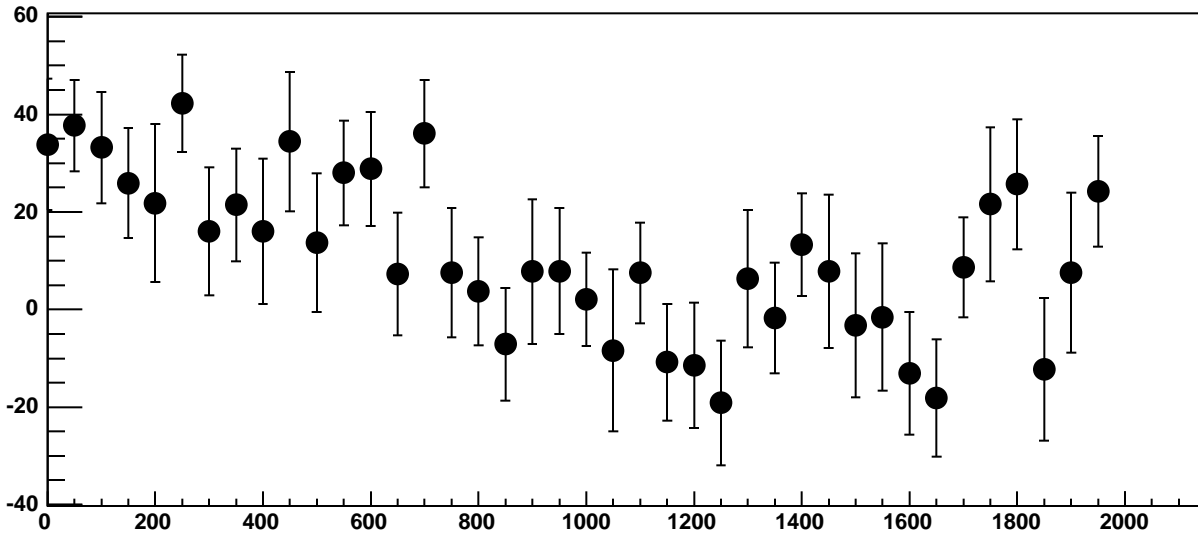


$\chi^2 / \text{ndf}$  7.638 / 11  
p0 -1310 ± 19.31  
p1 0.04234 ± 0.01727

Chip 0, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC

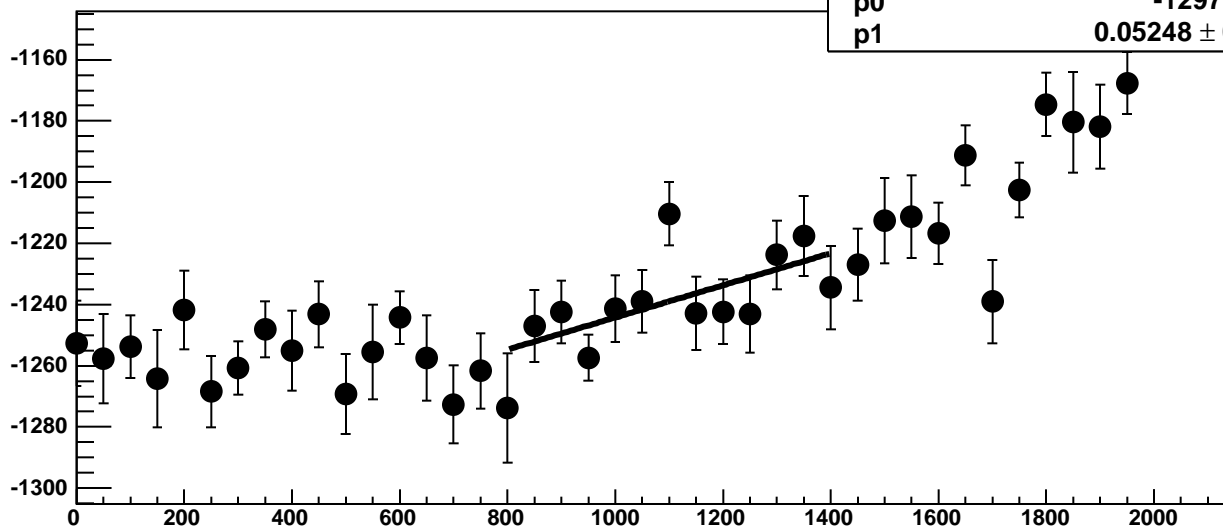


Chip 0, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC

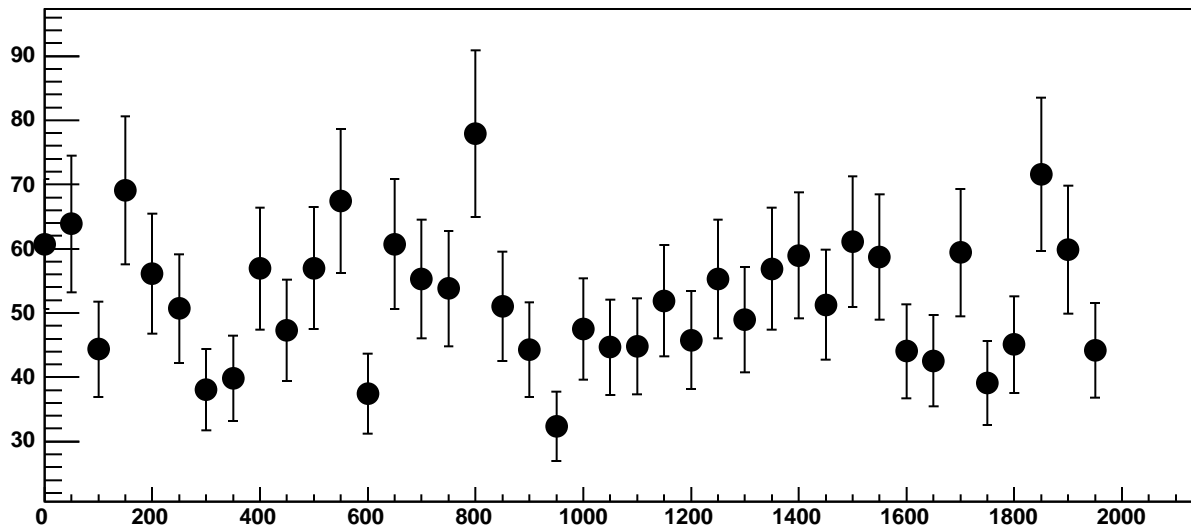




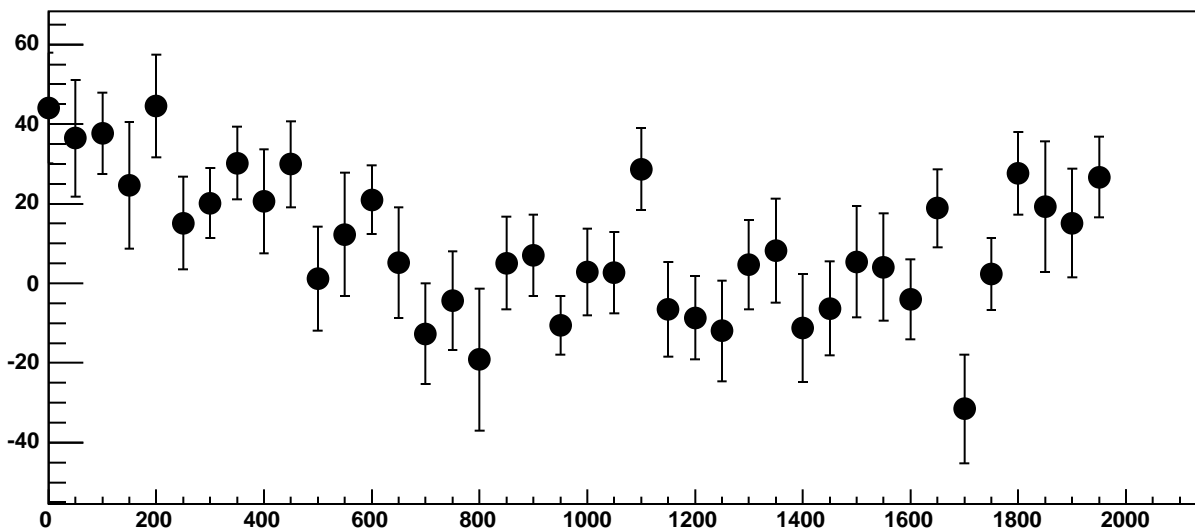
Chip 0, Channel 5, Enable 4, Hold=30, ADC Mean vs DAC



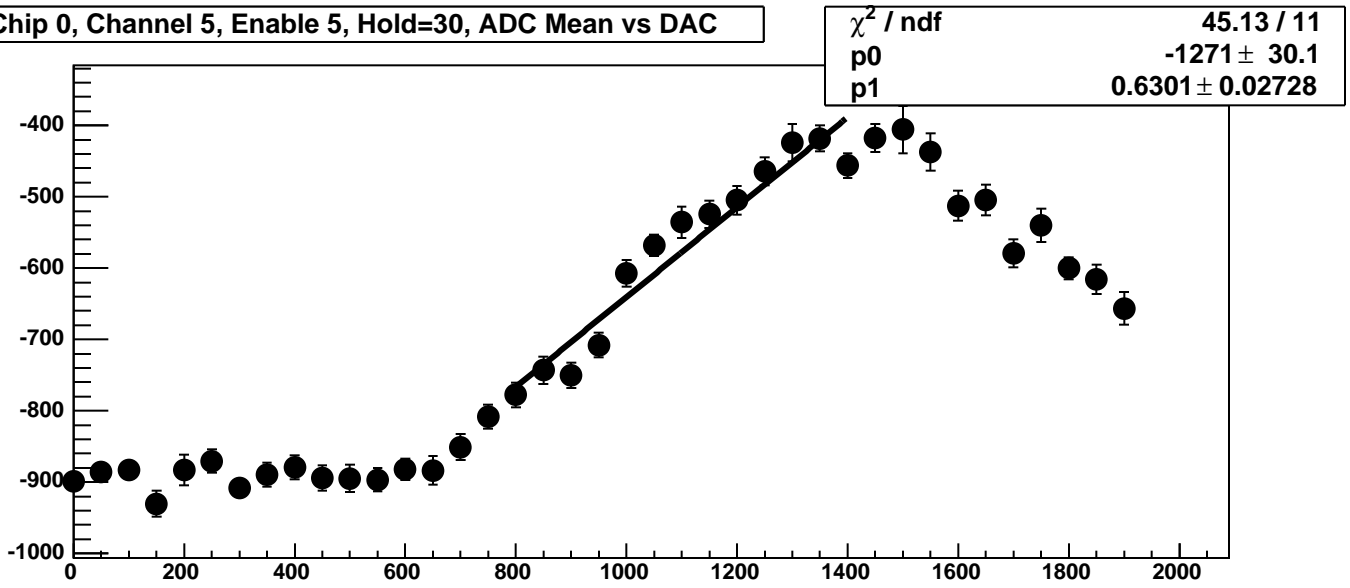
Chip 0, Channel 5, Enable 4, Hold=30, ADC Noise vs DAC



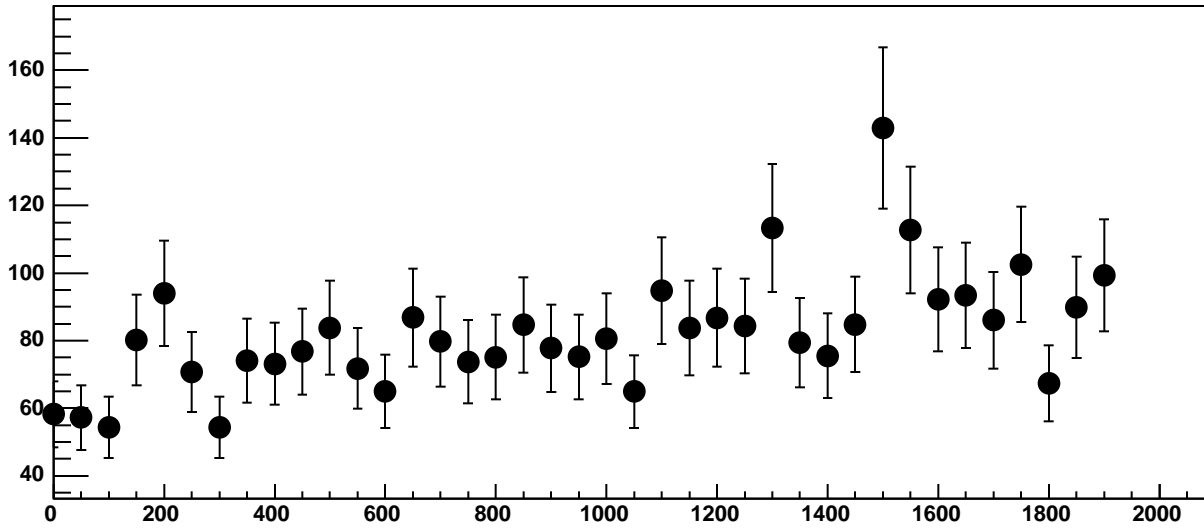
Chip 0, Channel 5, Enable 4, Hold=30, ADC Residuals vs DAC



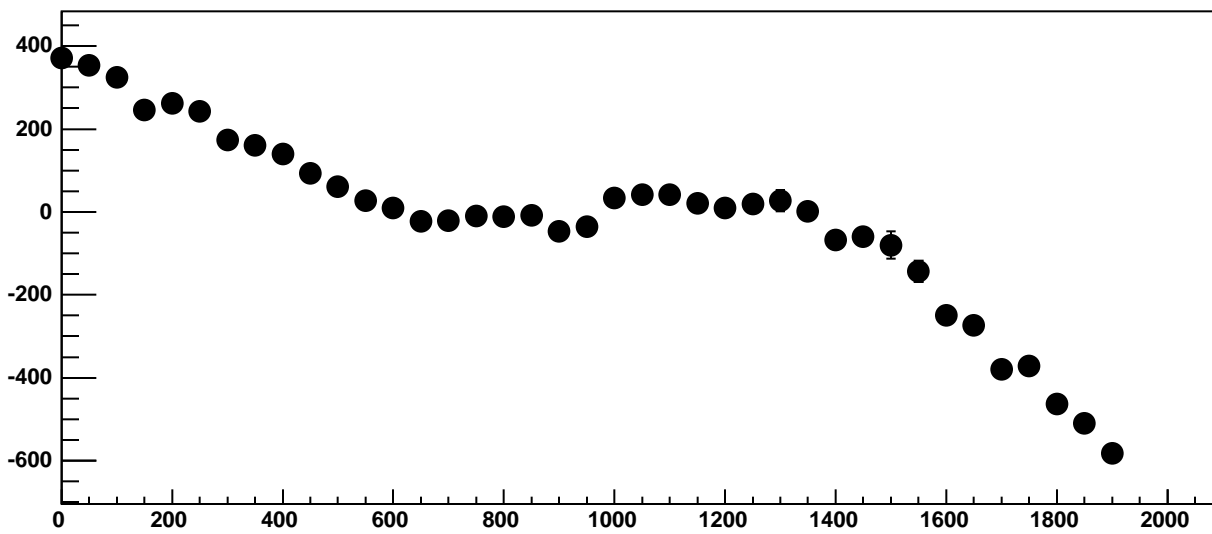
Chip 0, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC



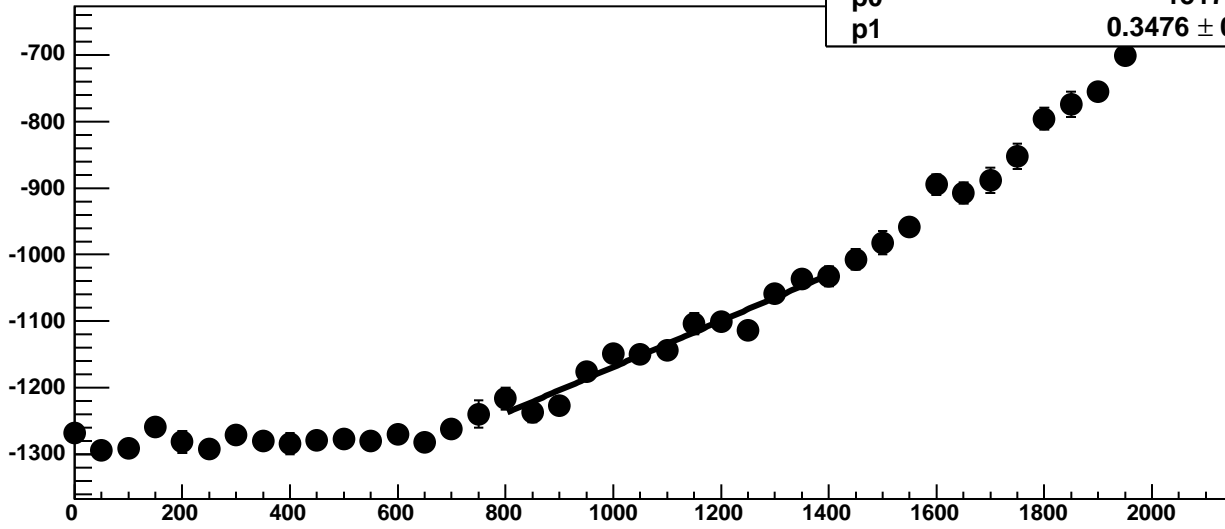
Chip 0, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC



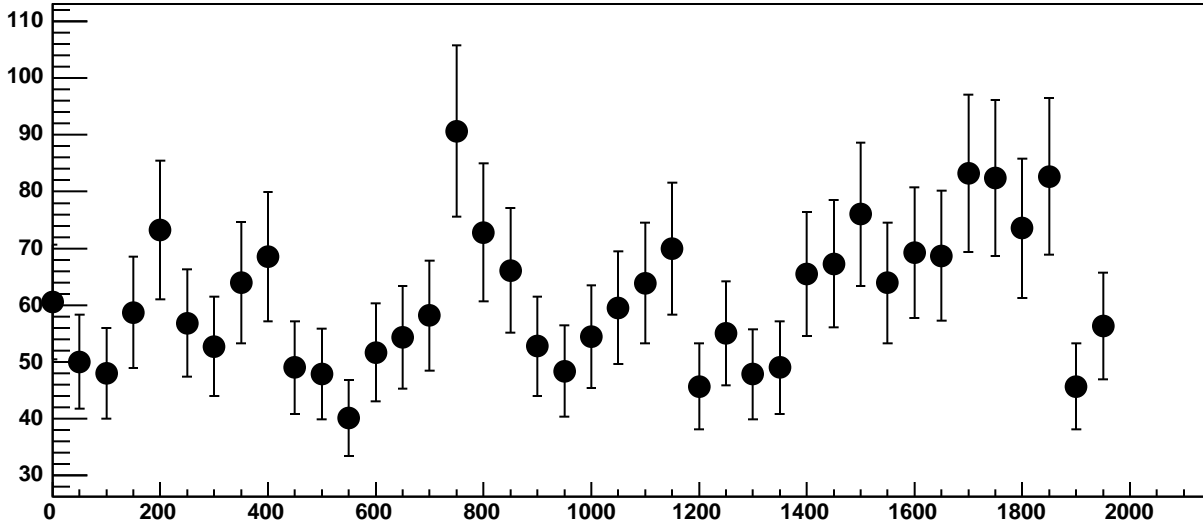
Chip 0, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC



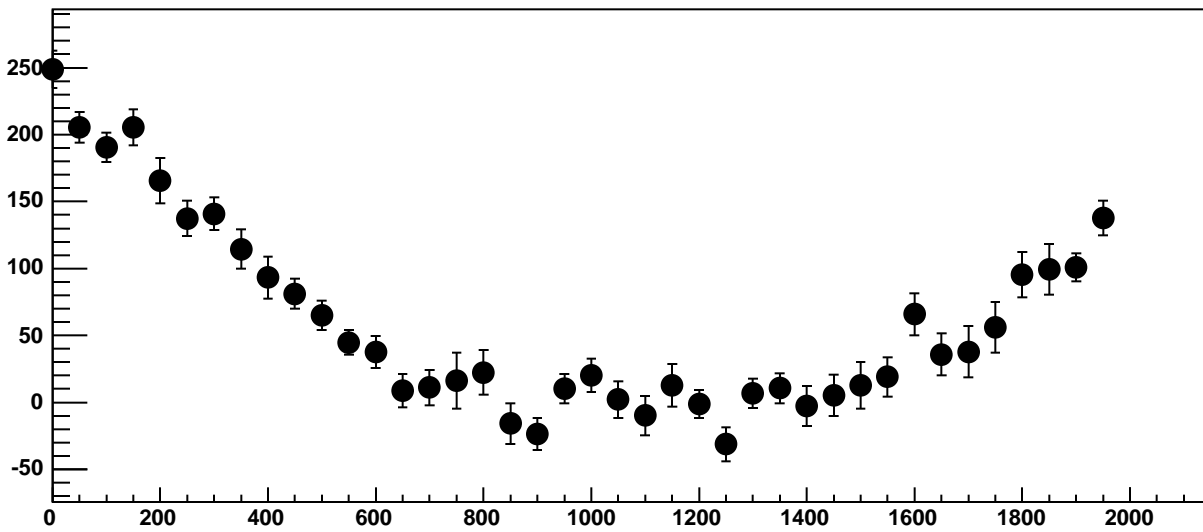
Chip 0, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC



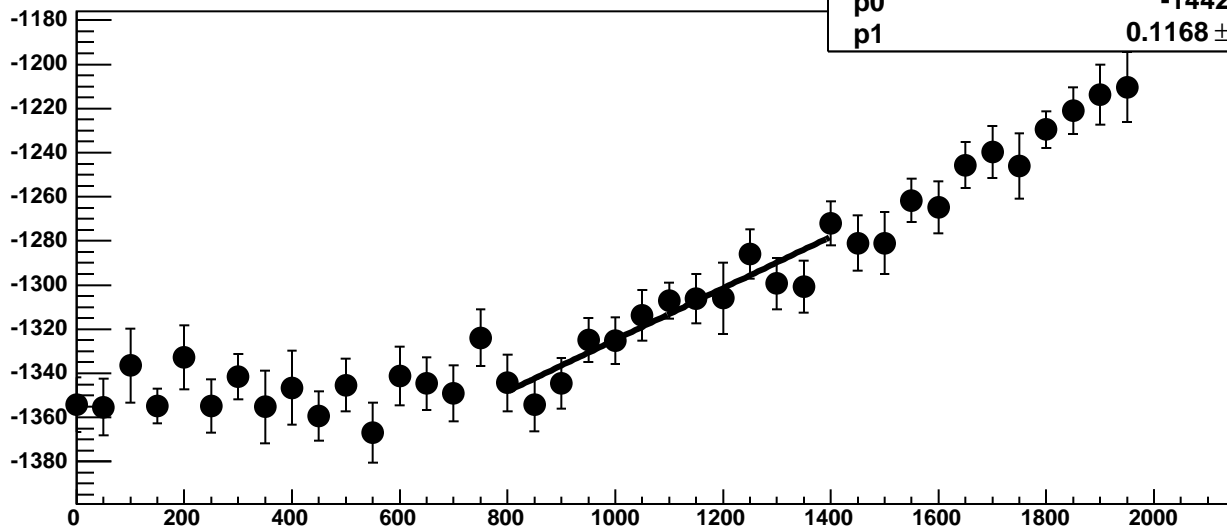
Chip 0, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC



Chip 0, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC

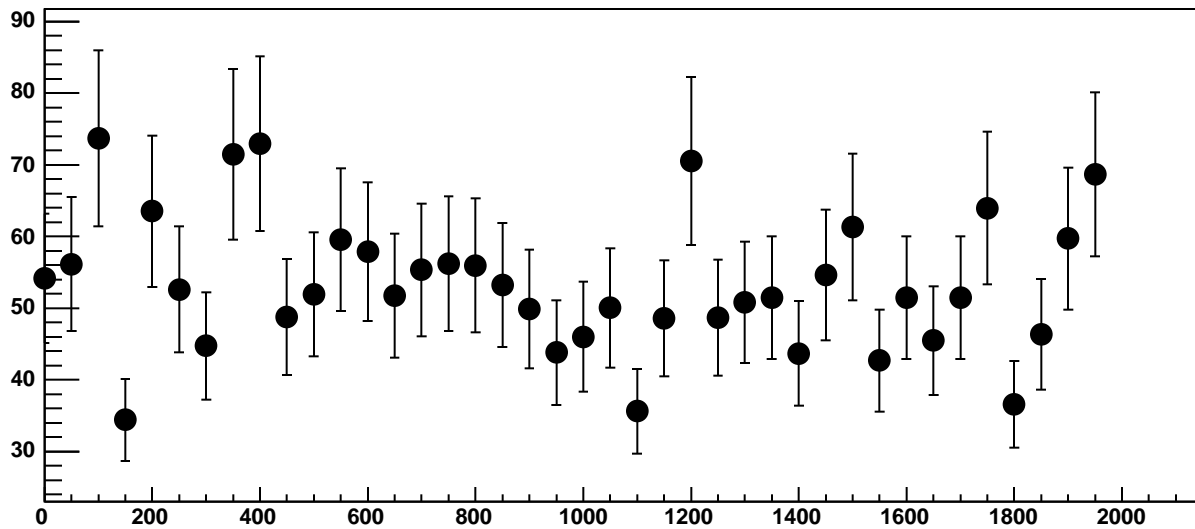


Chip 0, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC

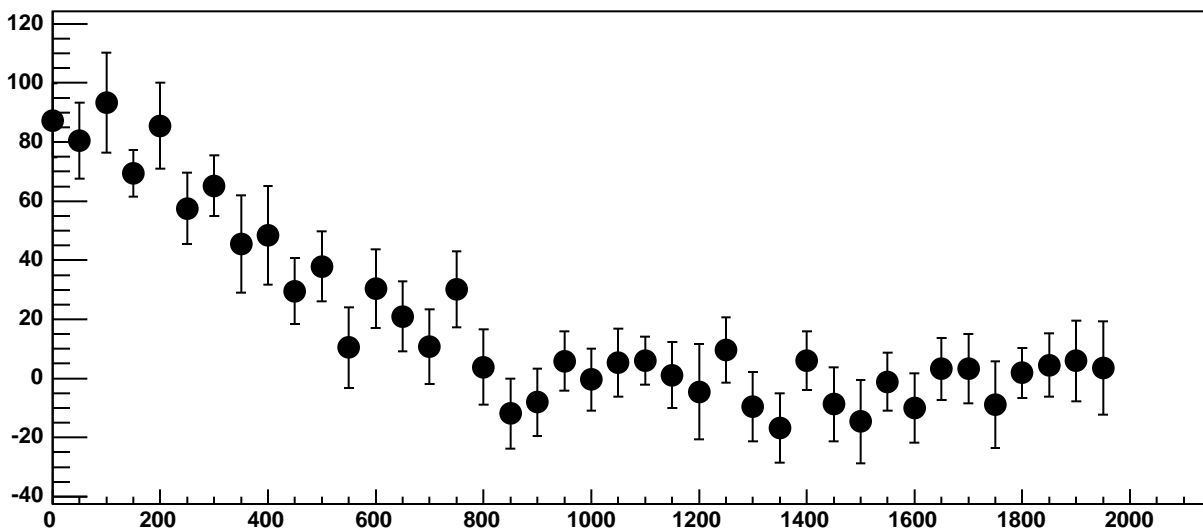


$\chi^2 / \text{ndf}$  6.553 / 11  
p0  $-1442 \pm 18.9$   
p1  $0.1168 \pm 0.0169$

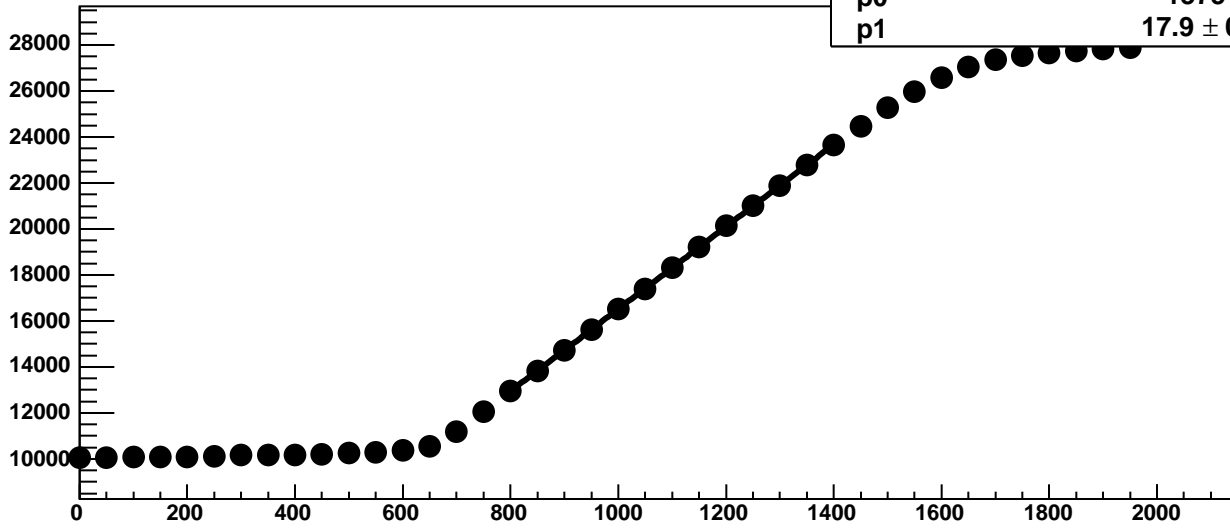
Chip 0, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC



Chip 0, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC

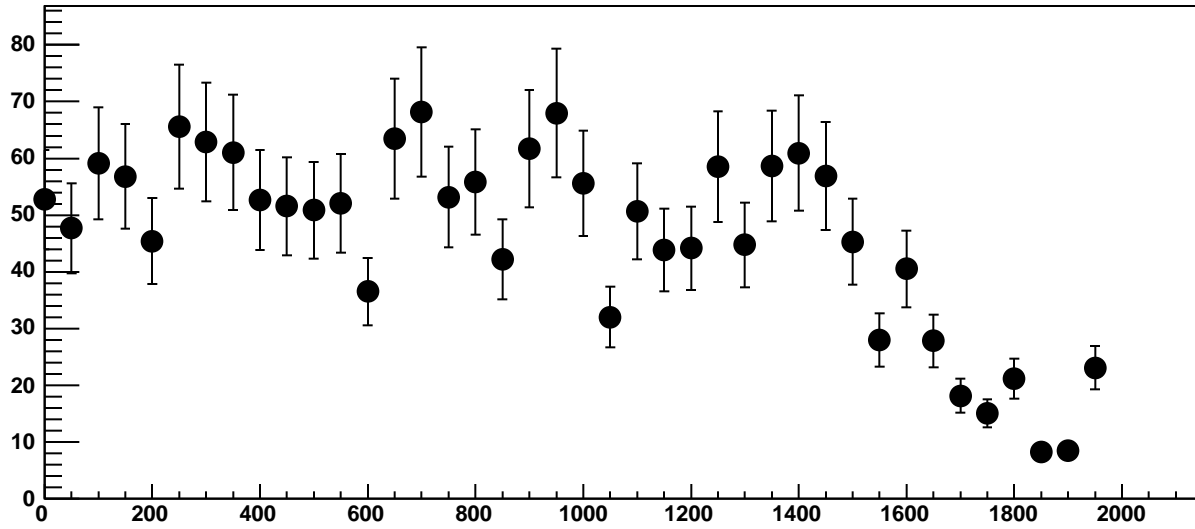


Chip 0, Channel 6, Enable 2!, Hold=30, ADC Mean vs DAC

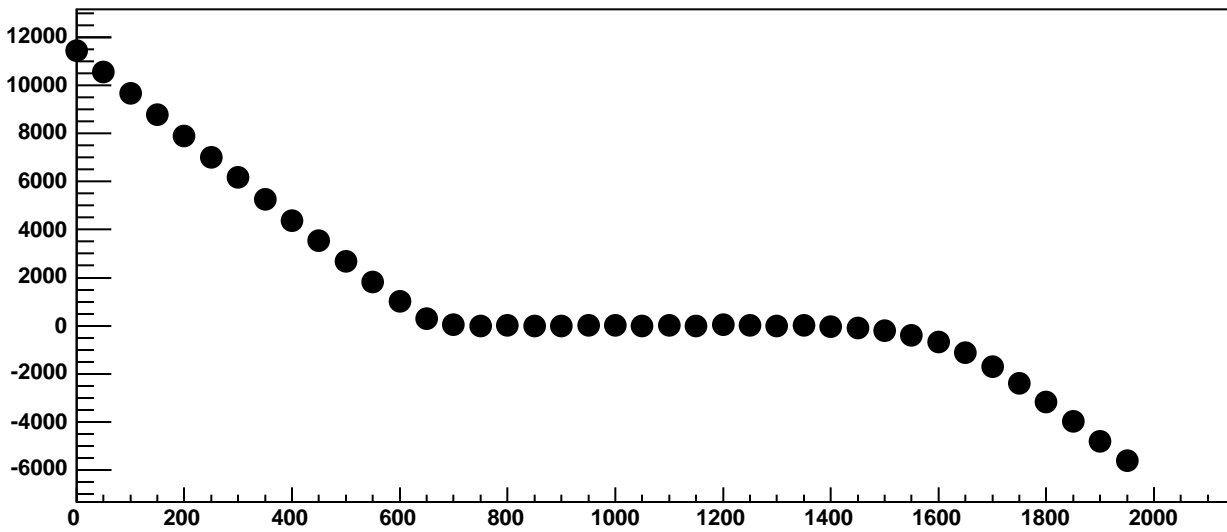


$\chi^2 / \text{ndf}$	27.56 / 11
p0	$-1375 \pm 20.09$
p1	$17.9 \pm 0.01813$

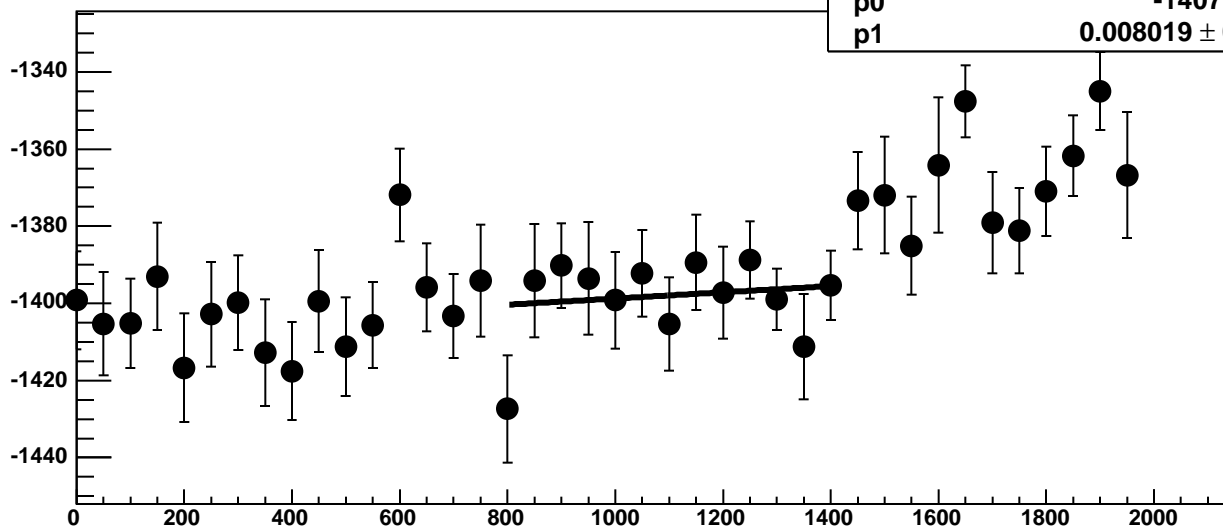
Chip 0, Channel 6, Enable 2!, Hold=30, ADC Noise vs DAC



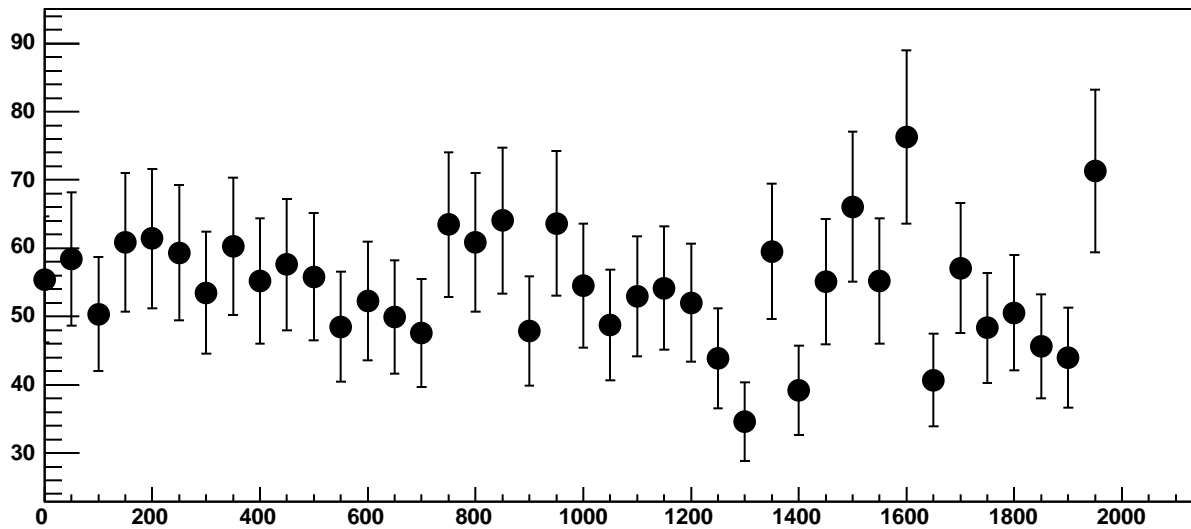
Chip 0, Channel 6, Enable 2!, Hold=30, ADC Residuals vs DAC



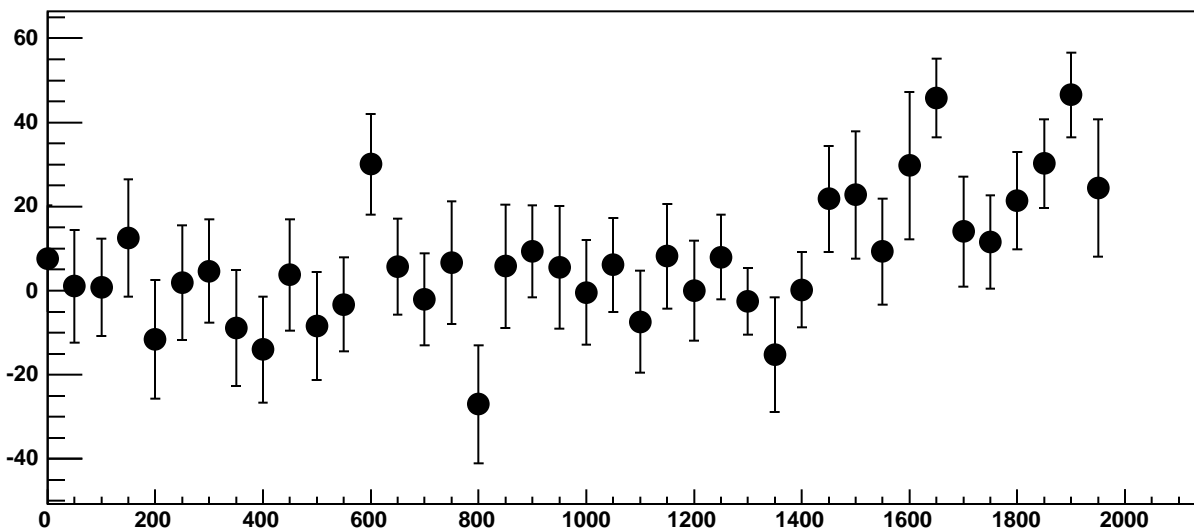
Chip 0, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC



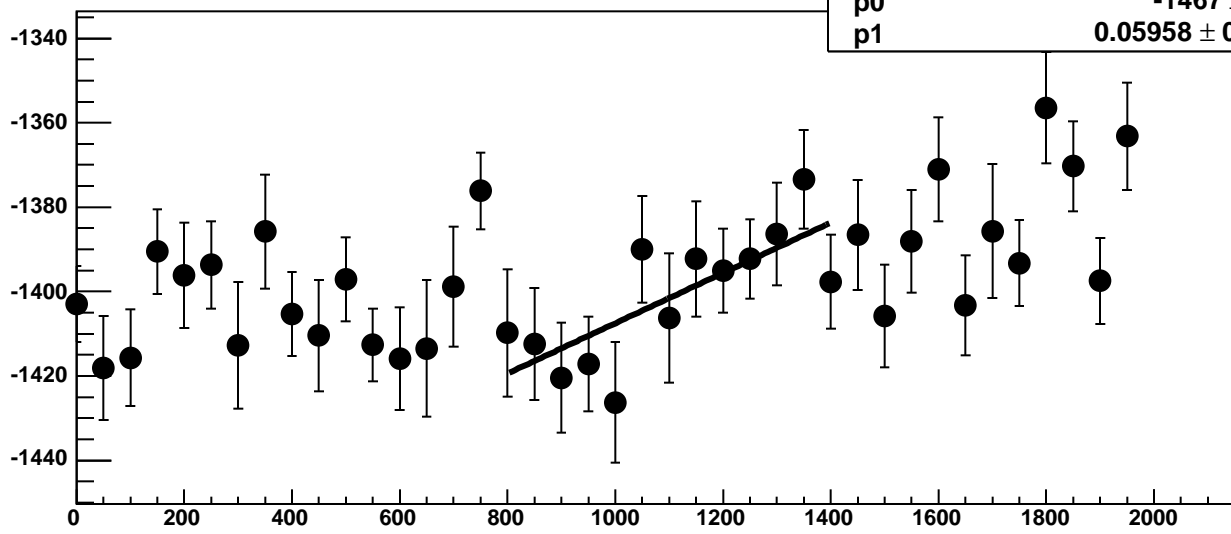
Chip 0, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC



Chip 0, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC

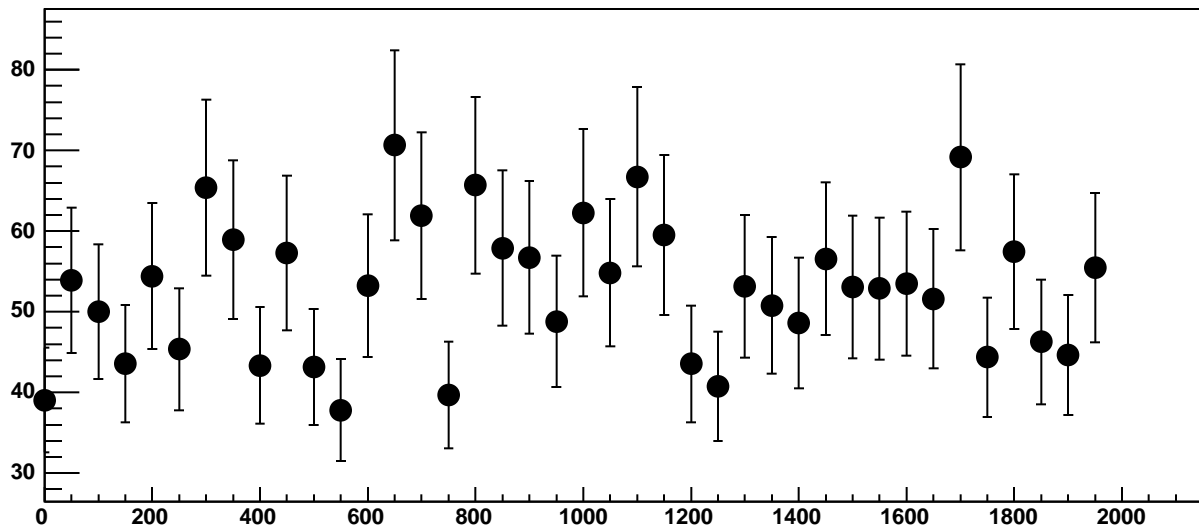


Chip 0, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC

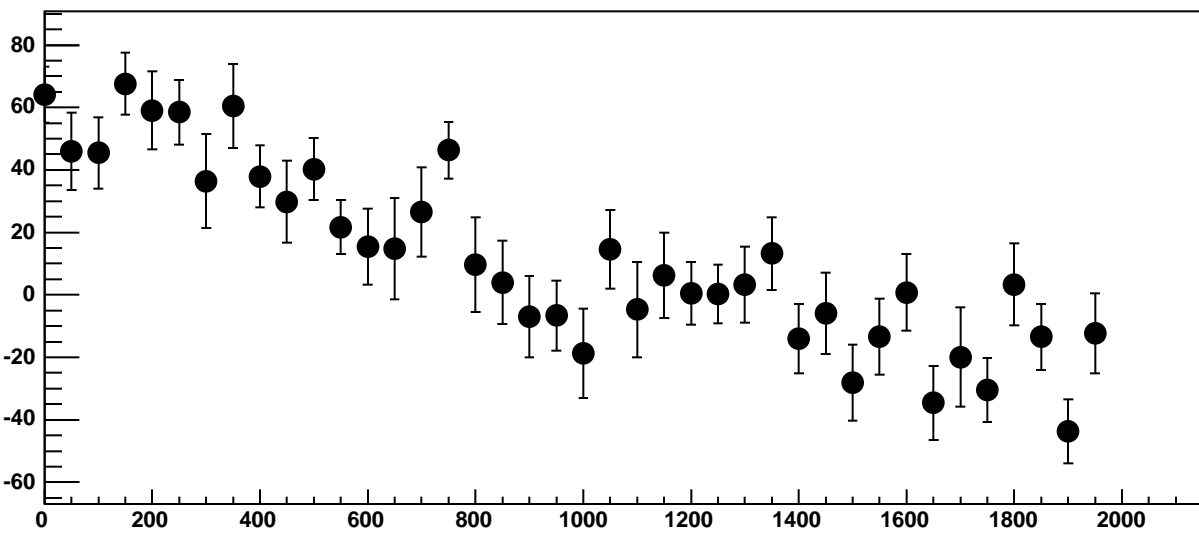


$\chi^2 / \text{ndf}$  7.448 / 11  
p0  $-1467 \pm 20.99$   
p1  $0.05958 \pm 0.01834$

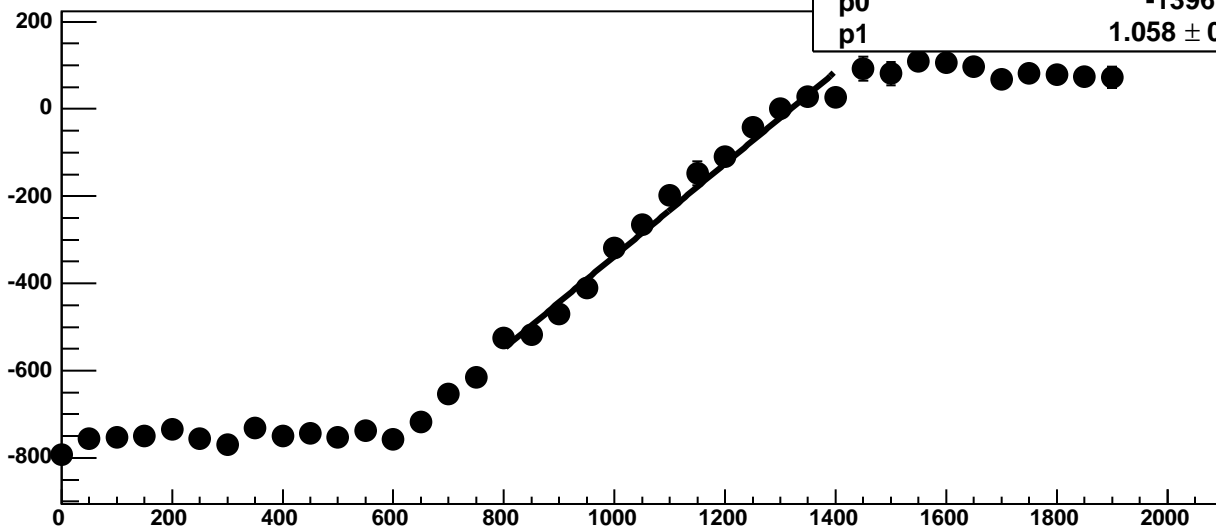
Chip 0, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC



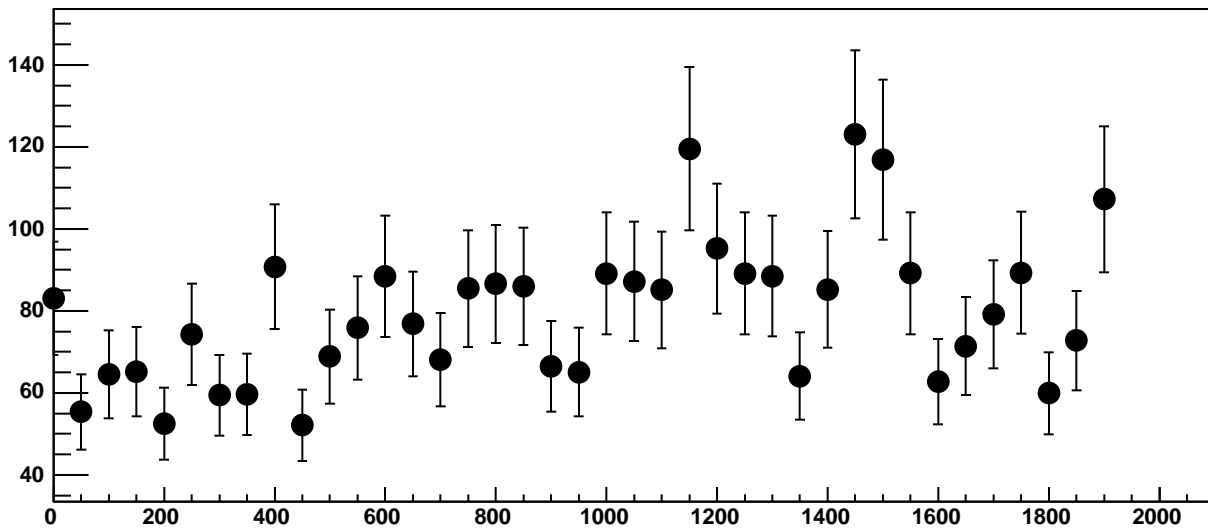
Chip 0, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC



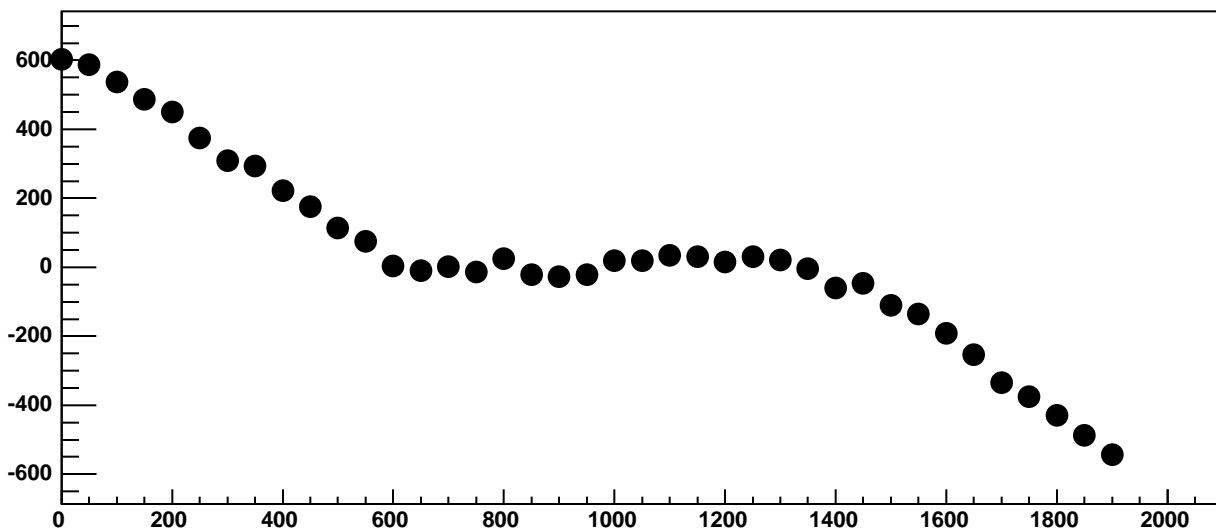
Chip 0, Channel 6, Enable 5, Hold=30, ADC Mean vs DAC



Chip 0, Channel 6, Enable 5, Hold=30, ADC Noise vs DAC

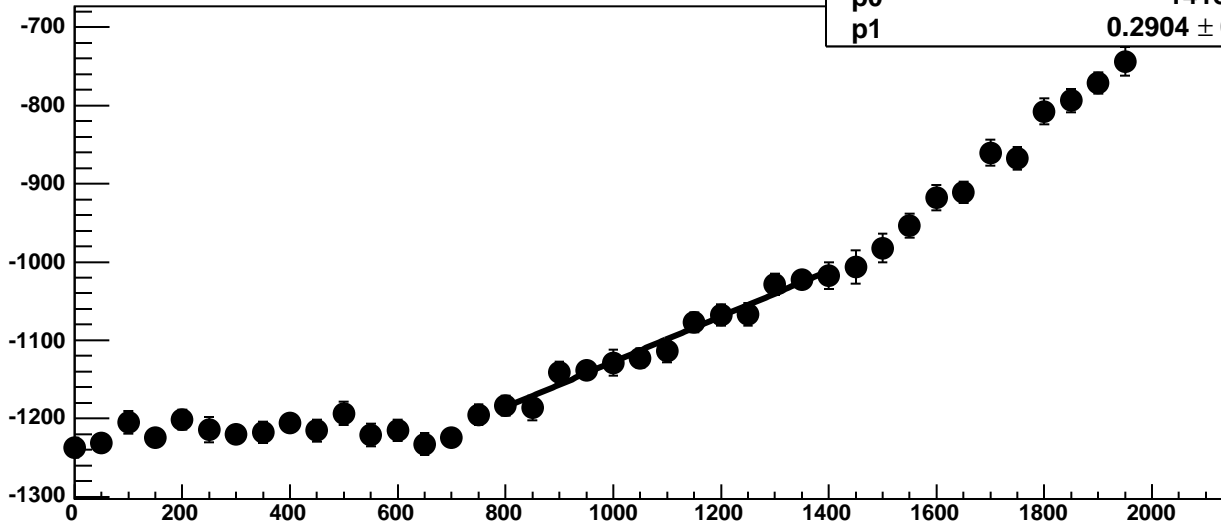


Chip 0, Channel 6, Enable 5, Hold=30, ADC Residuals vs DAC

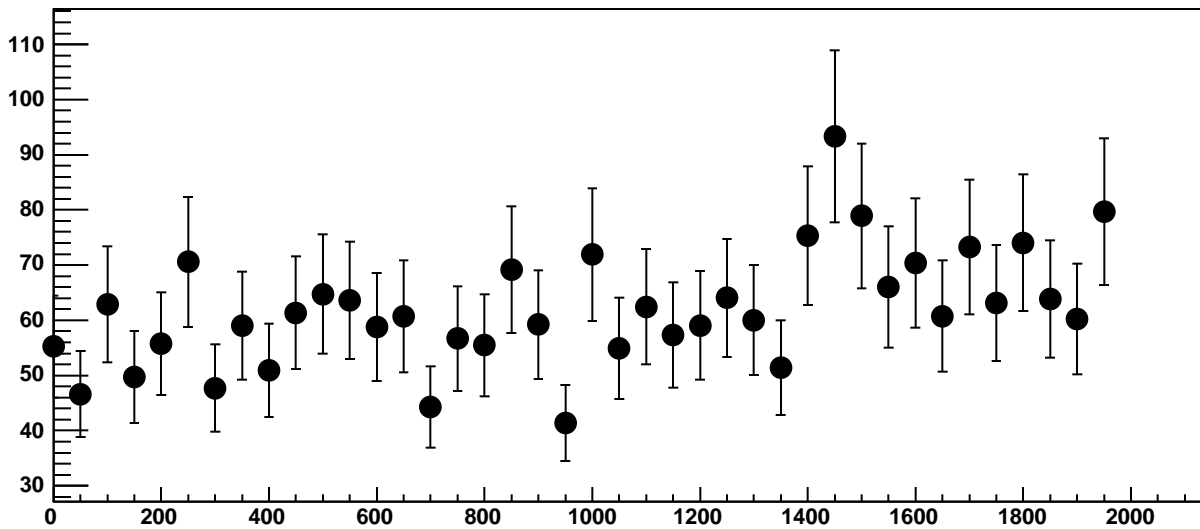




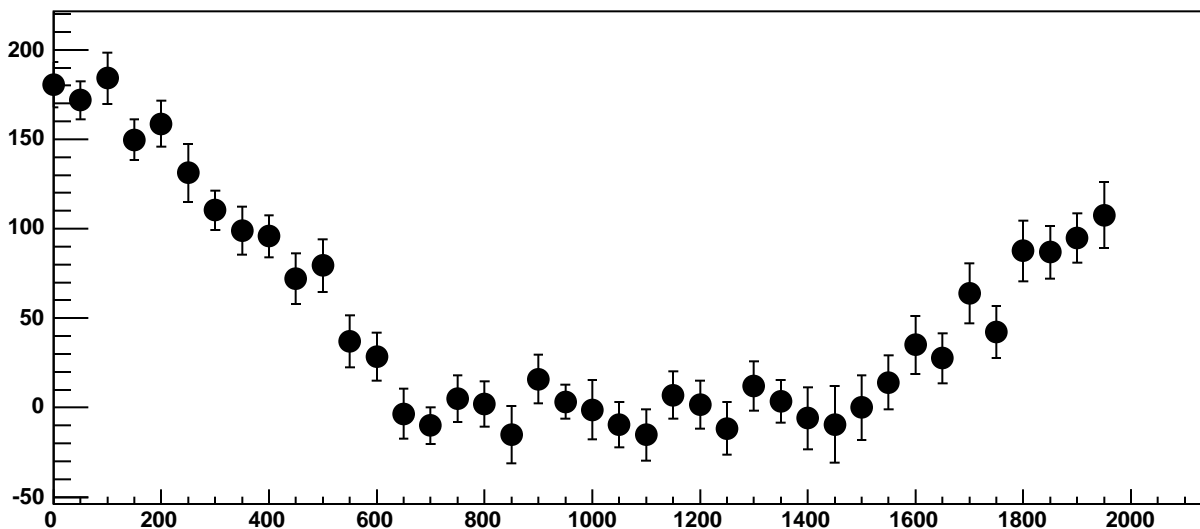
Chip 0, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC



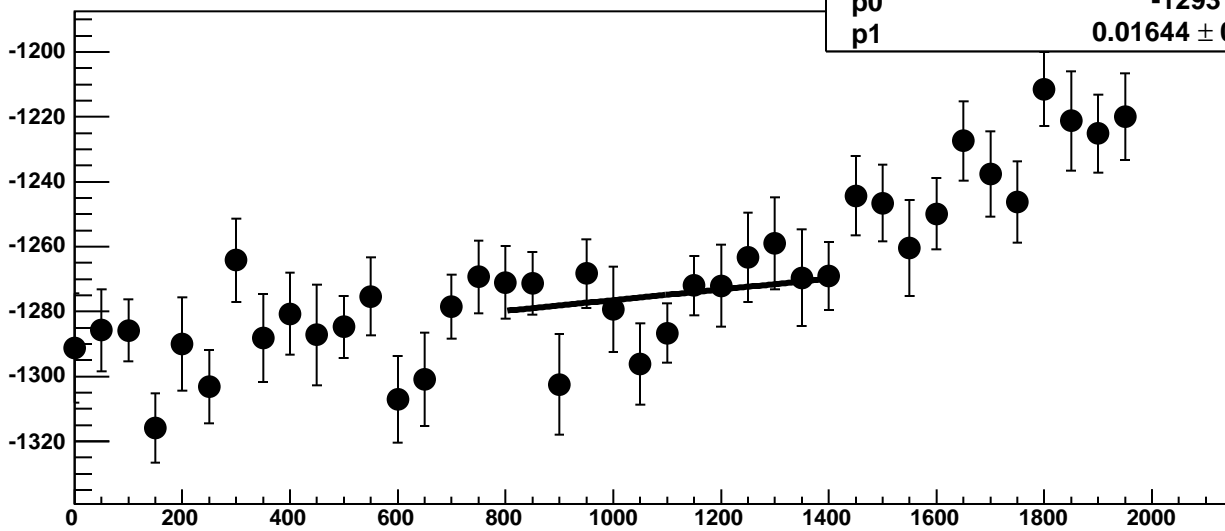
Chip 0, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC



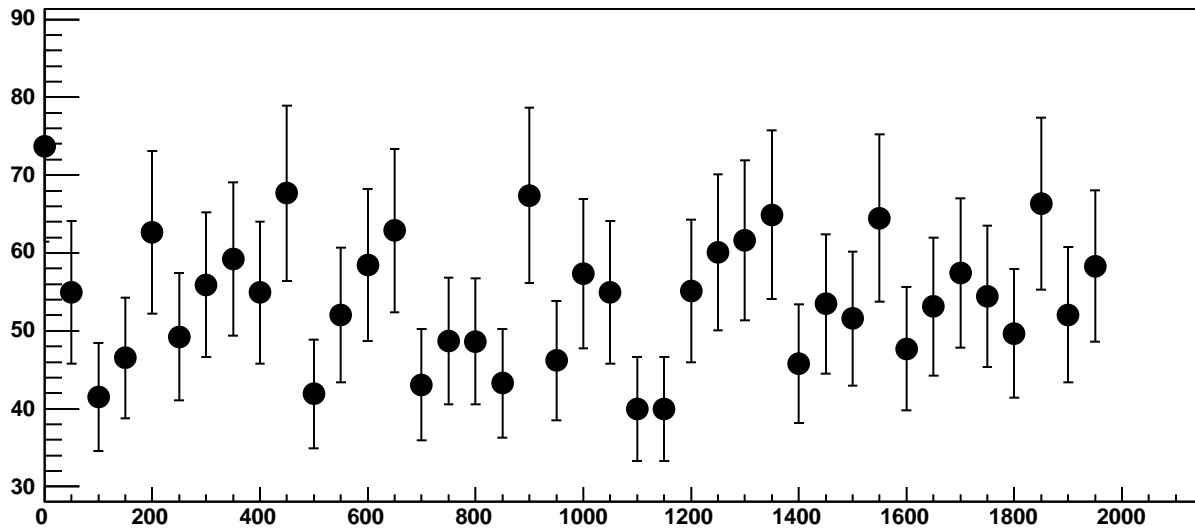
Chip 0, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC



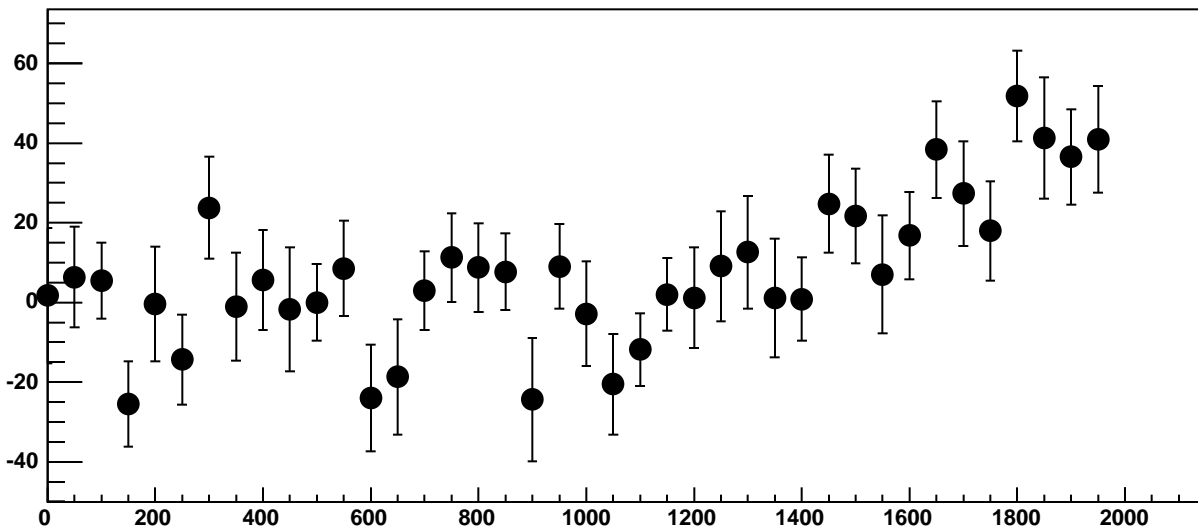
Chip 0, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC



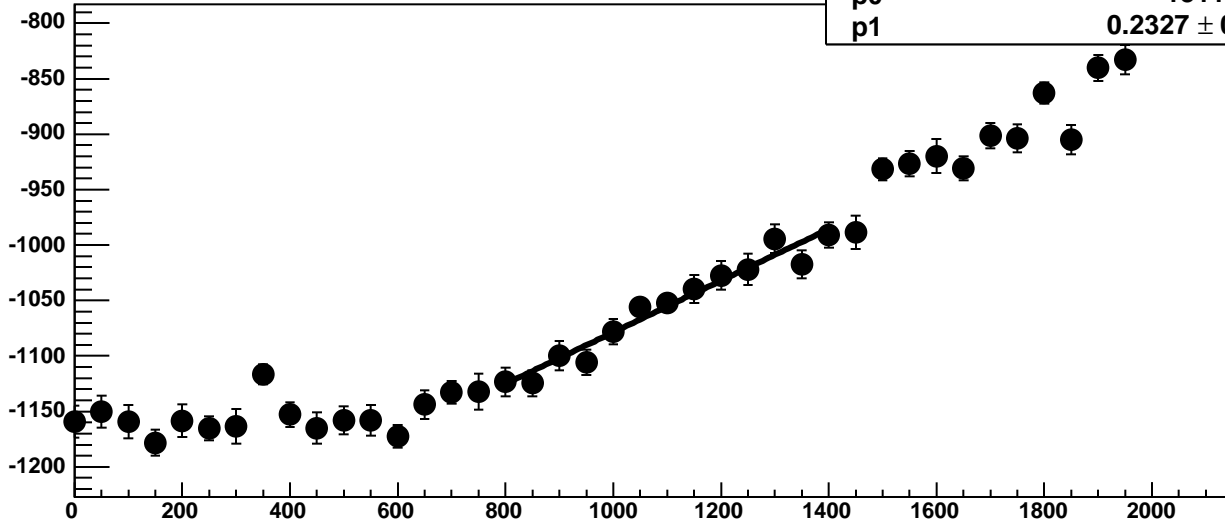
Chip 0, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC



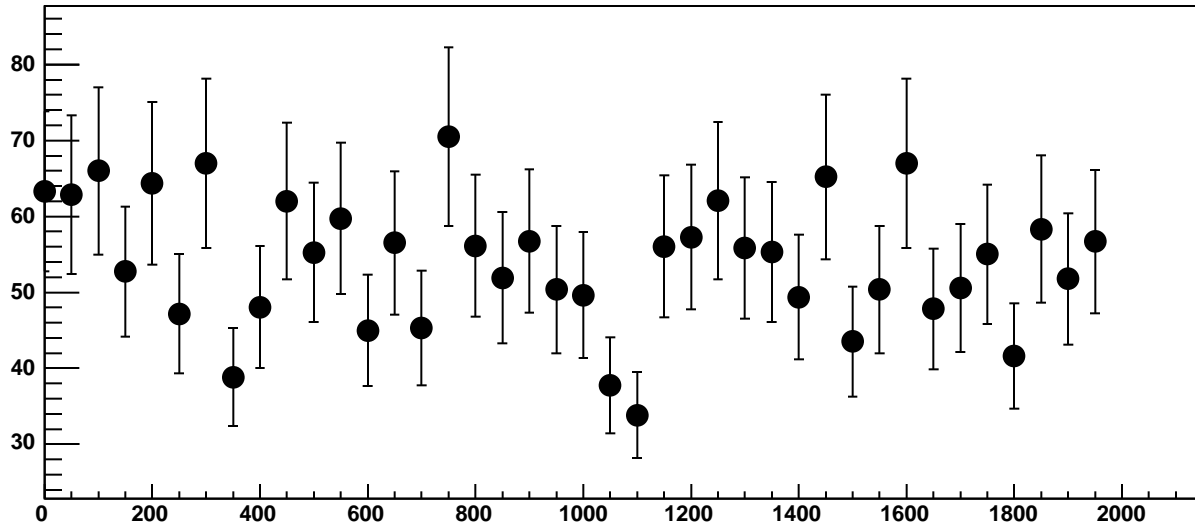
Chip 0, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC



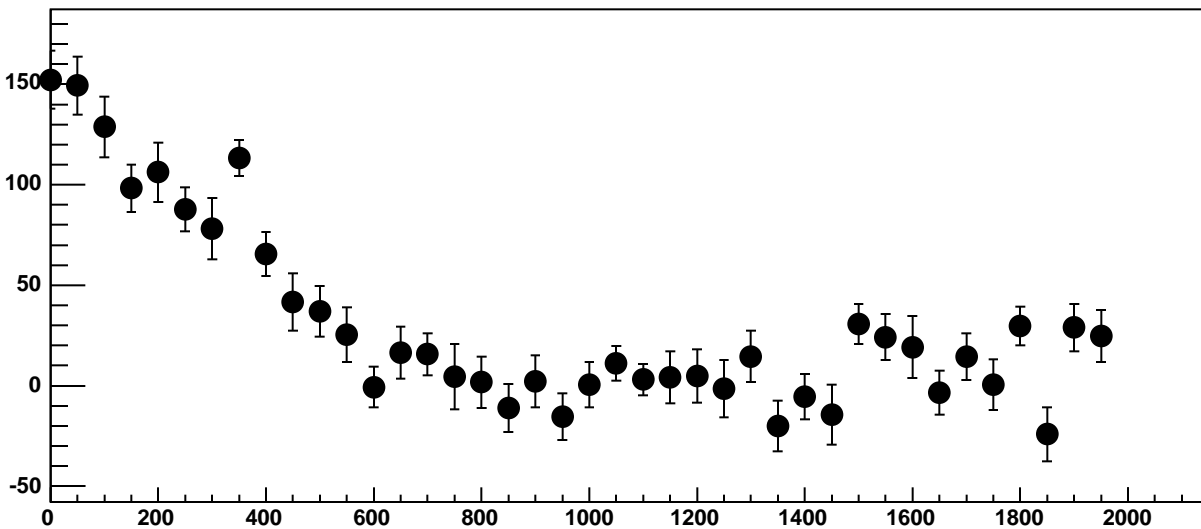
Chip 0, Channel 7, Enable 2, Hold=30, ADC Mean vs DAC



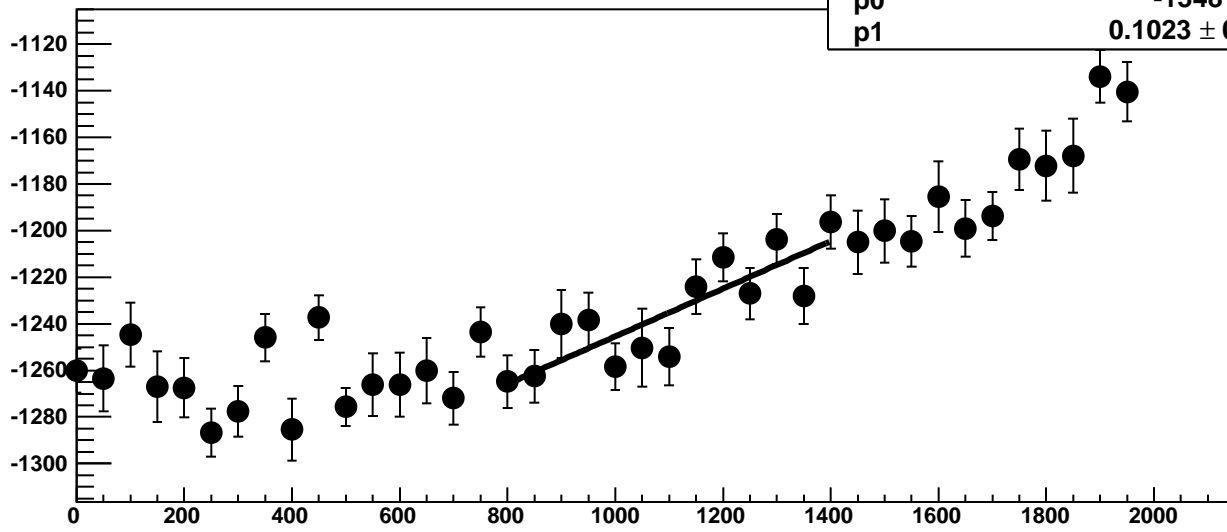
Chip 0, Channel 7, Enable 2, Hold=30, ADC Noise vs DAC



Chip 0, Channel 7, Enable 2, Hold=30, ADC Residuals vs DAC

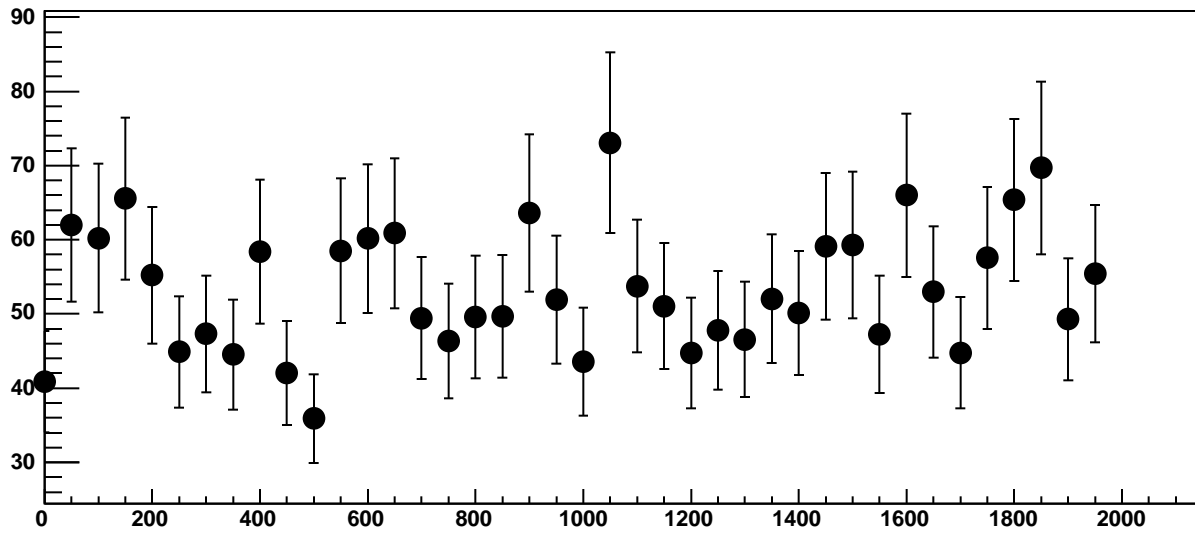


Chip 0, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC

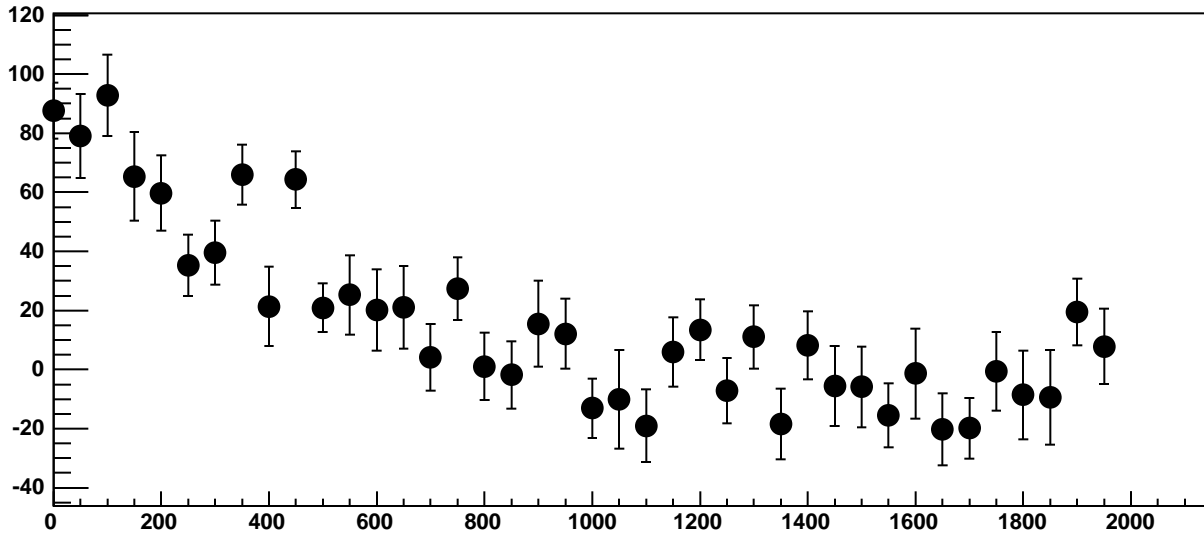


$\chi^2 / \text{ndf}$  13.01 / 11  
p0  $-1348 \pm 19.32$   
p1  $0.1023 \pm 0.01717$

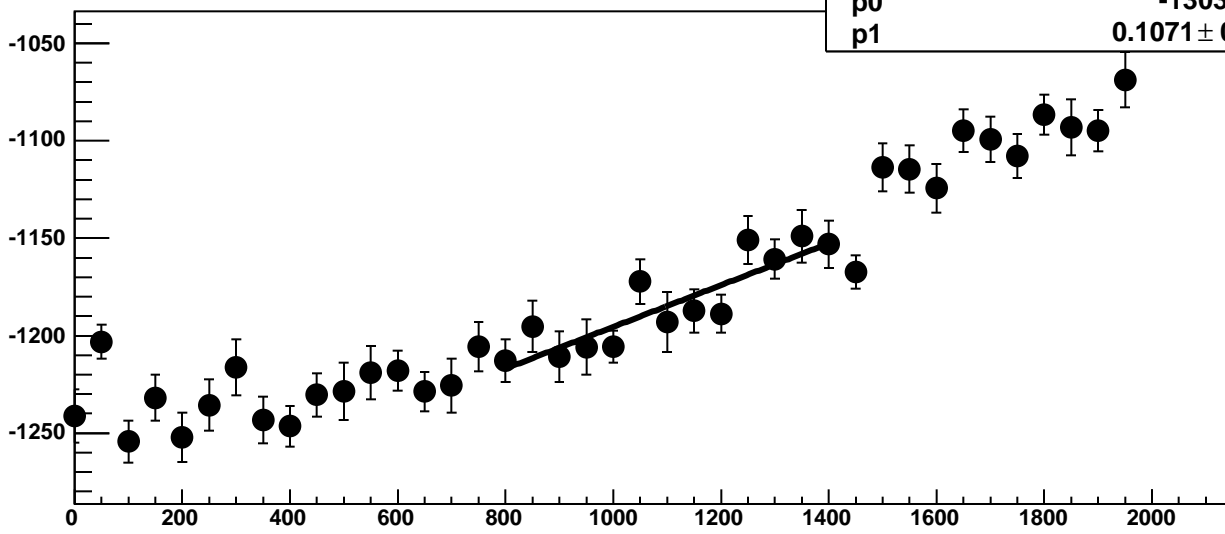
Chip 0, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC



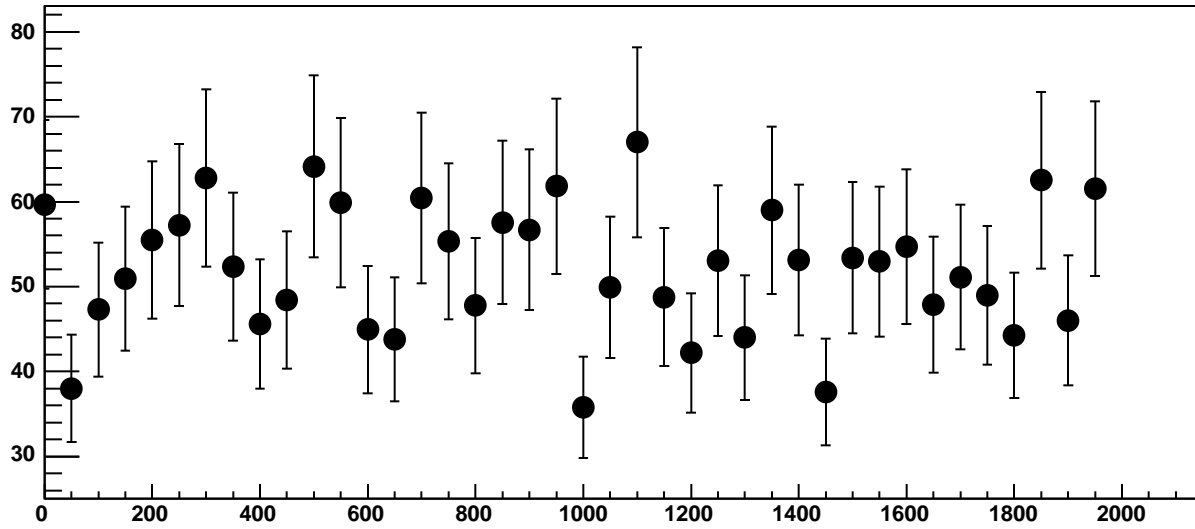
Chip 0, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC



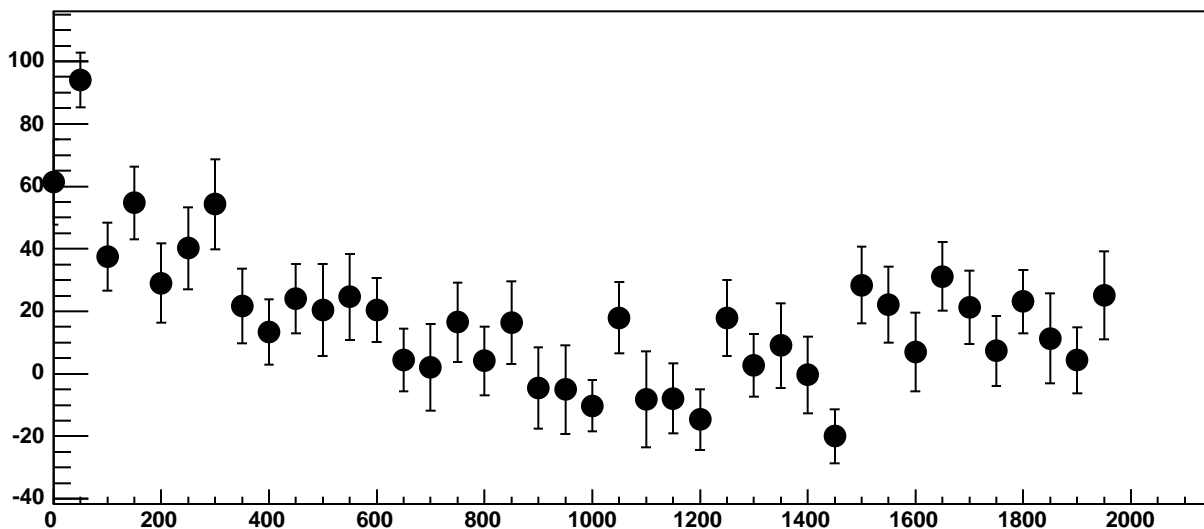
Chip 0, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC



Chip 0, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC

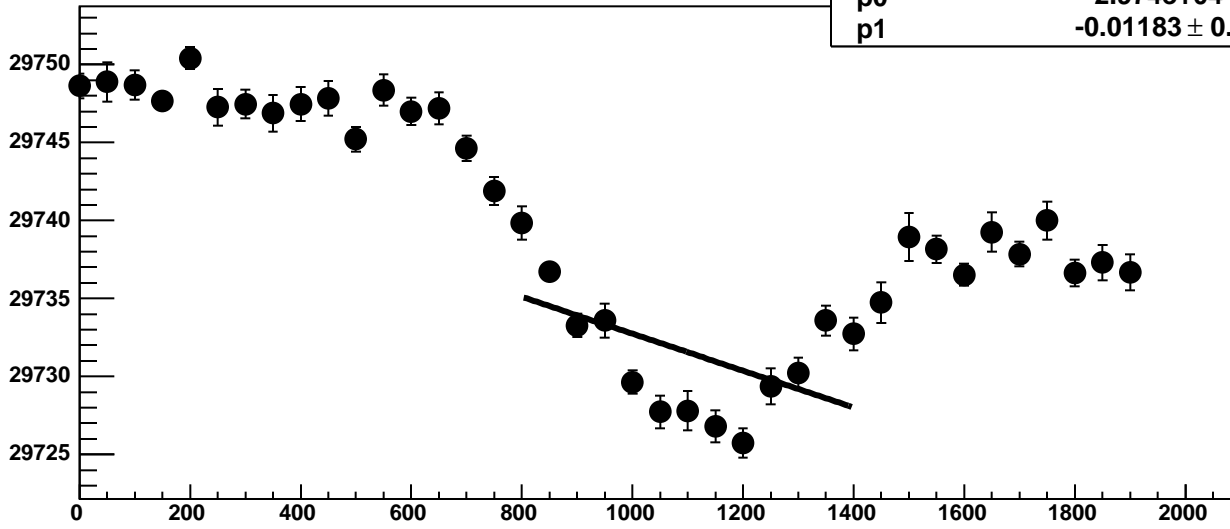


Chip 0, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC

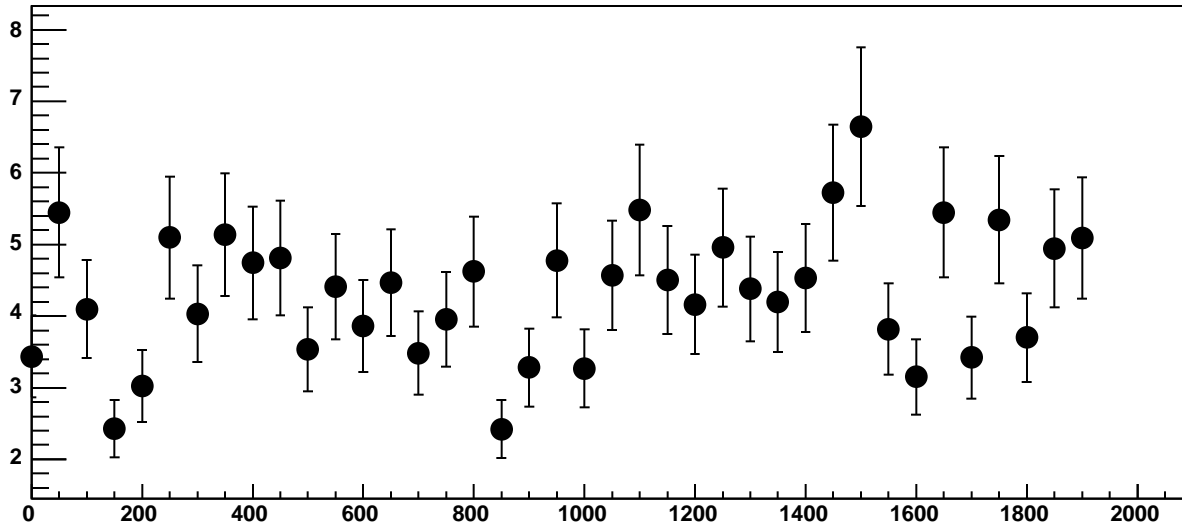


Chip 0, Channel 7, Enable 5!, Hold=30, ADC Mean vs DAC

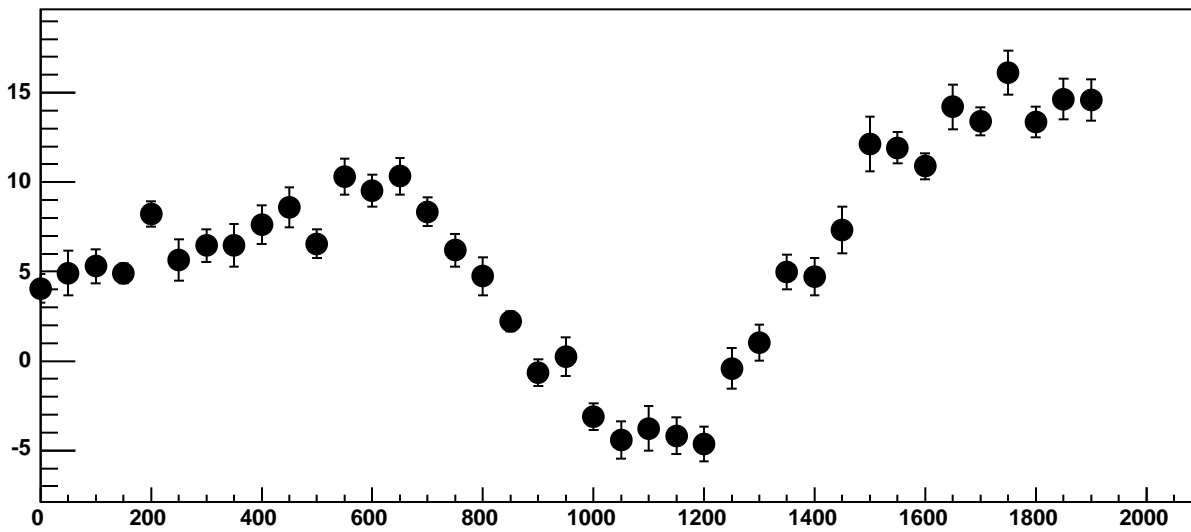
$\chi^2 / \text{ndf}$  169.2 / 11  
p0  $2.974\text{e}+04 \pm 1.408$   
p1  $-0.01183 \pm 0.001317$



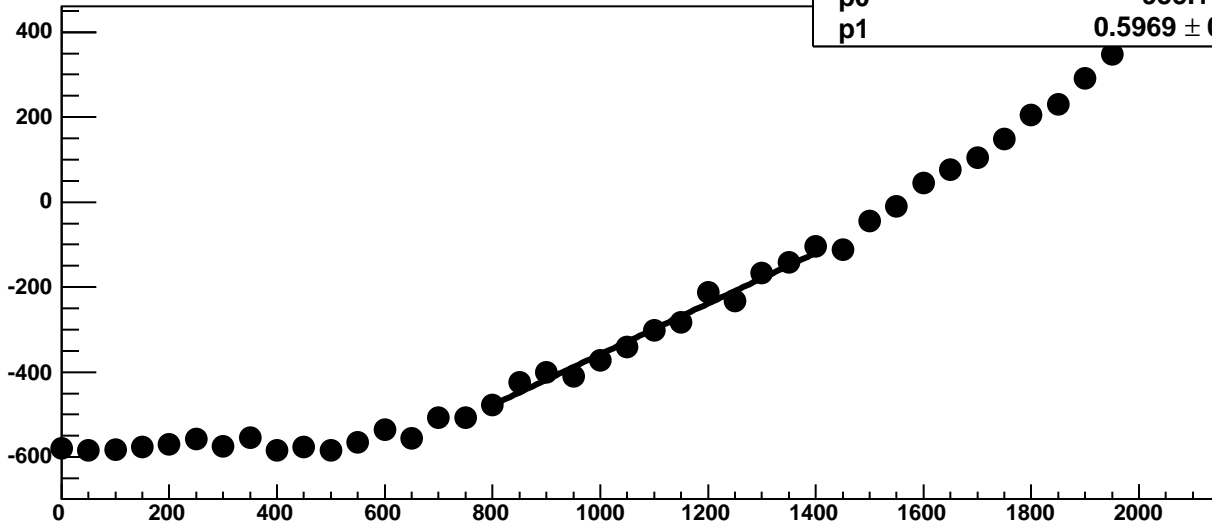
Chip 0, Channel 7, Enable 5!, Hold=30, ADC Noise vs DAC



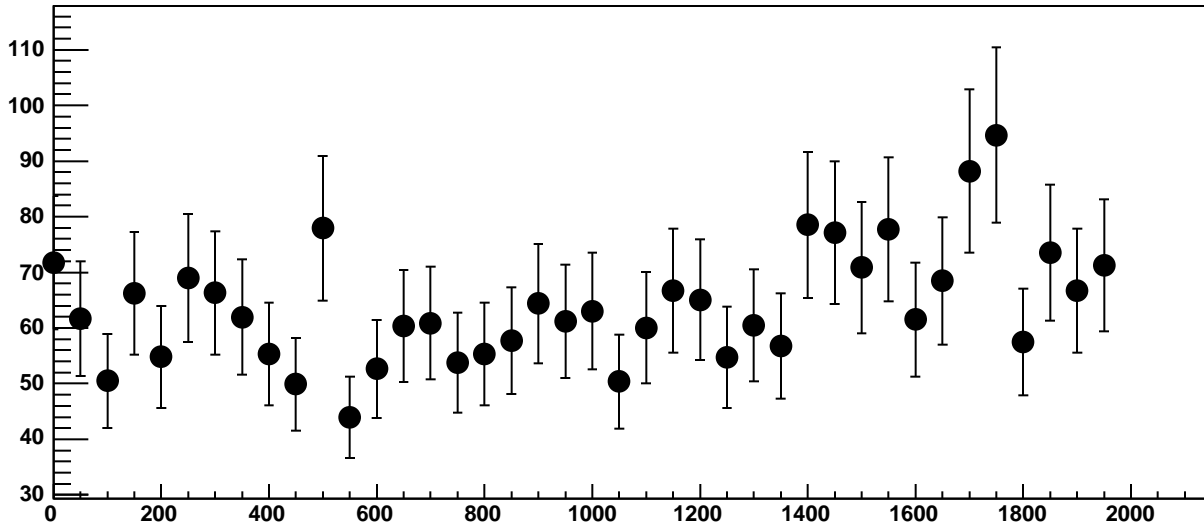
Chip 0, Channel 7, Enable 5!, Hold=30, ADC Residuals vs DAC



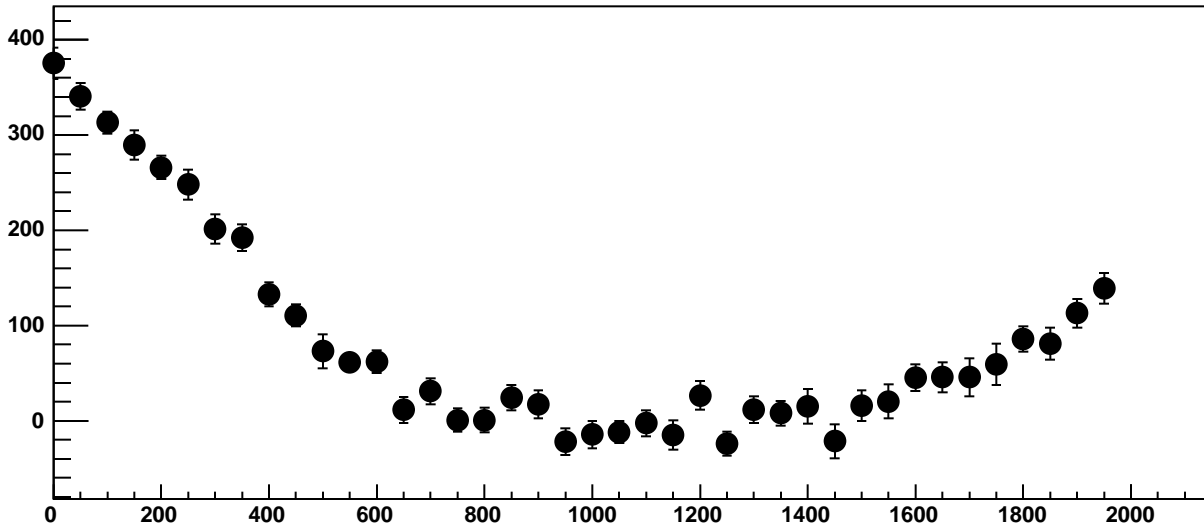
Chip 0, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC



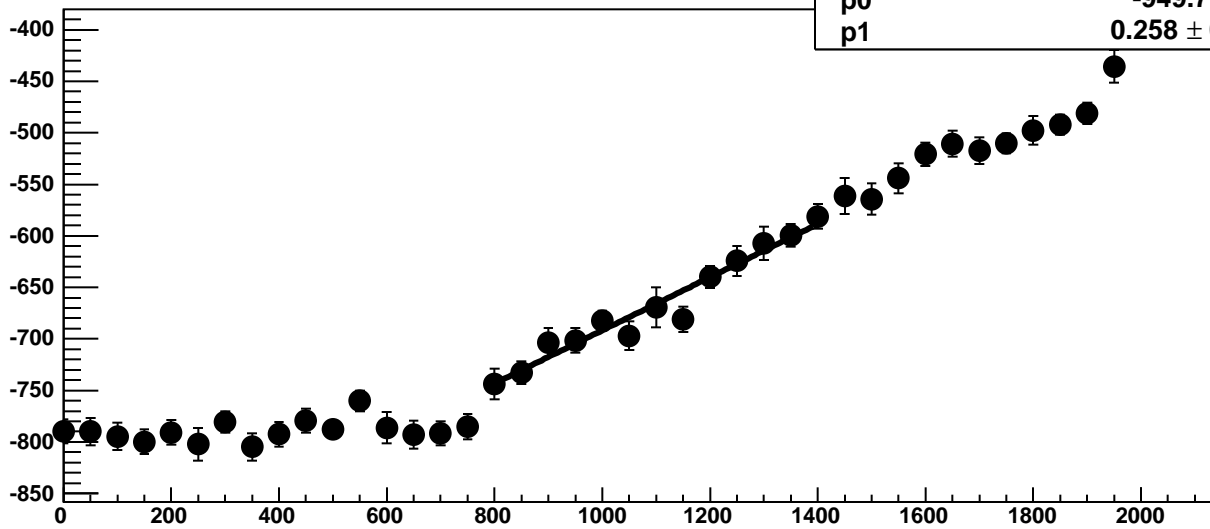
Chip 0, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC



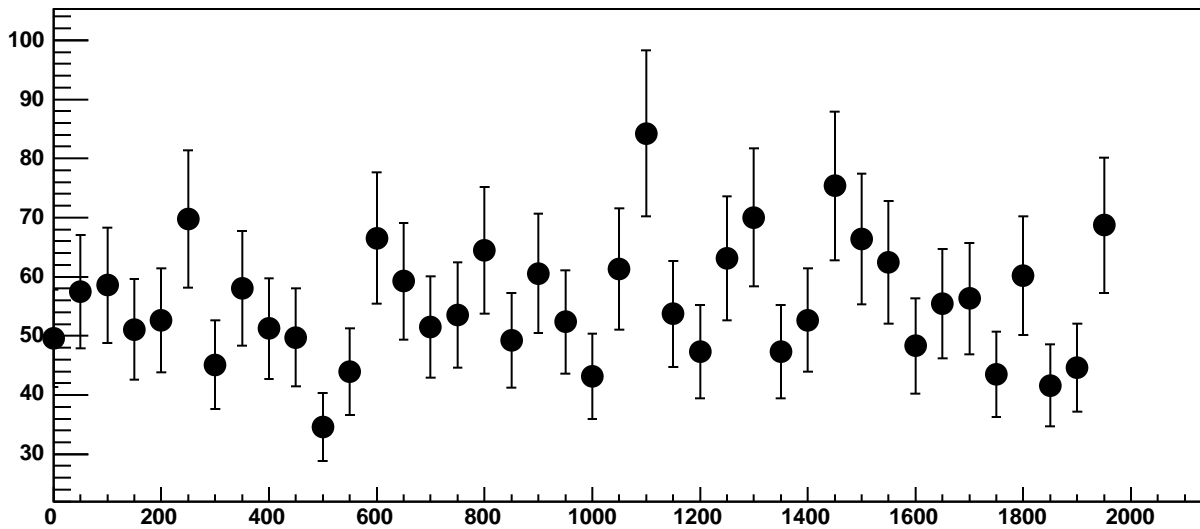
Chip 0, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC



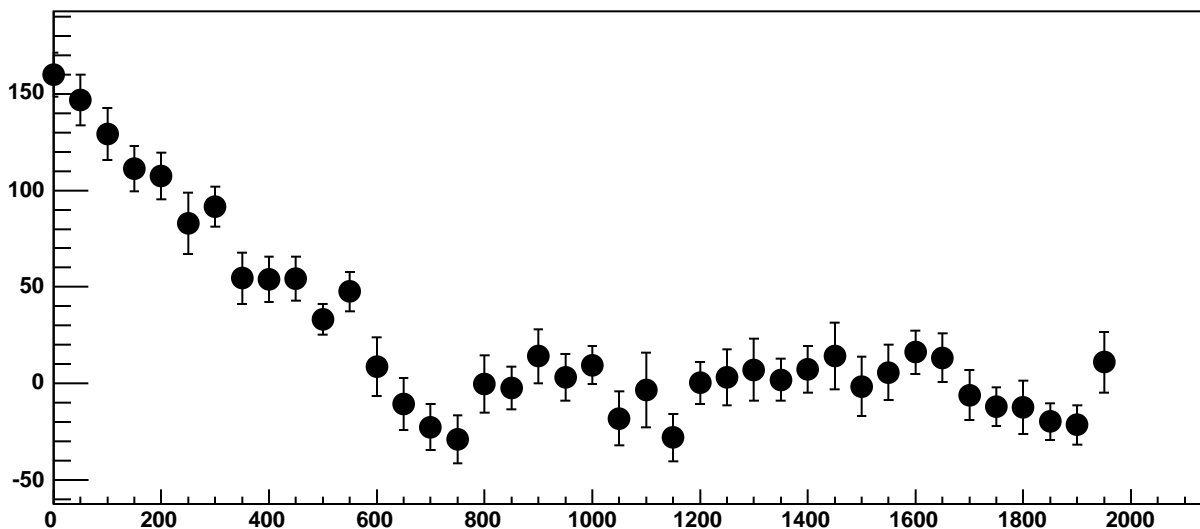
Chip 0, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC



Chip 0, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC

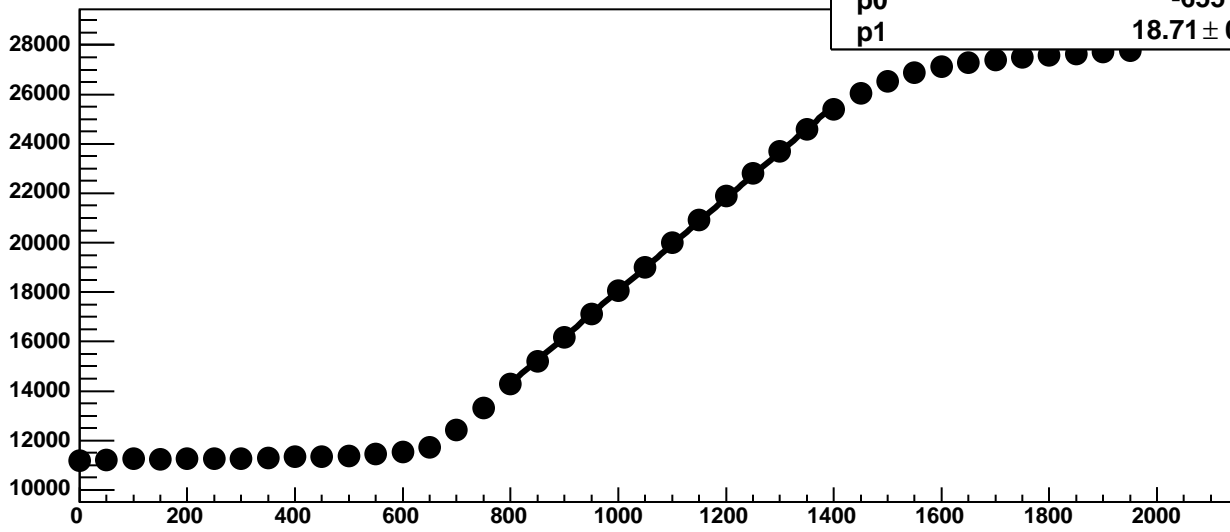


Chip 0, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC

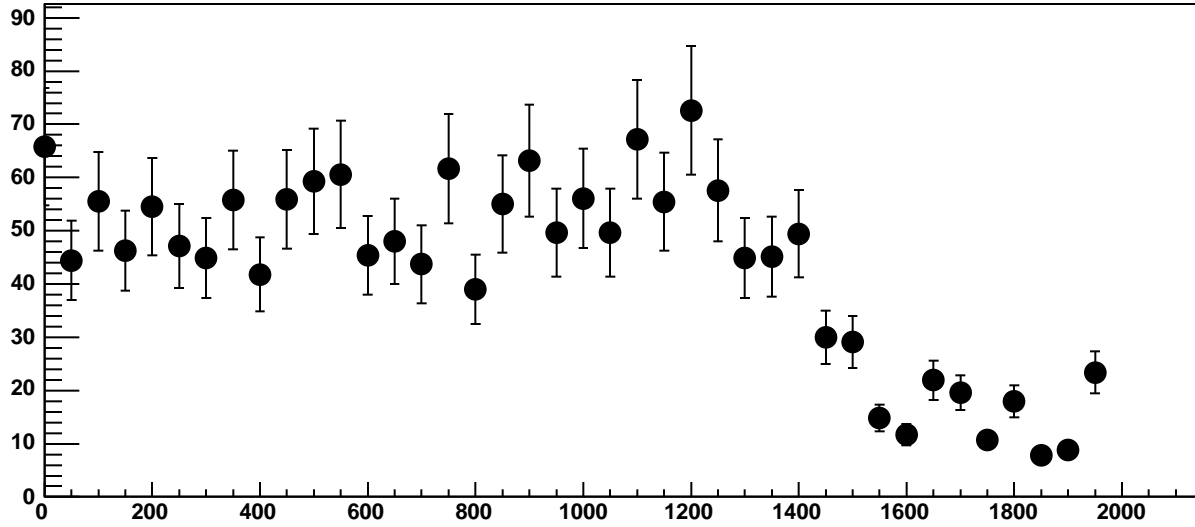




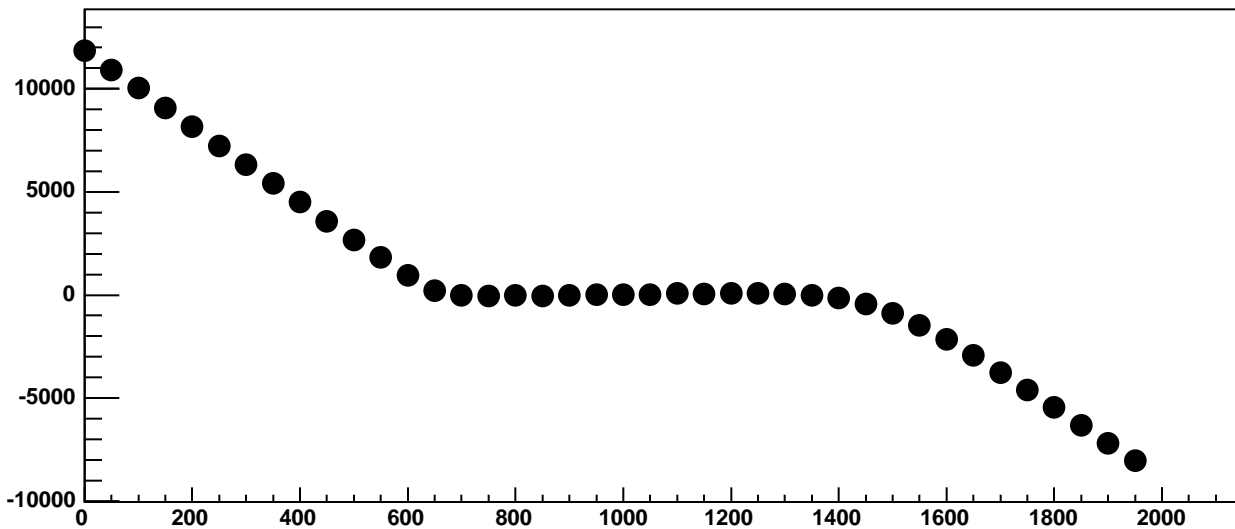
Chip 0, Channel 8, Enable 2!, Hold=30, ADC Mean vs DAC



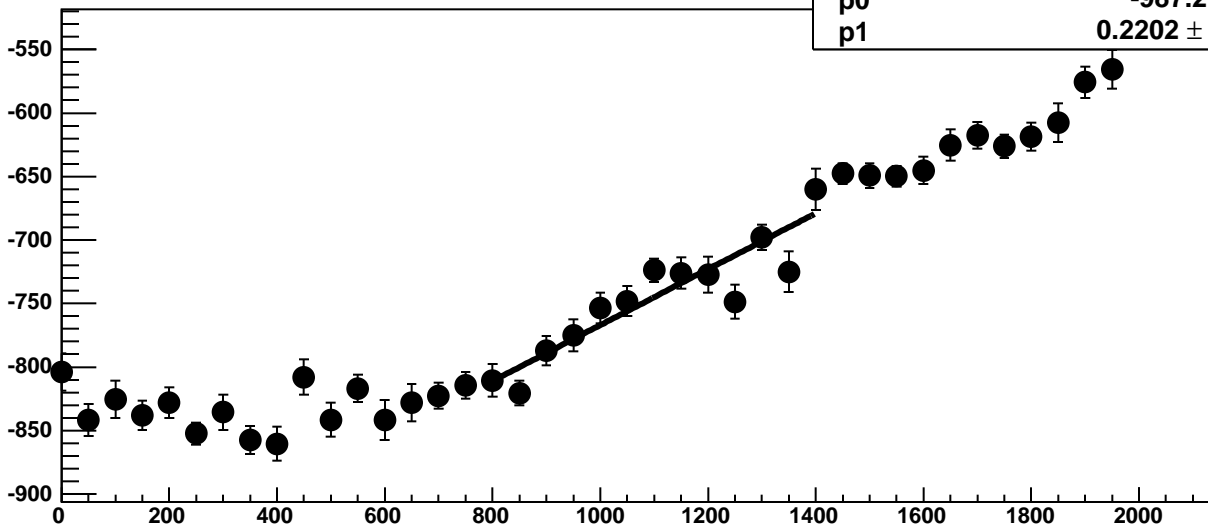
Chip 0, Channel 8, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 0, Channel 8, Enable 2!, Hold=30, ADC Residuals vs DAC

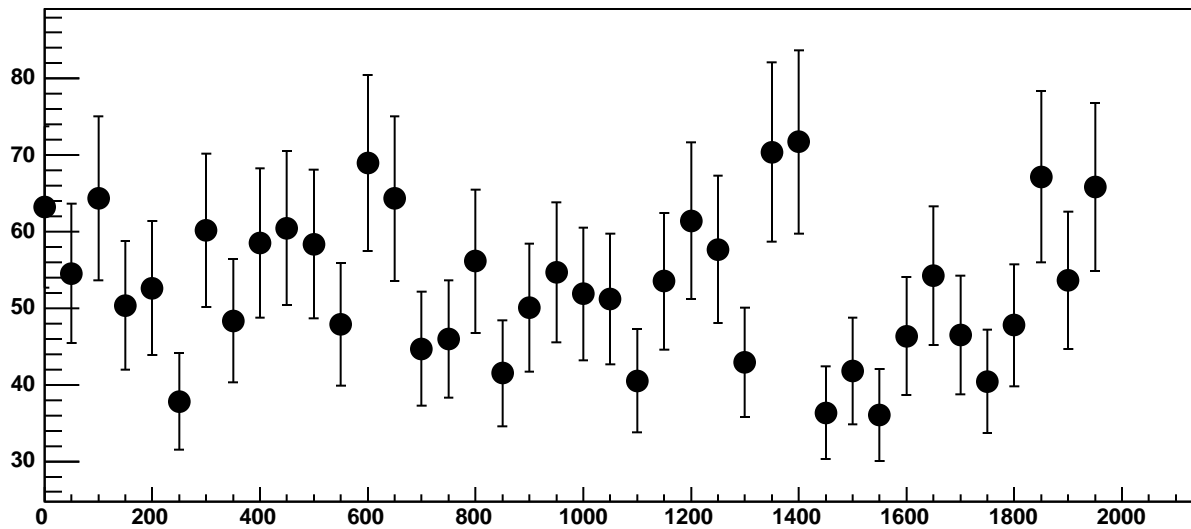


Chip 0, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC

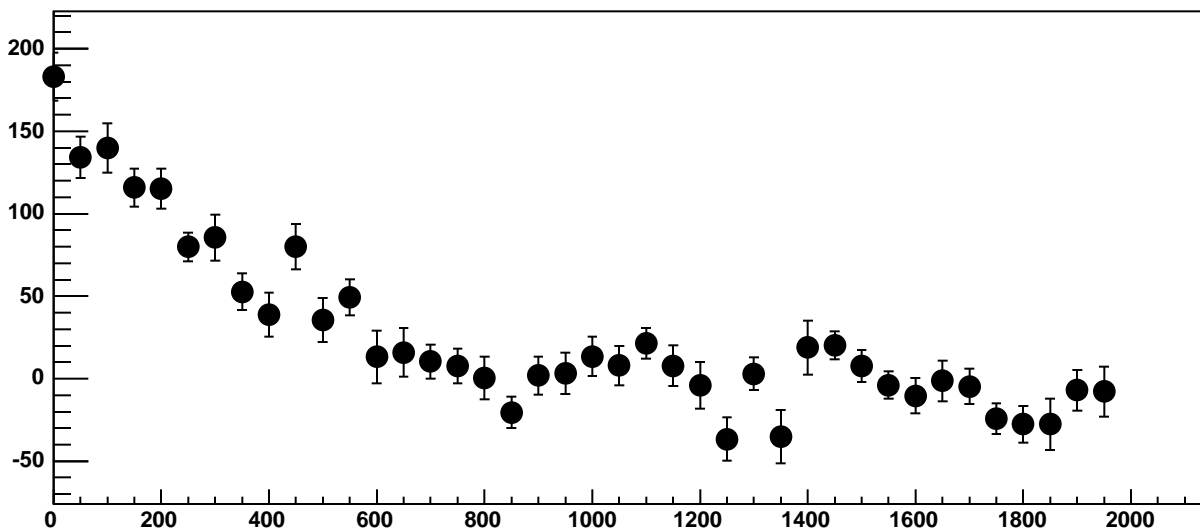


$\chi^2 / \text{ndf}$  25.94 / 11  
p0  $-987.2 \pm 20.28$   
p1  $0.2202 \pm 0.01861$

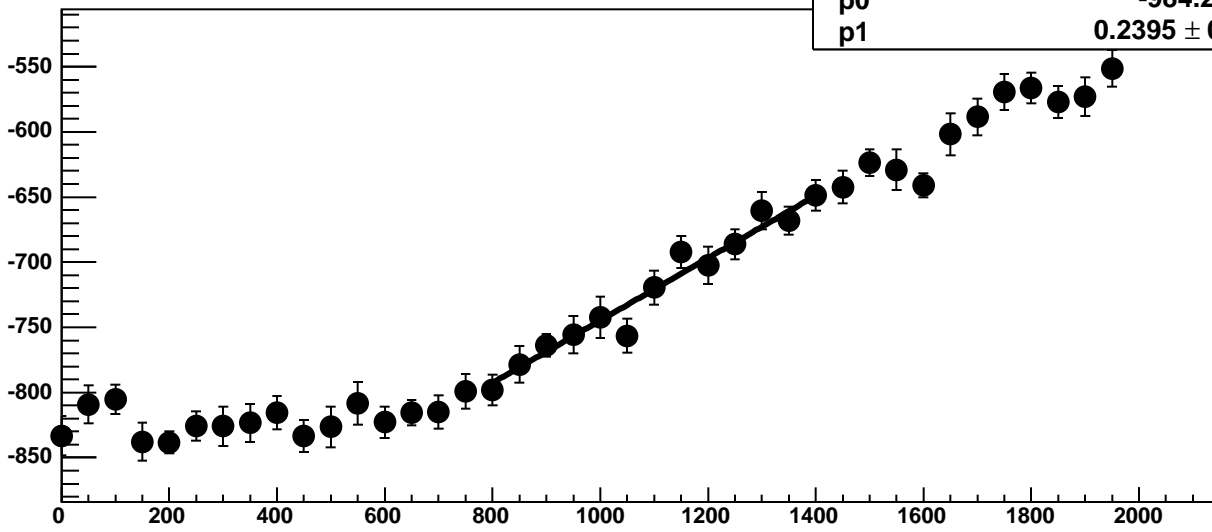
Chip 0, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC



Chip 0, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC

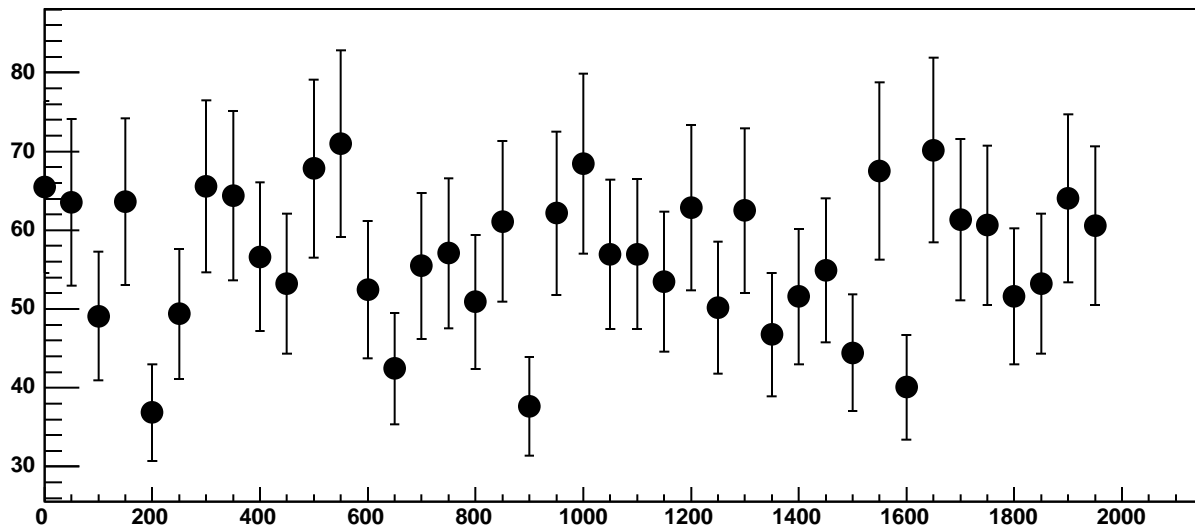


Chip 0, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC

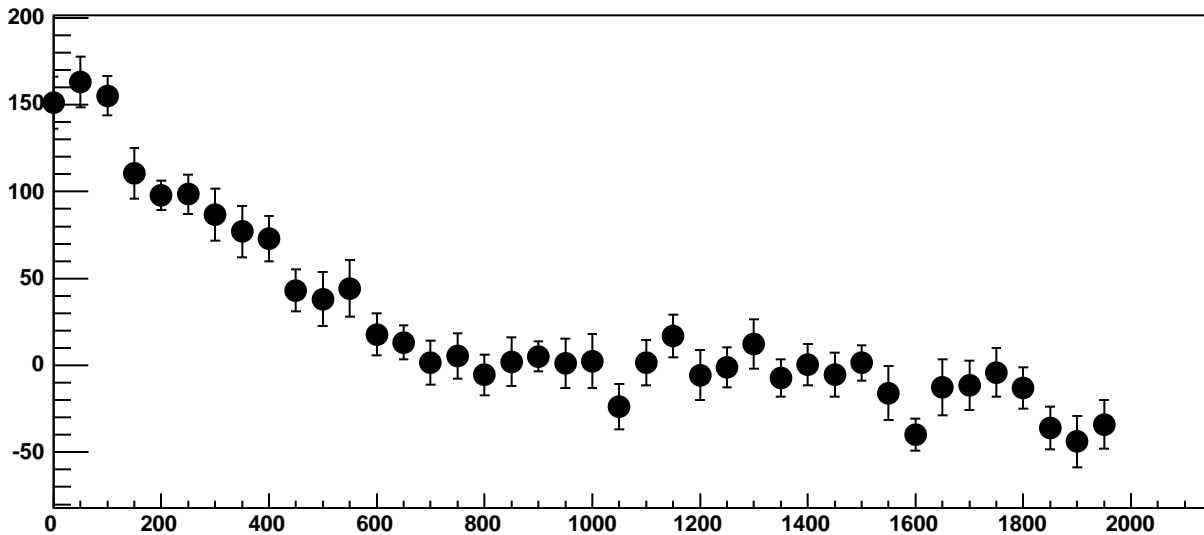


$\chi^2 / \text{ndf}$  7.133 / 11  
p0  $-984.2 \pm 19.4$   
p1  $0.2395 \pm 0.01743$

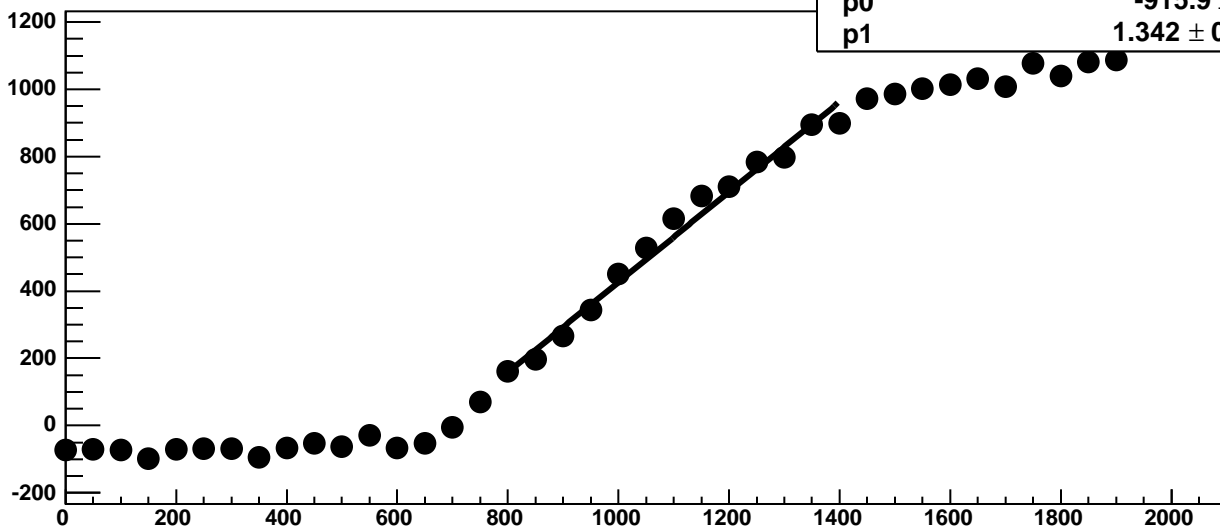
Chip 0, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC



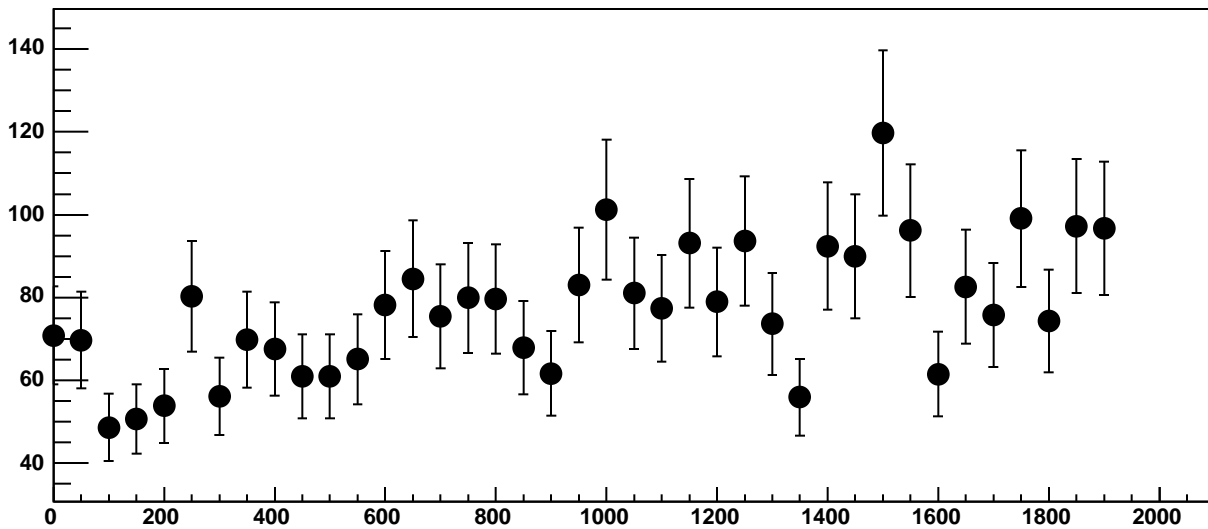
Chip 0, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC



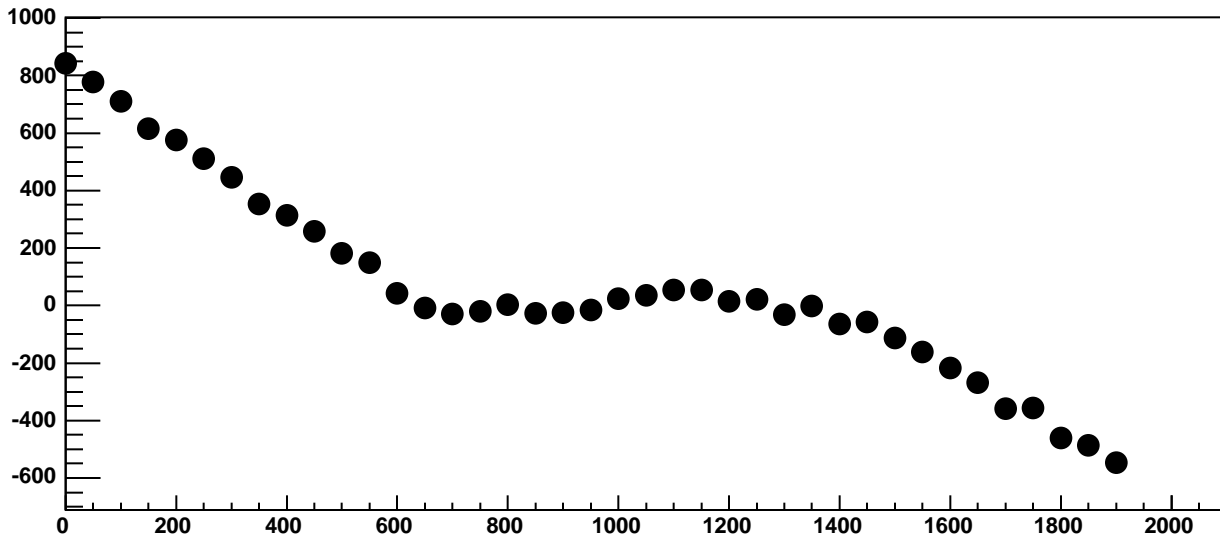
Chip 0, Channel 8, Enable 5, Hold=30, ADC Mean vs DAC



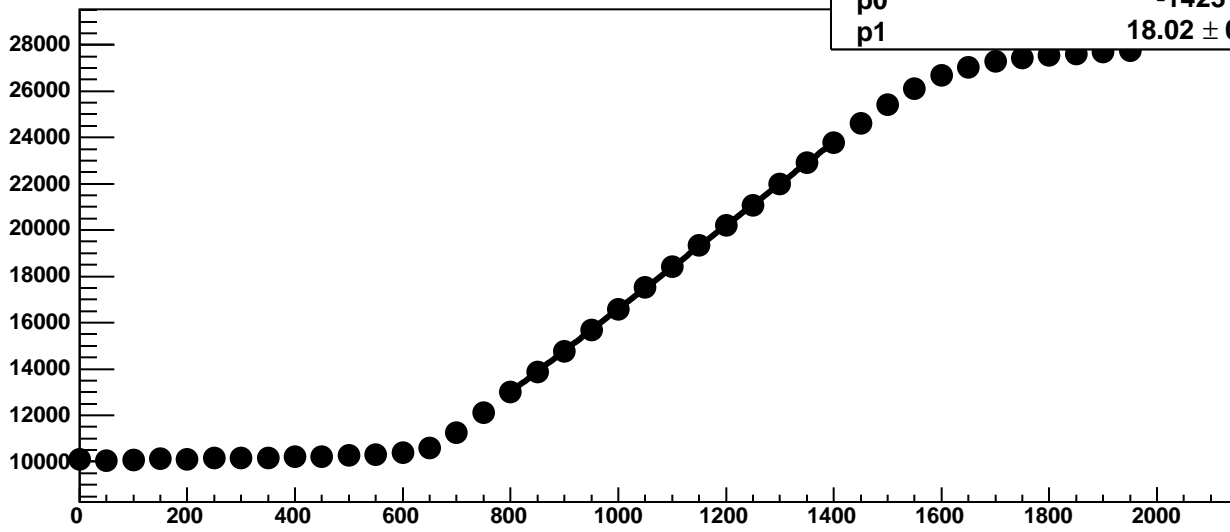
Chip 0, Channel 8, Enable 5, Hold=30, ADC Noise vs DAC



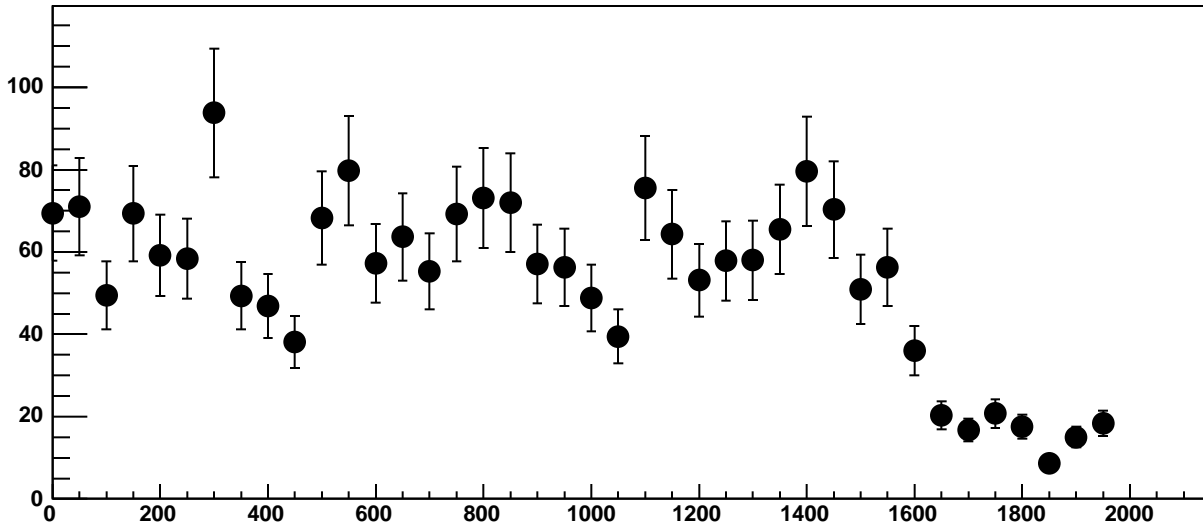
Chip 0, Channel 8, Enable 5, Hold=30, ADC Residuals vs DAC



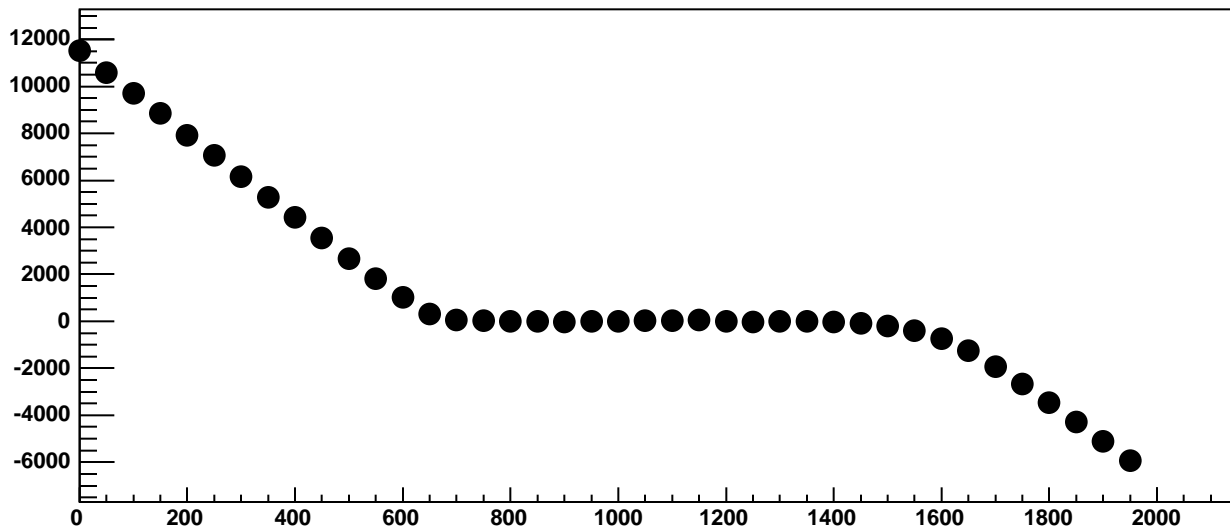
Chip 0, Channel 9, Enable 0!, Hold=30, ADC Mean vs DAC



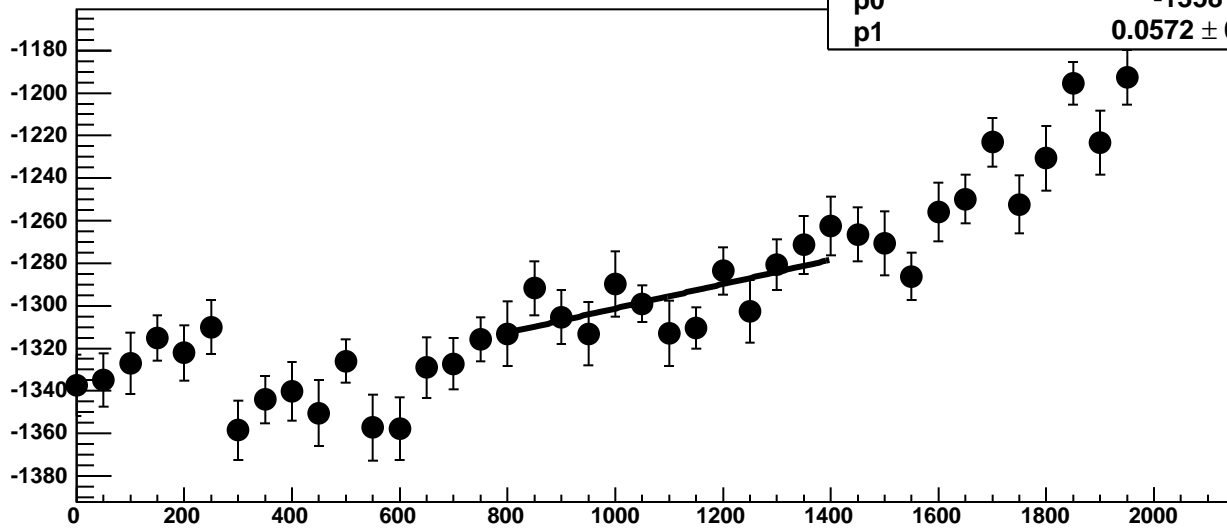
Chip 0, Channel 9, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 0, Channel 9, Enable 0!, Hold=30, ADC Residuals vs DAC

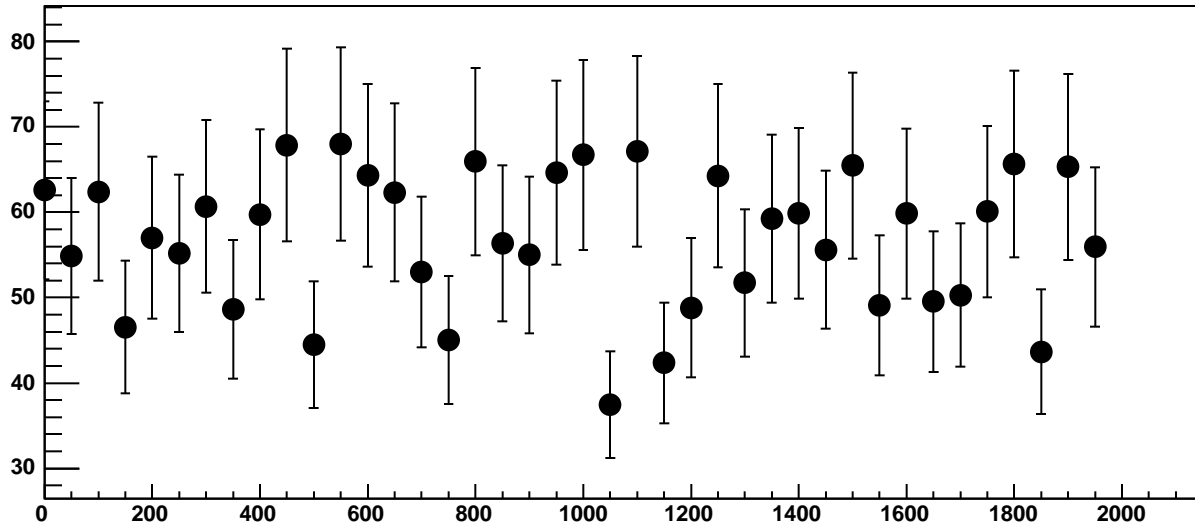


Chip 0, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC

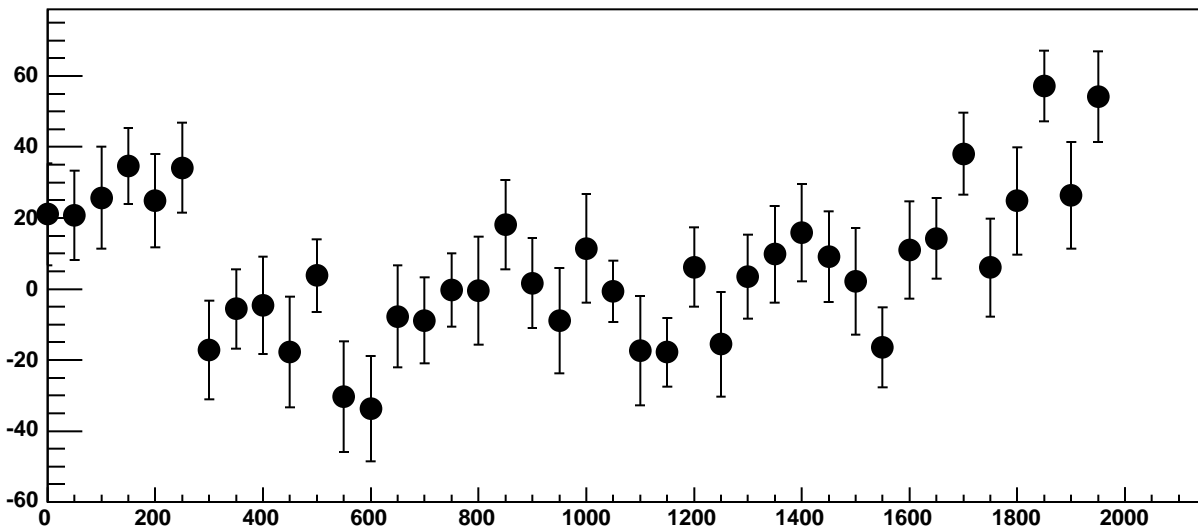


$\chi^2 / \text{ndf}$  10.98 / 11  
p0  $-1358 \pm 22.27$   
p1  $0.0572 \pm 0.01991$

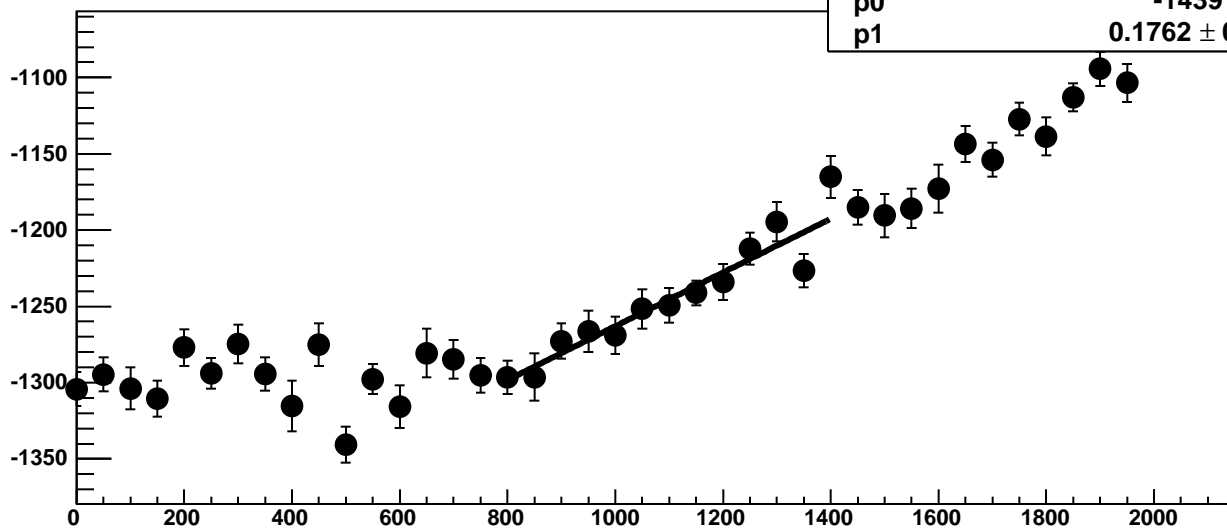
Chip 0, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC



Chip 0, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC

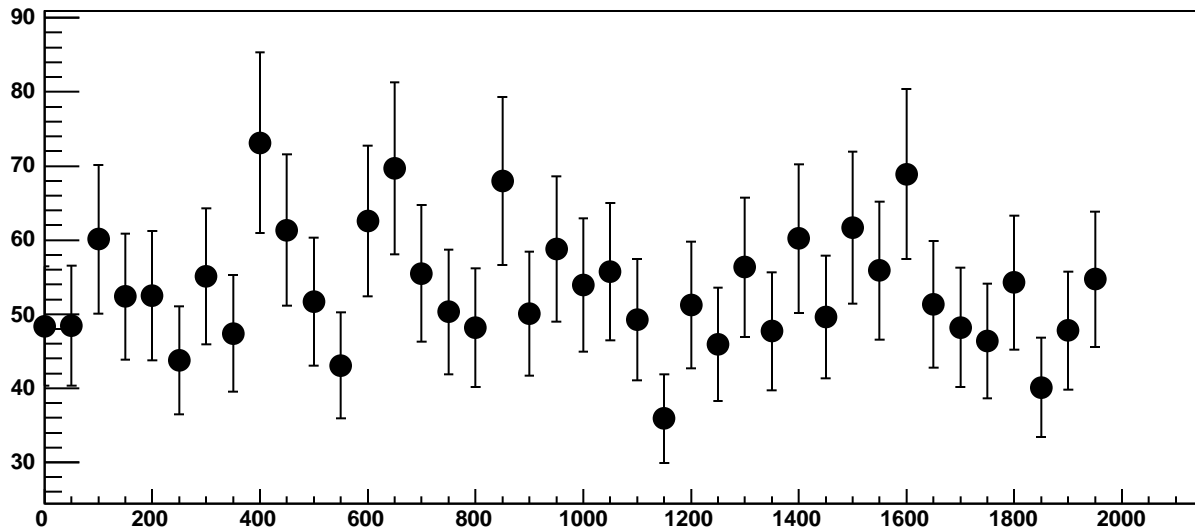


Chip 0, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC

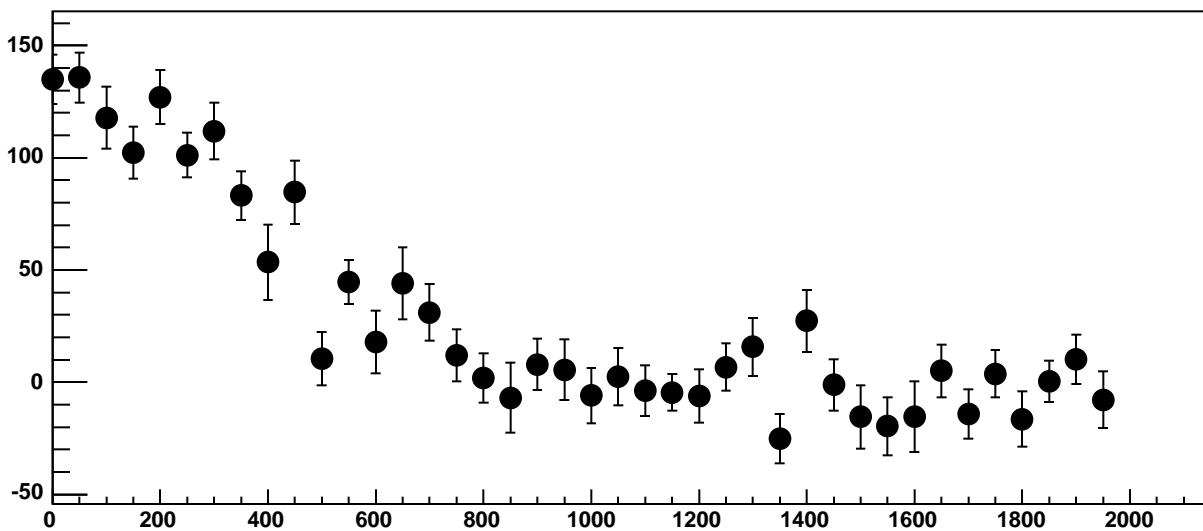


$\chi^2 / \text{ndf}$  12.9 / 11  
p0  $-1439 \pm 20.38$   
p1  $0.1762 \pm 0.01815$

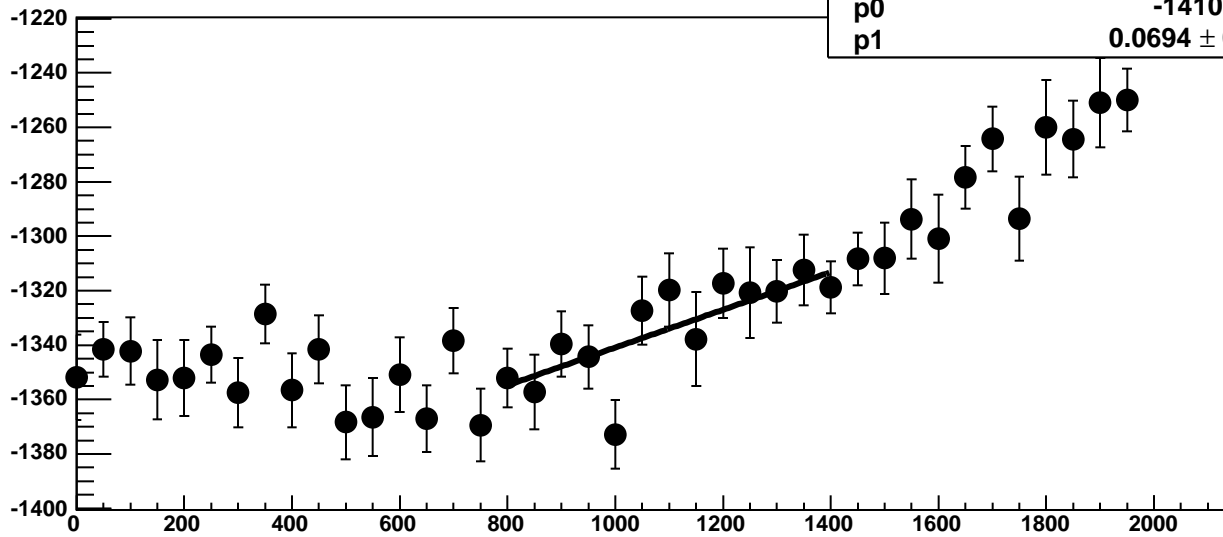
Chip 0, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC



Chip 0, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 0, Channel 9, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

10.08 / 11

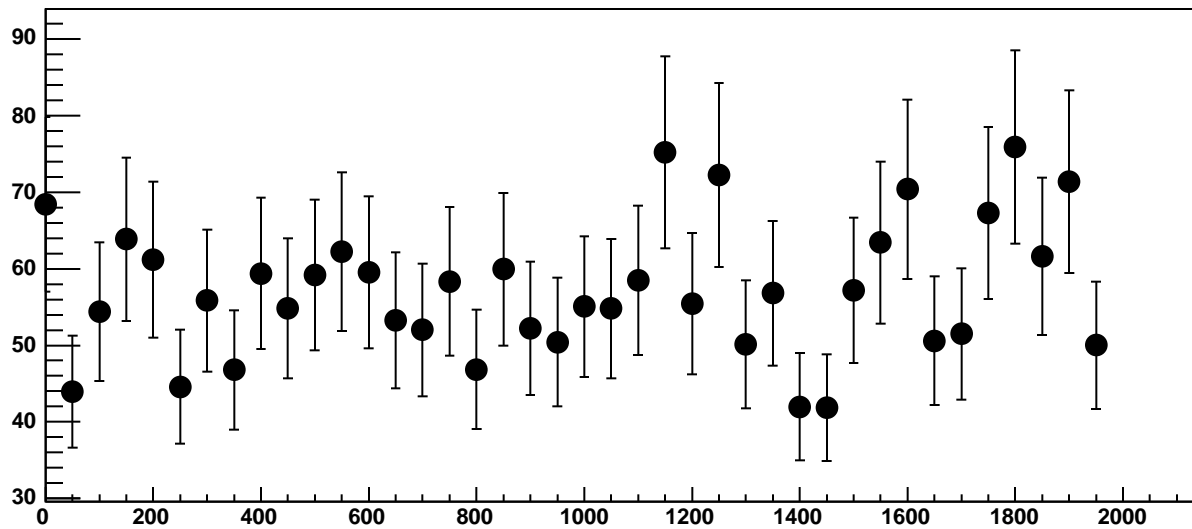
p0

$-1410 \pm 19.13$

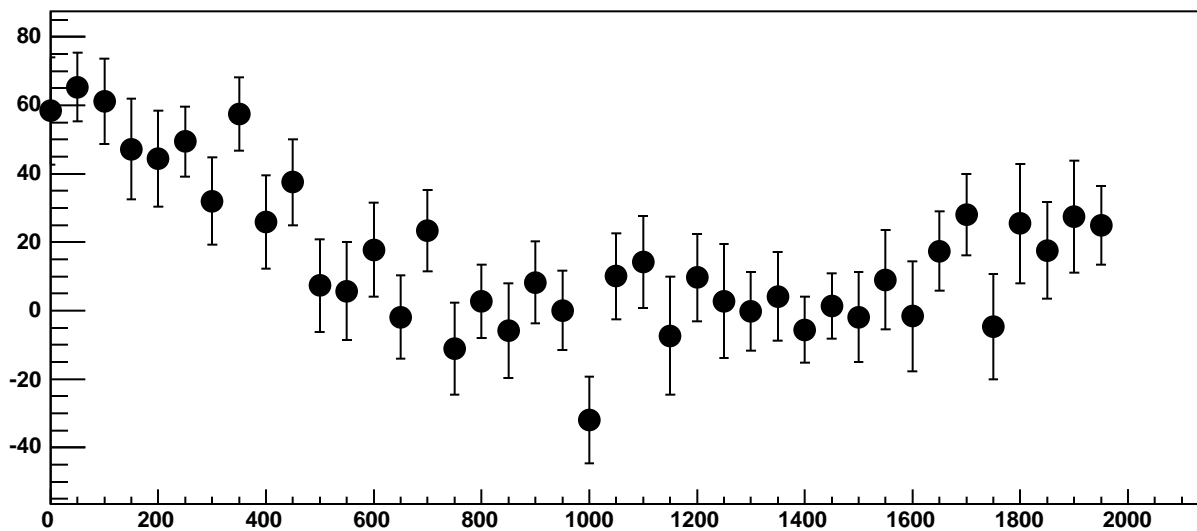
p1

$0.0694 \pm 0.01708$

Chip 0, Channel 9, Enable 3, Hold=30, ADC Noise vs DAC

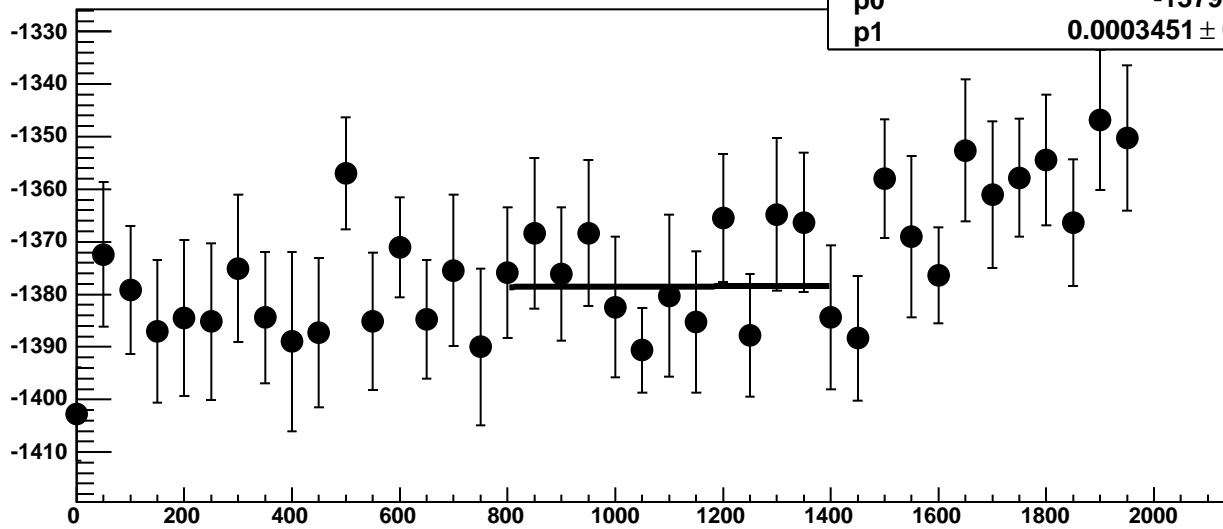


Chip 0, Channel 9, Enable 3, Hold=30, ADC Residuals vs DAC





Chip 0, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

7.467 / 11

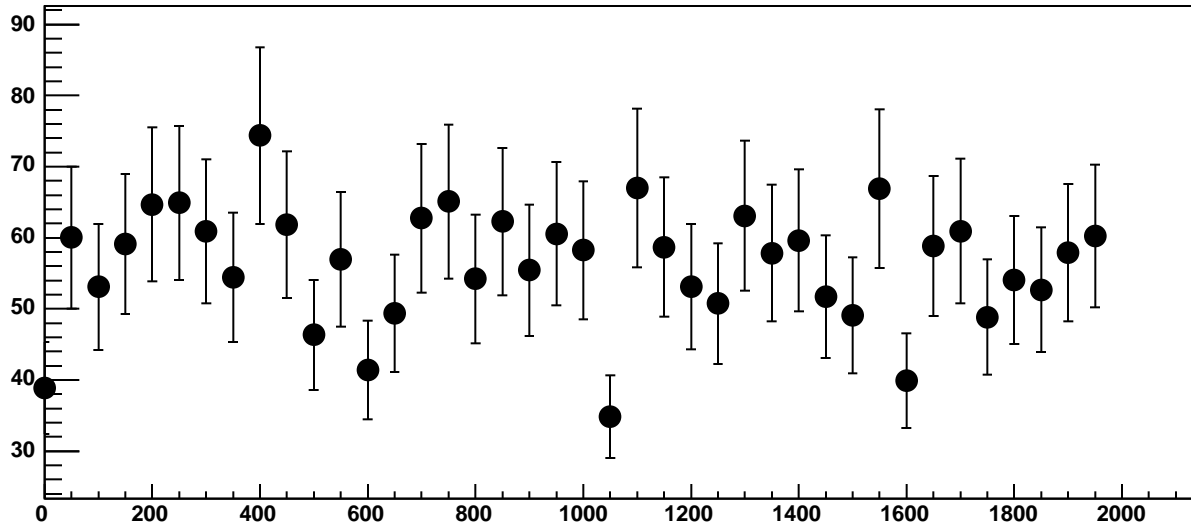
p0

$-1379 \pm 21.67$

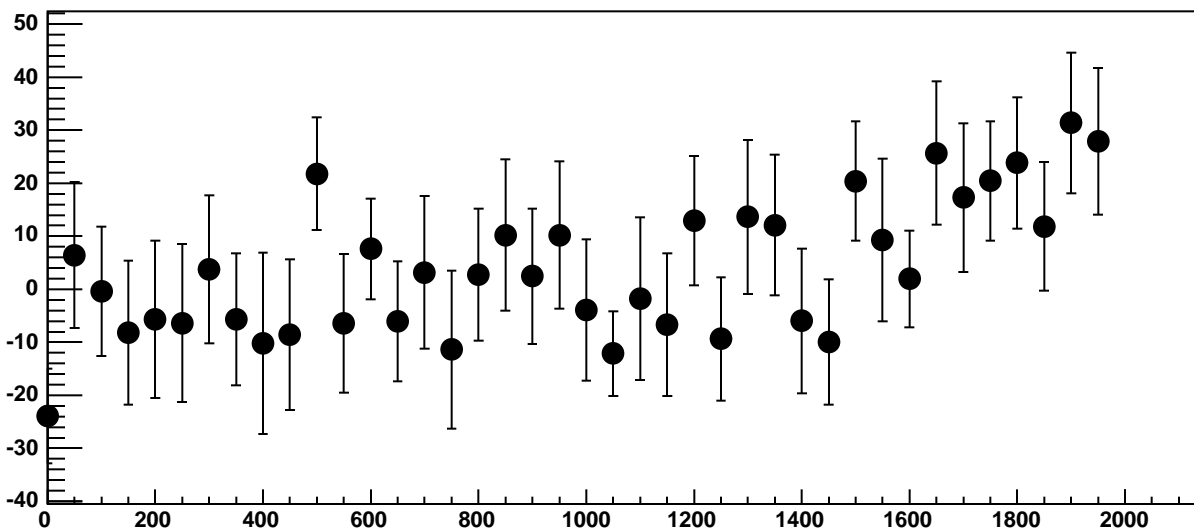
p1

$0.0003451 \pm 0.01955$

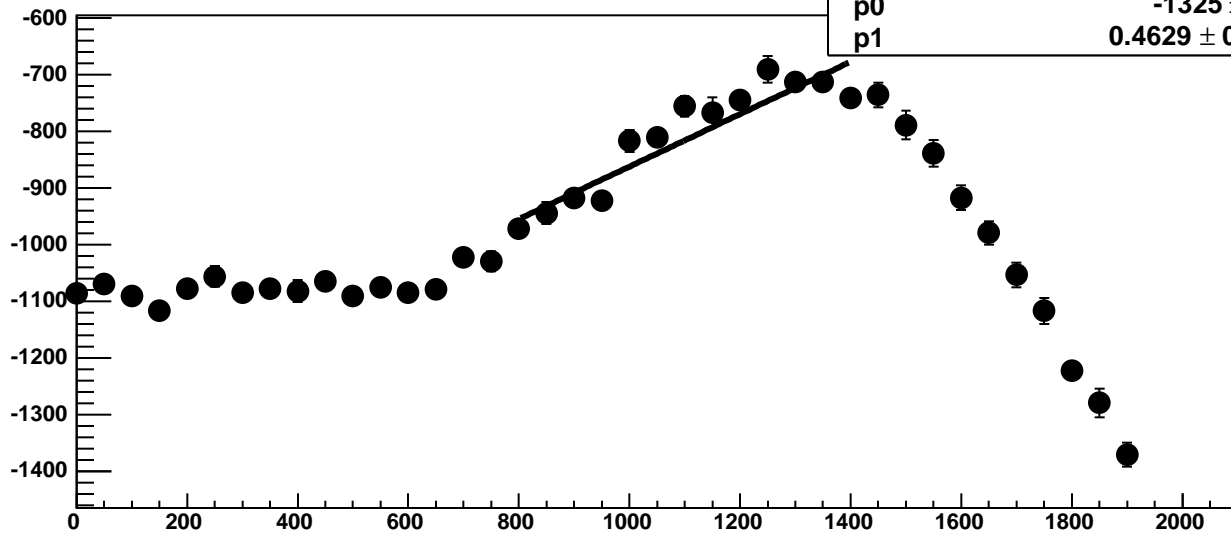
Chip 0, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC



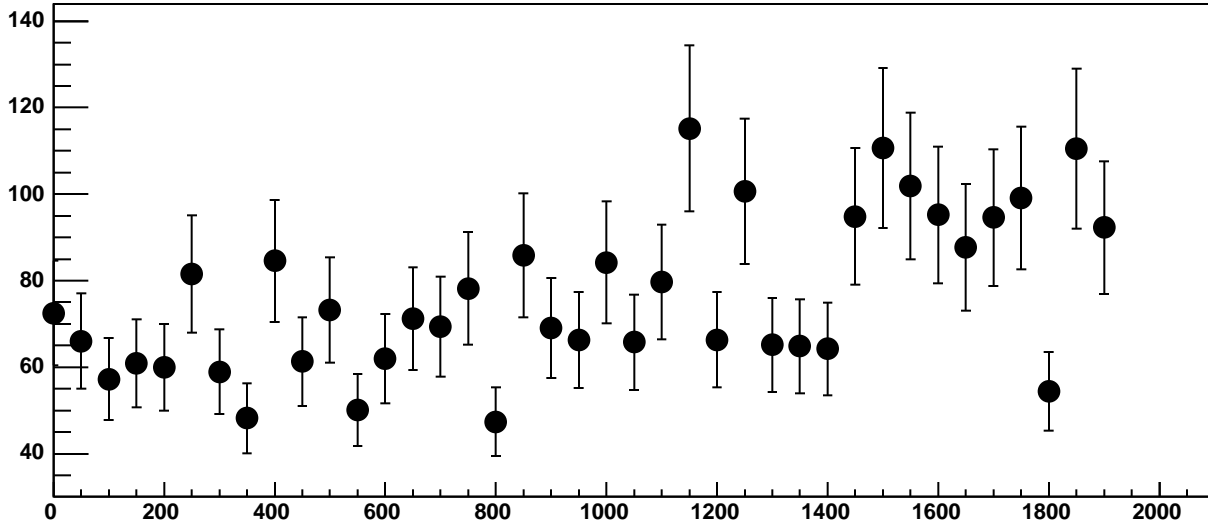
Chip 0, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC



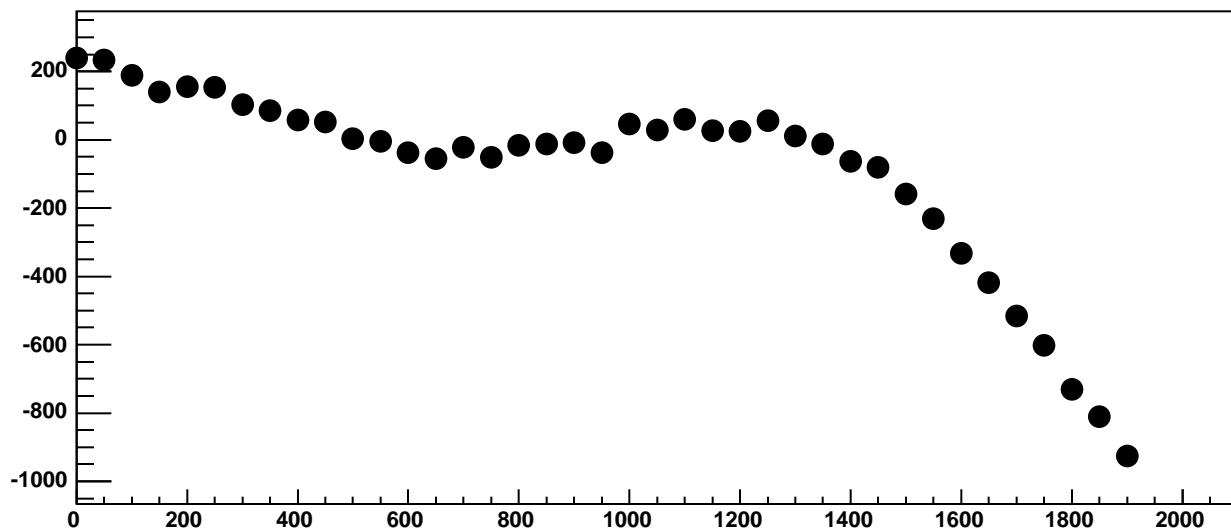
Chip 0, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC



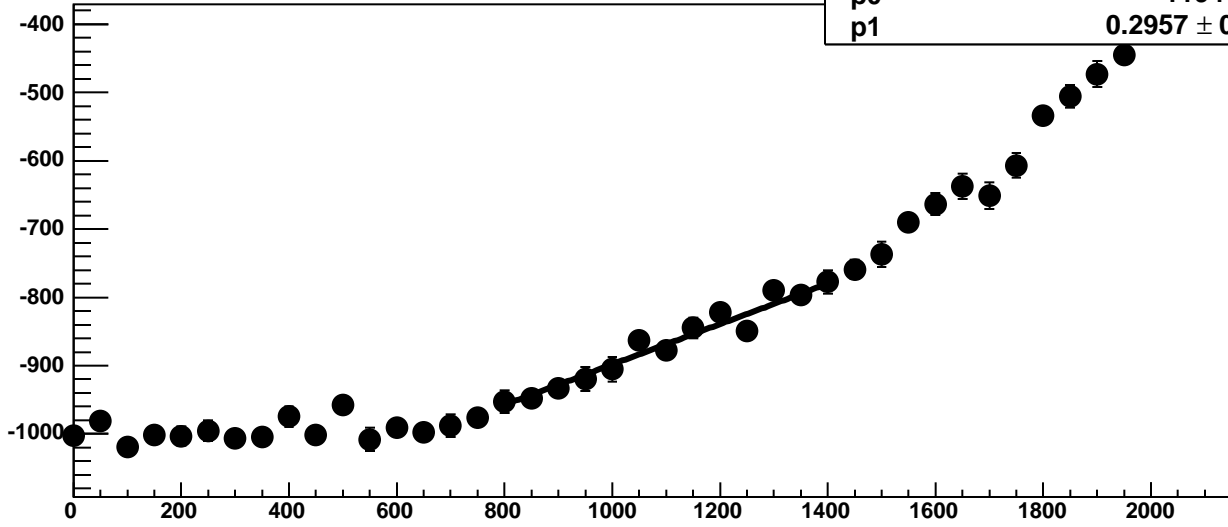
Chip 0, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC



Chip 0, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC

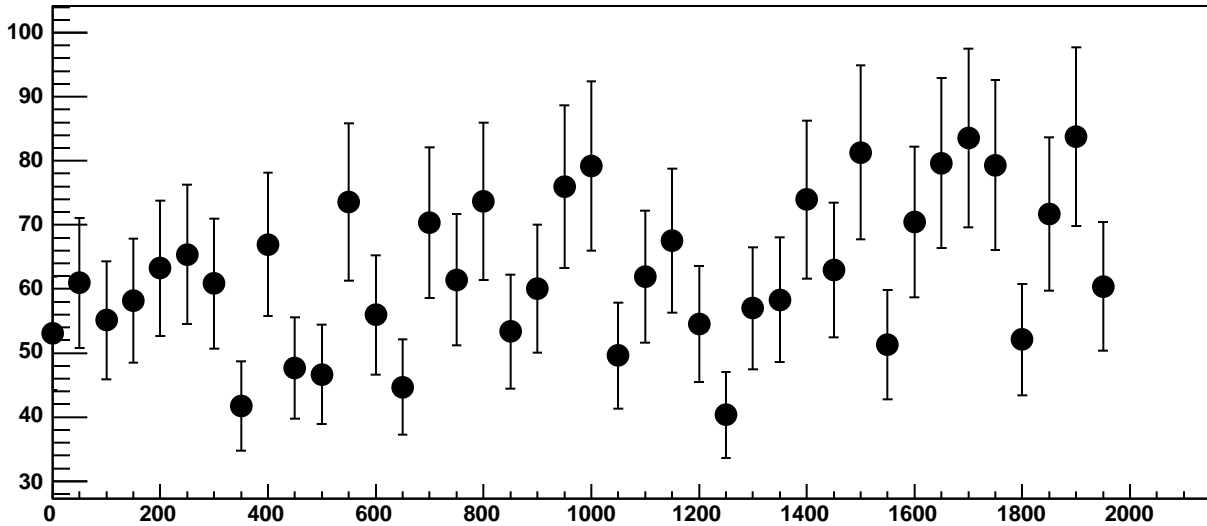


Chip 0, Channel 10, Enable 0, Hold=30, ADC Mean vs DAC

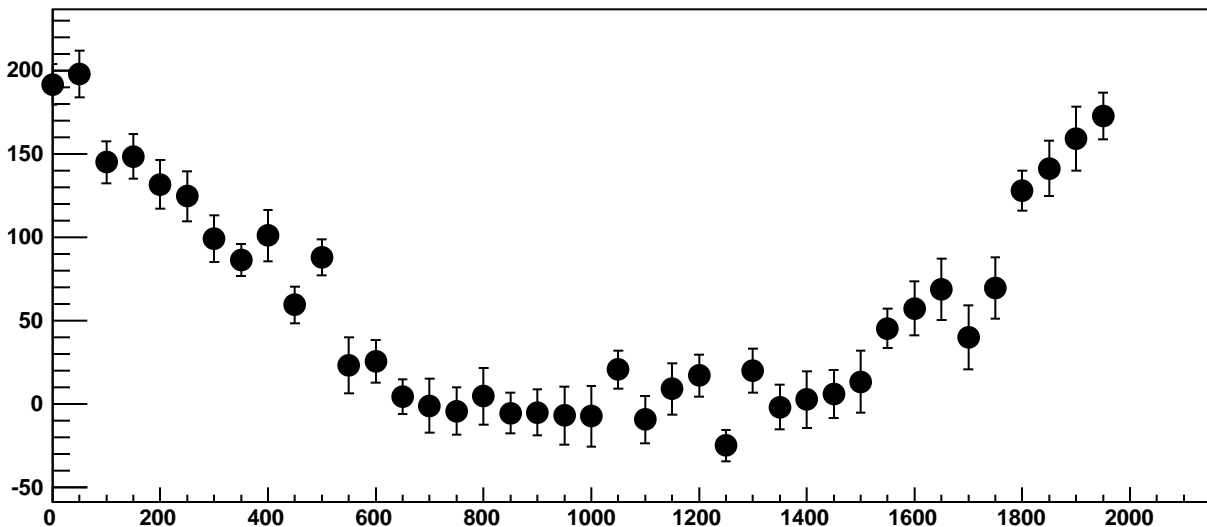


$\chi^2 / \text{ndf}$  16.27 / 11  
p0  $-1194 \pm 23.71$   
p1  $0.2957 \pm 0.02095$

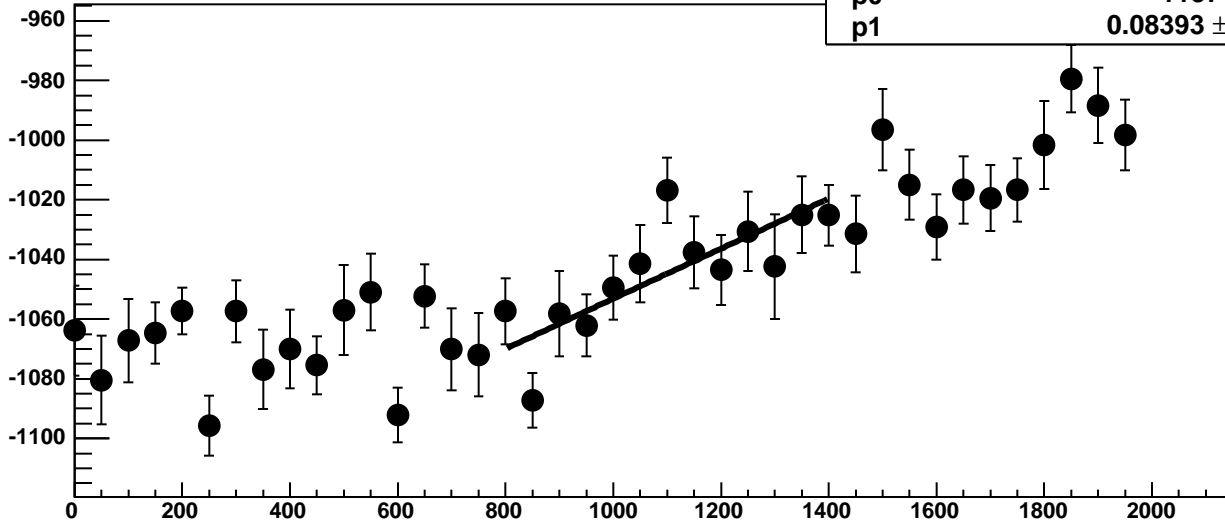
Chip 0, Channel 10, Enable 0, Hold=30, ADC Noise vs DAC



Chip 0, Channel 10, Enable 0, Hold=30, ADC Residuals vs DAC

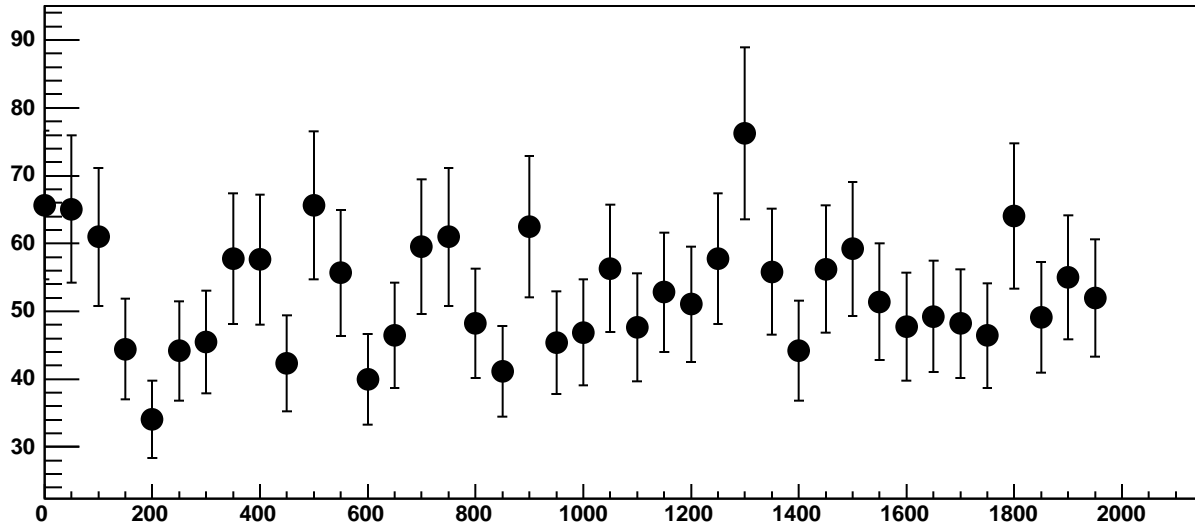


Chip 0, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC

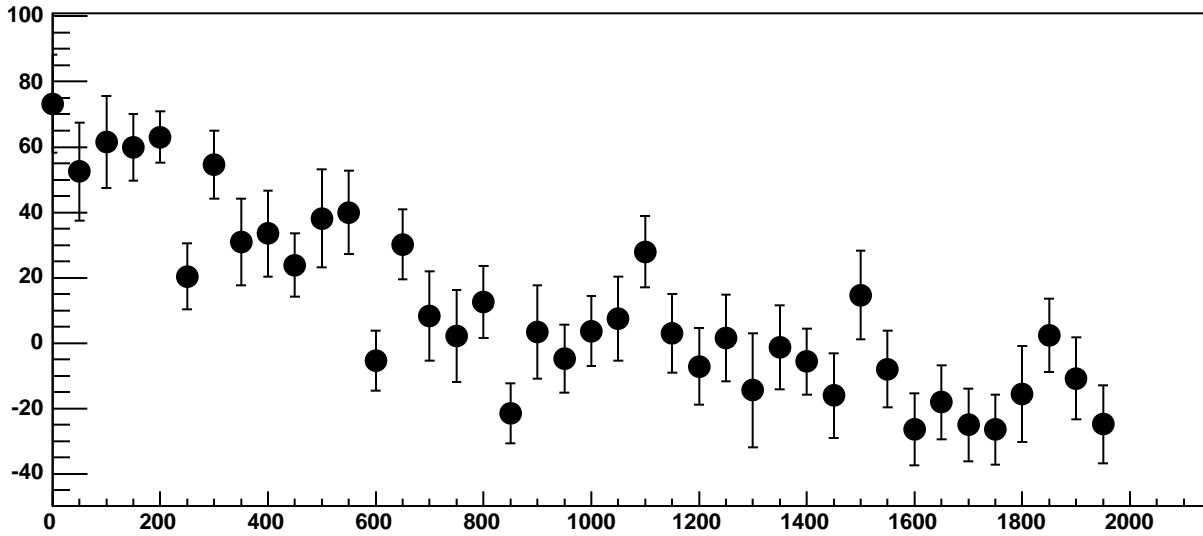


$\chi^2 / \text{ndf}$  15.47 / 11  
p0  $-1137 \pm 18.43$   
p1  $0.08393 \pm 0.0168$

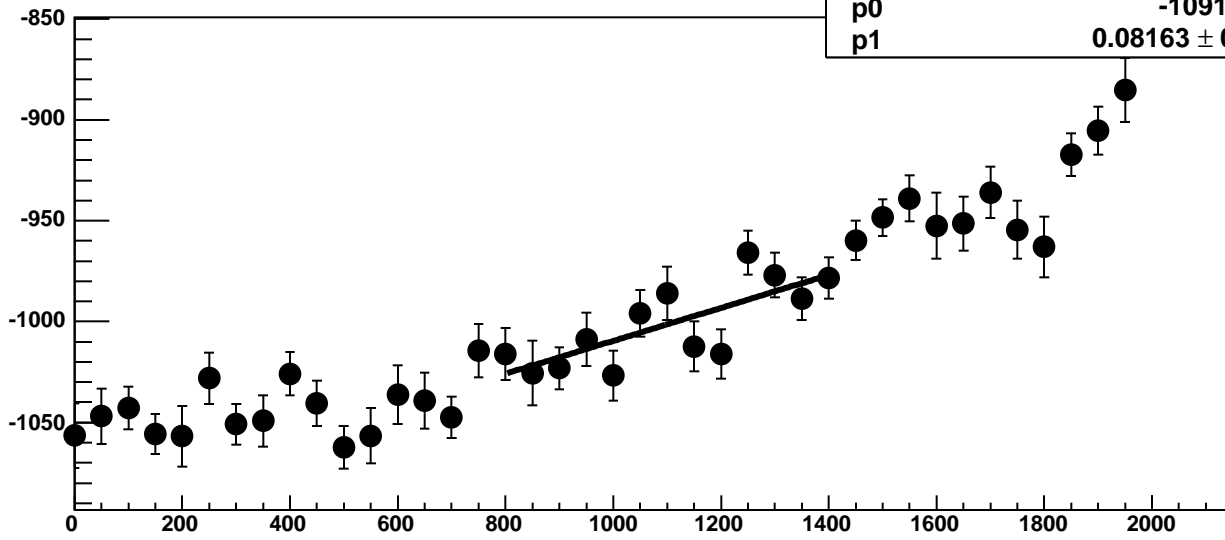
Chip 0, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC



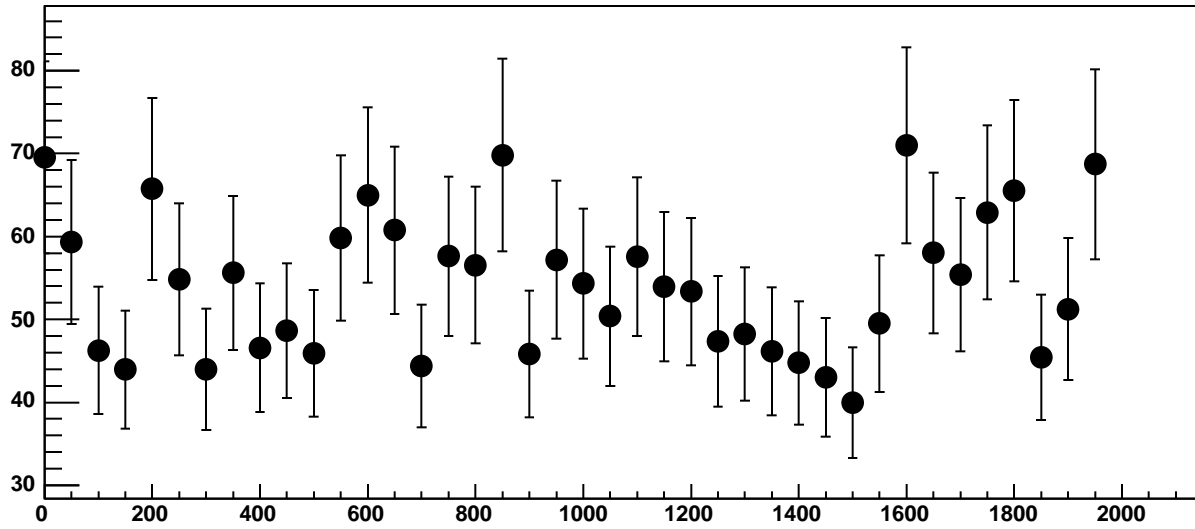
Chip 0, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC



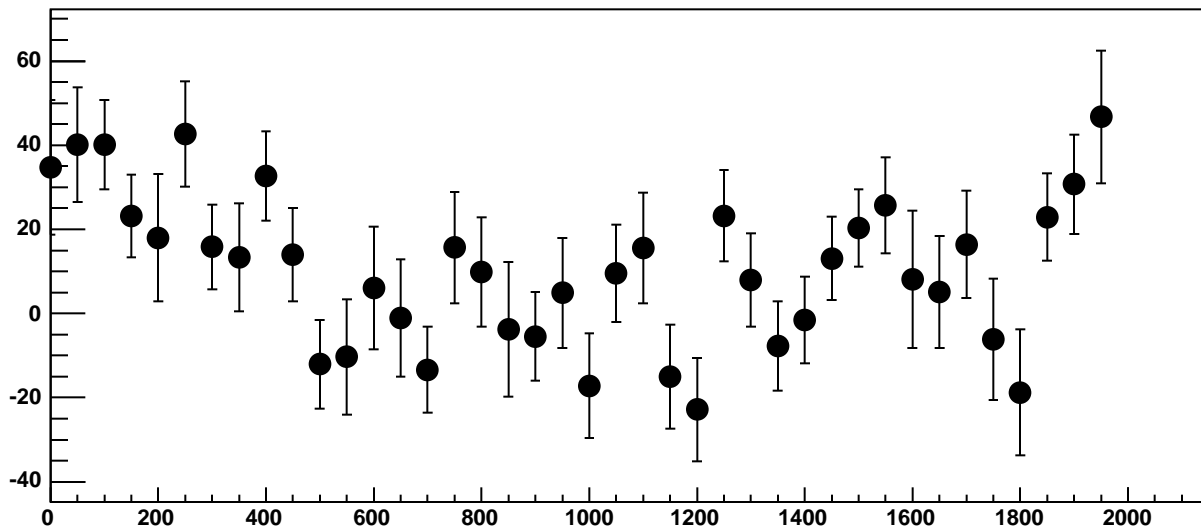
Chip 0, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC



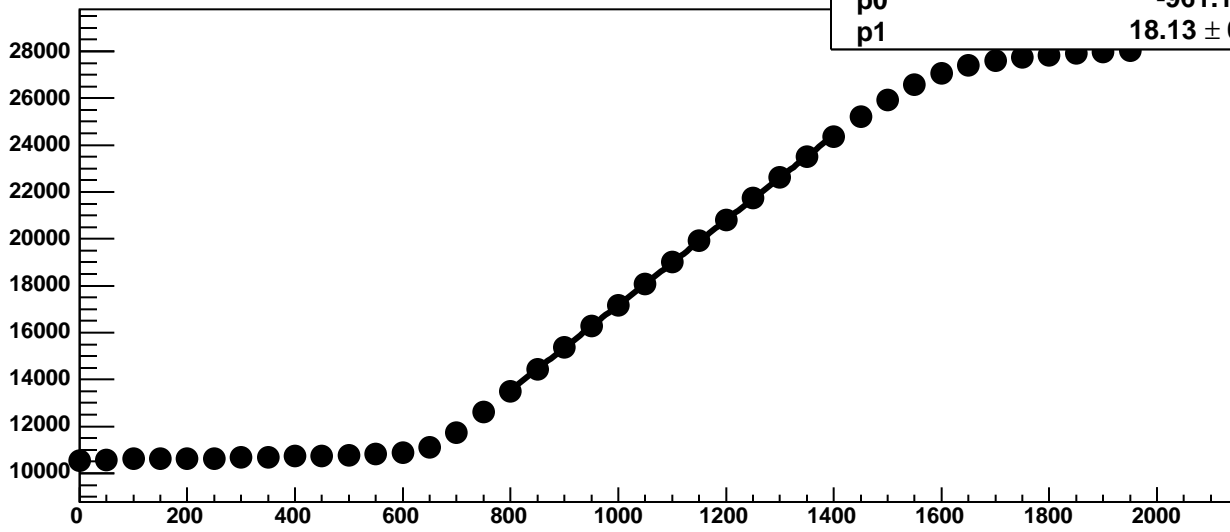
Chip 0, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC



Chip 0, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC

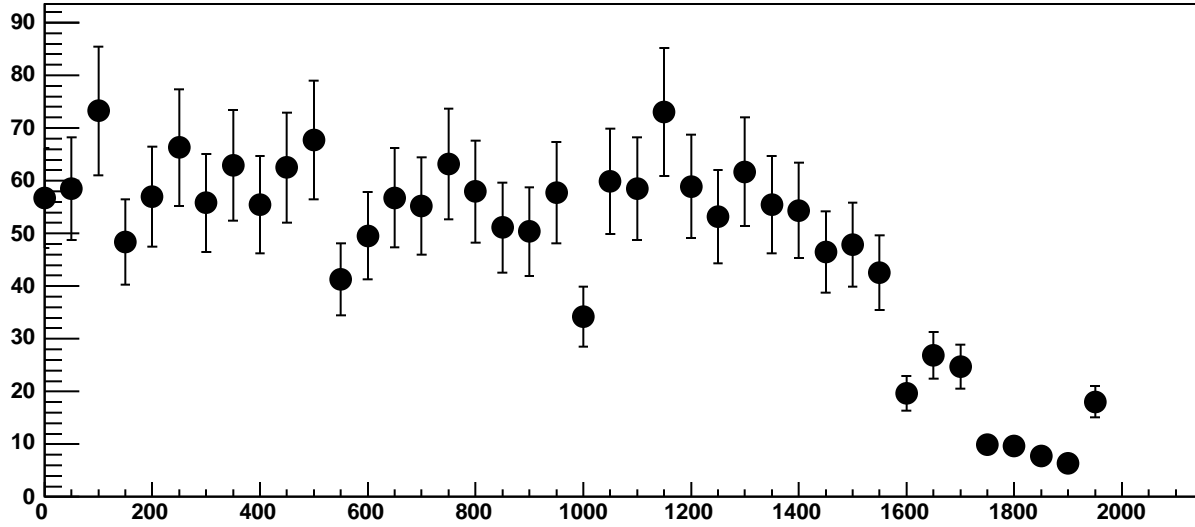


Chip 0, Channel 10, Enable 3!, Hold=30, ADC Mean vs DAC

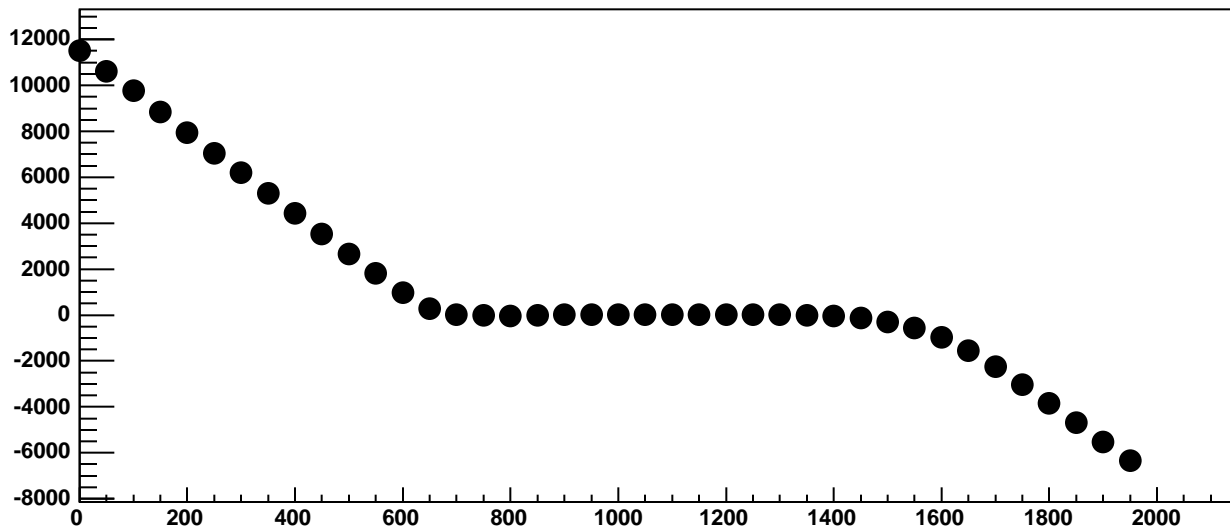


$\chi^2 / \text{ndf}$  43.82 / 11  
p0  $-961.1 \pm 20.31$   
p1  $18.13 \pm 0.01851$

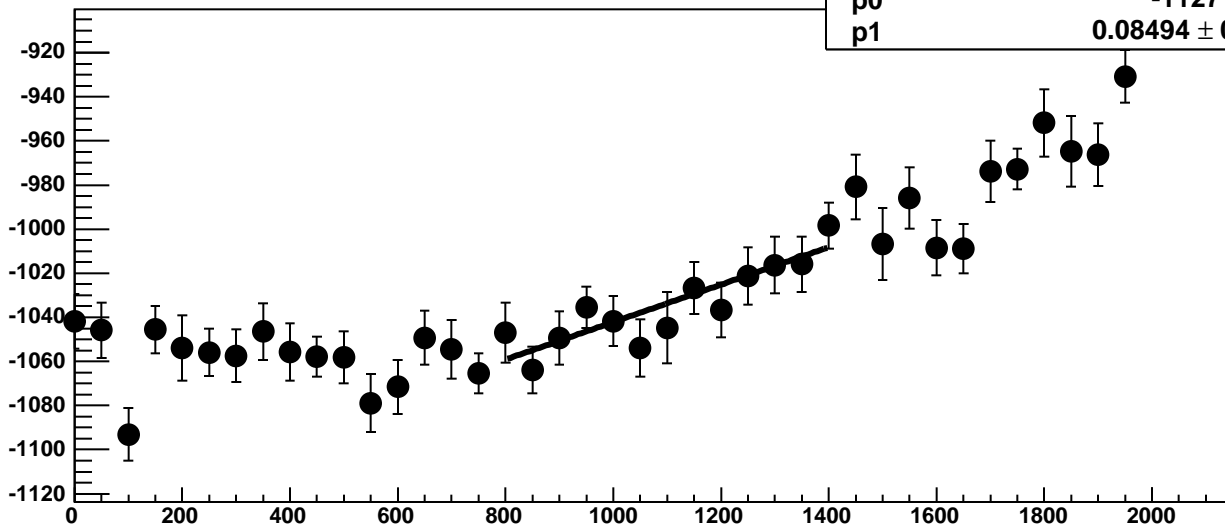
Chip 0, Channel 10, Enable 3!, Hold=30, ADC Noise vs DAC



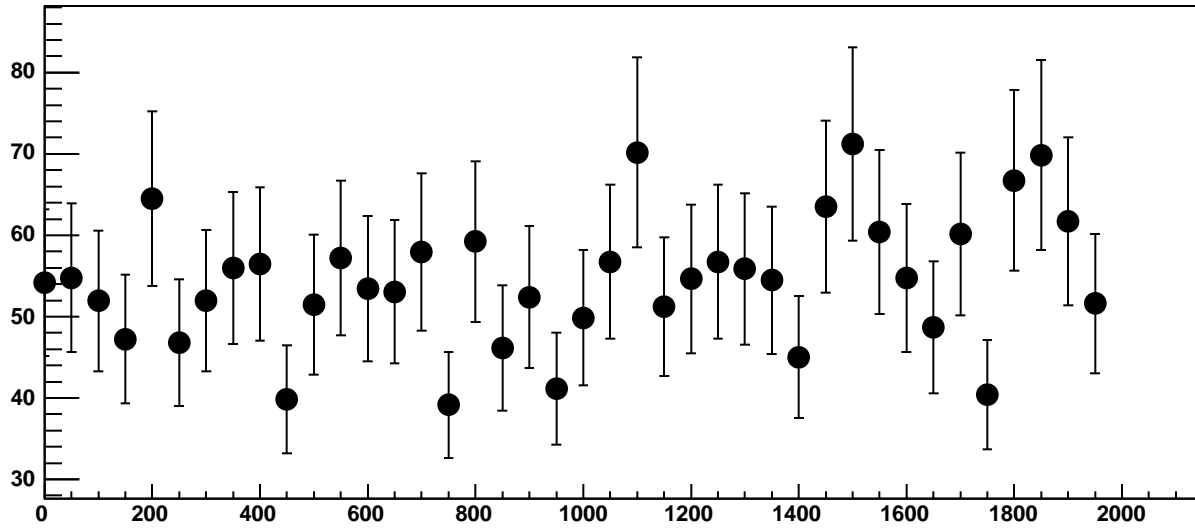
Chip 0, Channel 10, Enable 3!, Hold=30, ADC Residuals vs DAC



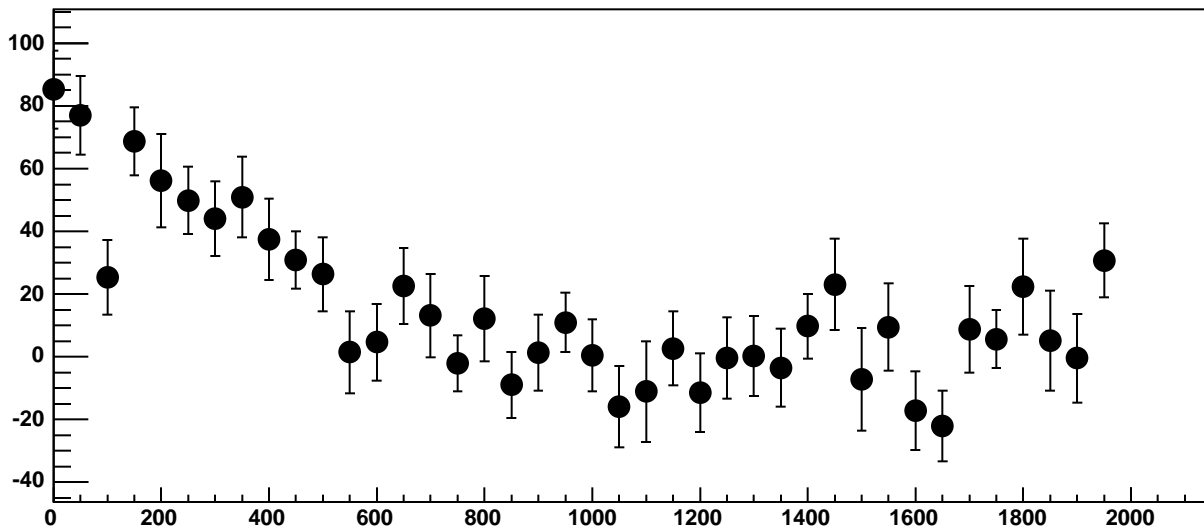
Chip 0, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC



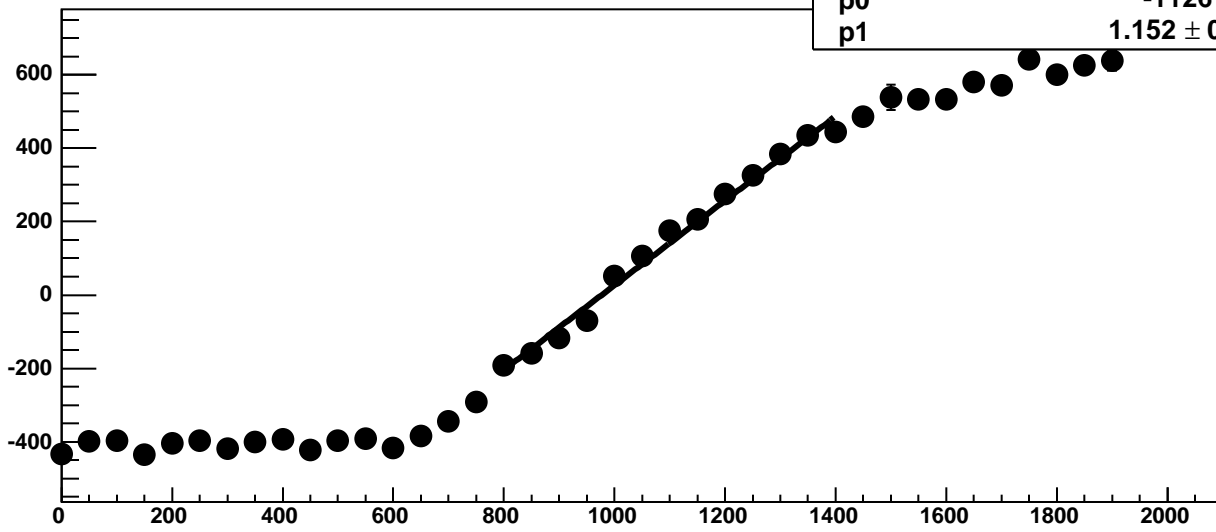
Chip 0, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC



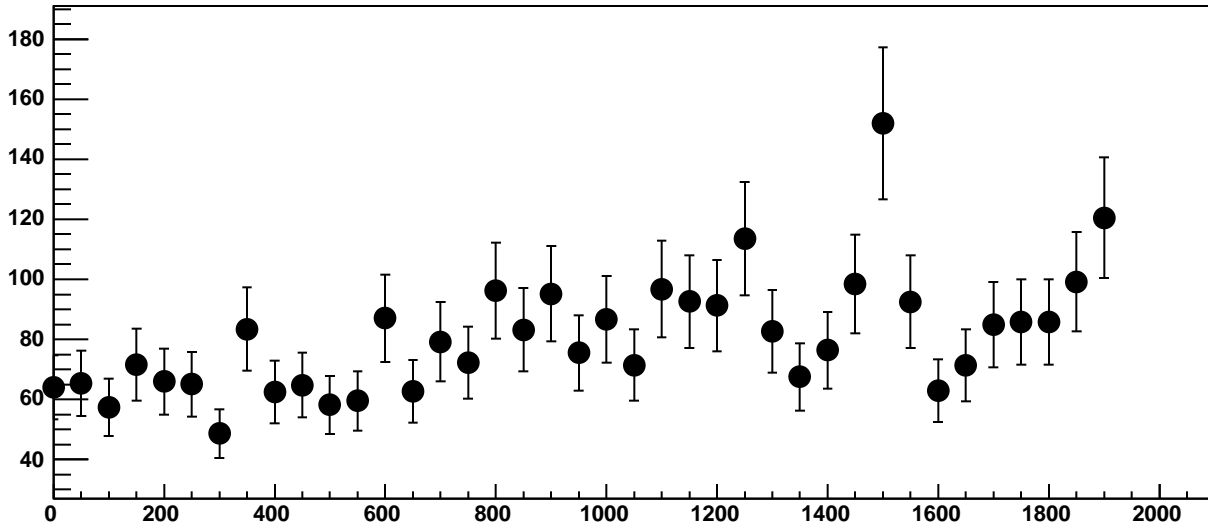
Chip 0, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC



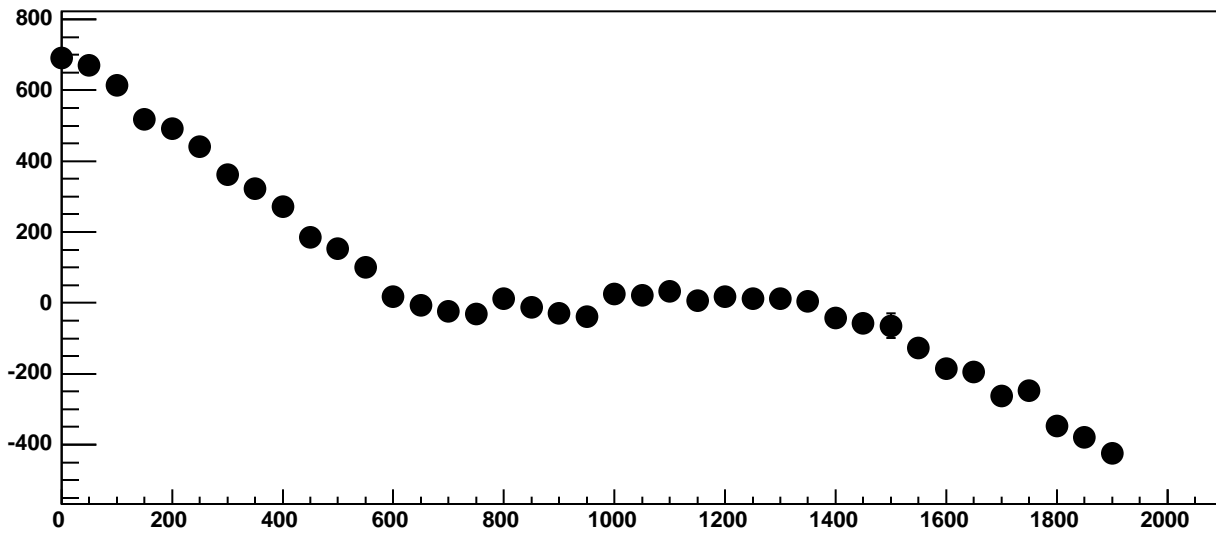
Chip 0, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC



Chip 0, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC

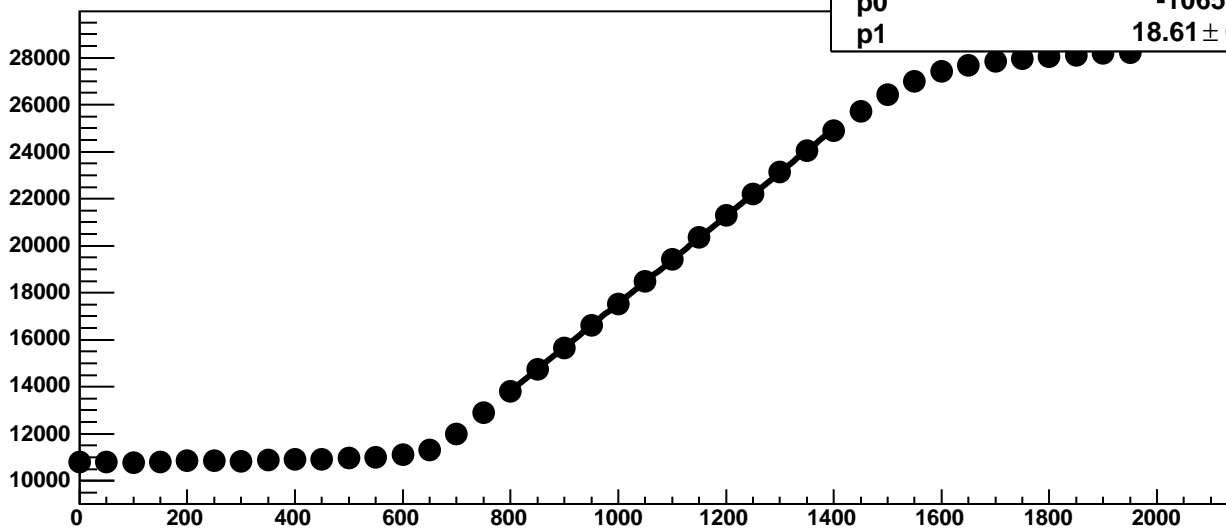


Chip 0, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC

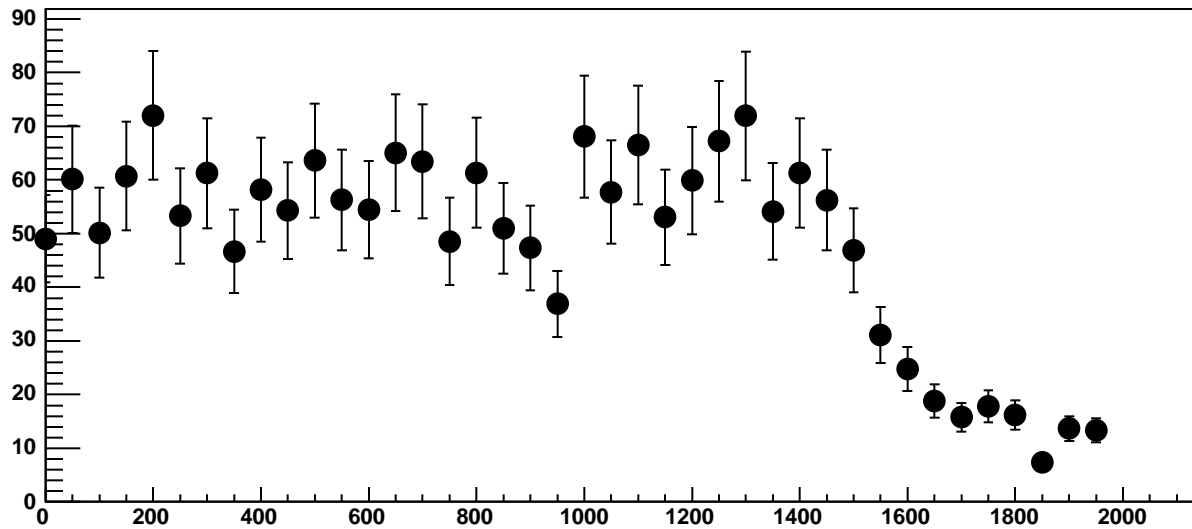




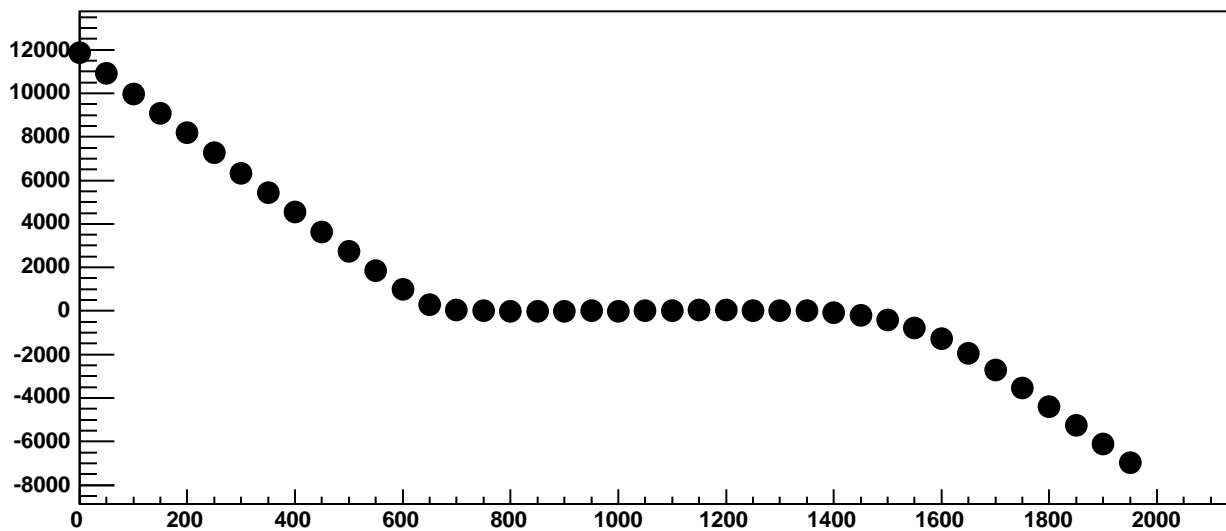
Chip 0, Channel 11, Enable 0!, Hold=30, ADC Mean vs DAC



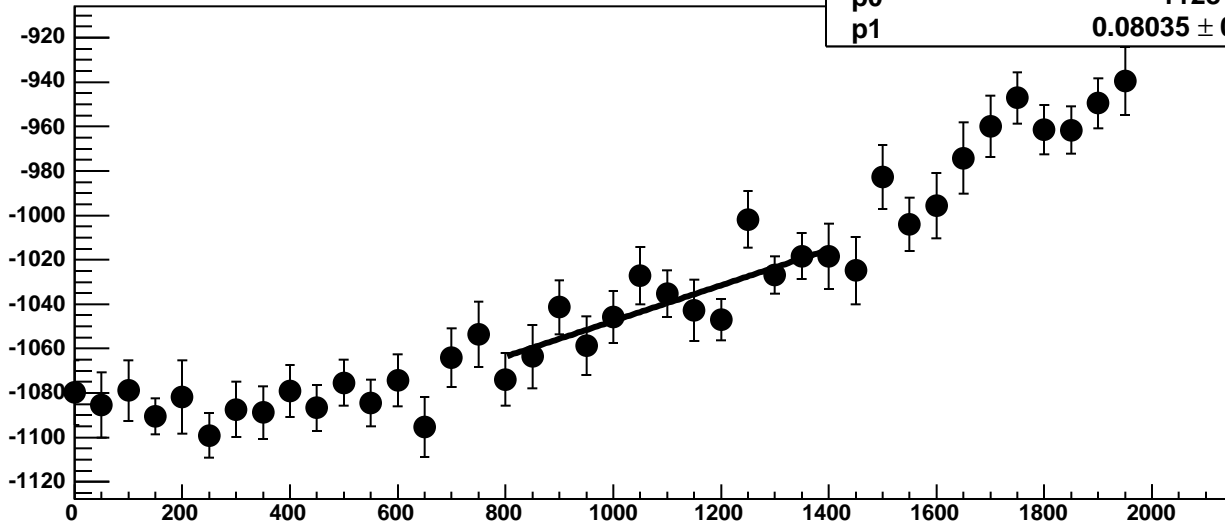
Chip 0, Channel 11, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 0, Channel 11, Enable 0!, Hold=30, ADC Residuals vs DAC

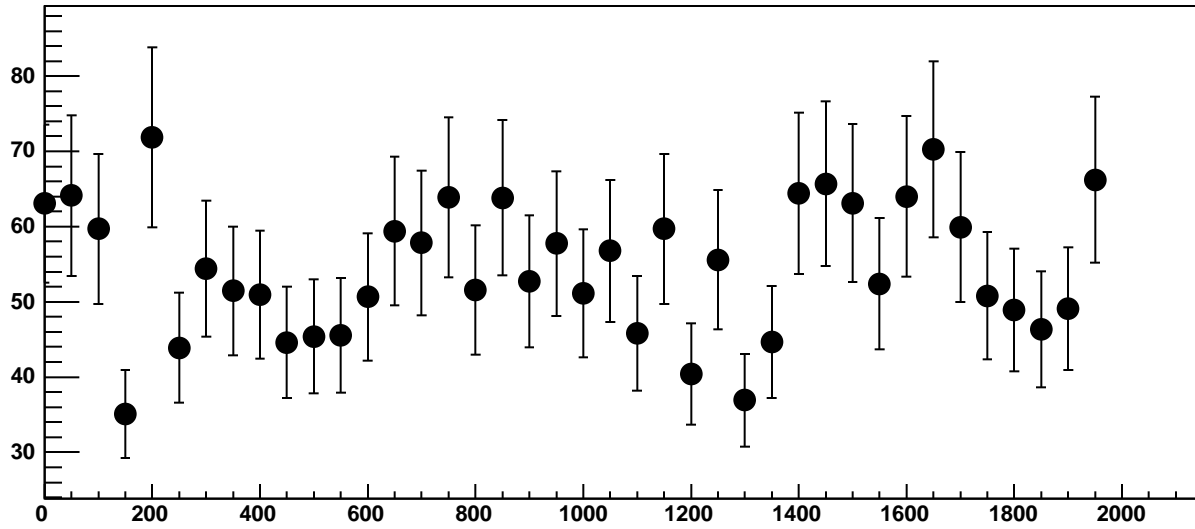


Chip 0, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC

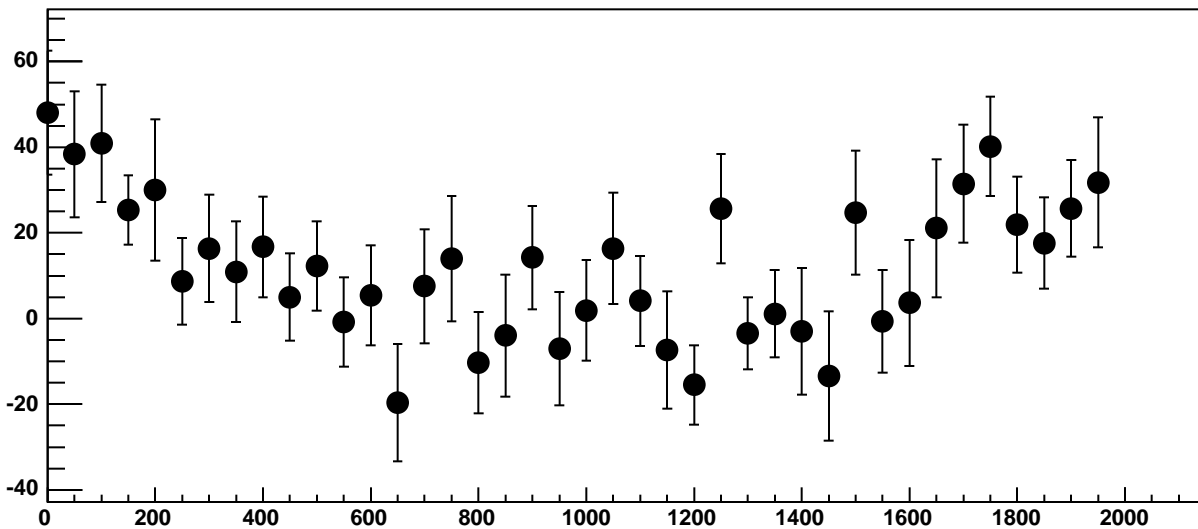


$\chi^2 / \text{ndf}$  11.58 / 11  
p0  $-1128 \pm 20.05$   
p1  $0.08035 \pm 0.01762$

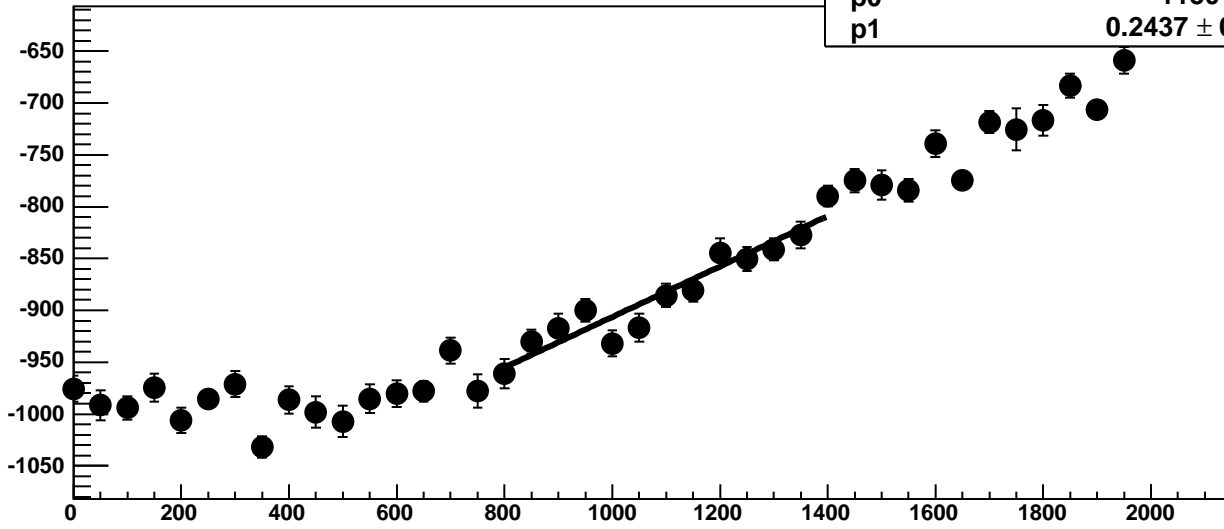
Chip 0, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC



Chip 0, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC

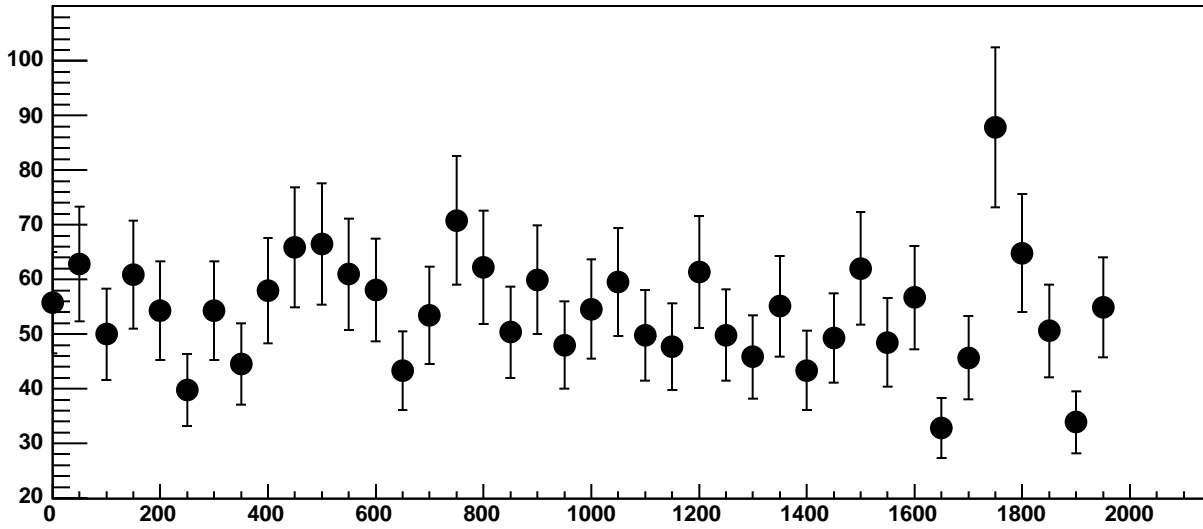


Chip 0, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC

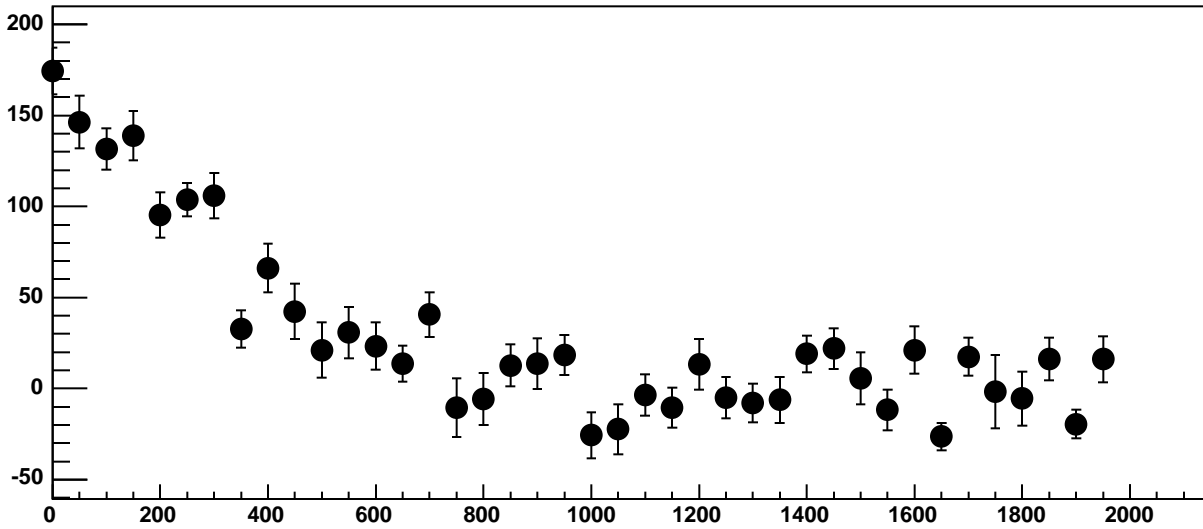


$\chi^2 / \text{ndf}$  18.62 / 11  
p0  $-1150 \pm 19.94$   
p1  $0.2437 \pm 0.01754$

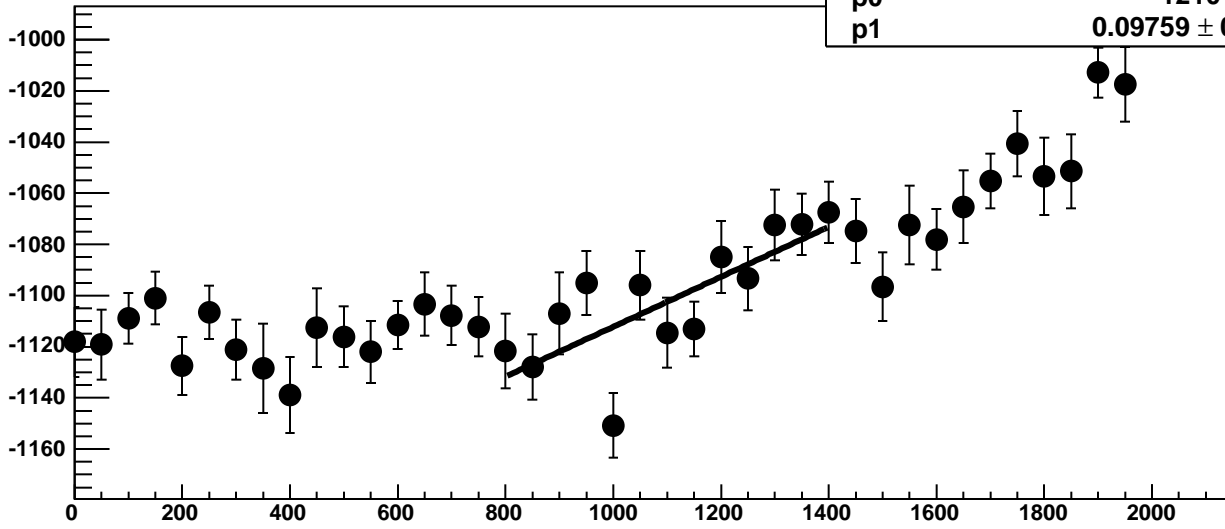
Chip 0, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC



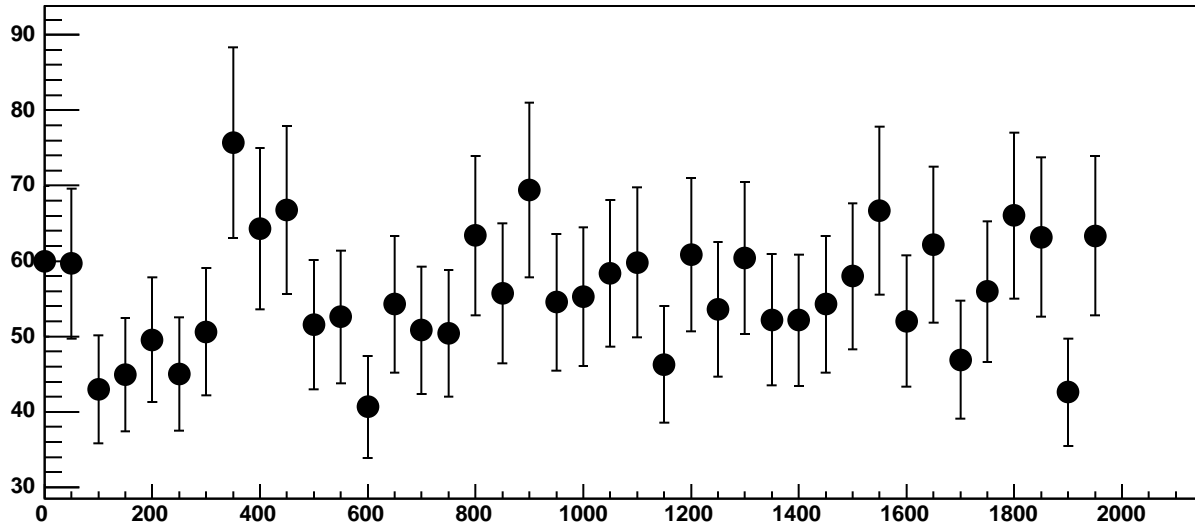
Chip 0, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC



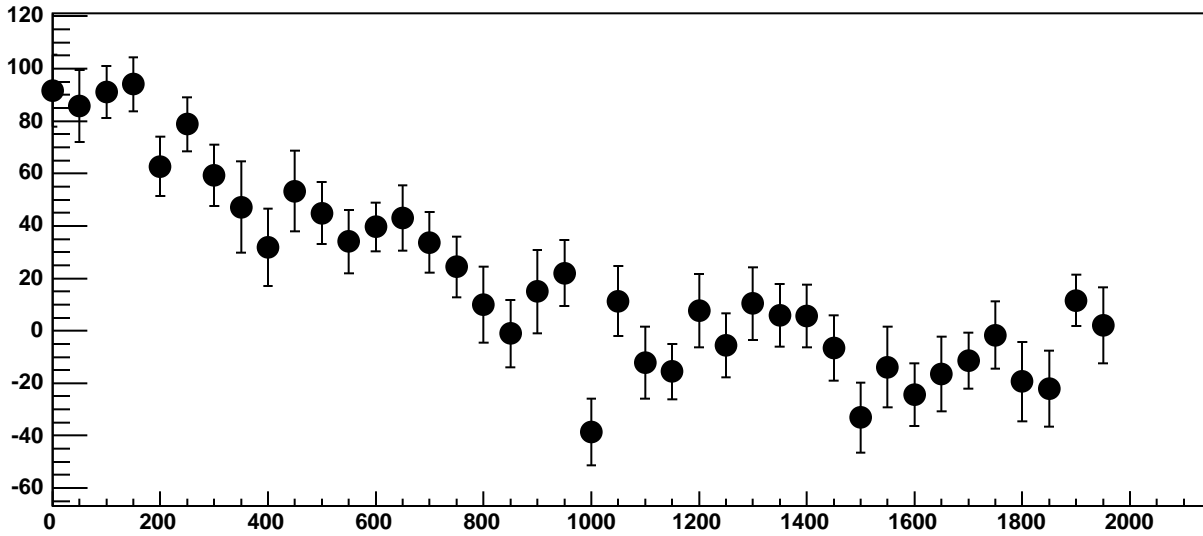
Chip 0, Channel 11, Enable 3, Hold=30, ADC Mean vs DAC



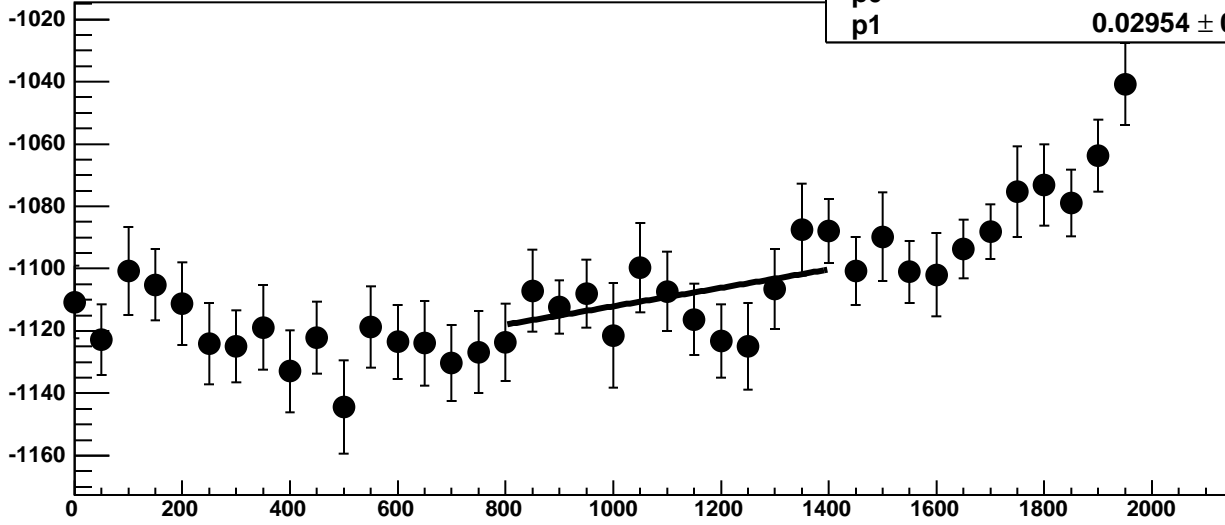
Chip 0, Channel 11, Enable 3, Hold=30, ADC Noise vs DAC



Chip 0, Channel 11, Enable 3, Hold=30, ADC Residuals vs DAC



Chip 0, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

9.23 / 11

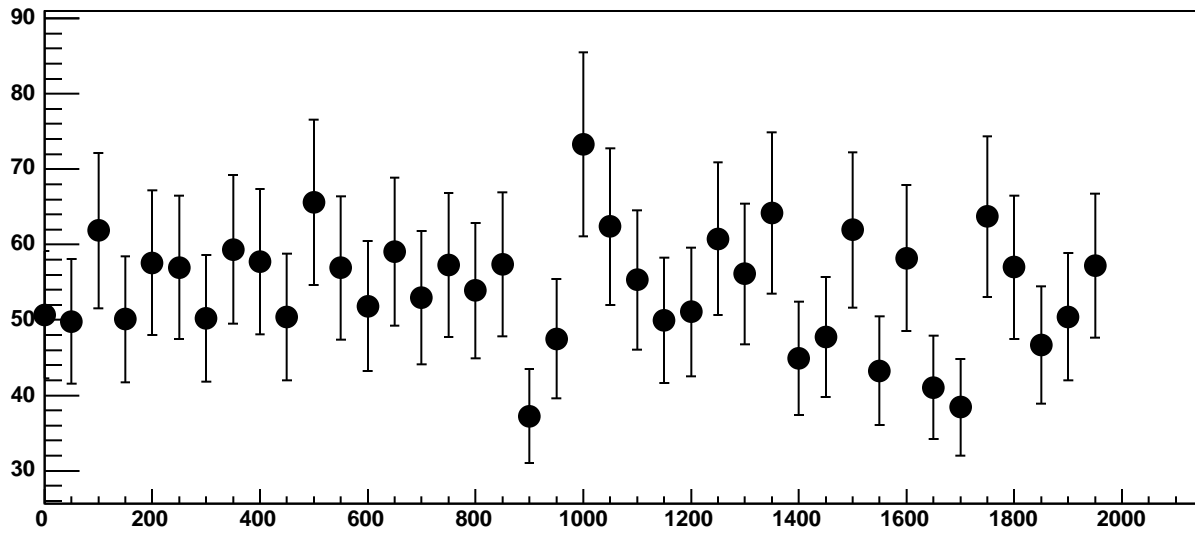
p0

$-1142 \pm 19.23$

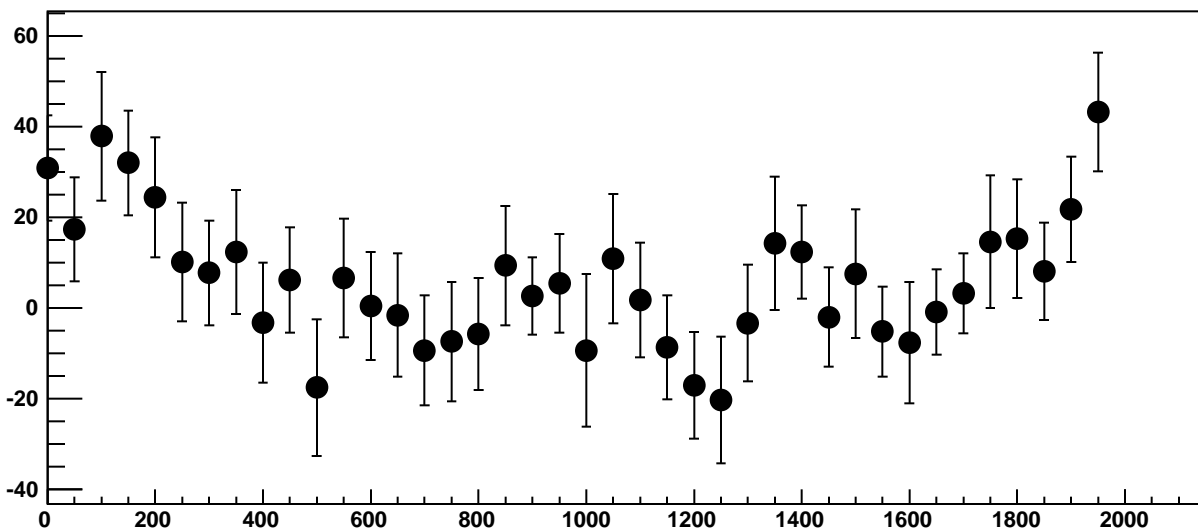
p1

$0.02954 \pm 0.01738$

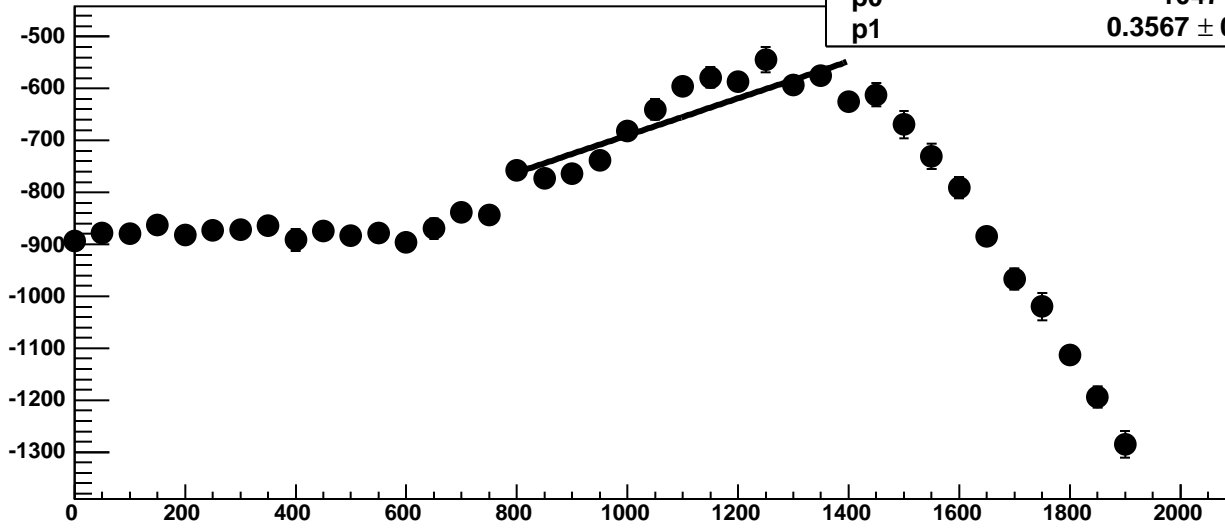
Chip 0, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC



Chip 0, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC

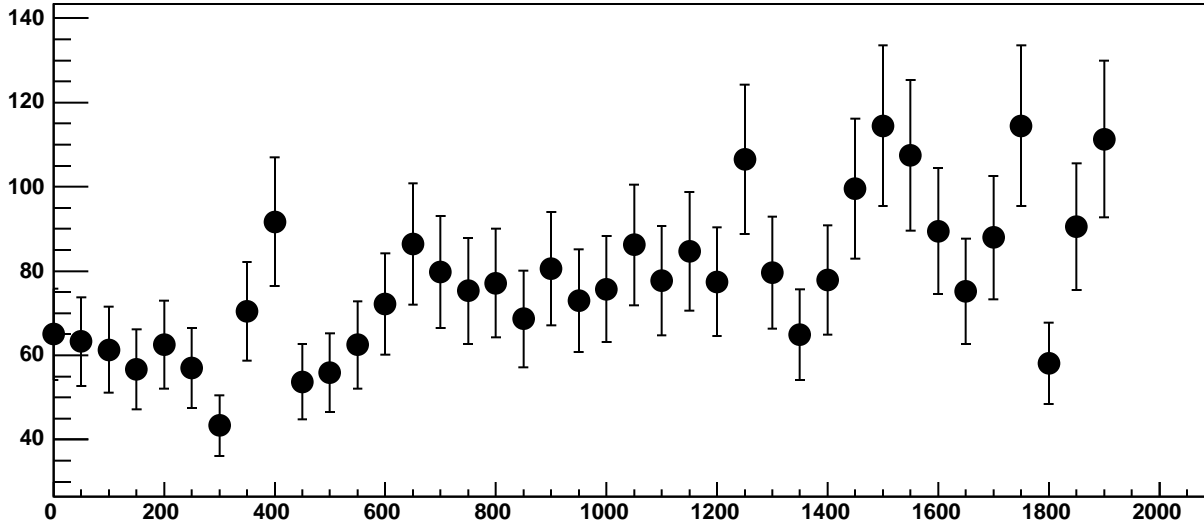


Chip 0, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC

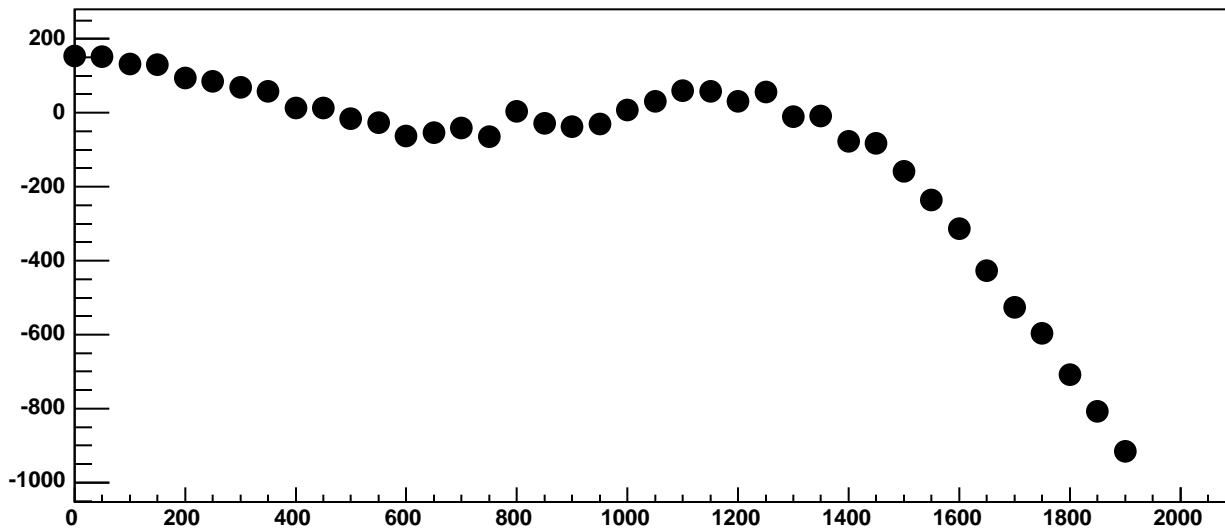


$\chi^2 / \text{ndf}$  61.83 / 11  
p0  $-1047 \pm 28.39$   
p1  $0.3567 \pm 0.02552$

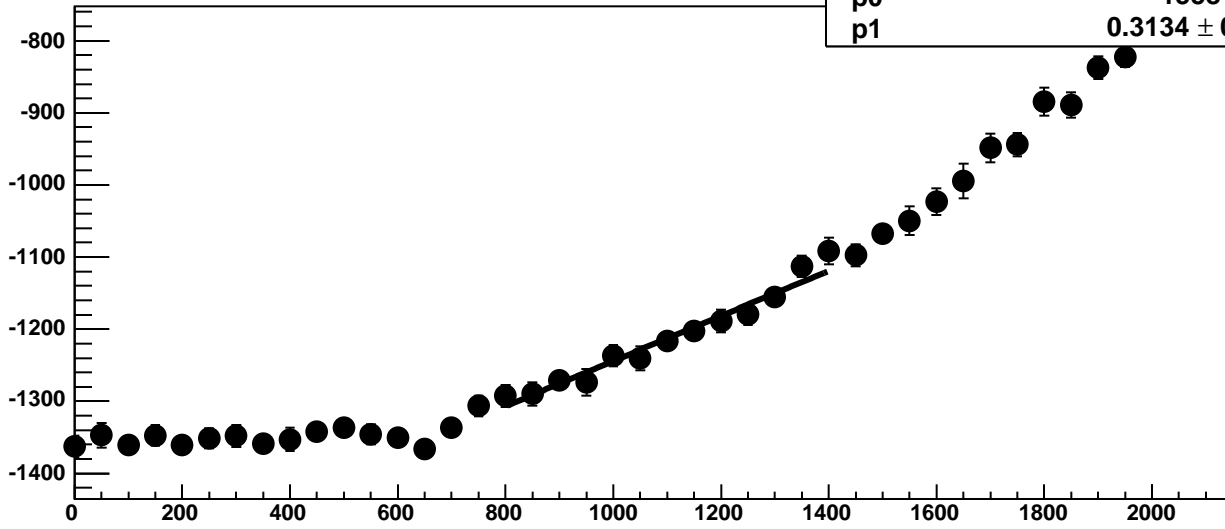
Chip 0, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC



Chip 0, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC

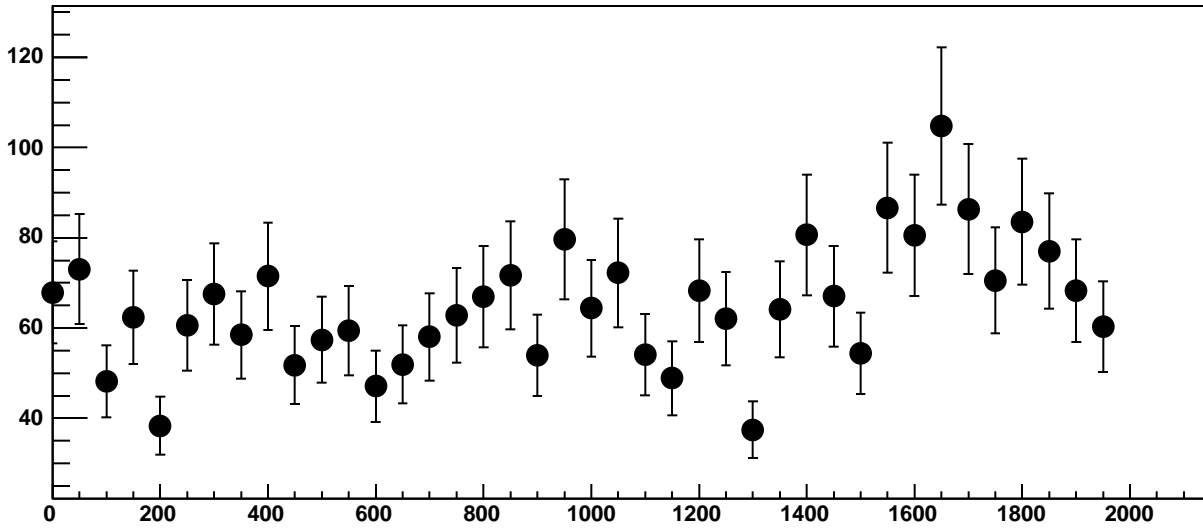


Chip 0, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC

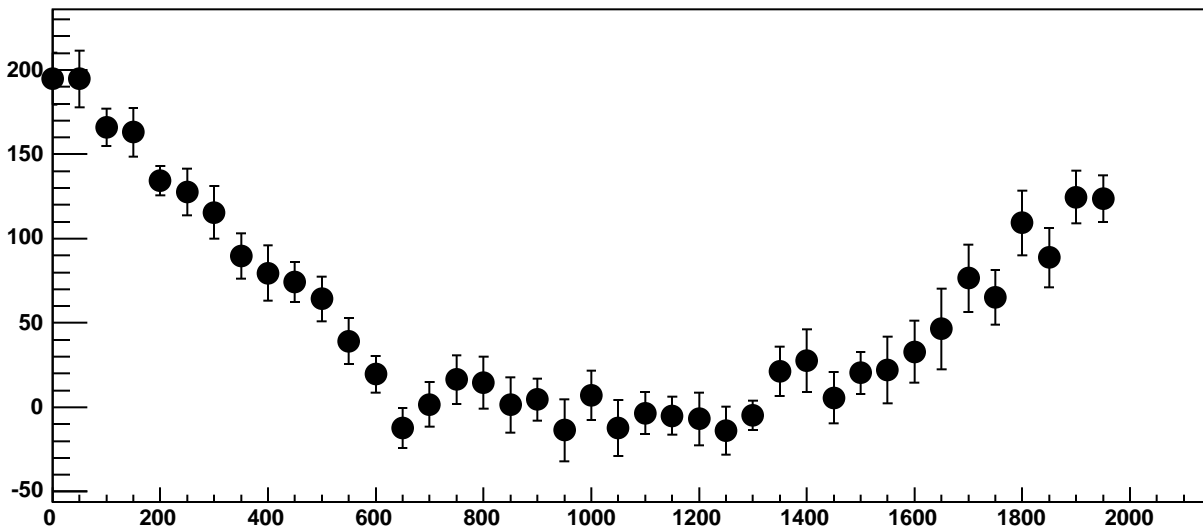


$\chi^2 / \text{ndf}$  8.413 / 11  
p0  $-1558 \pm 24.15$   
p1  $0.3134 \pm 0.02122$

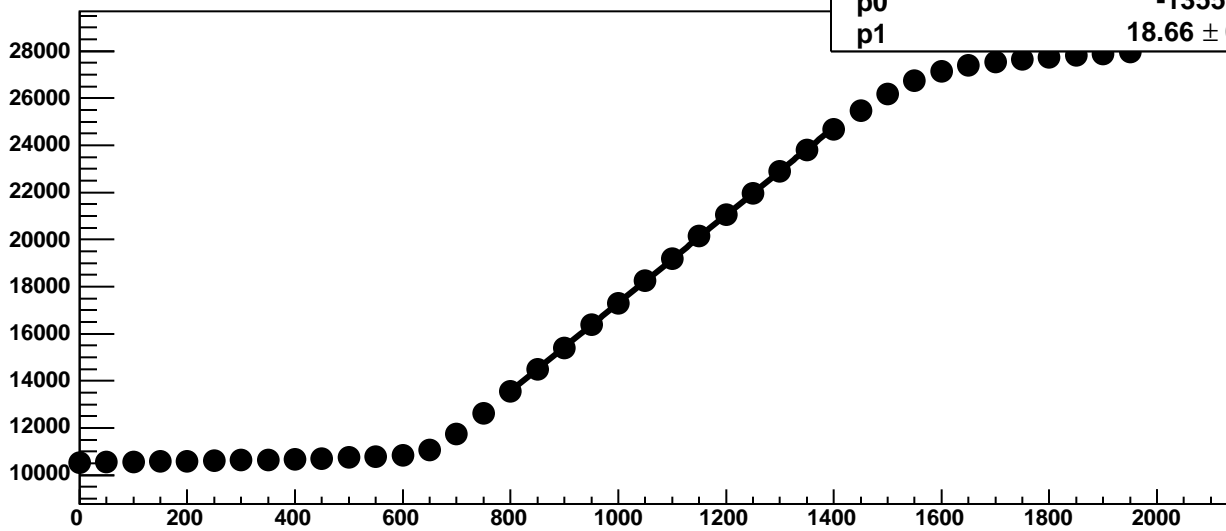
Chip 0, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC



Chip 0, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC

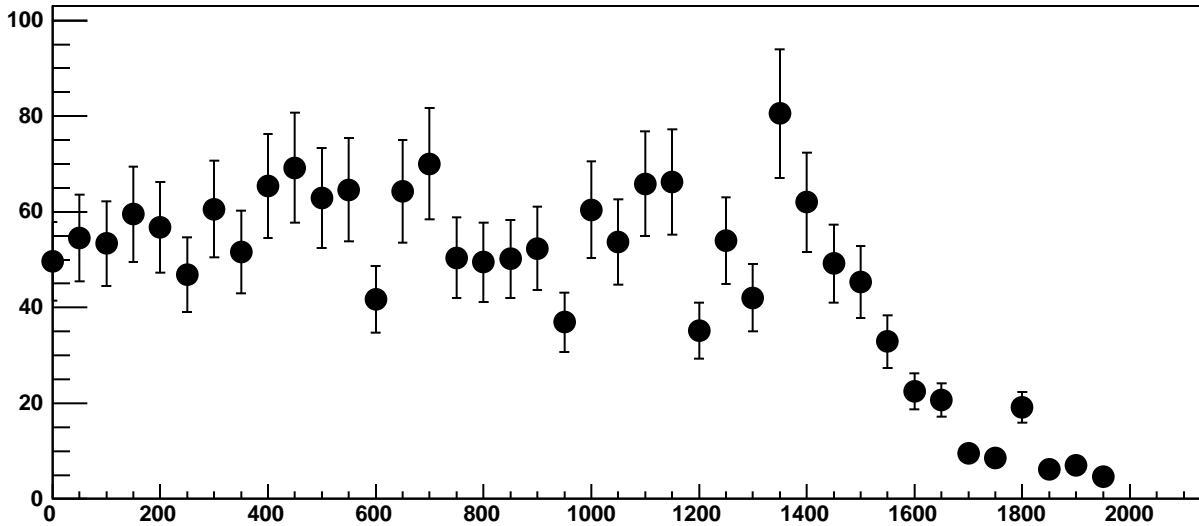


Chip 0, Channel 12, Enable 1!, Hold=30, ADC Mean vs DAC

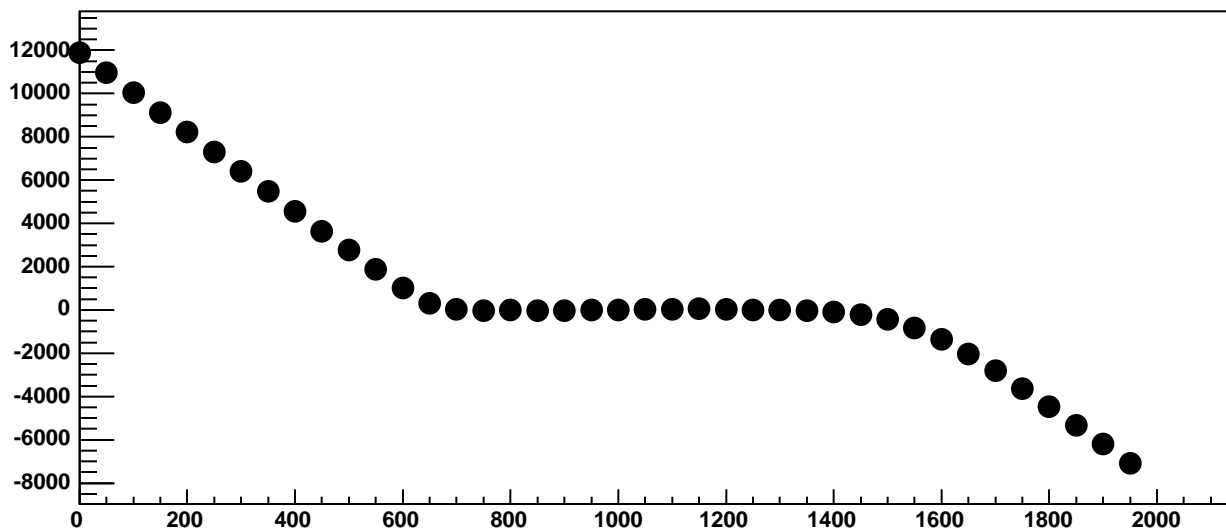


$\chi^2 / \text{ndf}$	83.38 / 11
p0	$-1355 \pm 19.43$
p1	$18.66 \pm 0.01768$

Chip 0, Channel 12, Enable 1!, Hold=30, ADC Noise vs DAC

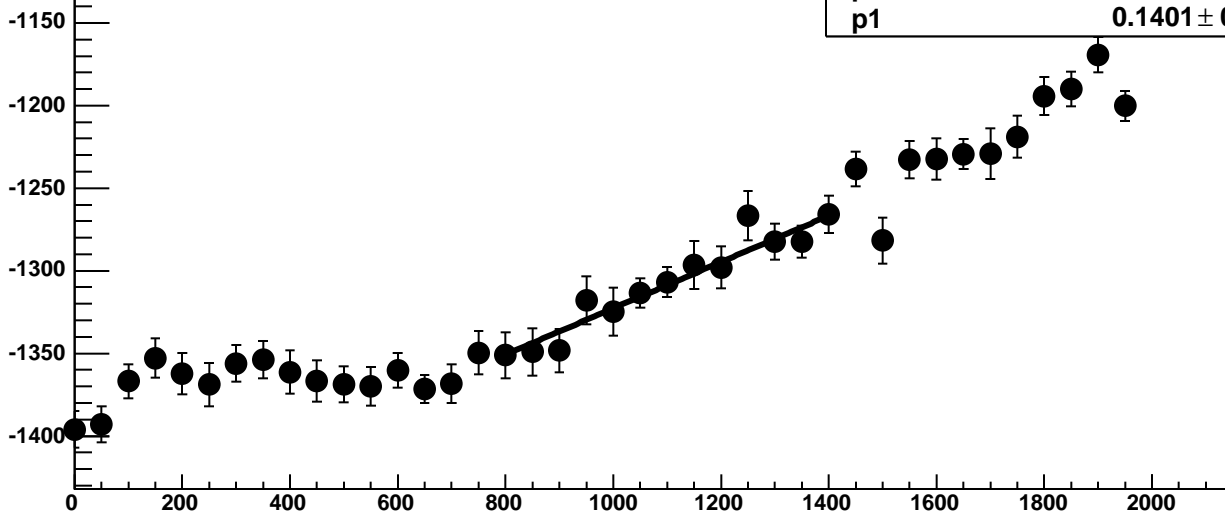


Chip 0, Channel 12, Enable 1!, Hold=30, ADC Residuals vs DAC

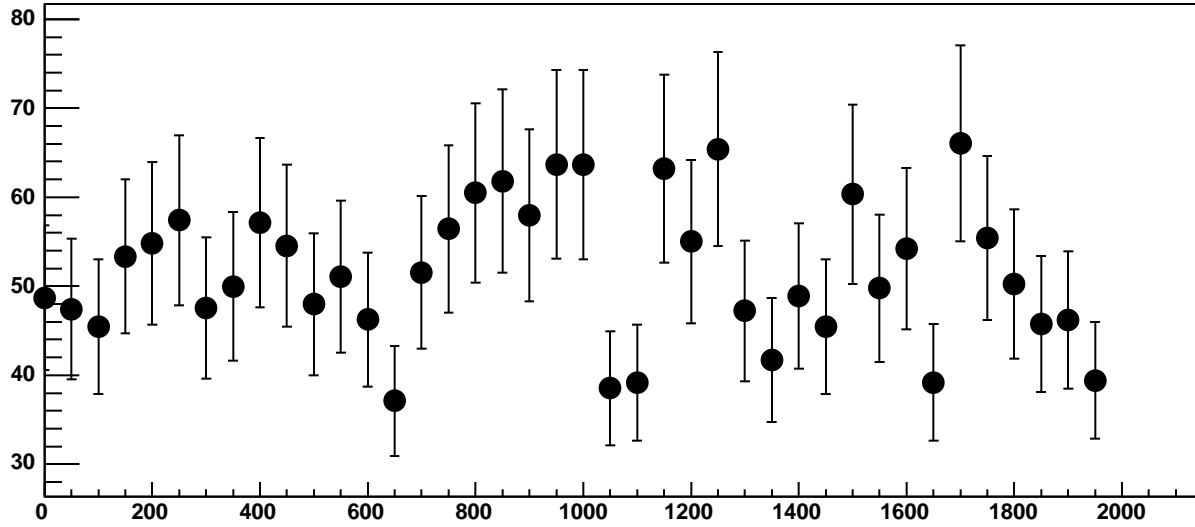




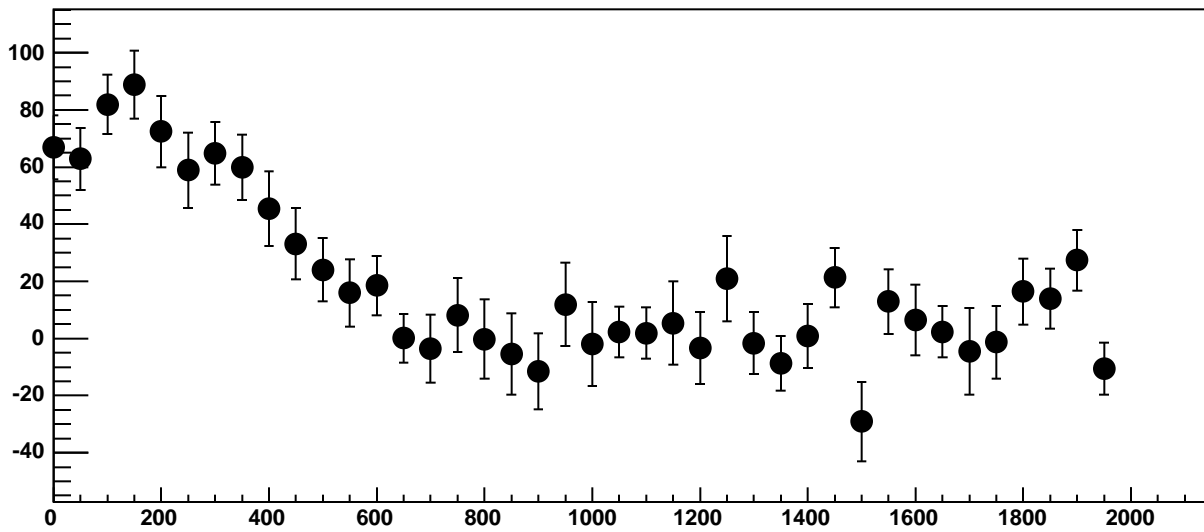
Chip 0, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC



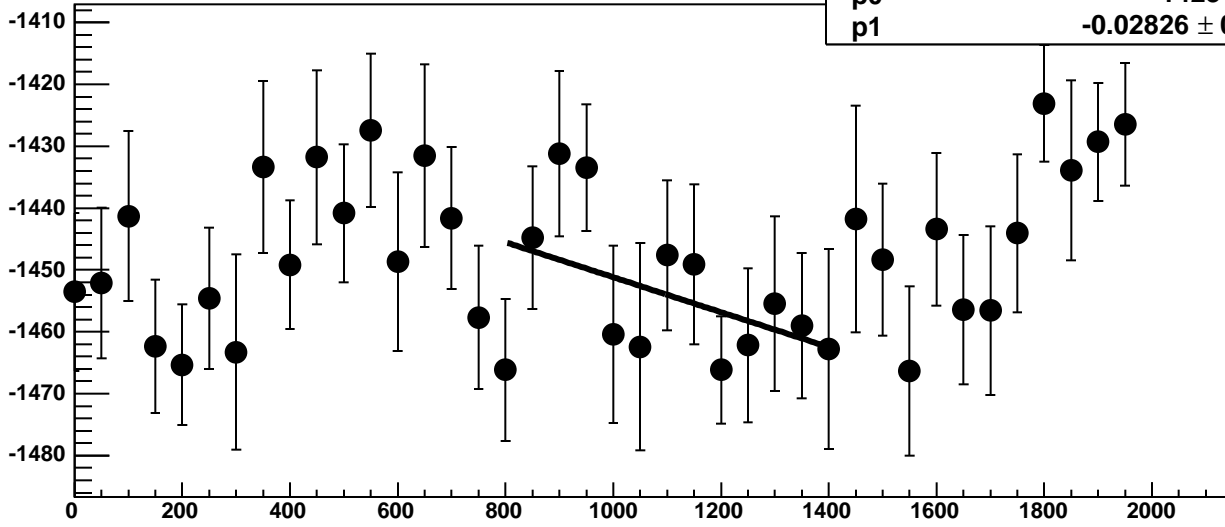
Chip 0, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC



Chip 0, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC

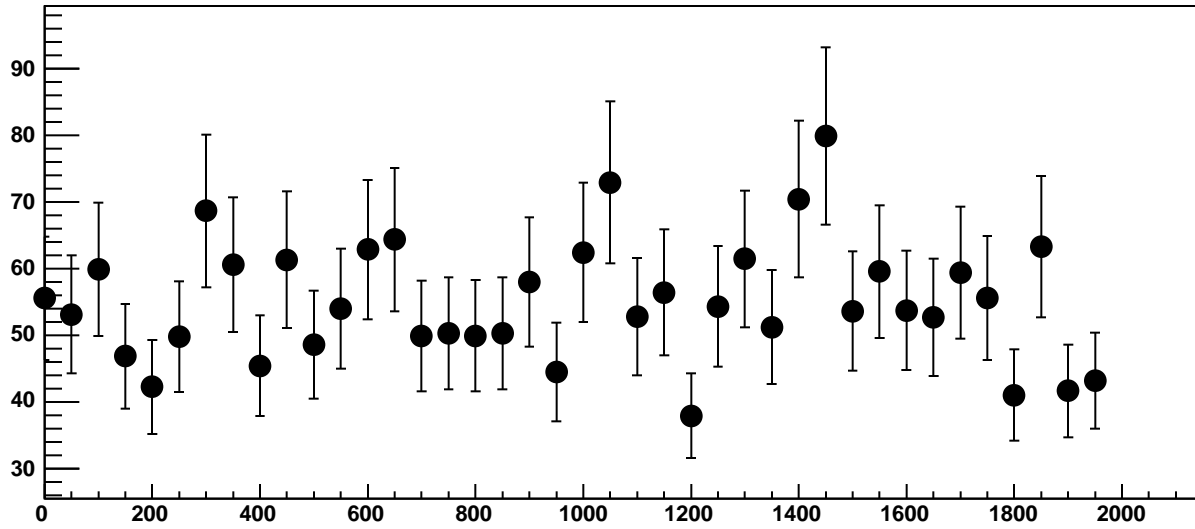


Chip 0, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC

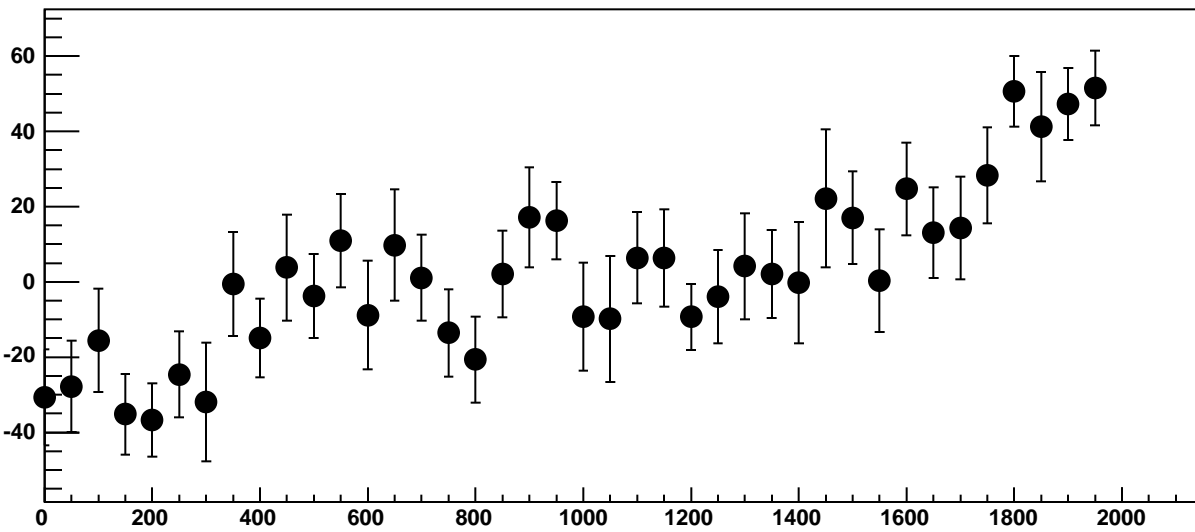


$\chi^2 / \text{ndf}$  10.11 / 11  
p0  $-1423 \pm 20.42$   
p1  $-0.02826 \pm 0.01847$

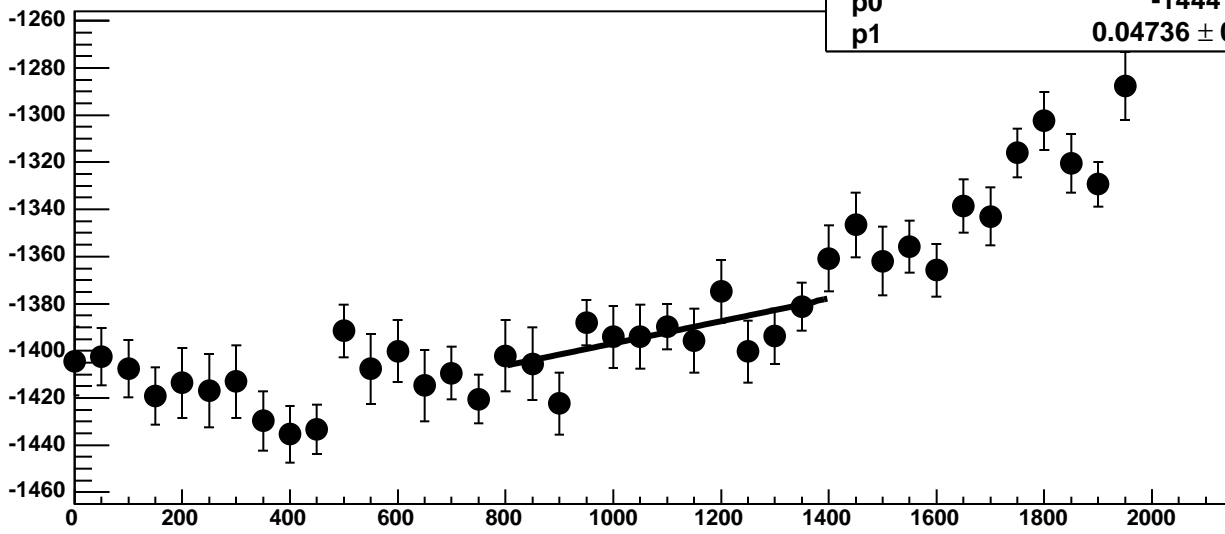
Chip 0, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC



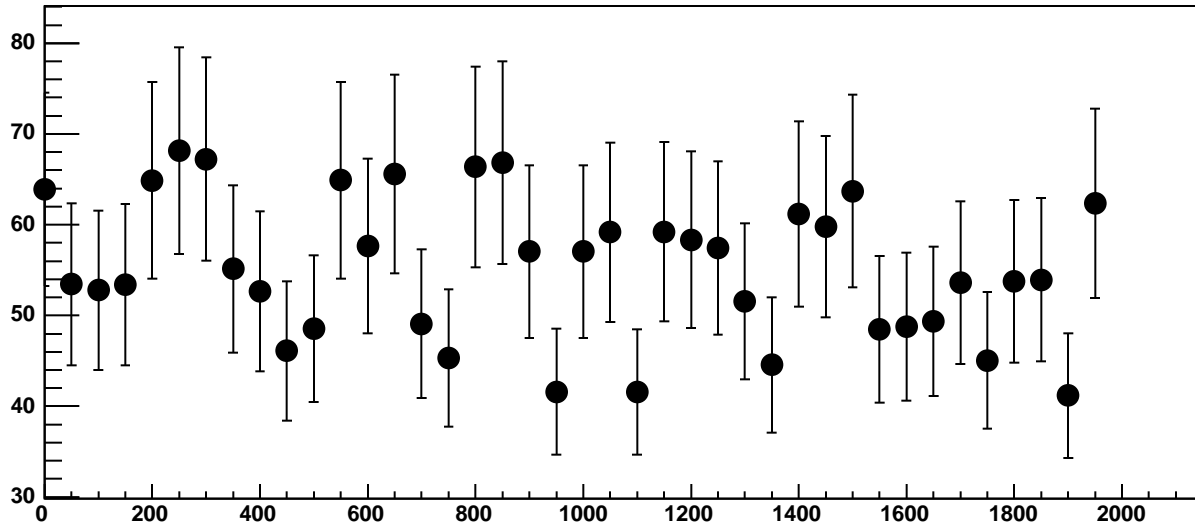
Chip 0, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC



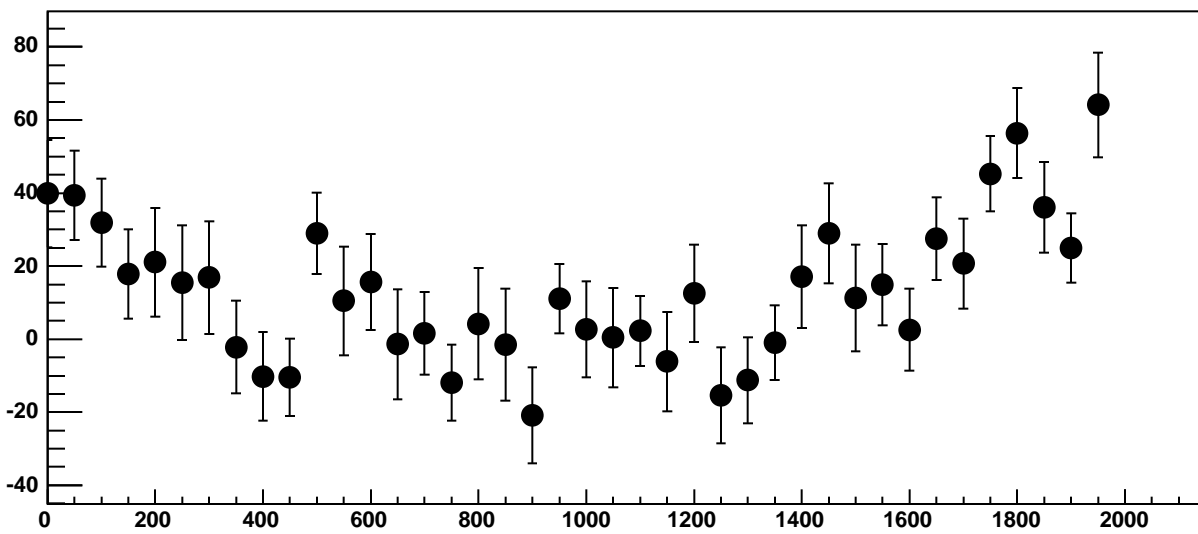
Chip 0, Channel 12, Enable 4, Hold=30, ADC Mean vs DAC



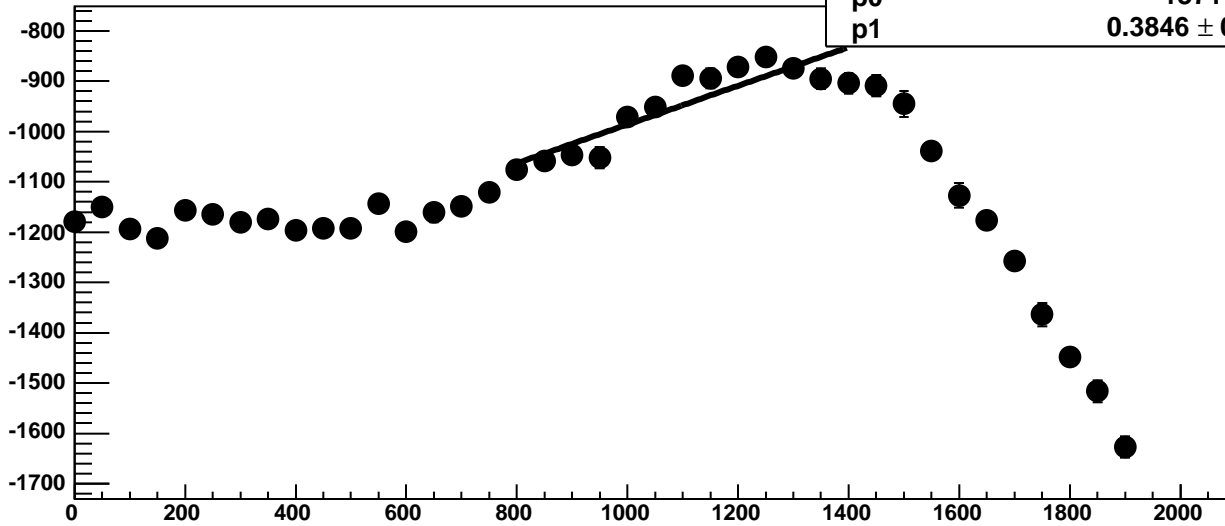
Chip 0, Channel 12, Enable 4, Hold=30, ADC Noise vs DAC



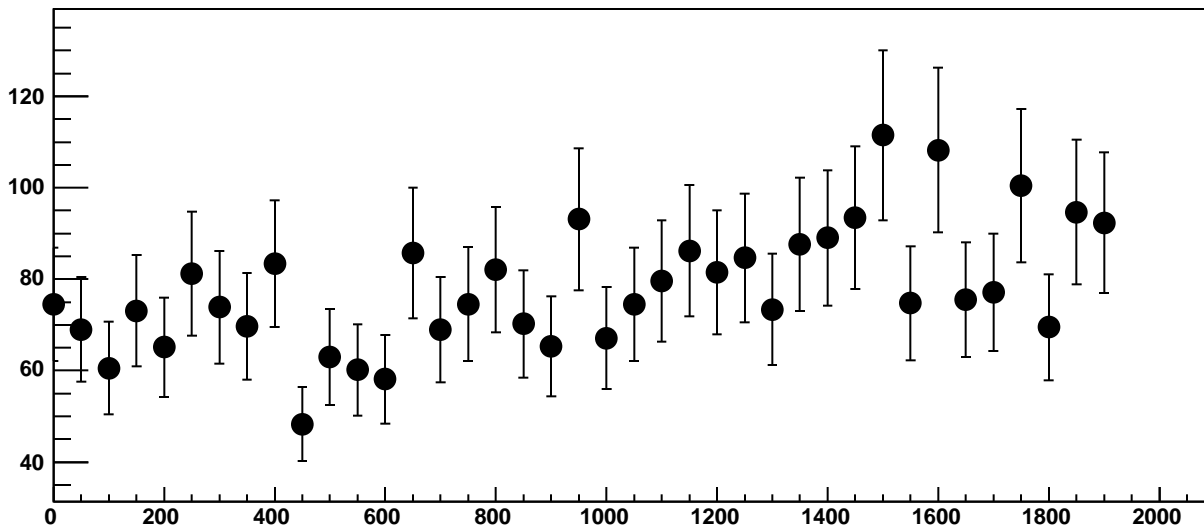
Chip 0, Channel 12, Enable 4, Hold=30, ADC Residuals vs DAC



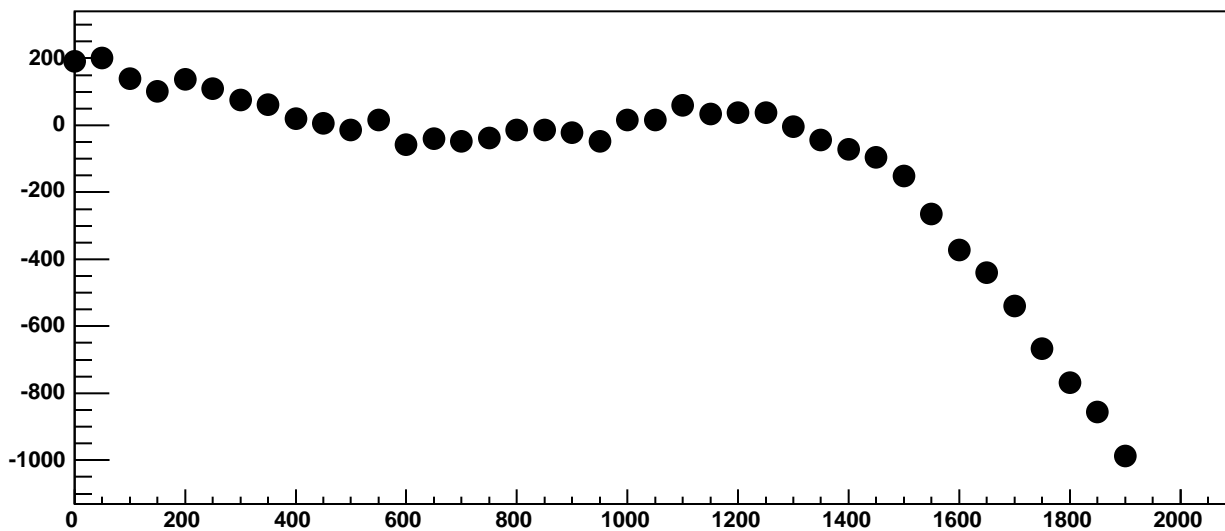
Chip 0, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC



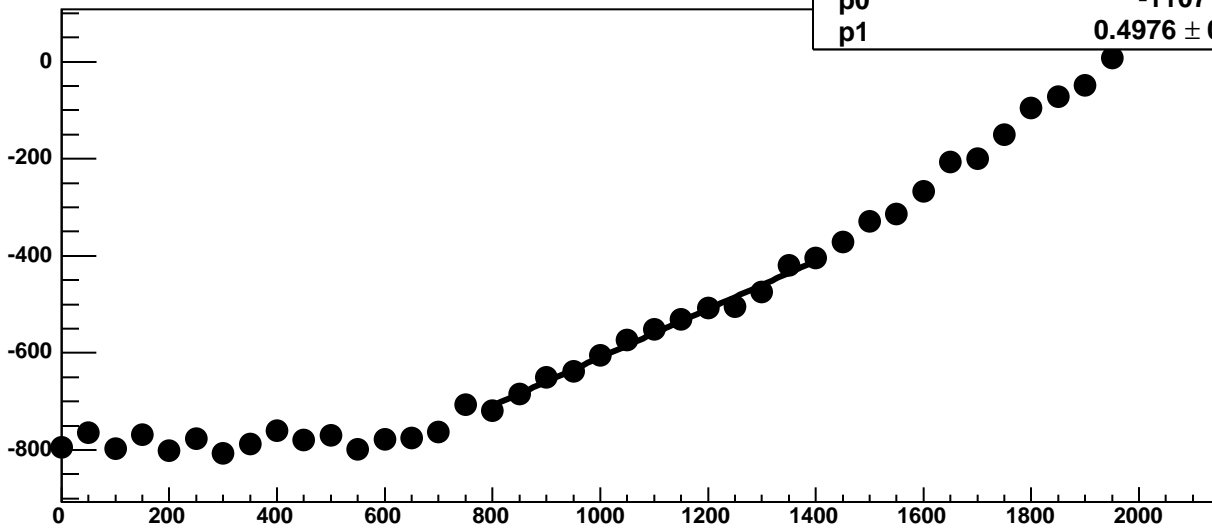
Chip 0, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC



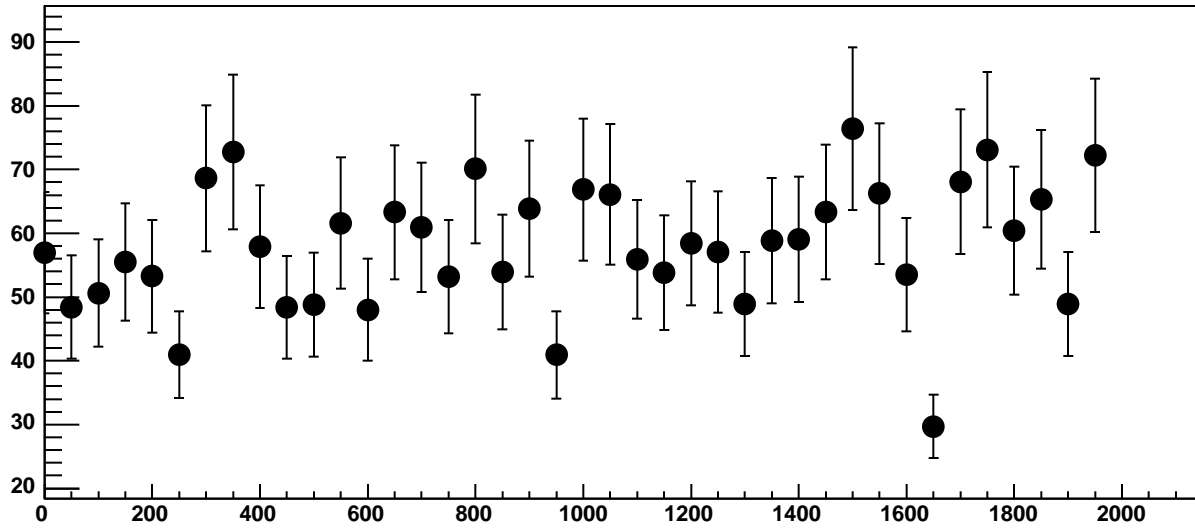
Chip 0, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC



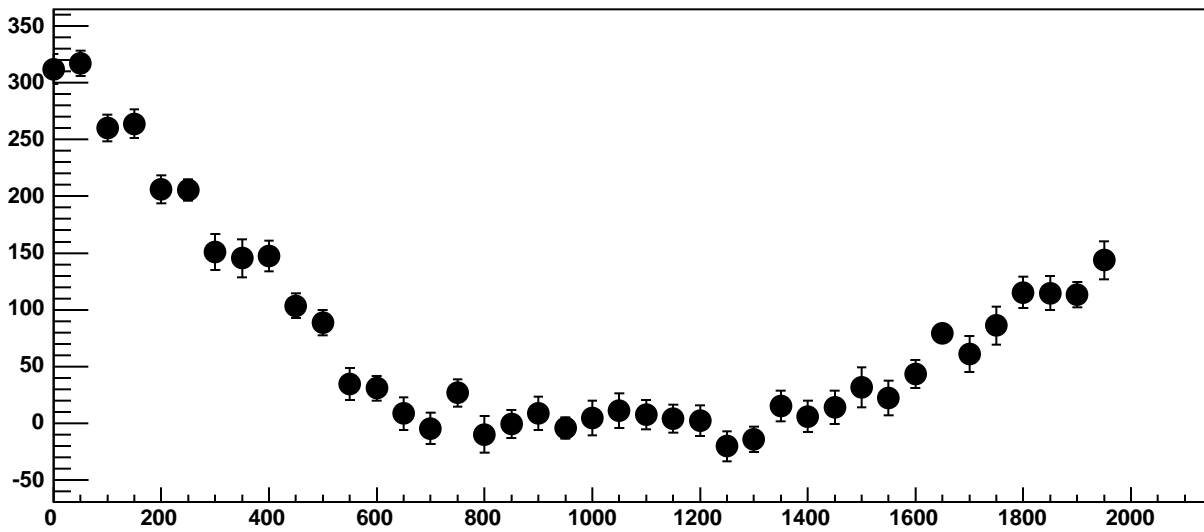
Chip 0, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC



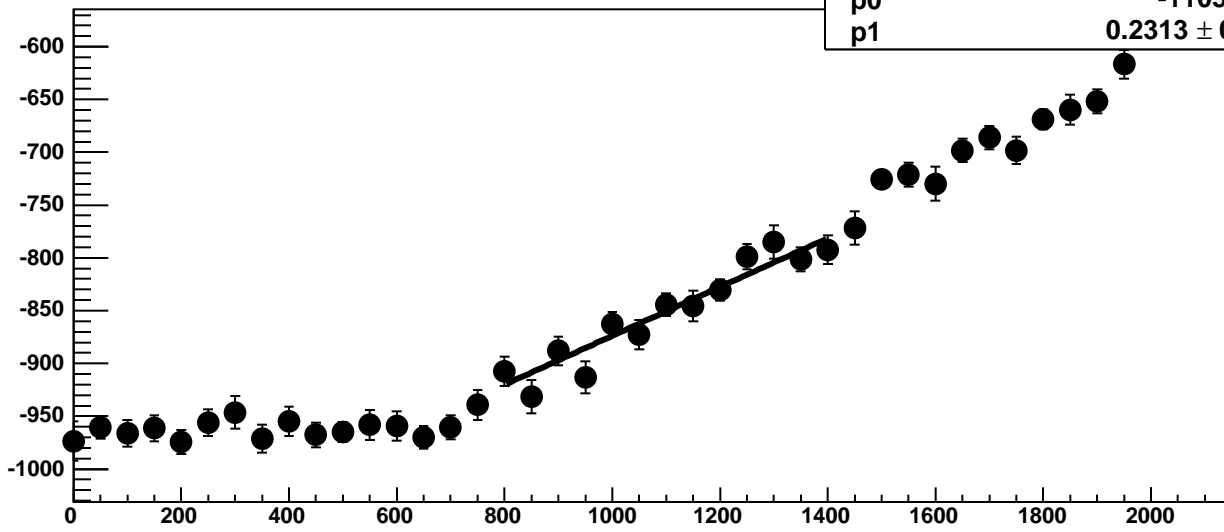
Chip 0, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC



Chip 0, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC

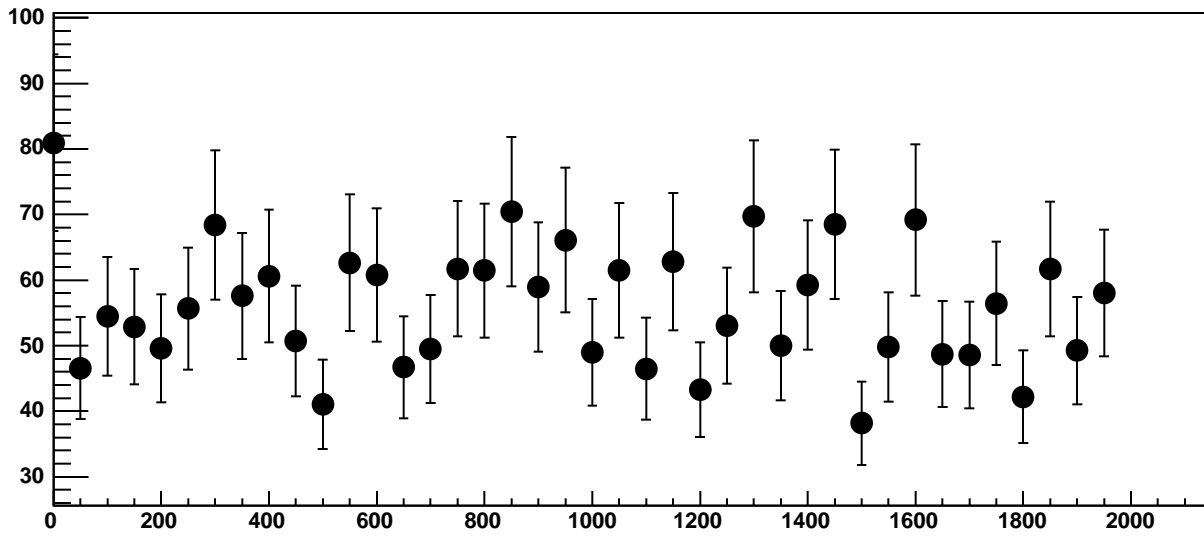


Chip 0, Channel 13, Enable 1, Hold=30, ADC Mean vs DAC

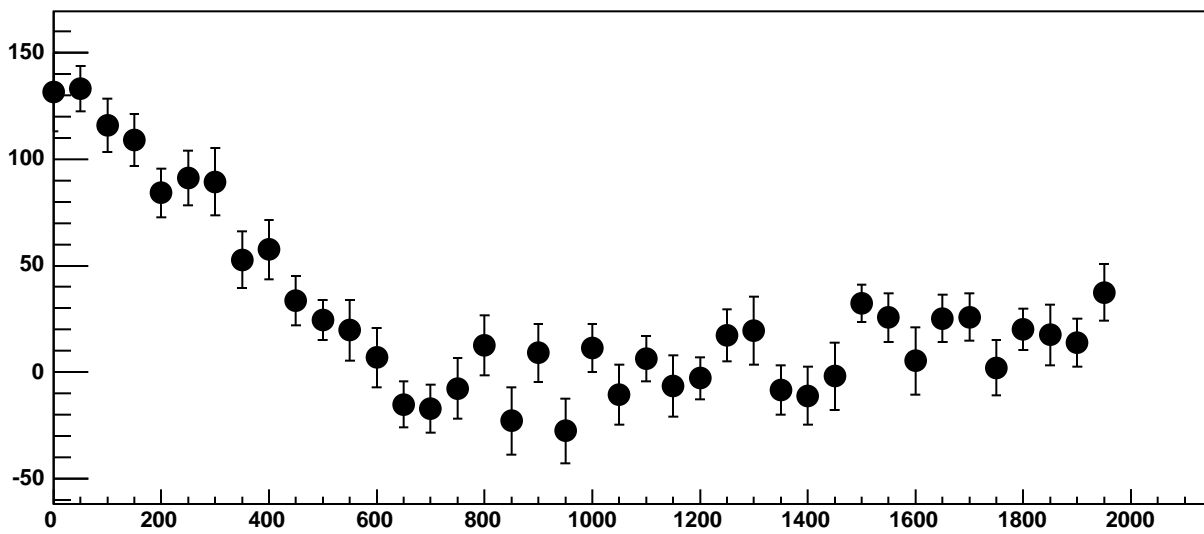


$\chi^2 / \text{ndf}$  13.52 / 11  
p0  $-1105 \pm 22.7$   
p1  $0.2313 \pm 0.02009$

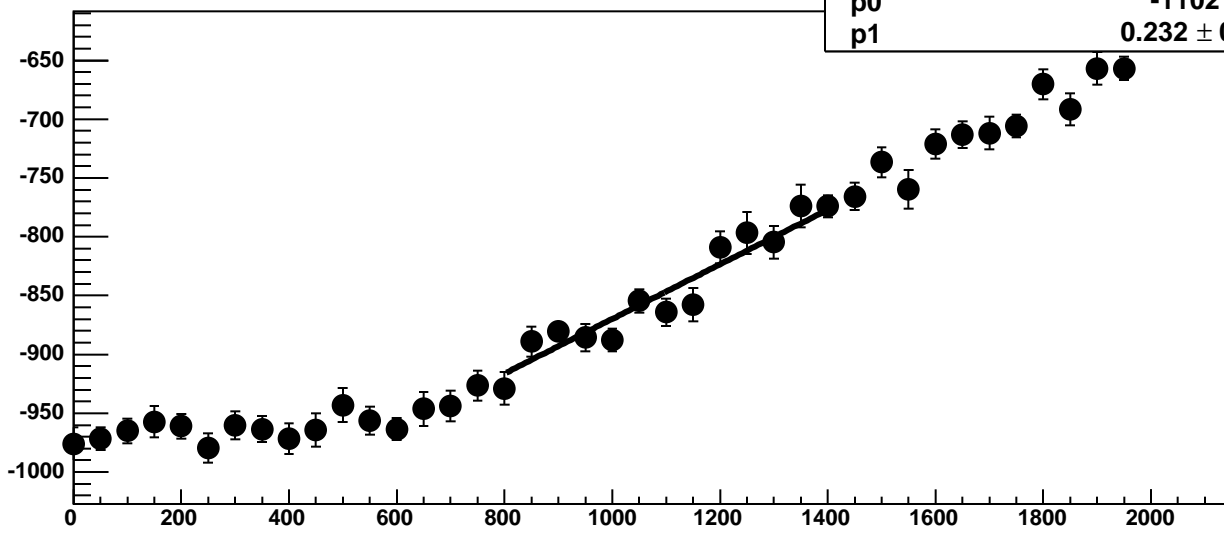
Chip 0, Channel 13, Enable 1, Hold=30, ADC Noise vs DAC



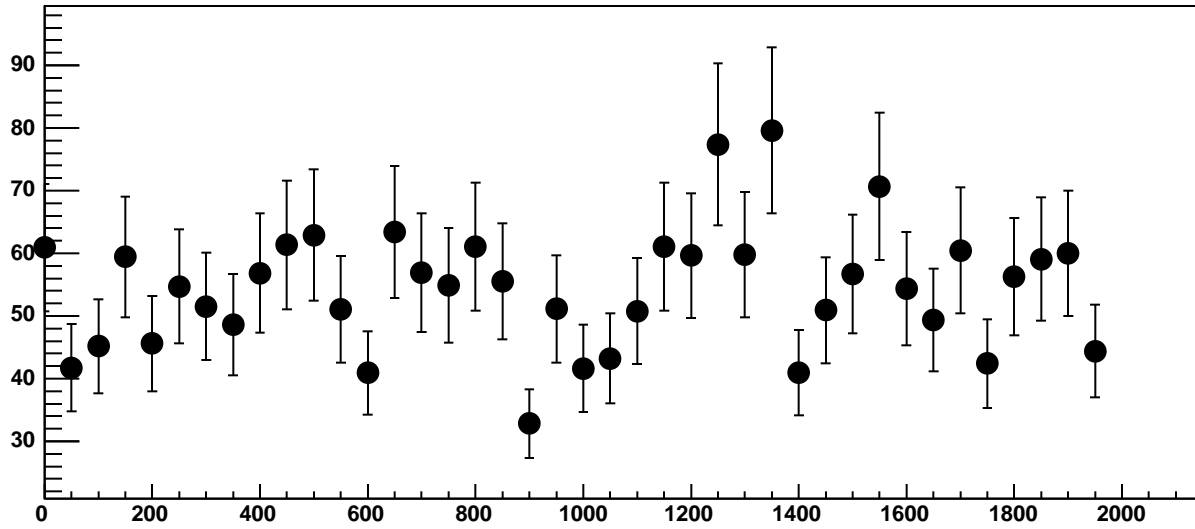
Chip 0, Channel 13, Enable 1, Hold=30, ADC Residuals vs DAC



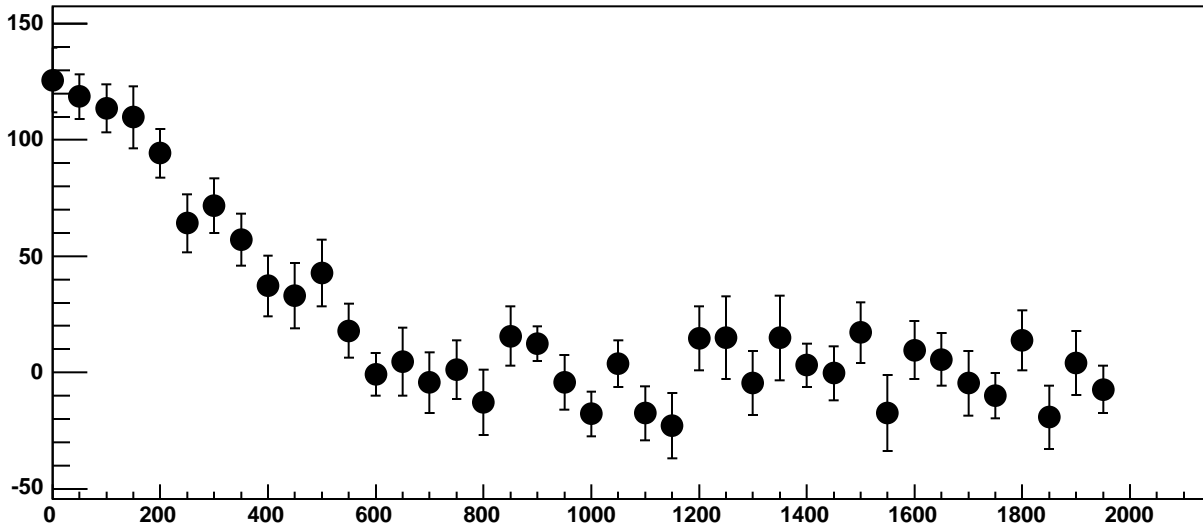
Chip 0, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC



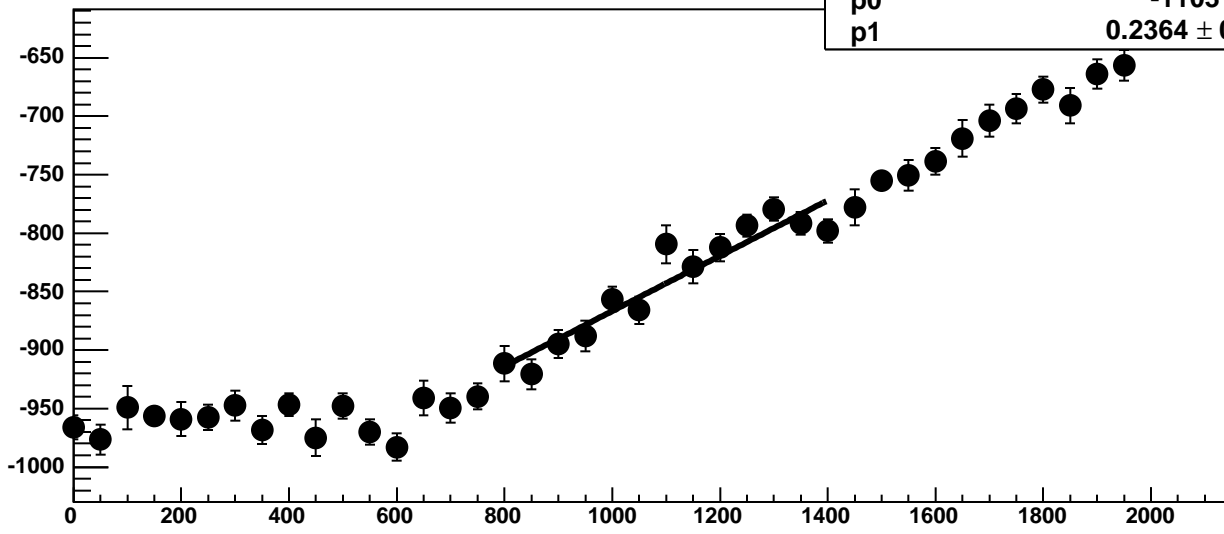
Chip 0, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC



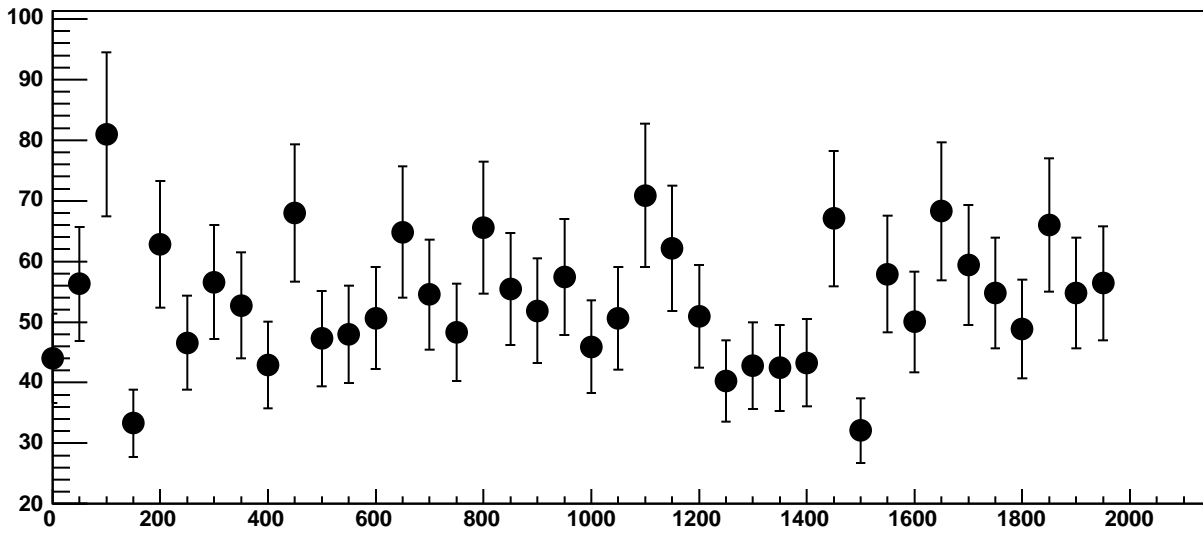
Chip 0, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC



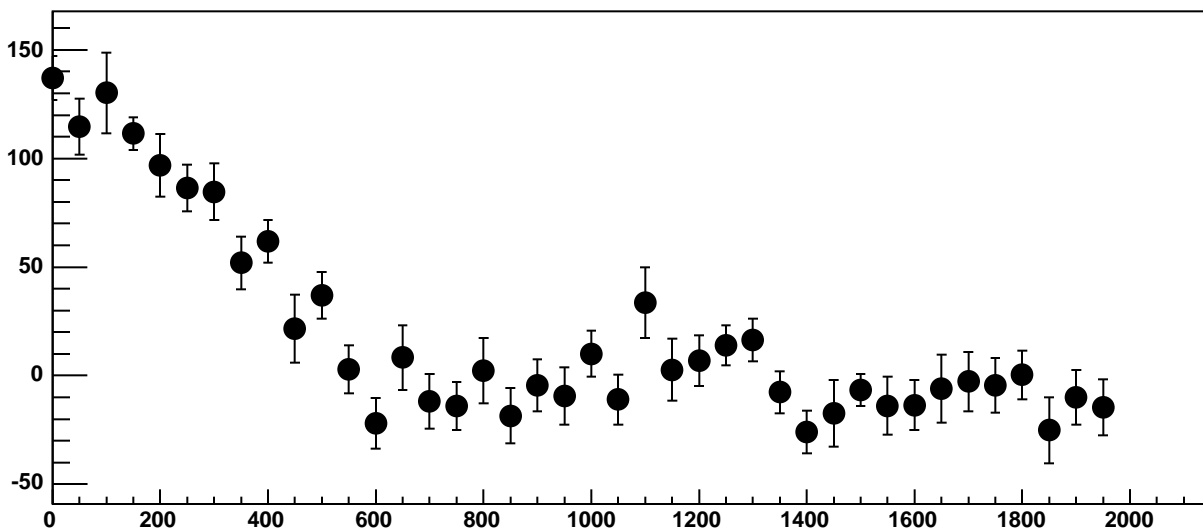
Chip 0, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC



Chip 0, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC

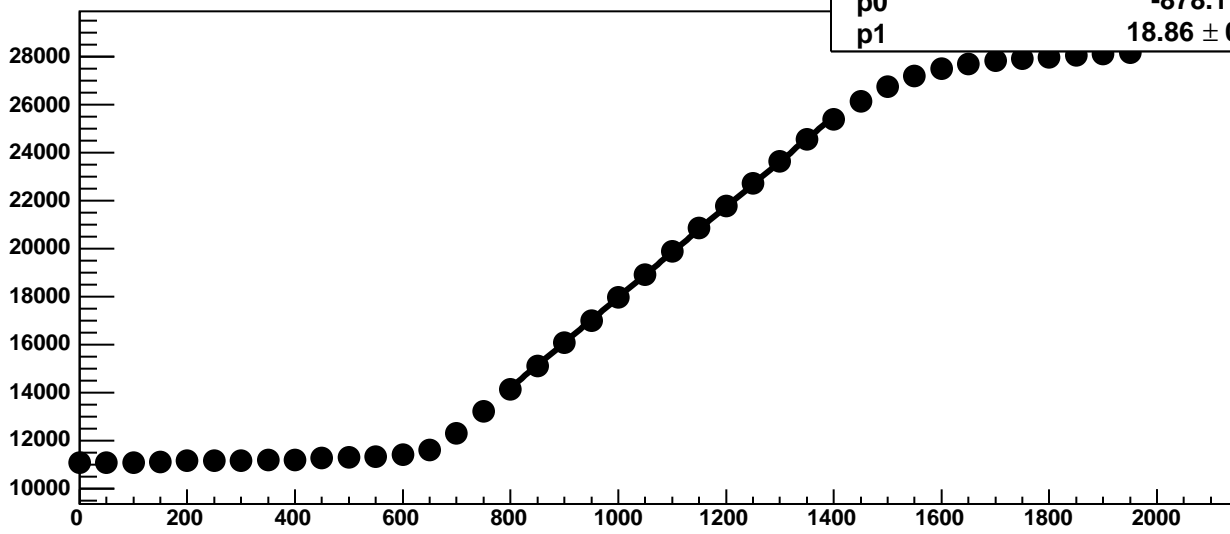


Chip 0, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC



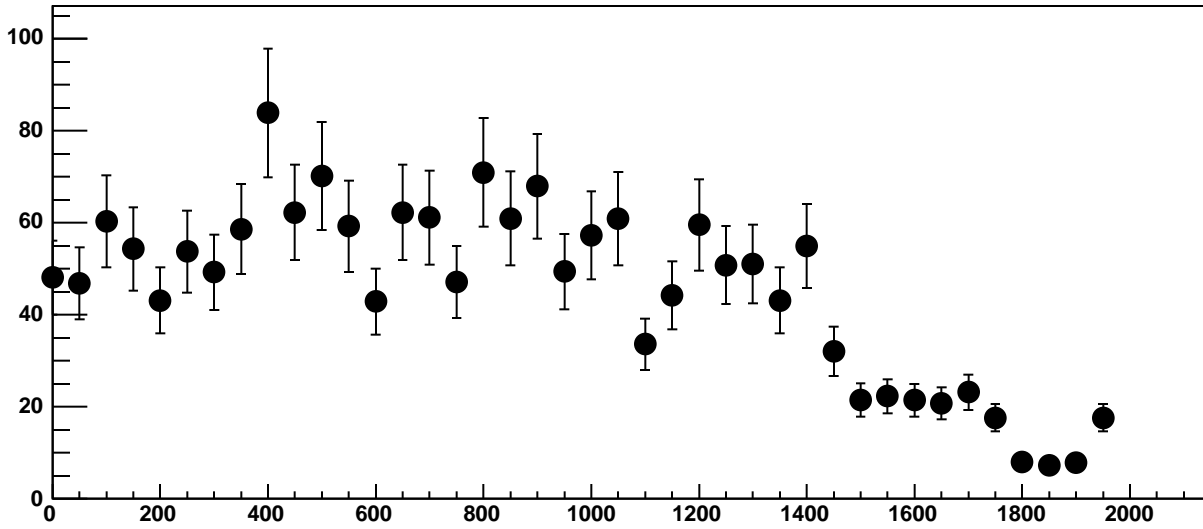


Chip 0, Channel 13, Enable 4!, Hold=30, ADC Mean vs DAC

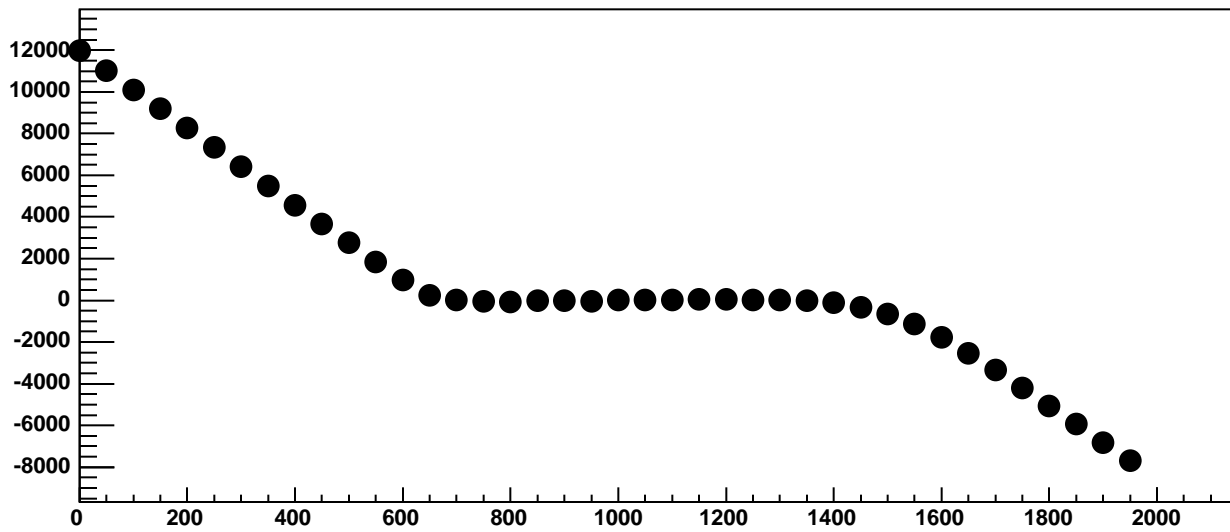


$\chi^2 / \text{ndf}$  200.8 / 11  
p0  $-878.1 \pm 21.87$   
p1  $18.86 \pm 0.01915$

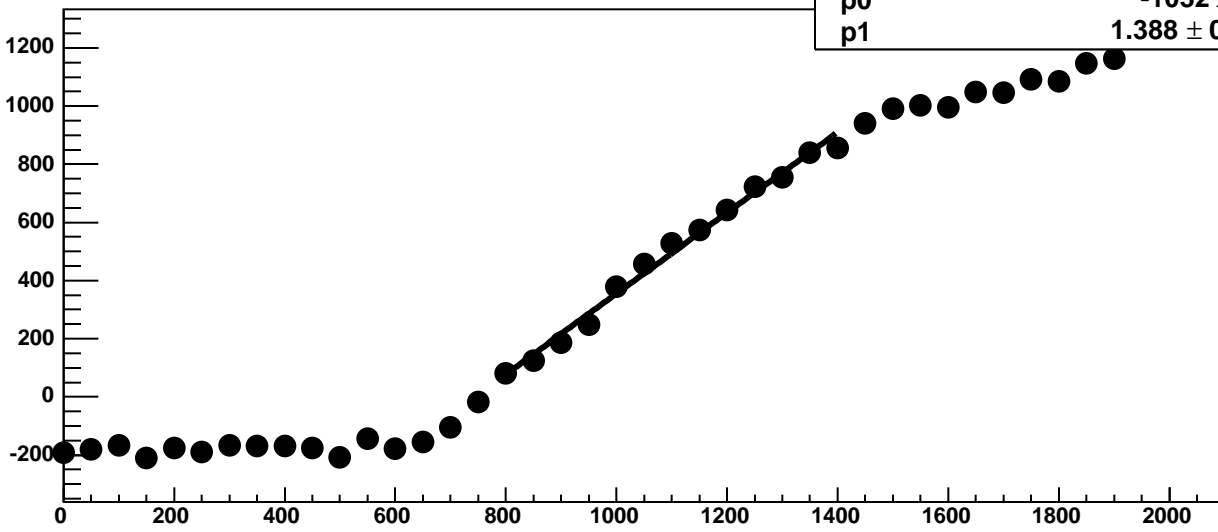
Chip 0, Channel 13, Enable 4!, Hold=30, ADC Noise vs DAC



Chip 0, Channel 13, Enable 4!, Hold=30, ADC Residuals vs DAC

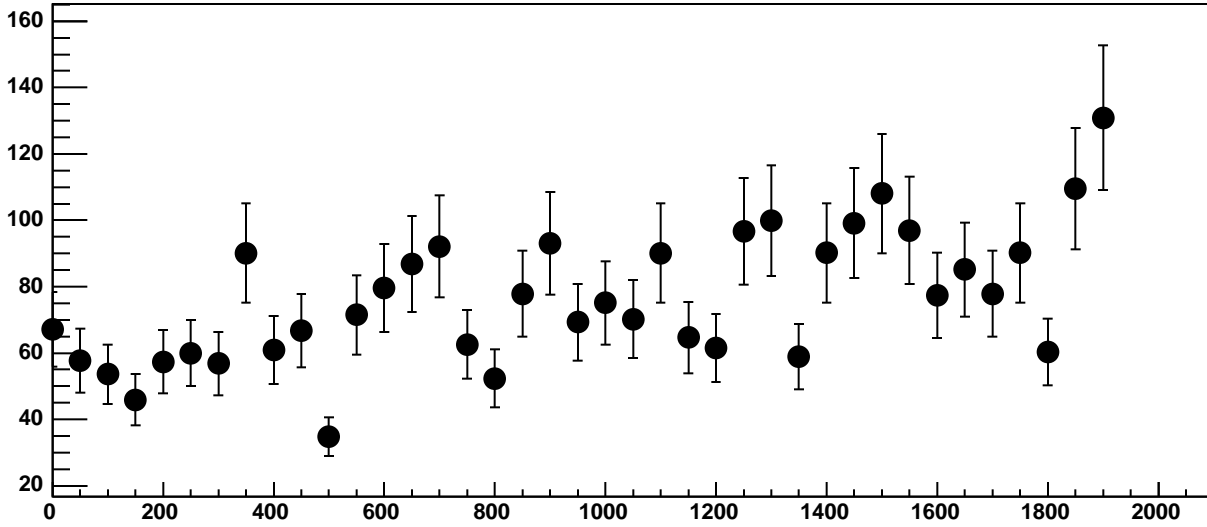


Chip 0, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC

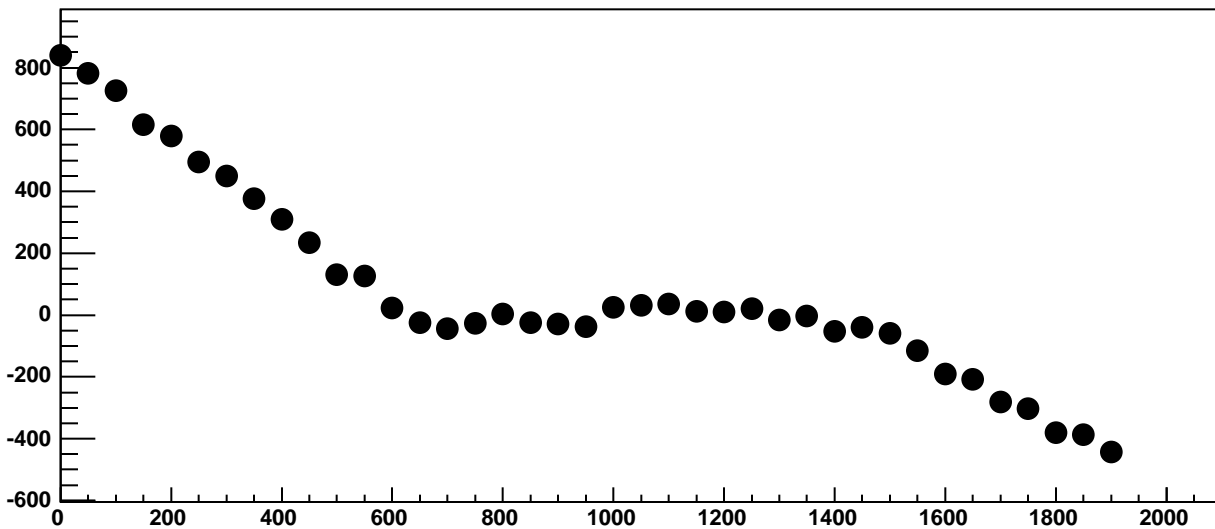


$\chi^2 / \text{ndf}$  27.43 / 11  
p0  $-1032 \pm 26.18$   
p1  $1.388 \pm 0.02386$

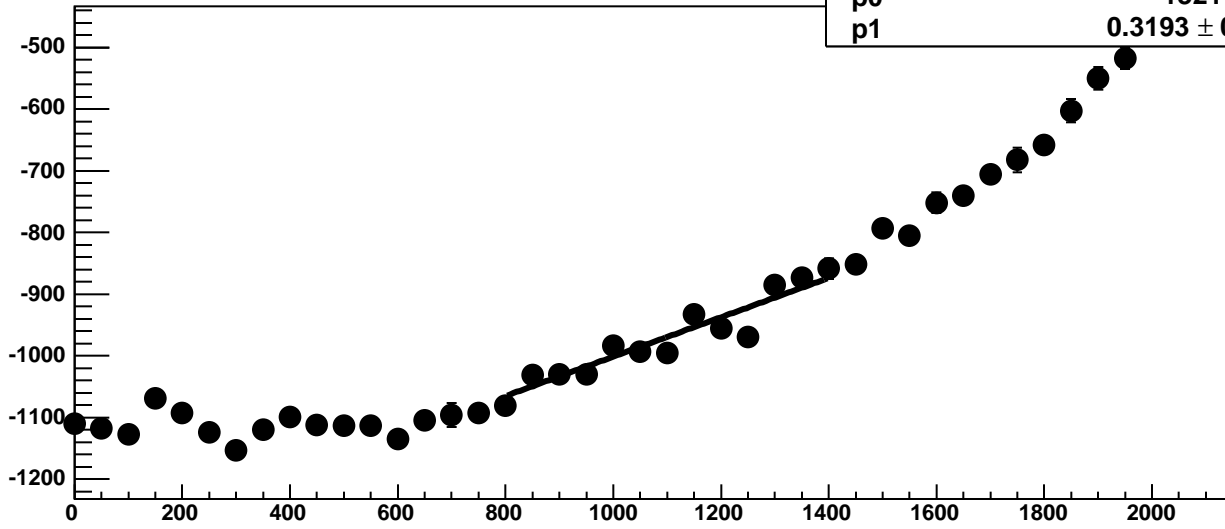
Chip 0, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC



Chip 0, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC

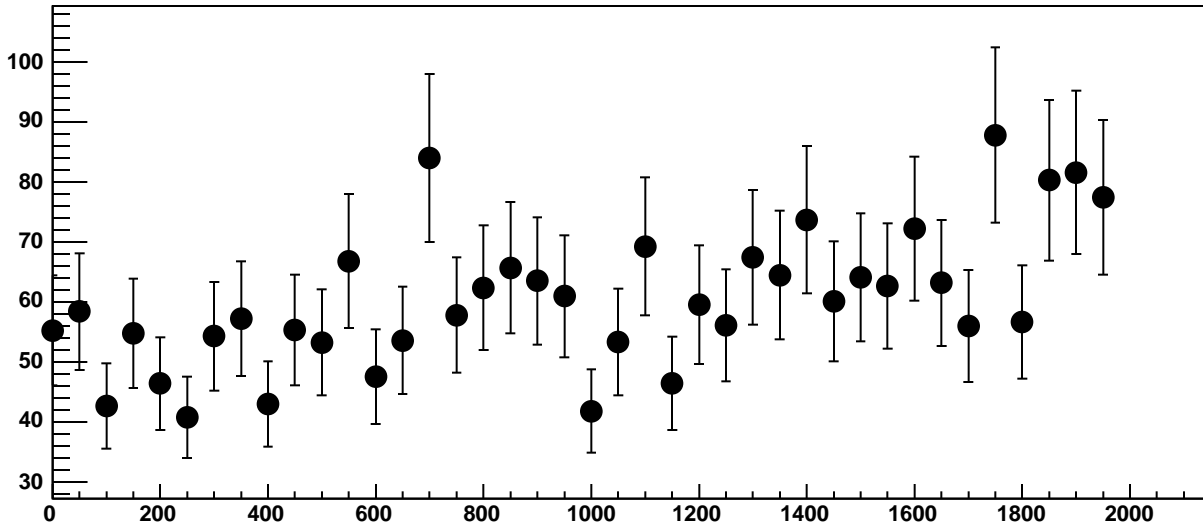


Chip 0, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC

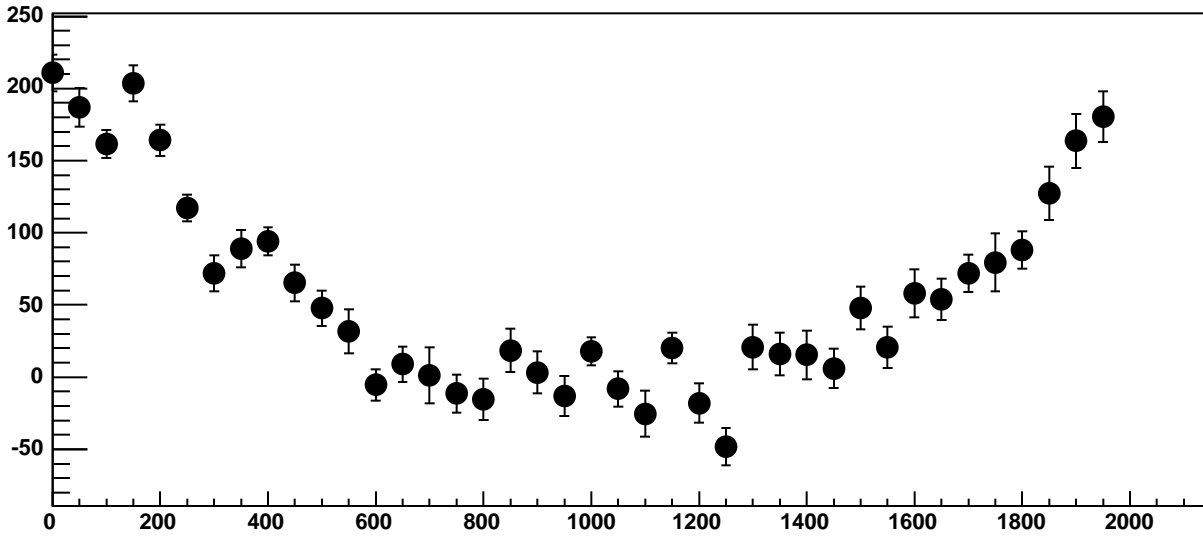


$\chi^2 / \text{ndf}$  33.27 / 11  
p0  $-1321 \pm 23.96$   
p1  $0.3193 \pm 0.02173$

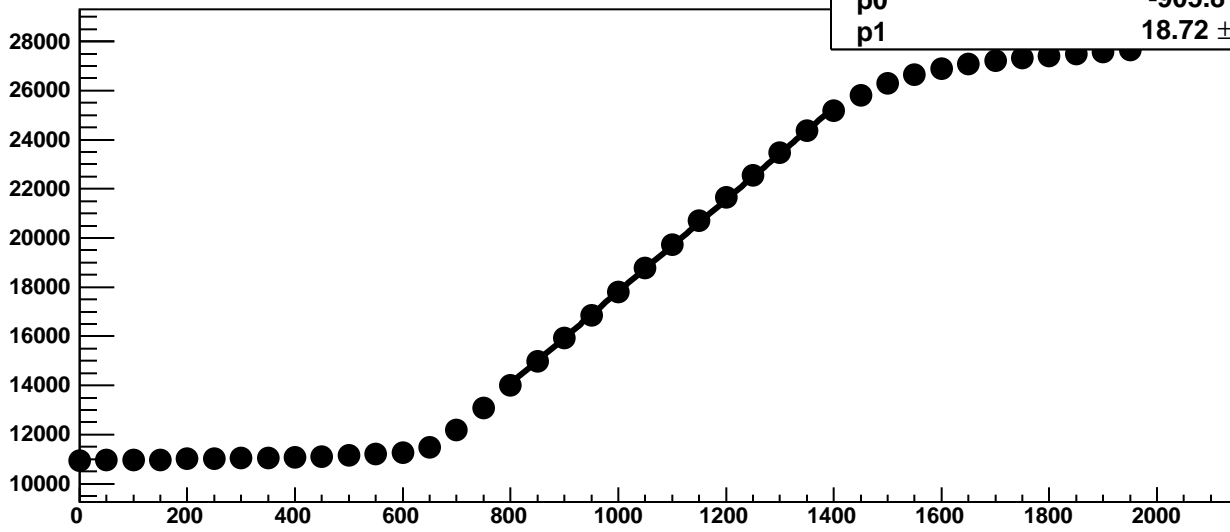
Chip 0, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC



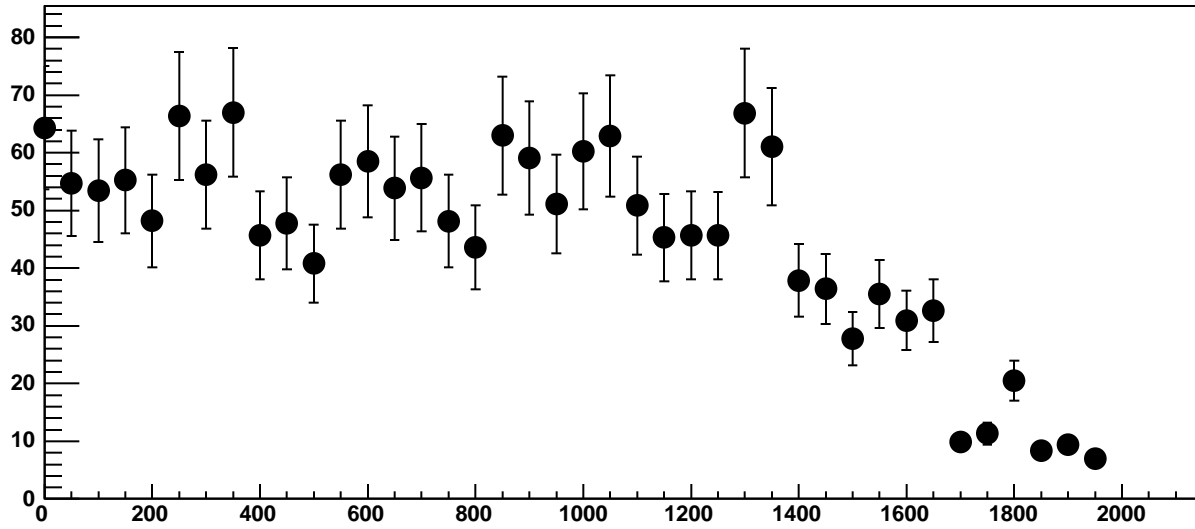
Chip 0, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC



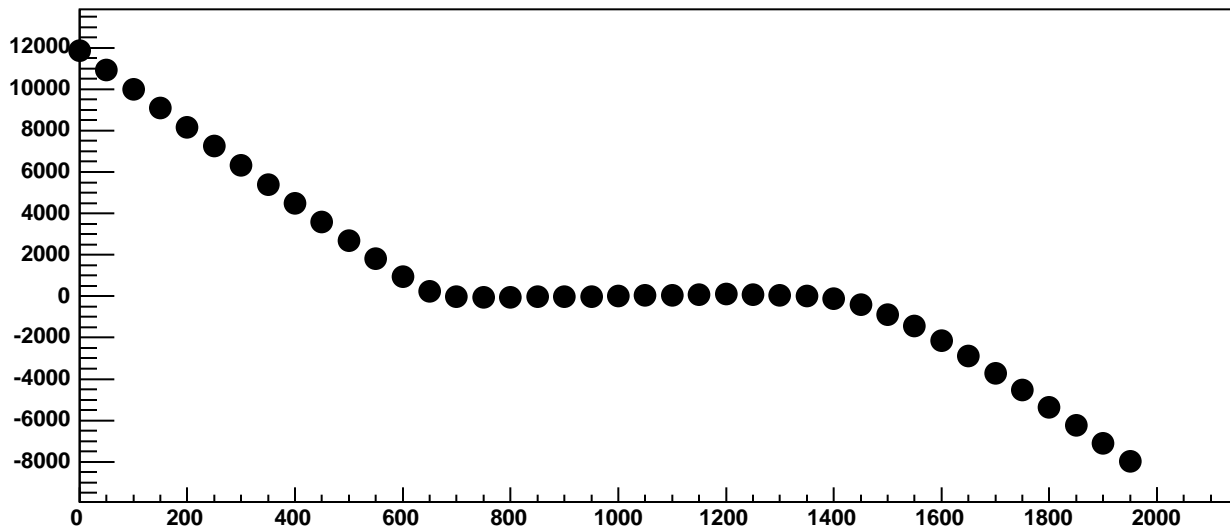
Chip 0, Channel 14, Enable 1!, Hold=30, ADC Mean vs DAC



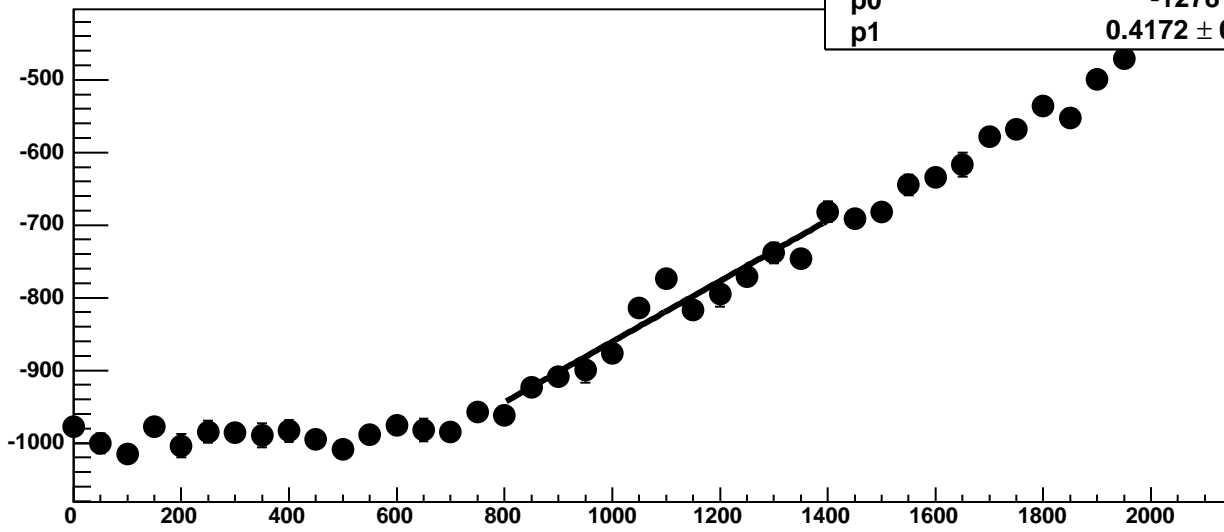
Chip 0, Channel 14, Enable 1!, Hold=30, ADC Noise vs DAC



Chip 0, Channel 14, Enable 1!, Hold=30, ADC Residuals vs DAC

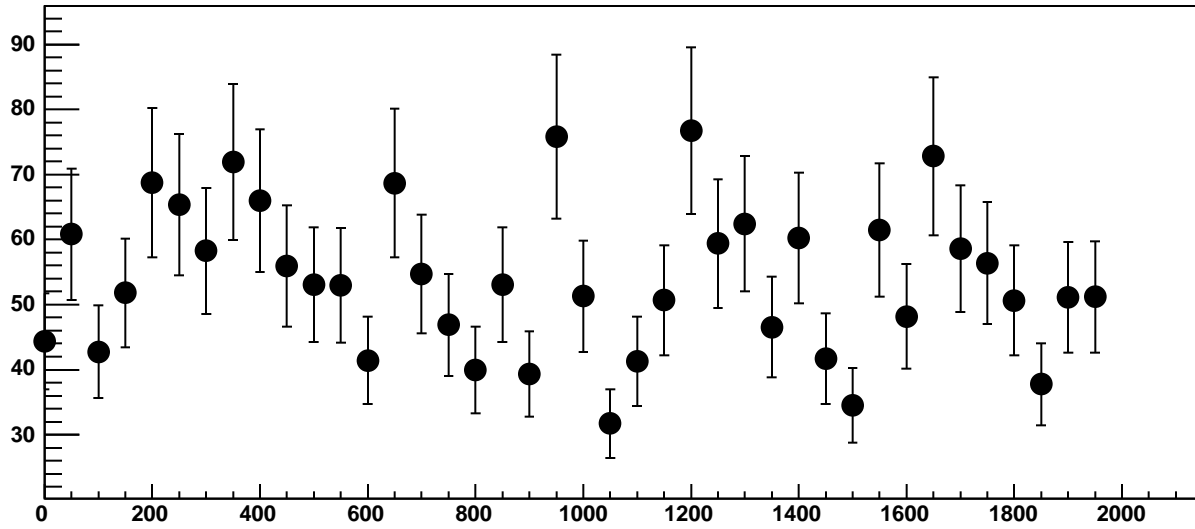


Chip 0, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC

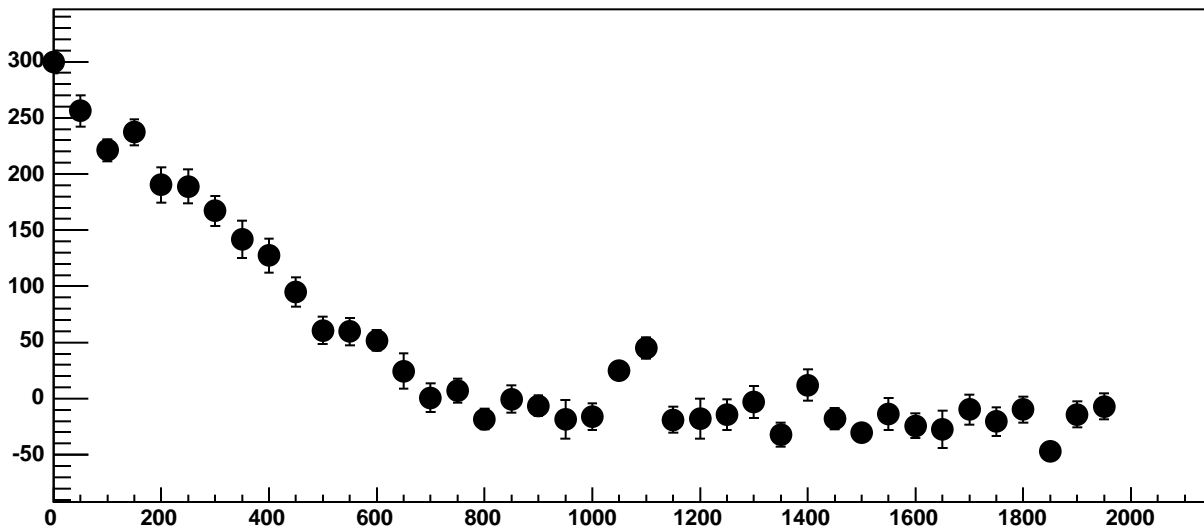


$\chi^2 / \text{ndf}$  56.75 / 11  
p0 -1278 ± 18.43  
p1 0.4172 ± 0.01705

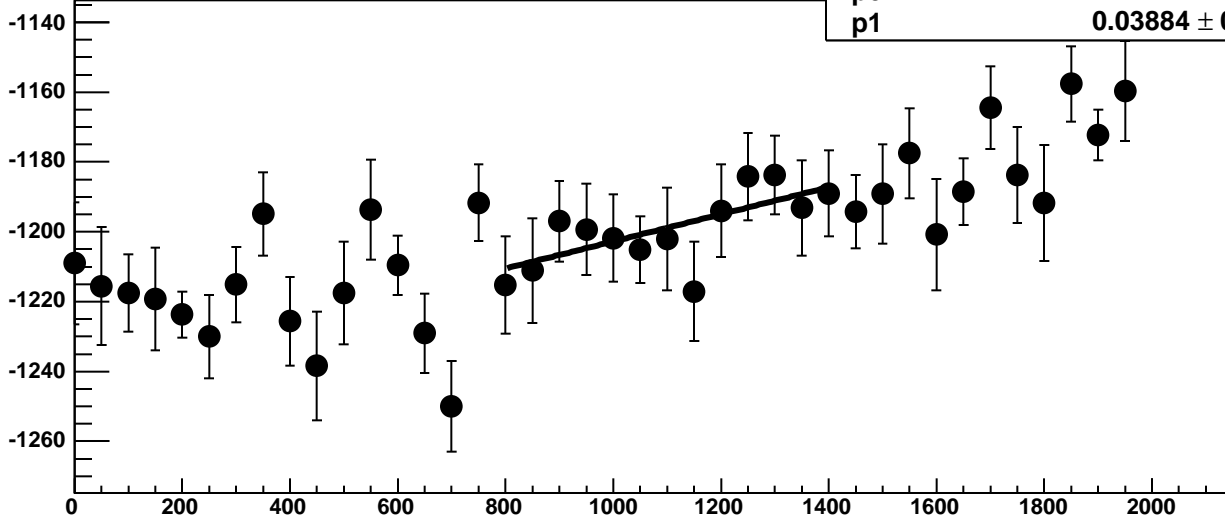
Chip 0, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC



Chip 0, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC

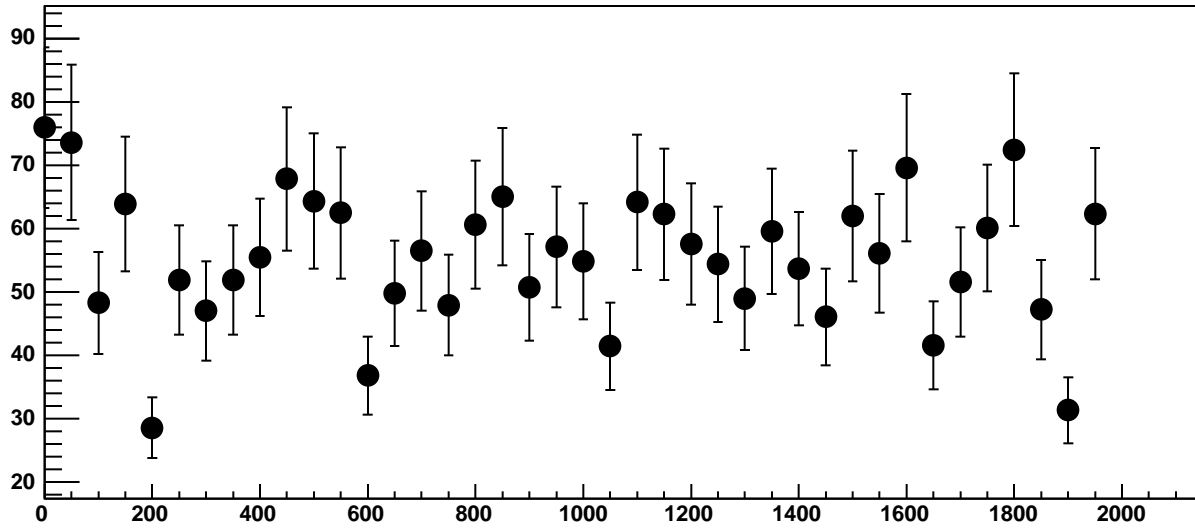


Chip 0, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC

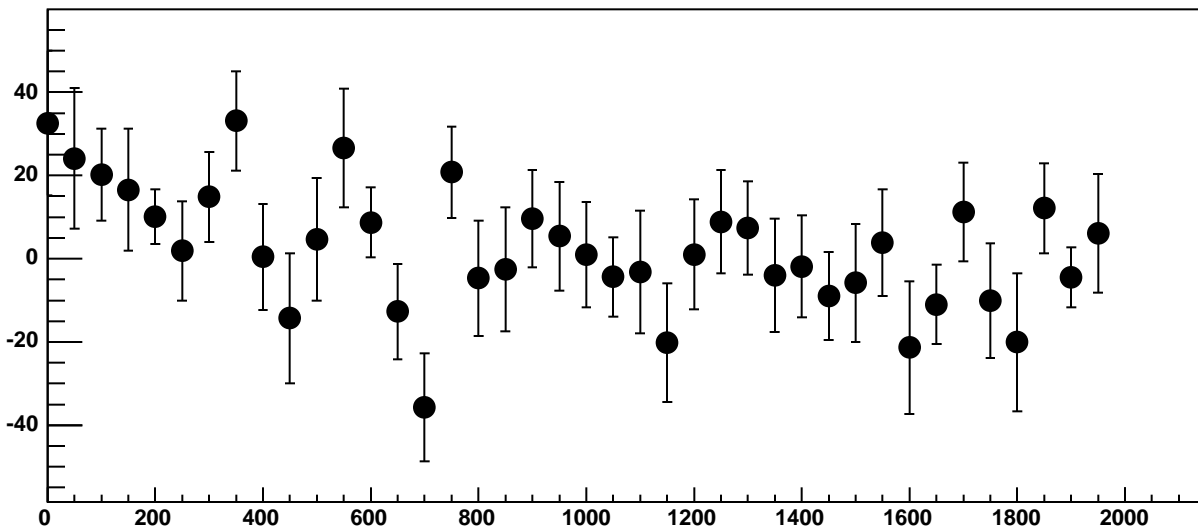


$\chi^2 / \text{ndf}$  4.298 / 11  
p0  $-1242 \pm 21.49$   
p1  $0.03884 \pm 0.01917$

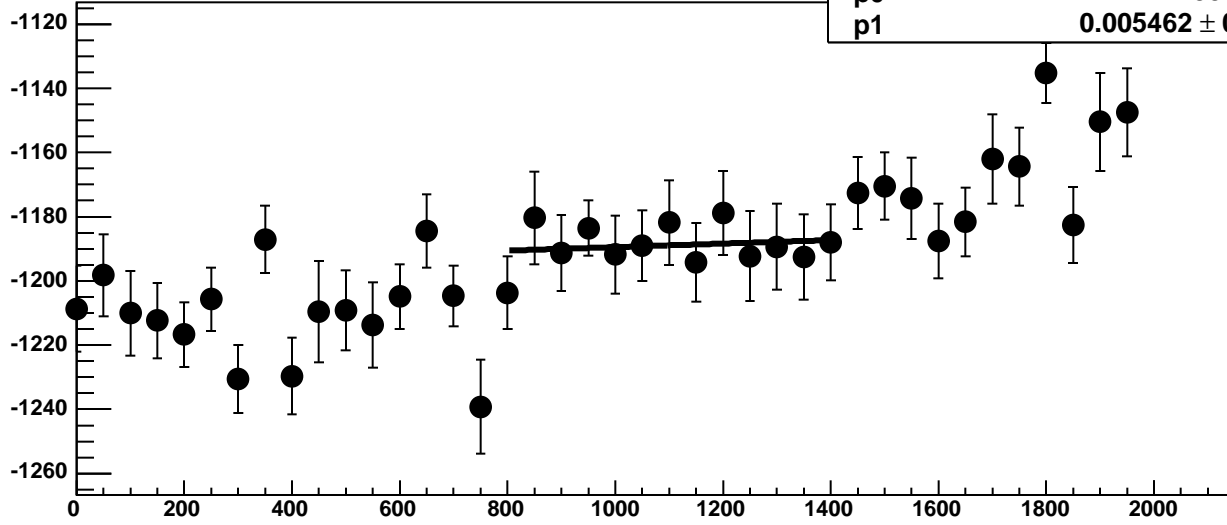
Chip 0, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC



Chip 0, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC



Chip 0, Channel 14, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

3.647 / 11

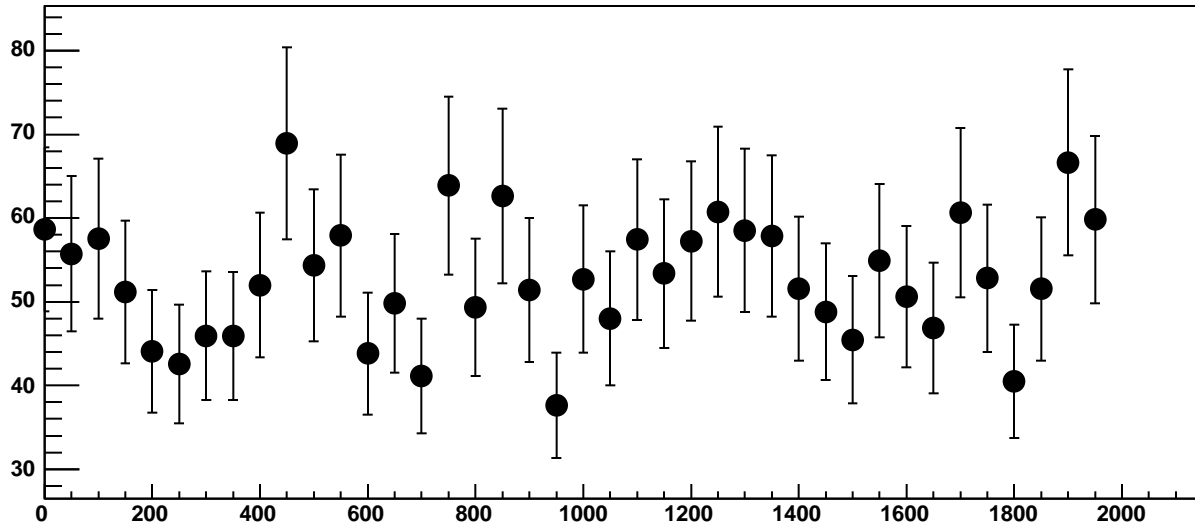
p0

$-1195 \pm 19.82$

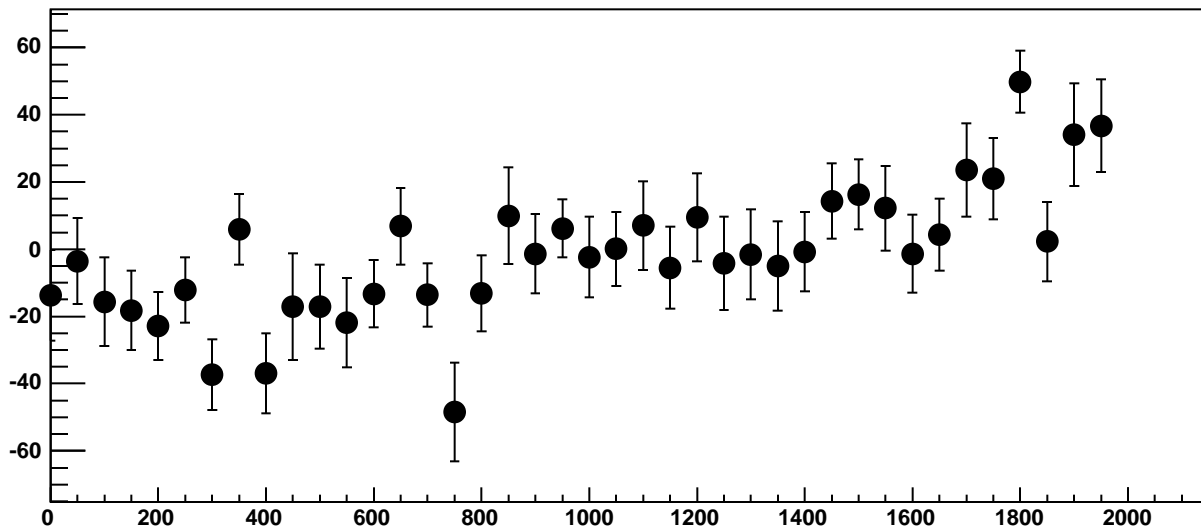
p1

$0.005462 \pm 0.01808$

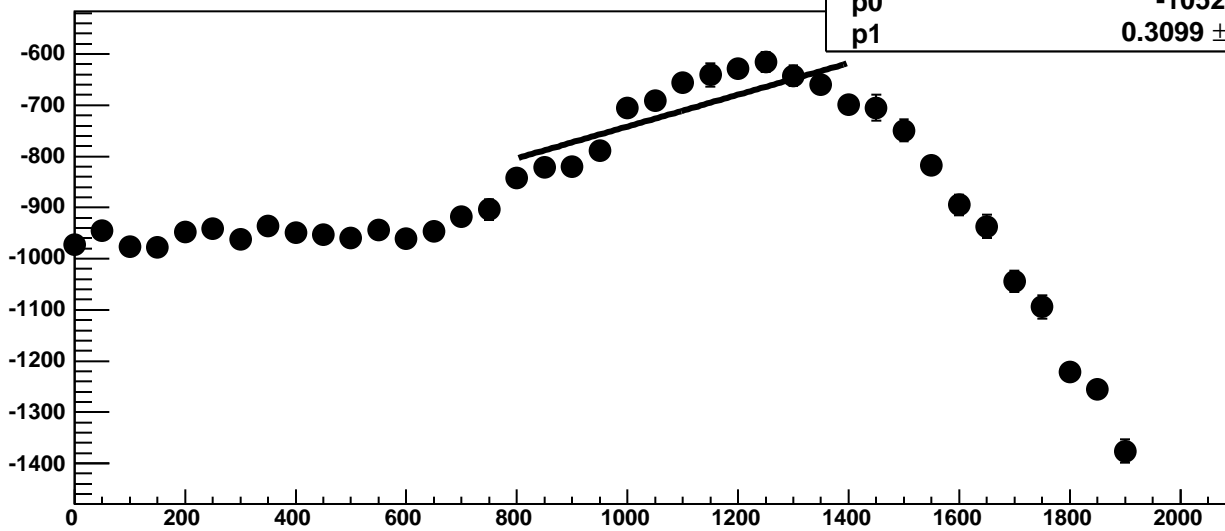
Chip 0, Channel 14, Enable 4, Hold=30, ADC Noise vs DAC



Chip 0, Channel 14, Enable 4, Hold=30, ADC Residuals vs DAC

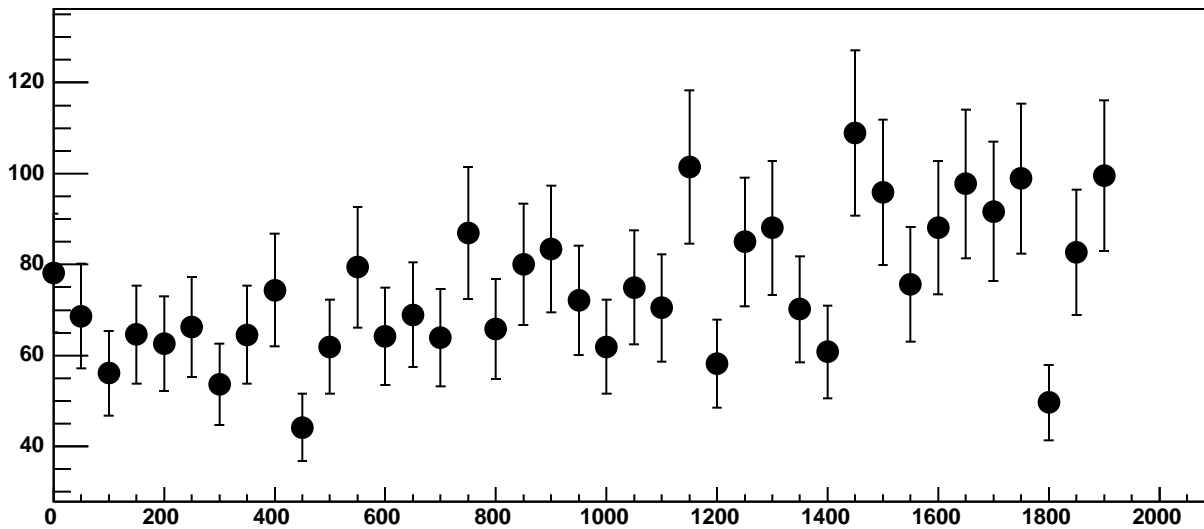


Chip 0, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC

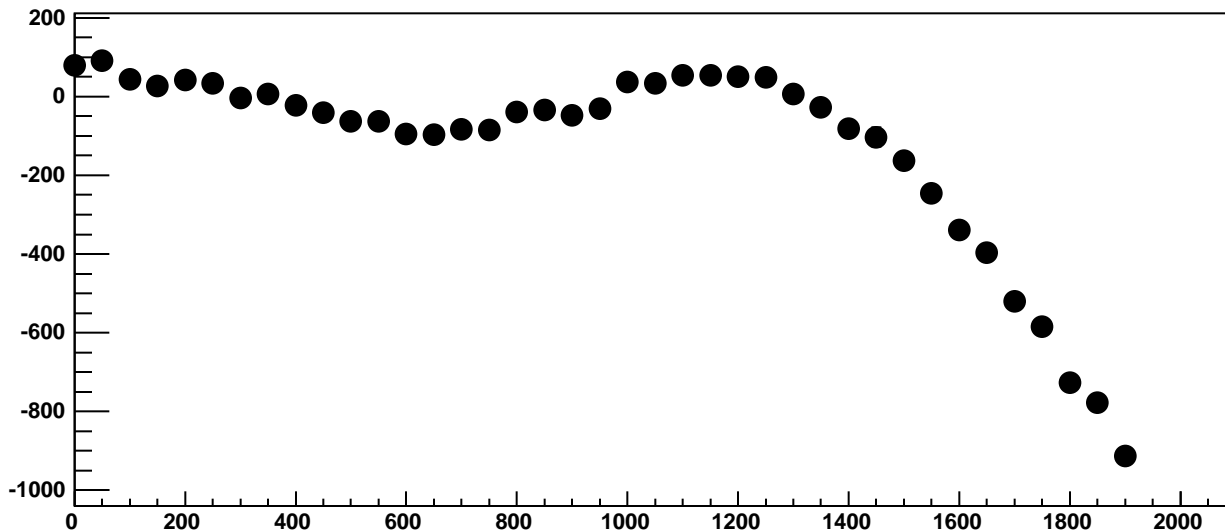


$\chi^2 / \text{ndf}$  103.9 / 11  
p0  $-1052 \pm 26.81$   
p1  $0.3099 \pm 0.0239$

Chip 0, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC

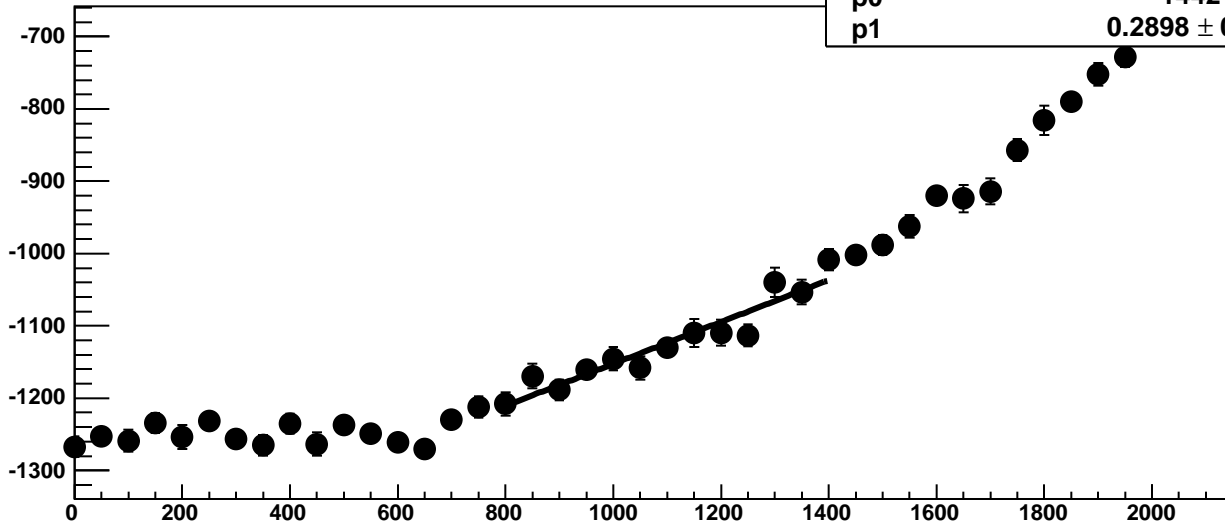


Chip 0, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC



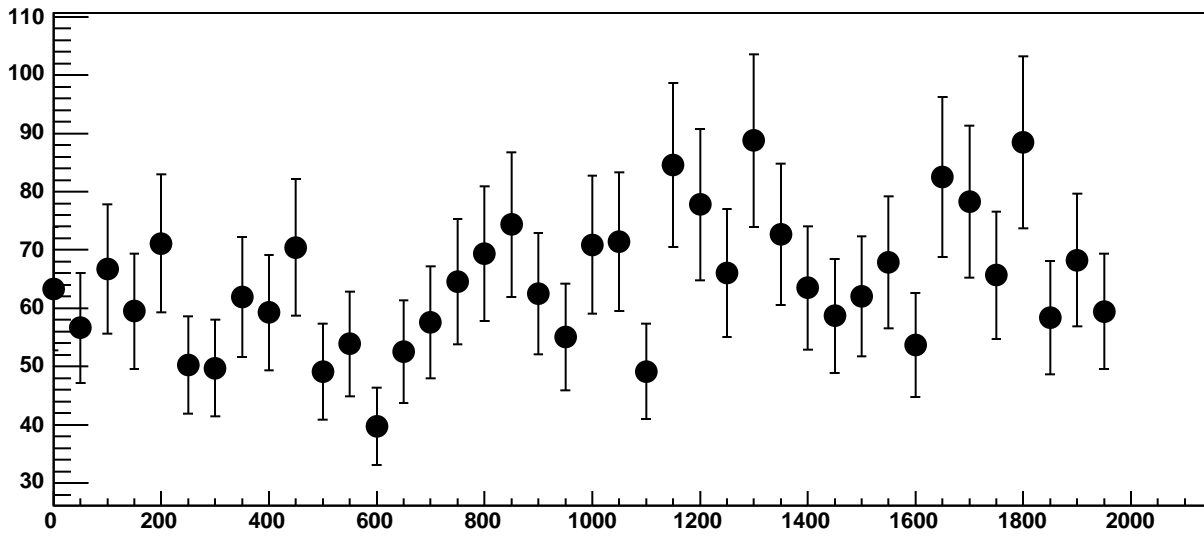


Chip 0, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC

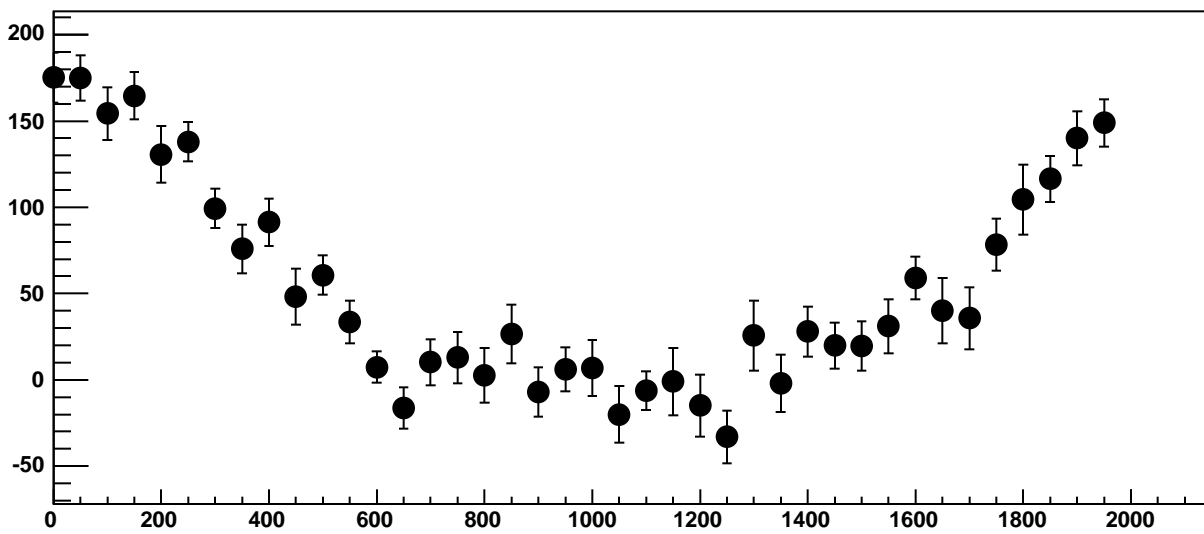


$\chi^2 / \text{ndf}$  15.71 / 11  
p0  $-1442 \pm 25.82$   
p1  $0.2898 \pm 0.02339$

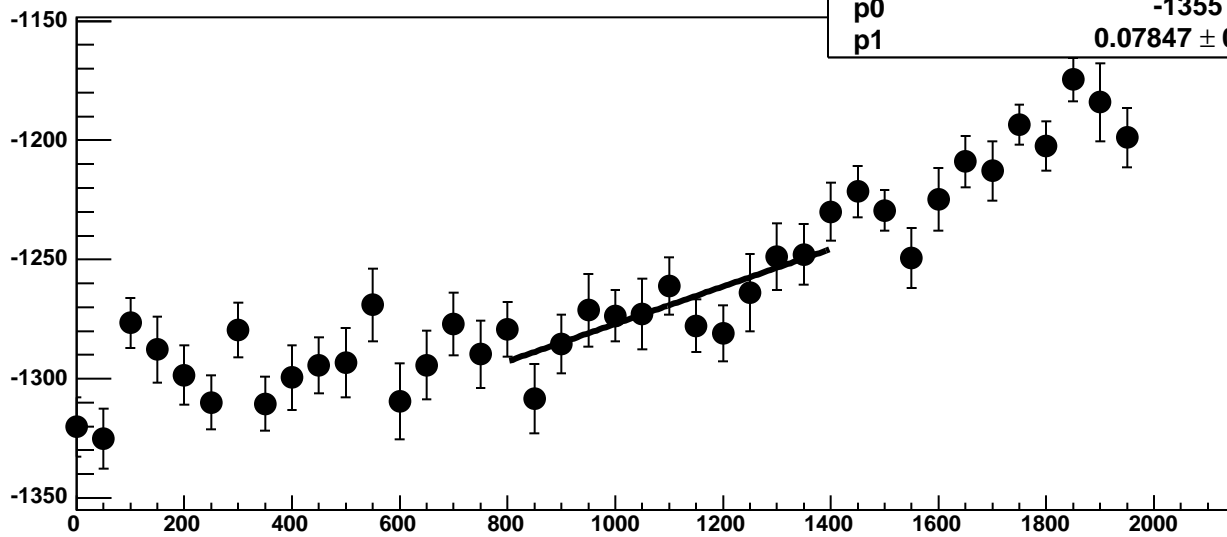
Chip 0, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC



Chip 0, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC

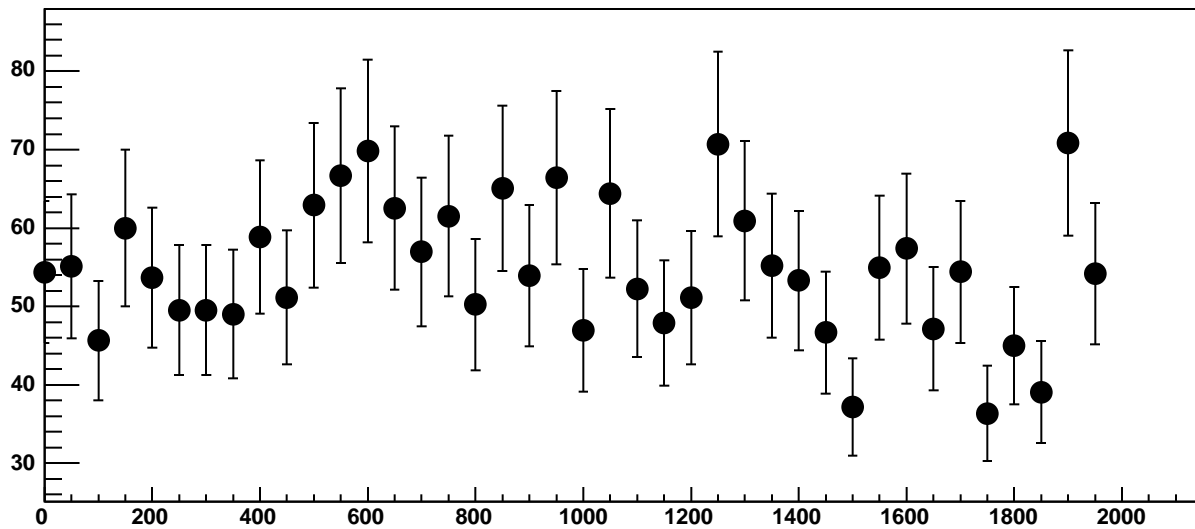


Chip 0, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC

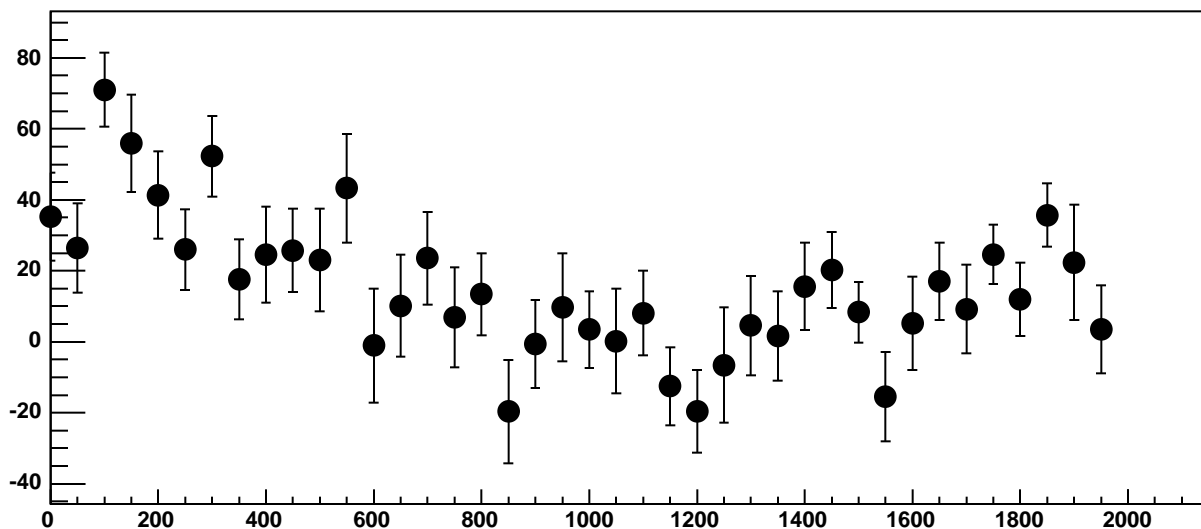


$\chi^2 / \text{ndf}$  10.14 / 11  
p0  $-1355 \pm 21.03$   
p1  $0.07847 \pm 0.01889$

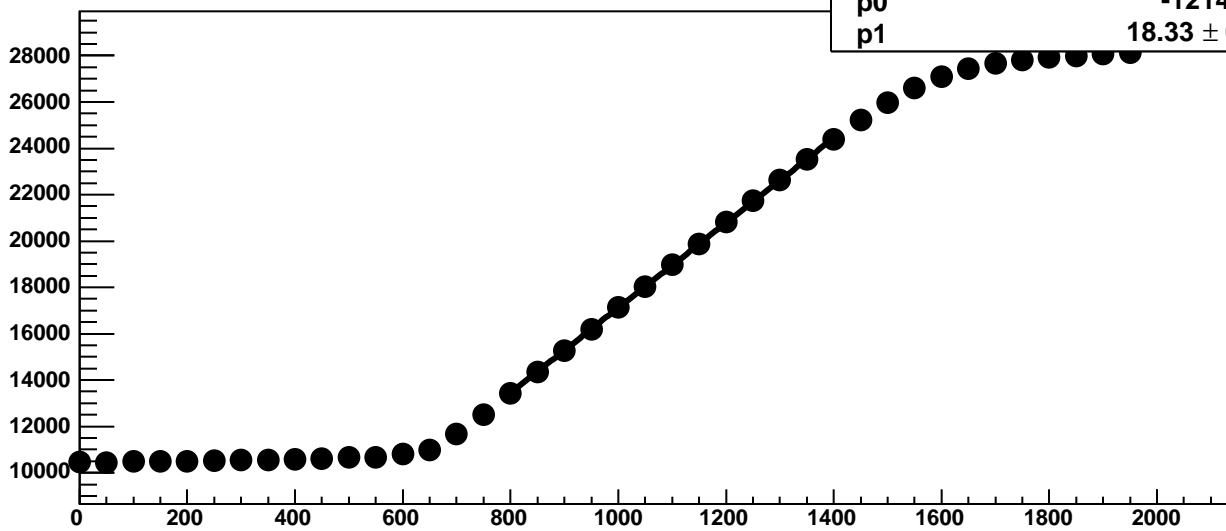
Chip 0, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC



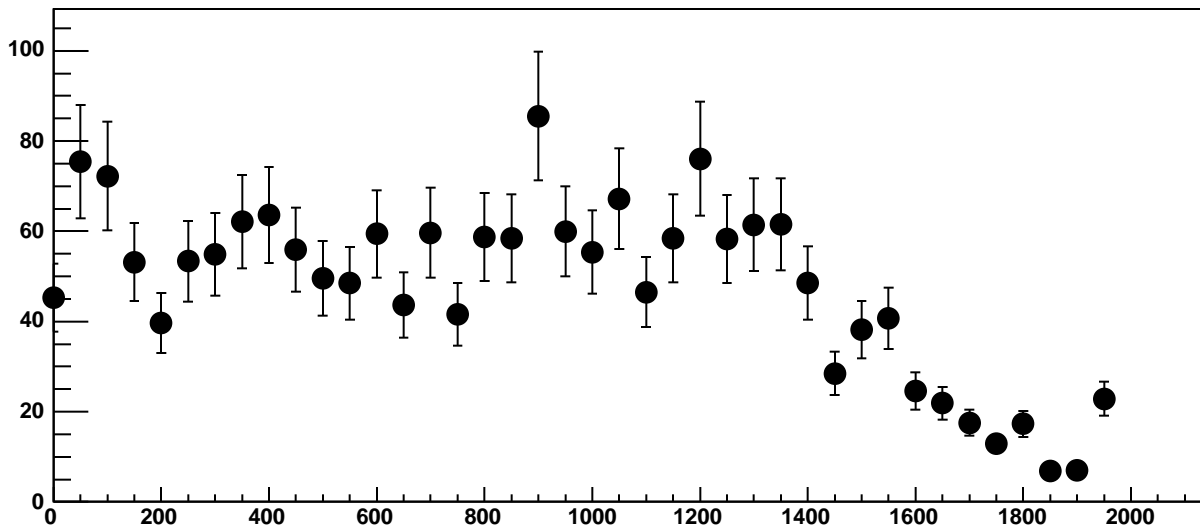
Chip 0, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC



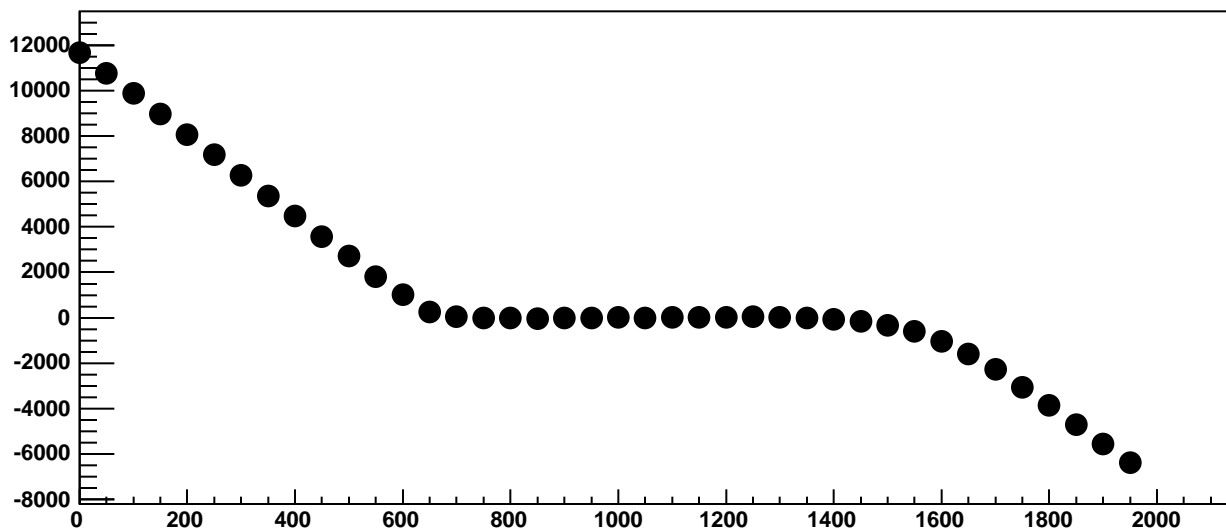
Chip 0, Channel 15, Enable 2!, Hold=30, ADC Mean vs DAC



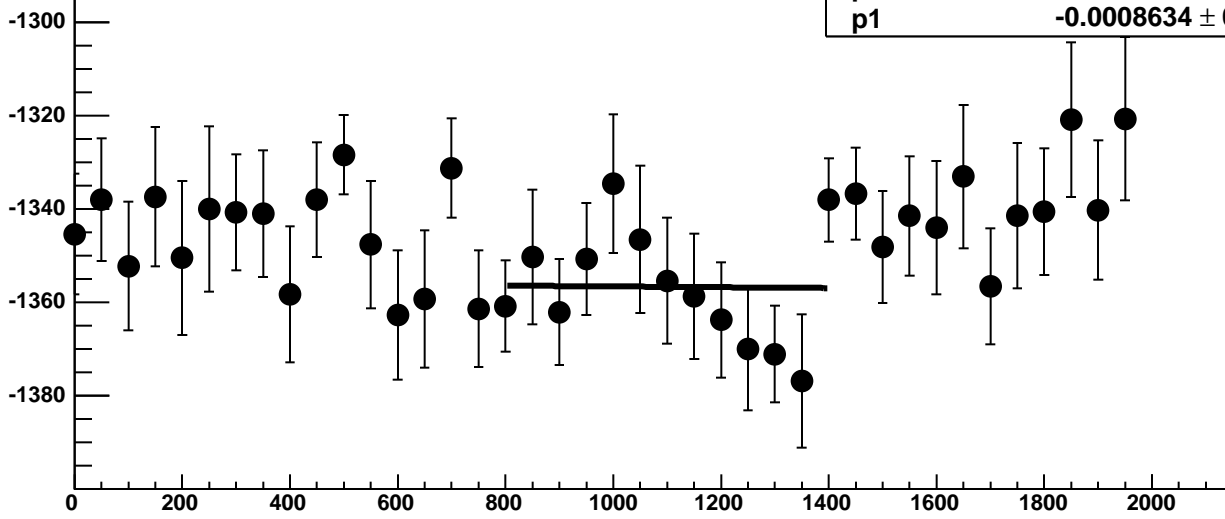
Chip 0, Channel 15, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 0, Channel 15, Enable 2!, Hold=30, ADC Residuals vs DAC

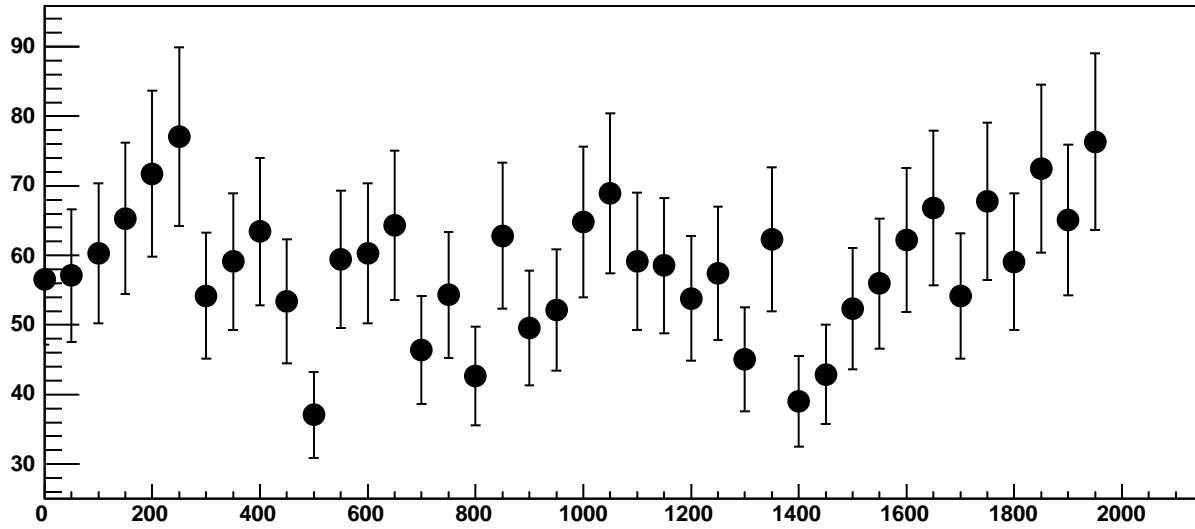


Chip 0, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC

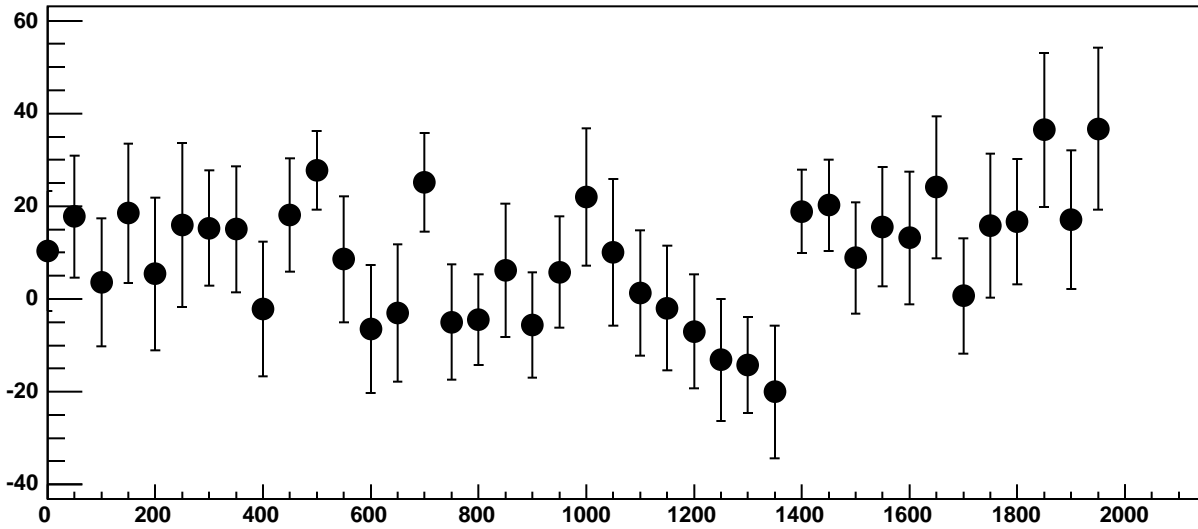


$\chi^2 / \text{ndf}$  13.12 / 11  
p0  $-1356 \pm 18.44$   
p1  $-0.0008634 \pm 0.01631$

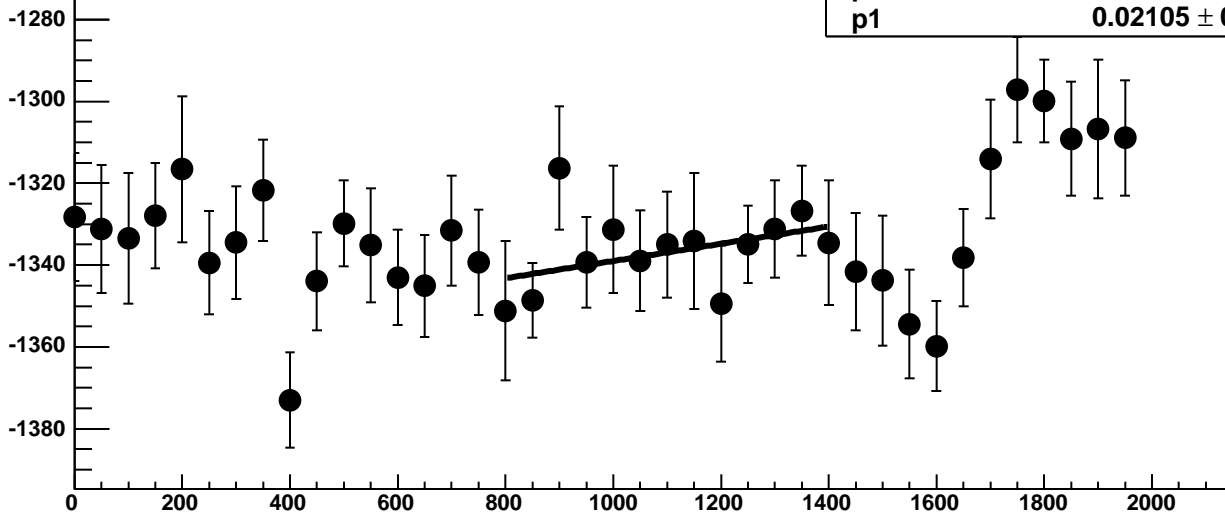
Chip 0, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC



Chip 0, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC

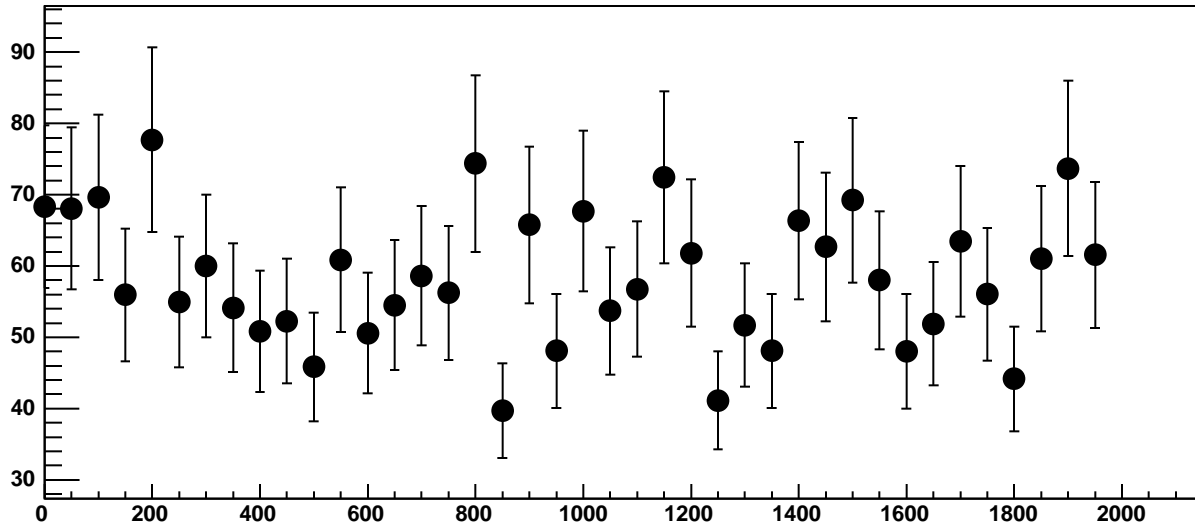


Chip 0, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC

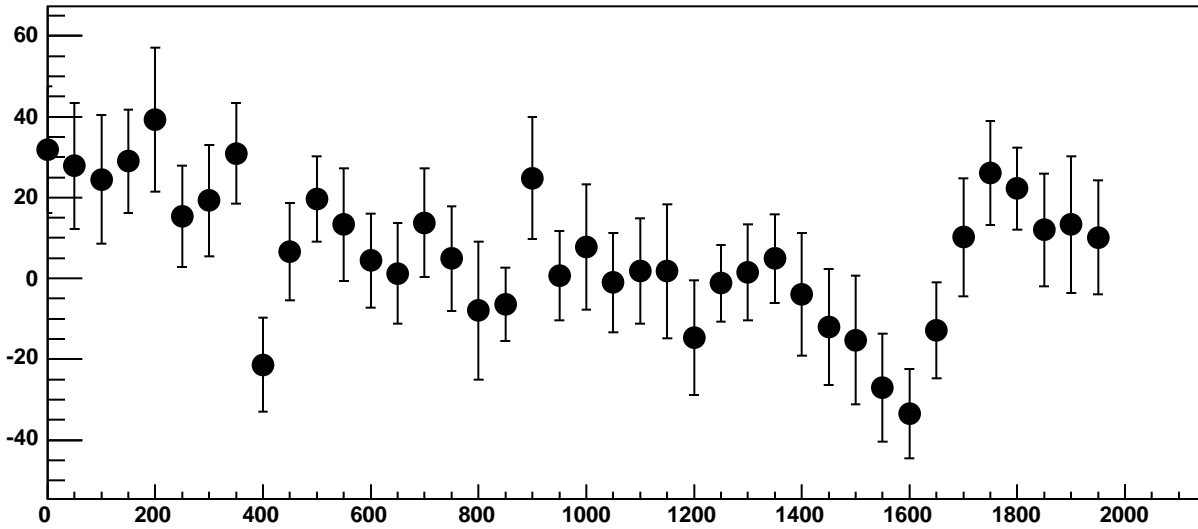


$\chi^2 / \text{ndf}$  5.078 / 11  
p0  $-1360 \pm 20.5$   
p1  $0.02105 \pm 0.01833$

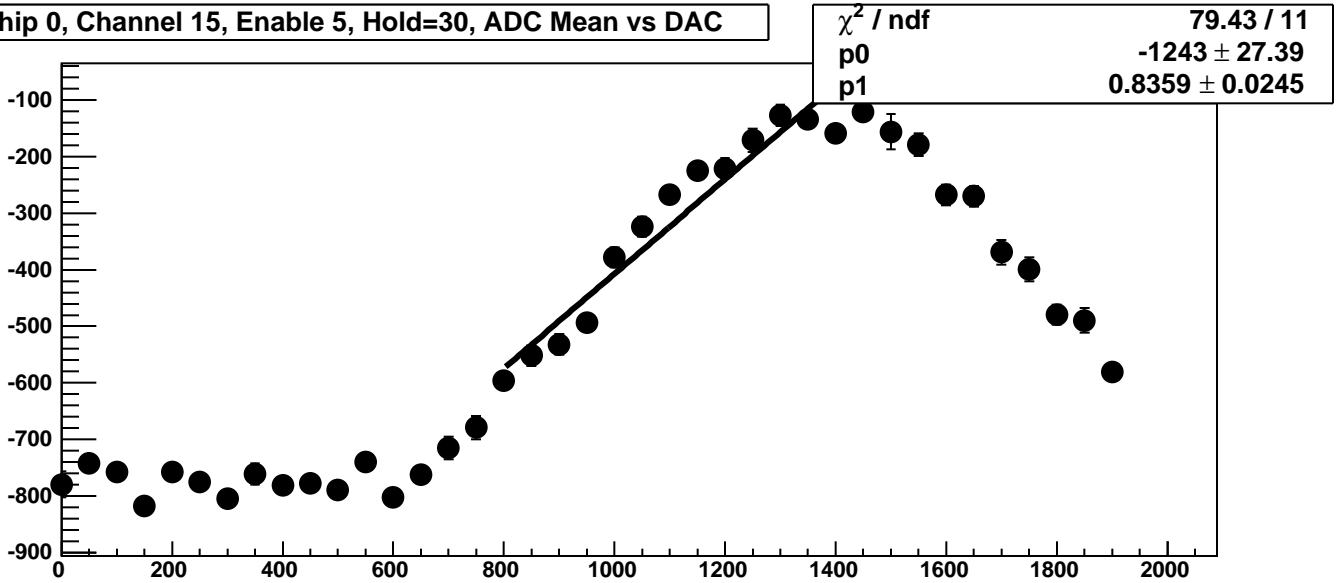
Chip 0, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC



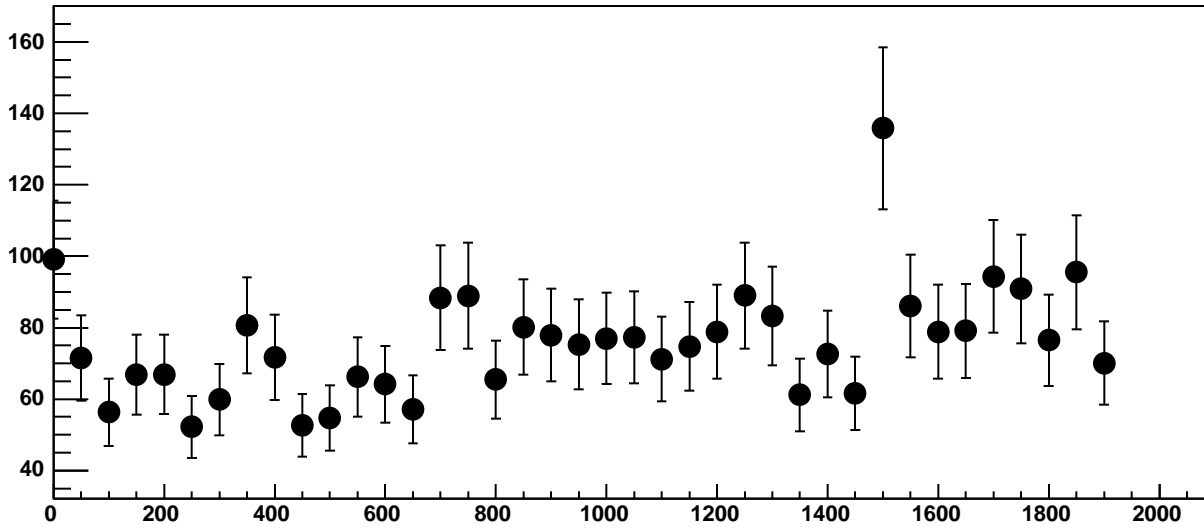
Chip 0, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC



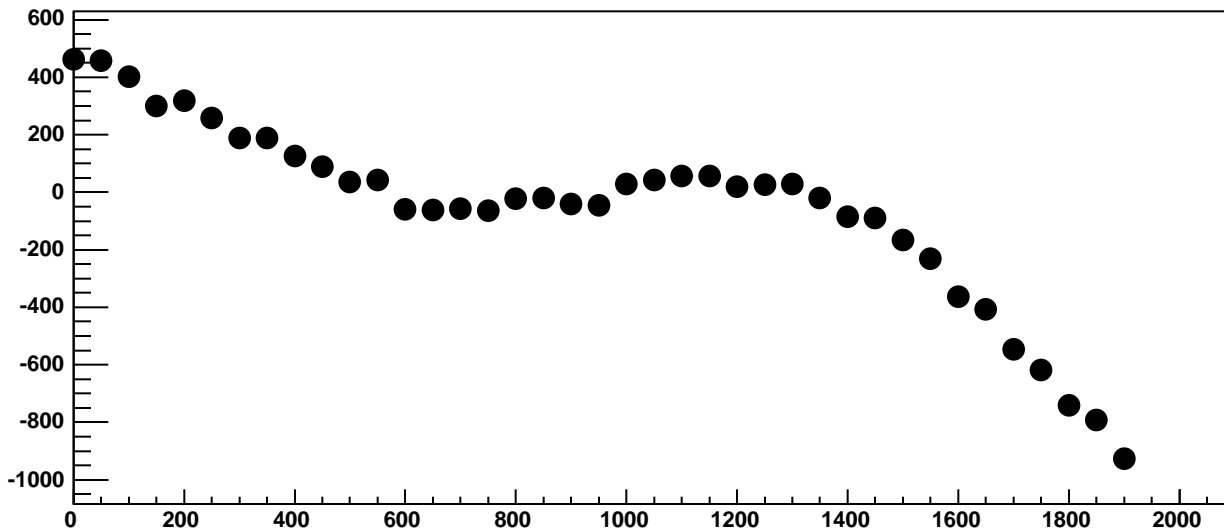
Chip 0, Channel 15, Enable 5, Hold=30, ADC Mean vs DAC



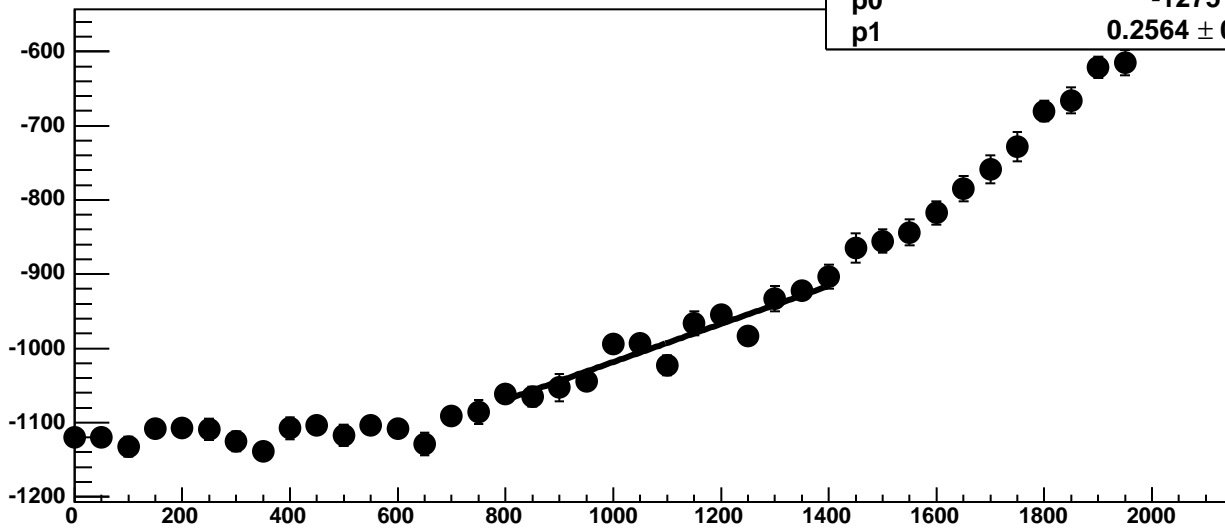
Chip 0, Channel 15, Enable 5, Hold=30, ADC Noise vs DAC



Chip 0, Channel 15, Enable 5, Hold=30, ADC Residuals vs DAC

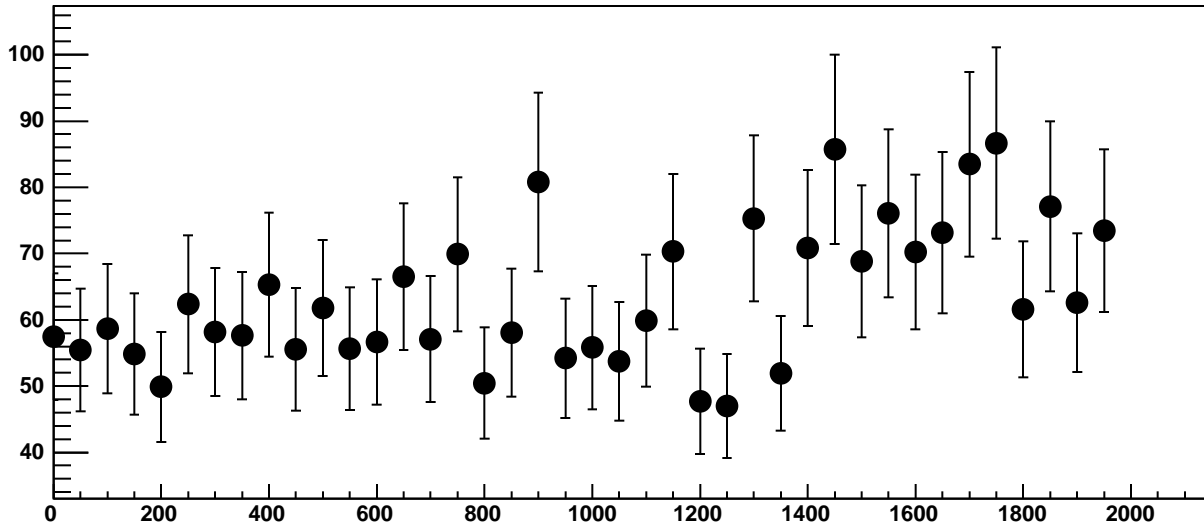


Chip 0, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC

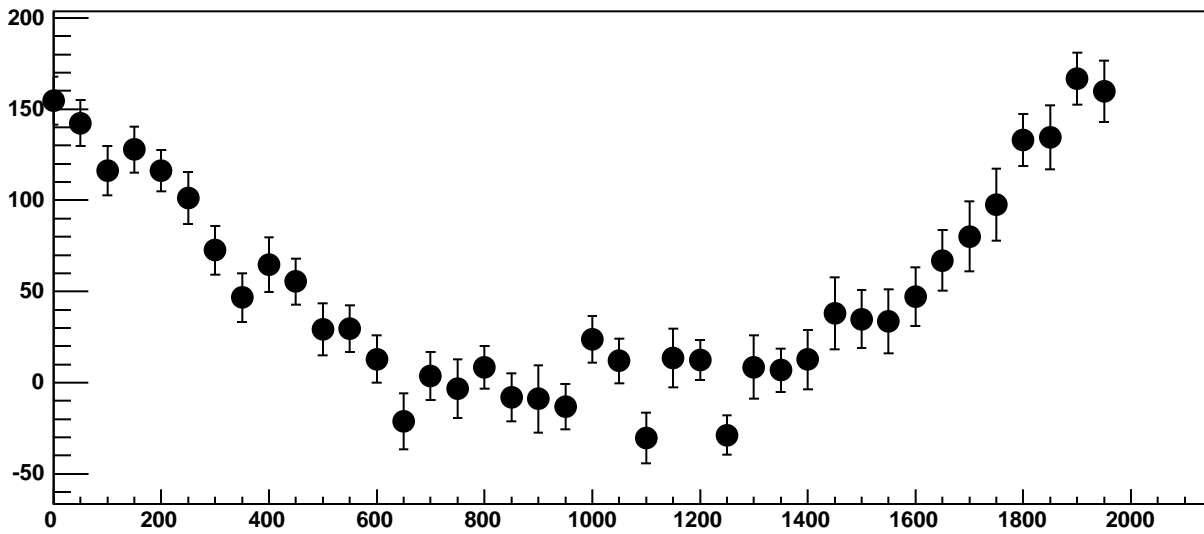


$\chi^2 / \text{ndf}$  21.87 / 11  
p0  $-1275 \pm 21.96$   
p1  $0.2564 \pm 0.01975$

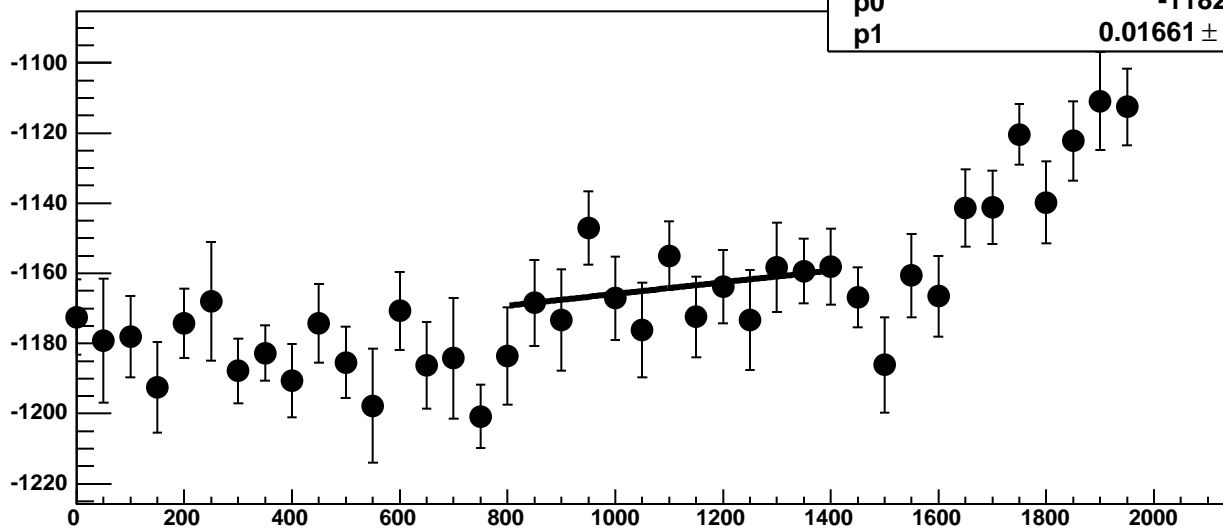
Chip 0, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC



Chip 0, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 0, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

7.724 / 11

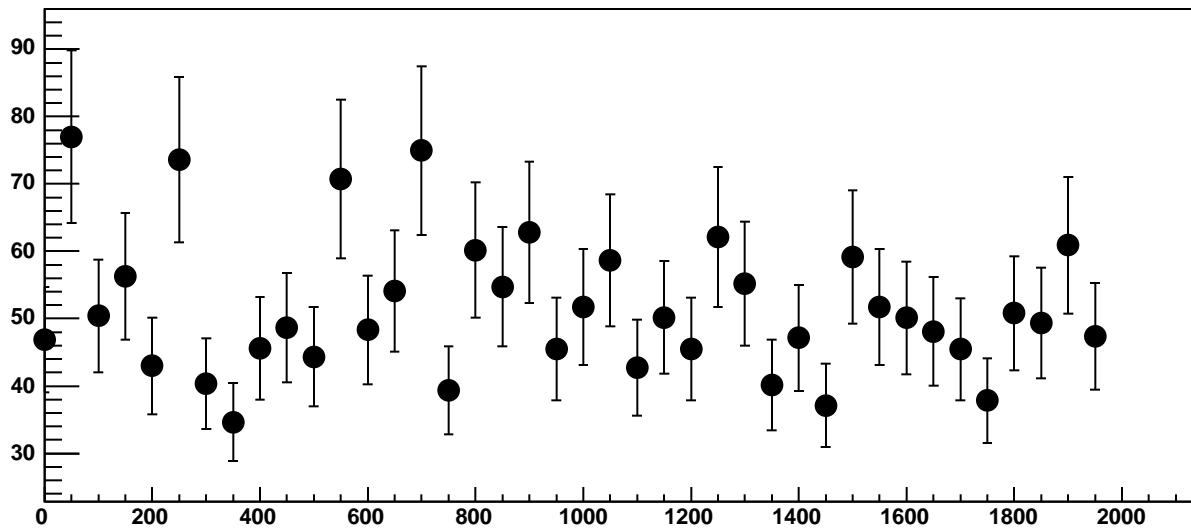
p0

$-1182 \pm 19.81$

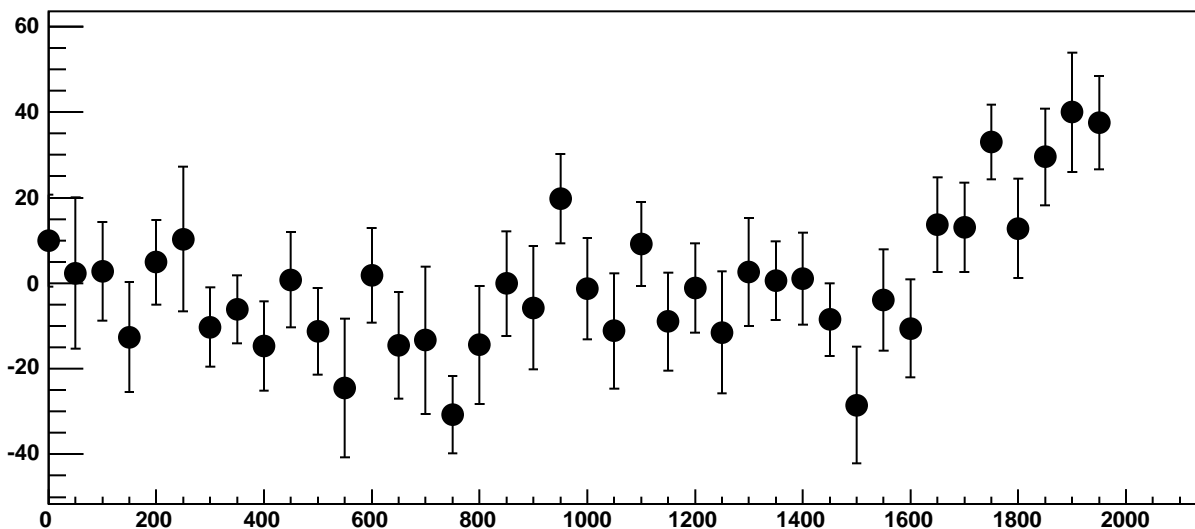
p1

$0.01661 \pm 0.01741$

Chip 0, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC

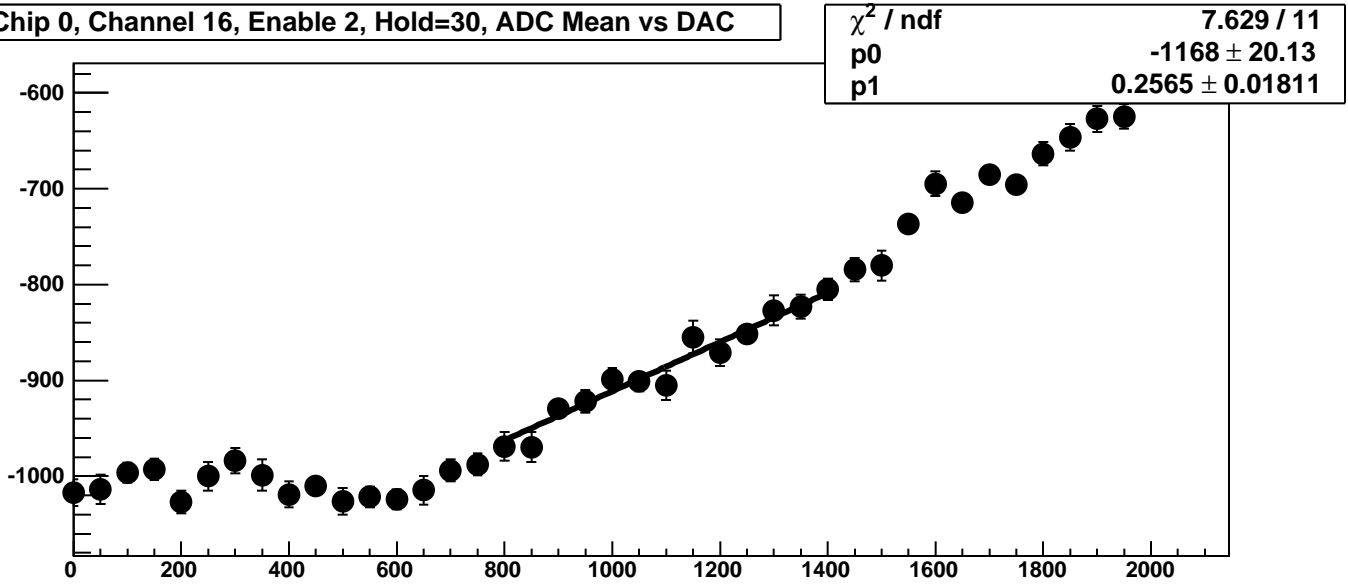


Chip 0, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC

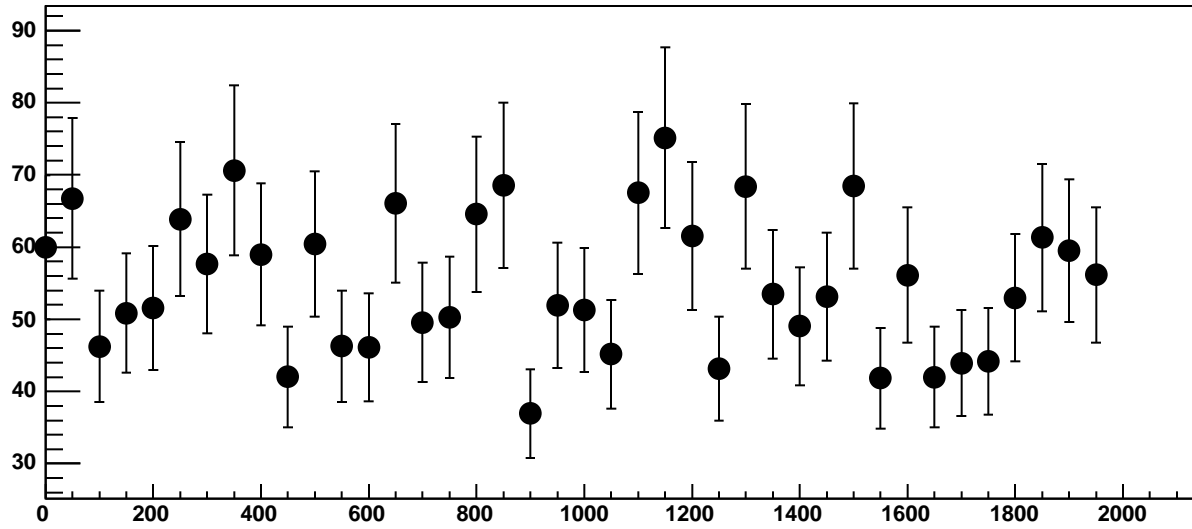




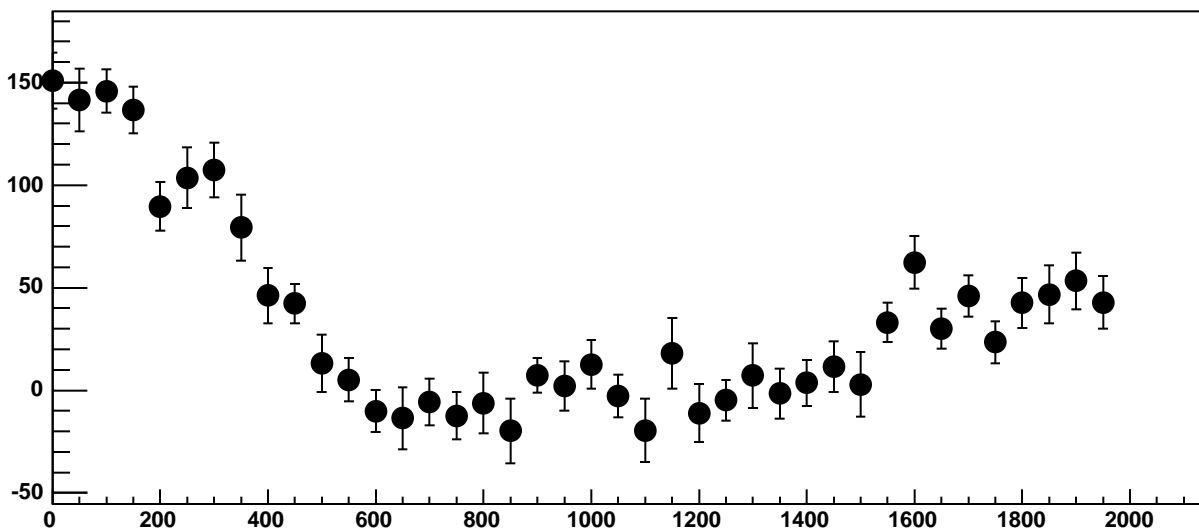
Chip 0, Channel 16, Enable 2, Hold=30, ADC Mean vs DAC



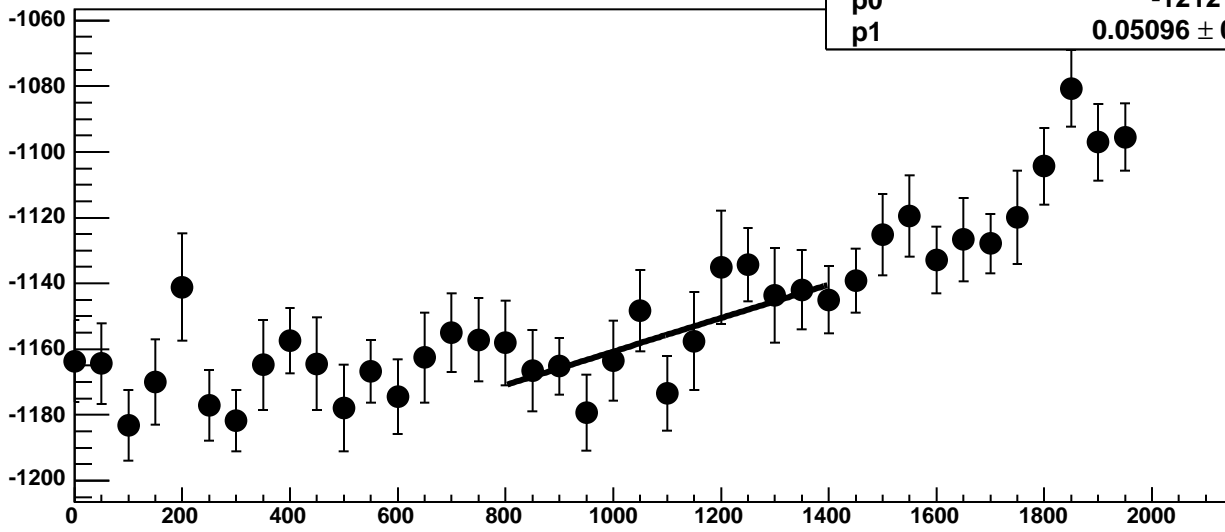
Chip 0, Channel 16, Enable 2, Hold=30, ADC Noise vs DAC



Chip 0, Channel 16, Enable 2, Hold=30, ADC Residuals vs DAC

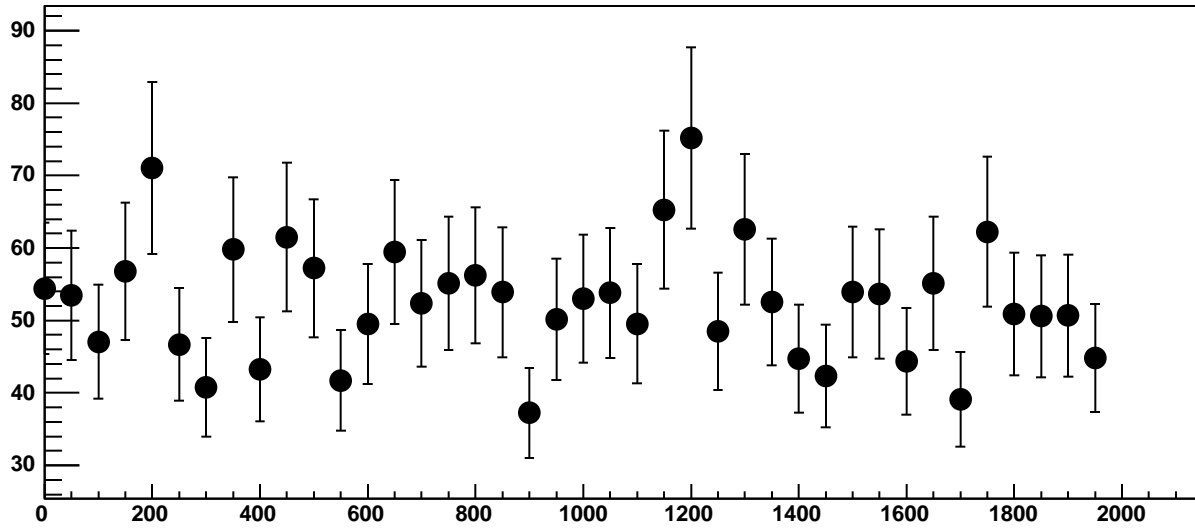


Chip 0, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC

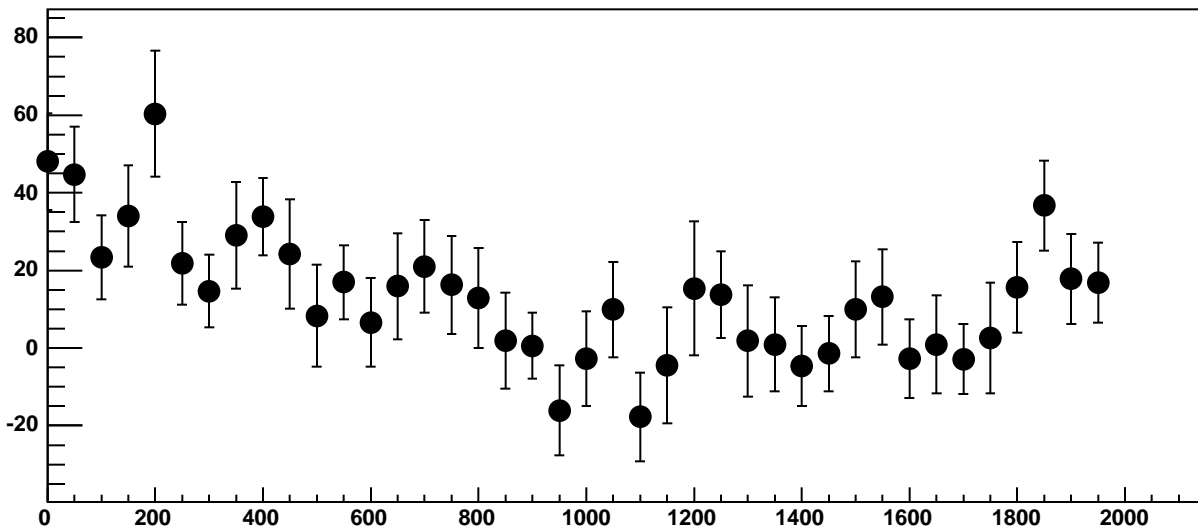


$\chi^2 / \text{ndf}$  8.731 / 11  
p0  $-1212 \pm 18.84$   
p1  $0.05096 \pm 0.01704$

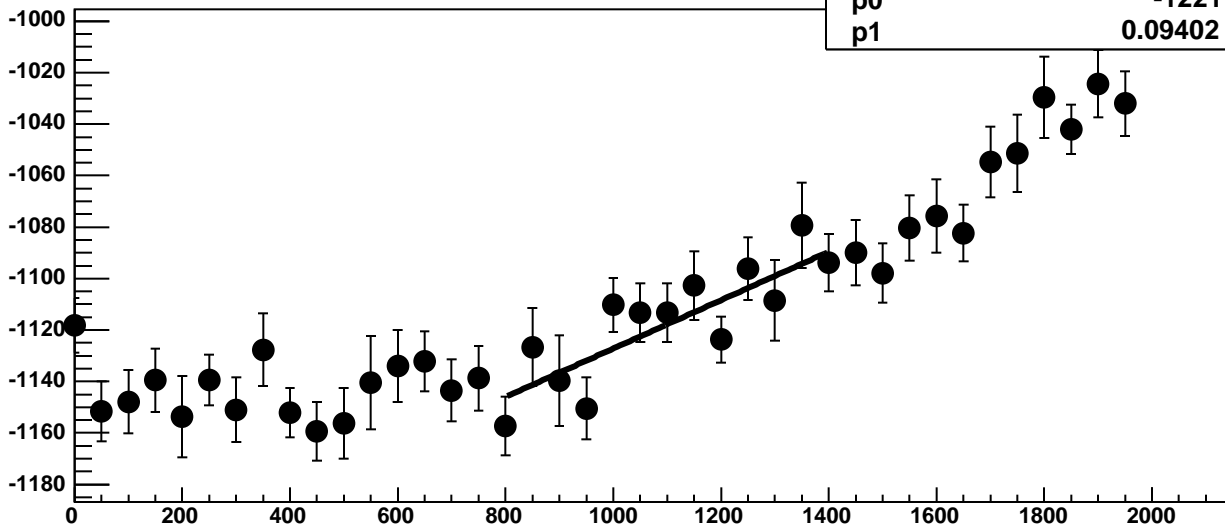
Chip 0, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC



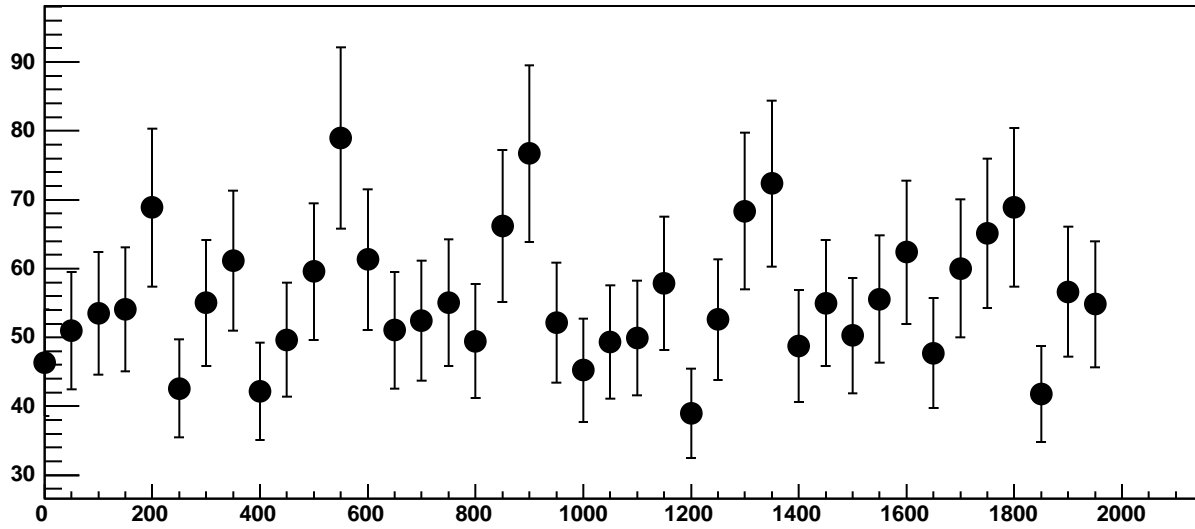
Chip 0, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC



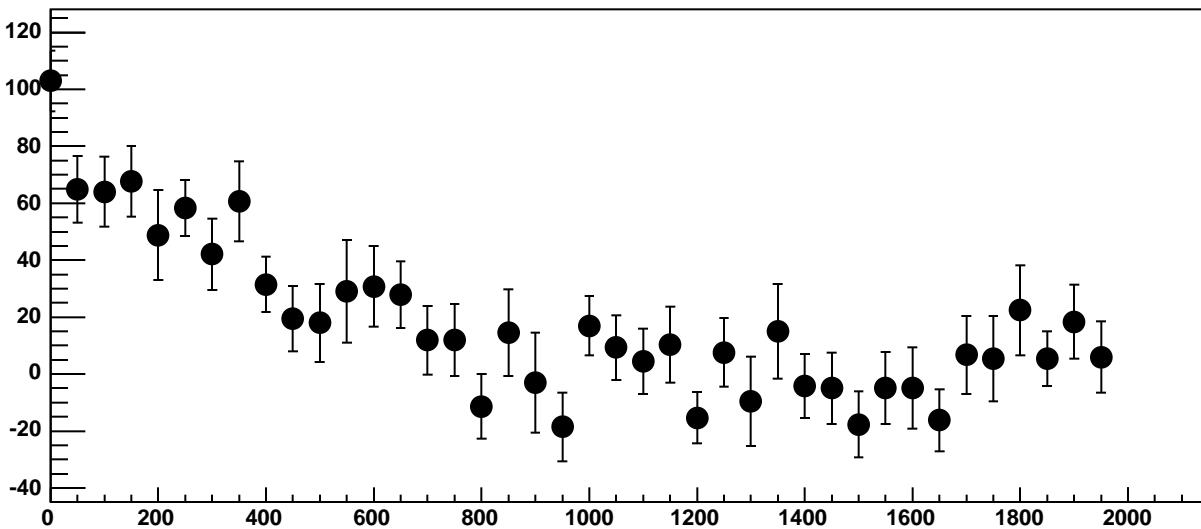
Chip 0, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC



Chip 0, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC



Chip 0, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC



Chip 0, Channel 16, Enable 5!, Hold=30, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

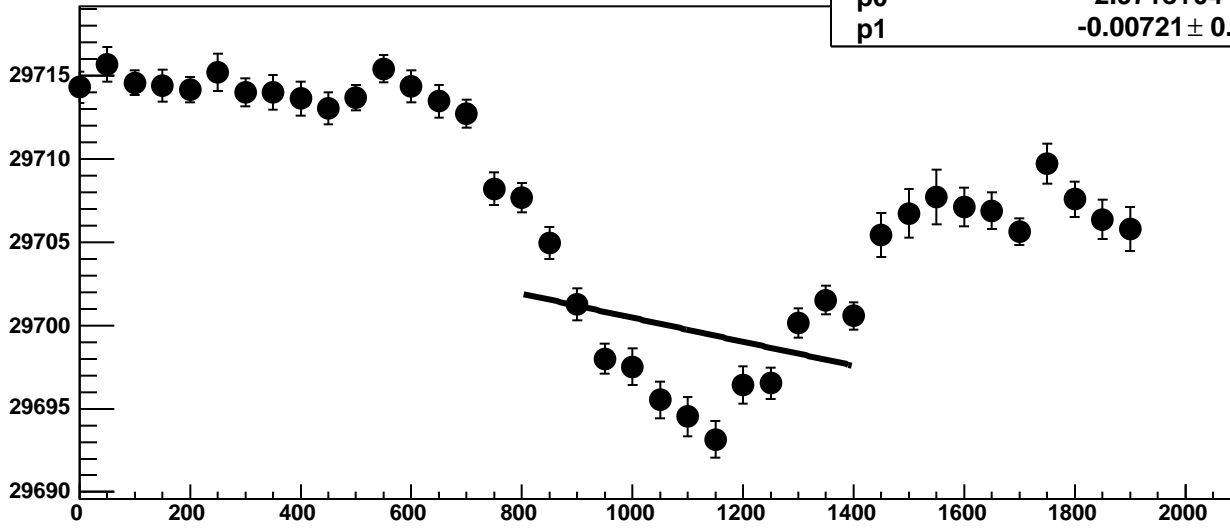
186.7 / 11

p0

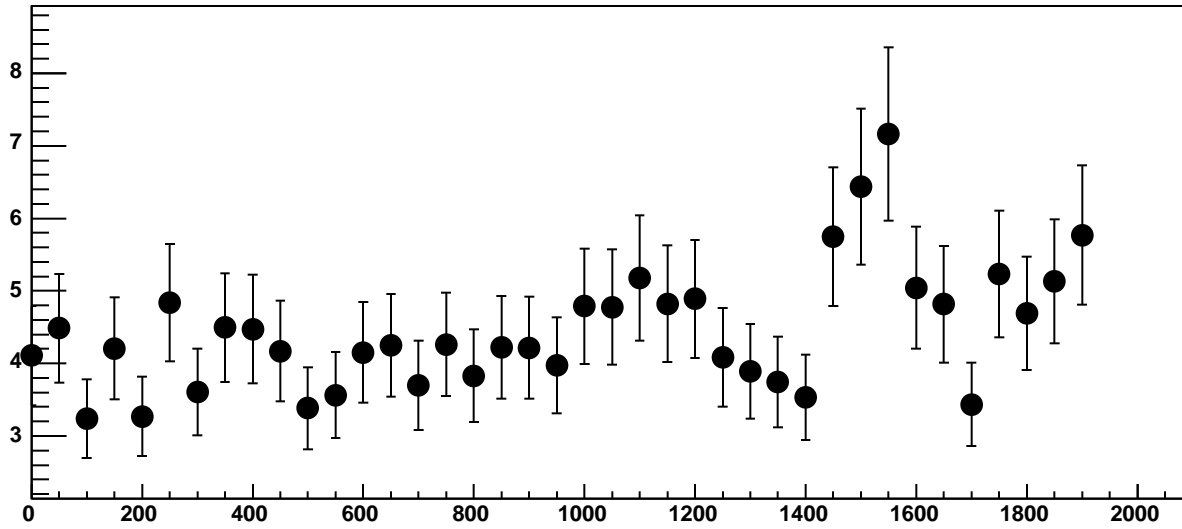
$2.971\text{e}+04 \pm 1.498$

p1

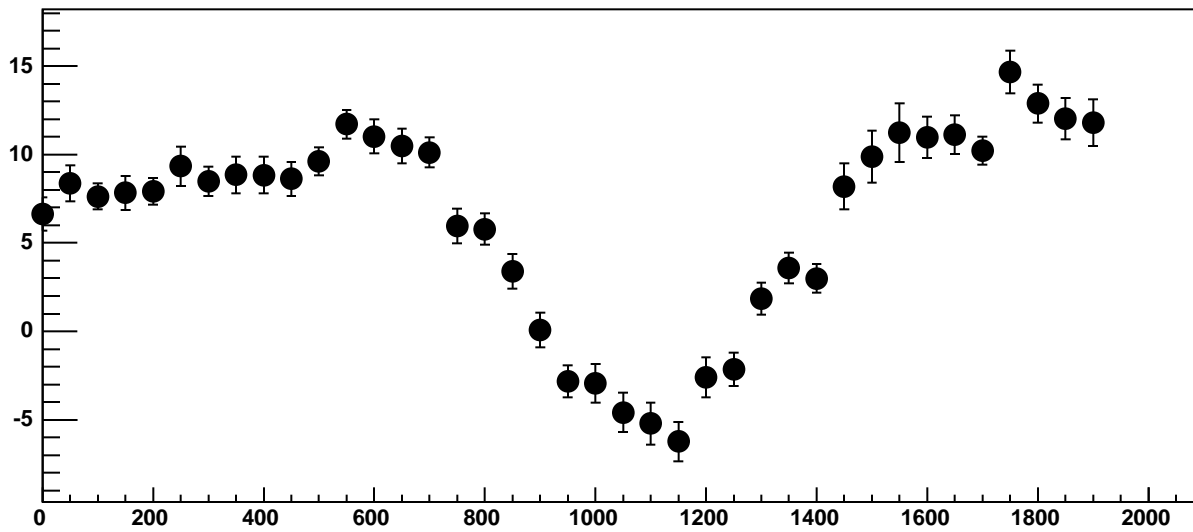
$-0.00721 \pm 0.001326$



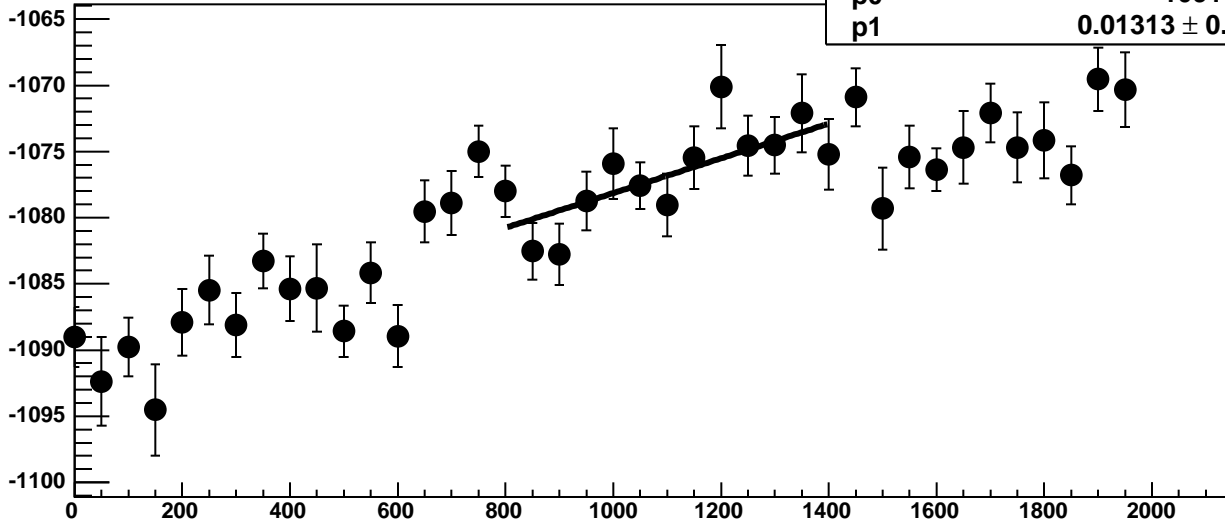
Chip 0, Channel 16, Enable 5!, Hold=30, ADC Noise vs DAC



Chip 0, Channel 16, Enable 5!, Hold=30, ADC Residuals vs DAC



Chip 0, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

11.09 / 11

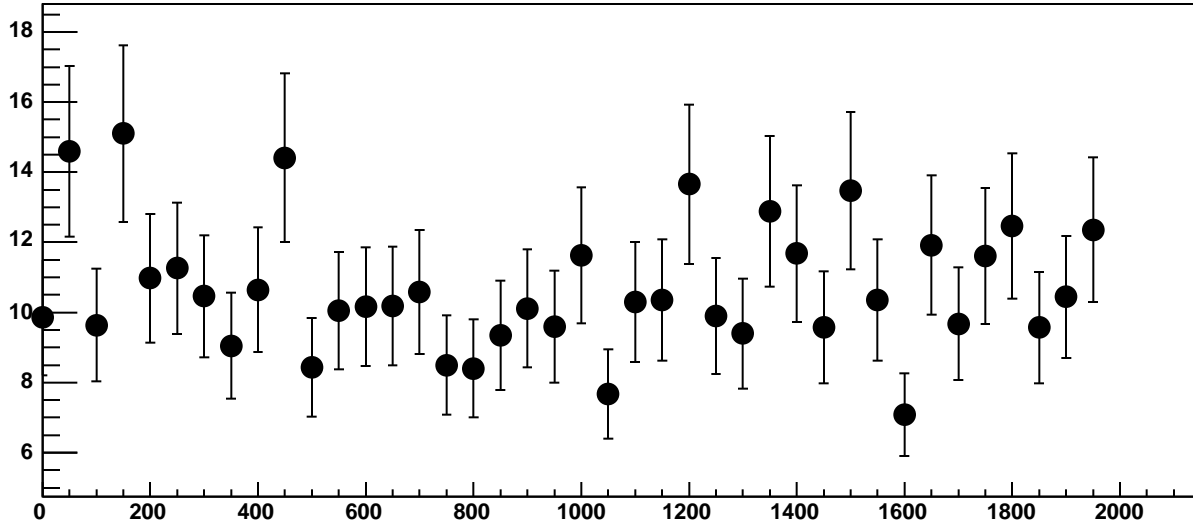
p0

$-1091 \pm 3.746$

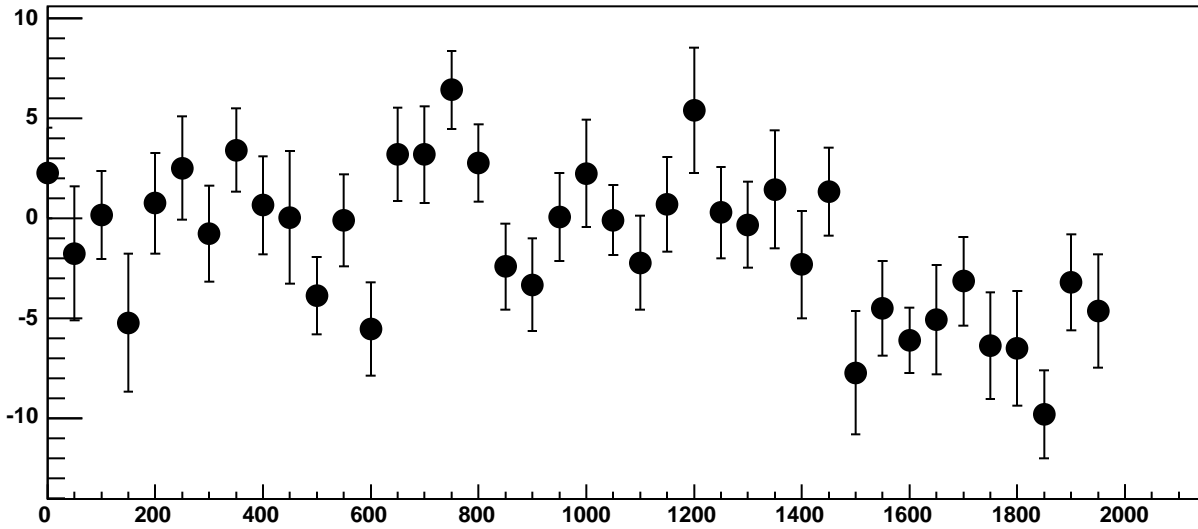
p1

$0.01313 \pm 0.003447$

Chip 0, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC

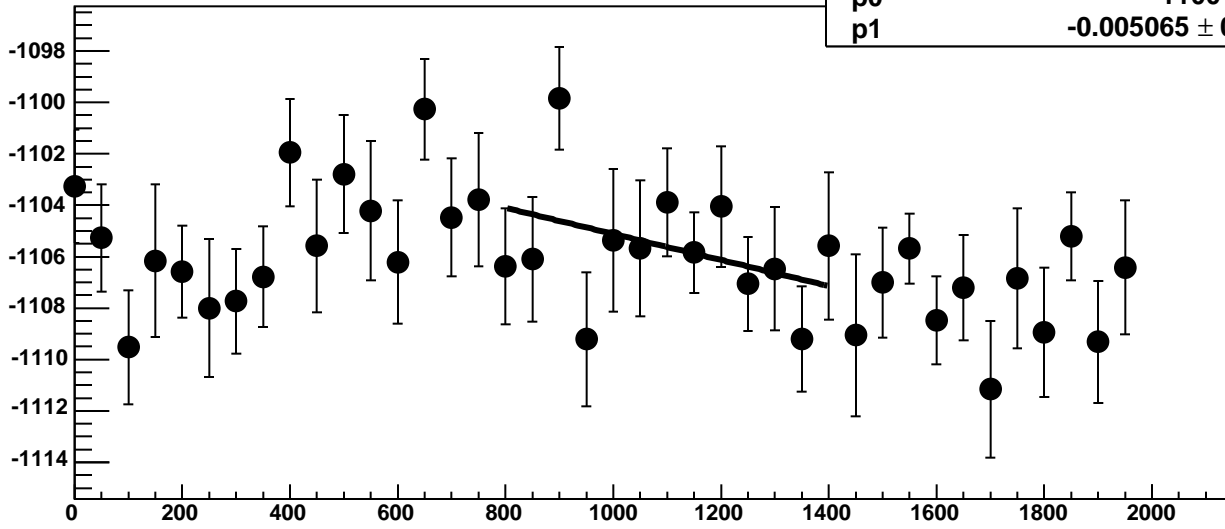


Chip 0, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC

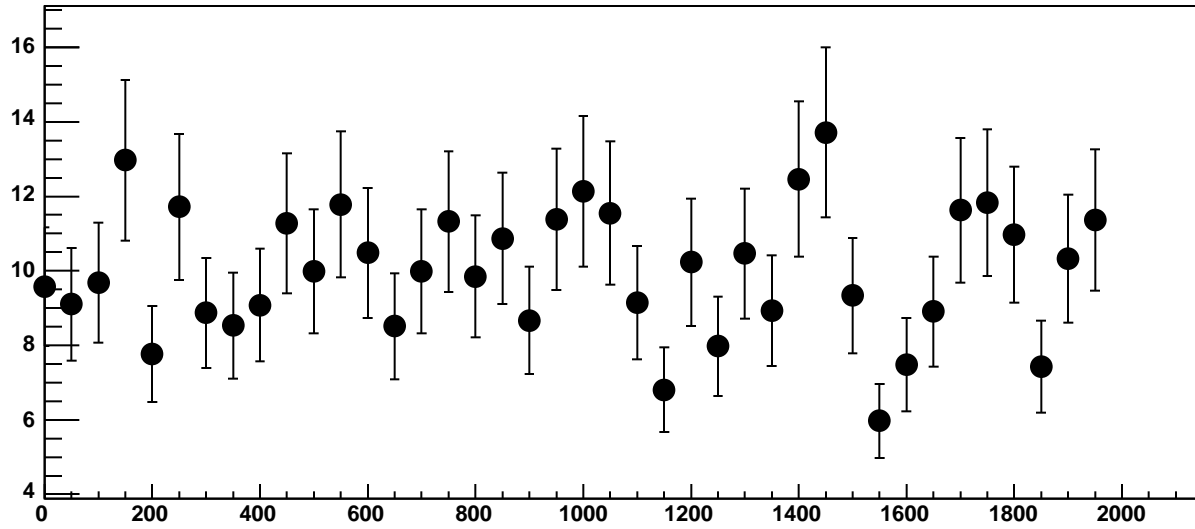


Chip 0, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC

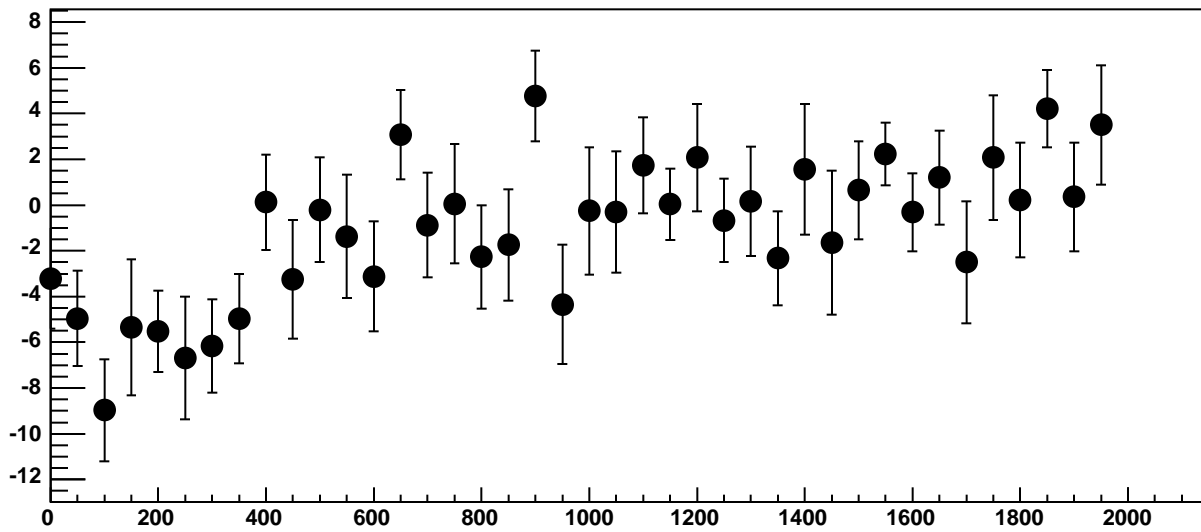
$\chi^2 / \text{ndf}$  13.25 / 11  
p0  $-1100 \pm 3.837$   
p1  $-0.005065 \pm 0.00342$



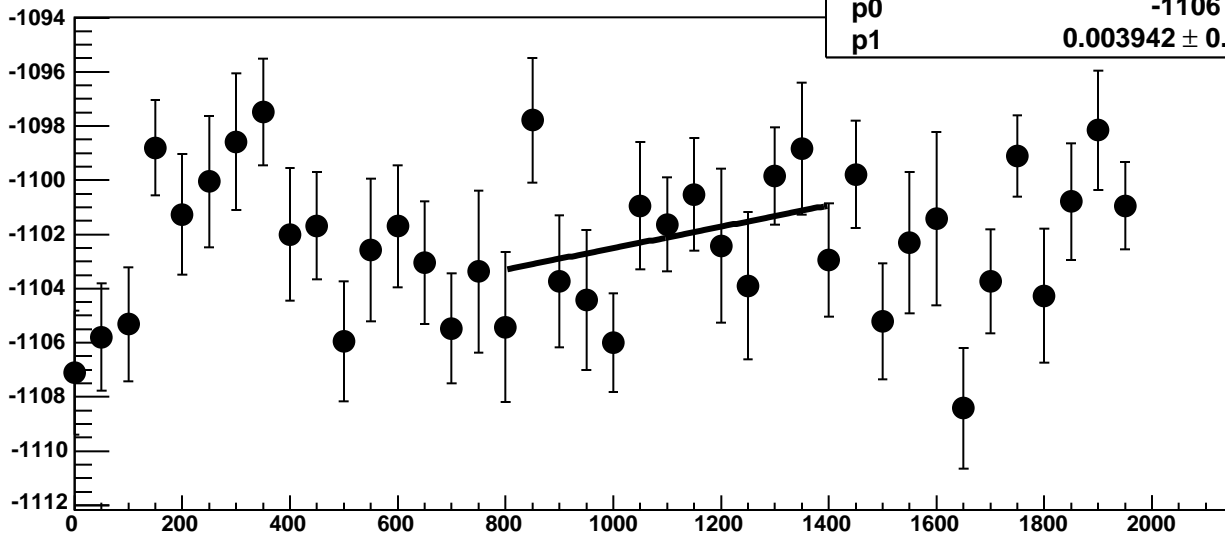
Chip 0, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC



Chip 0, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC

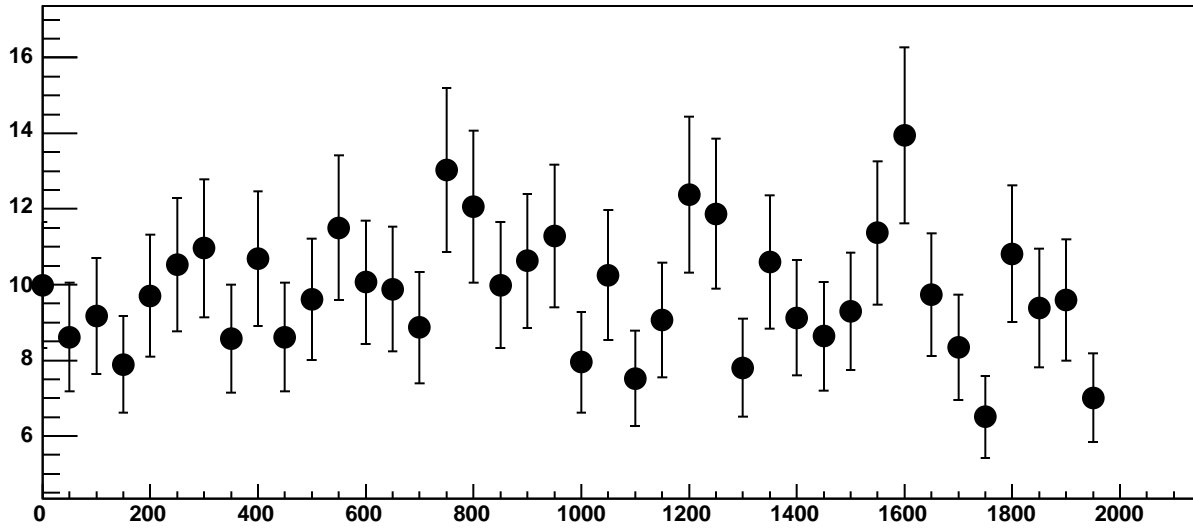


Chip 0, Channel 17, Enable 2!, Hold=30, ADC Mean vs DAC

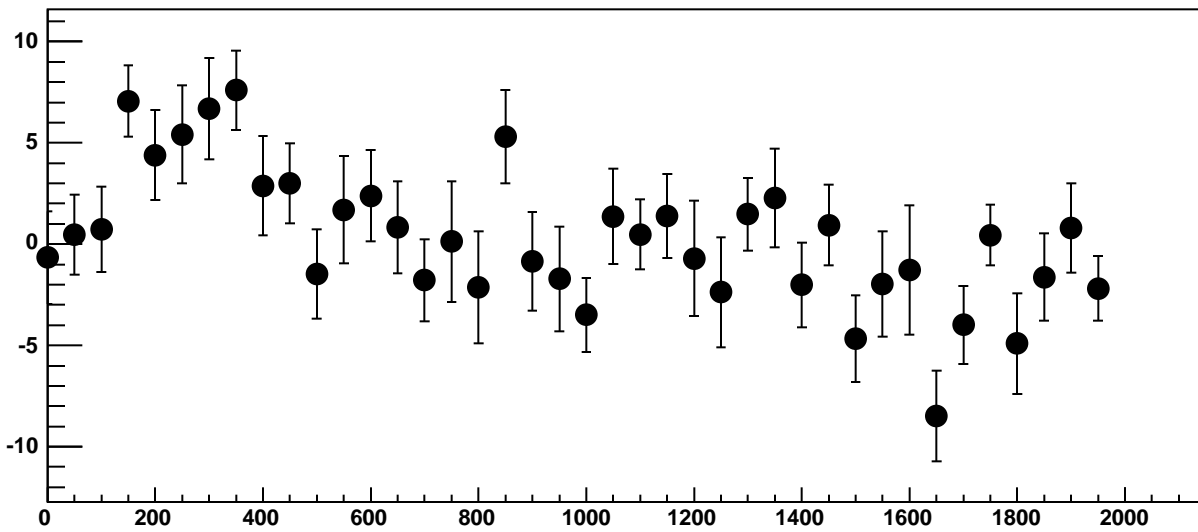


$\chi^2 / \text{ndf}$  14.36 / 11  
p0  $-1106 \pm 3.864$   
p1  $0.003942 \pm 0.003427$

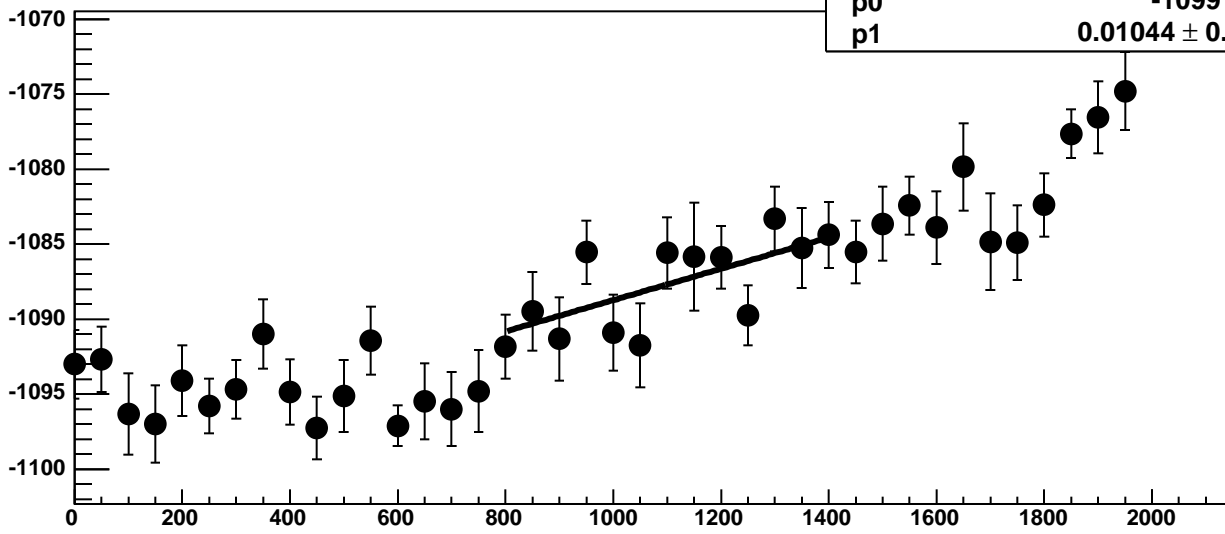
Chip 0, Channel 17, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 0, Channel 17, Enable 2!, Hold=30, ADC Residuals vs DAC

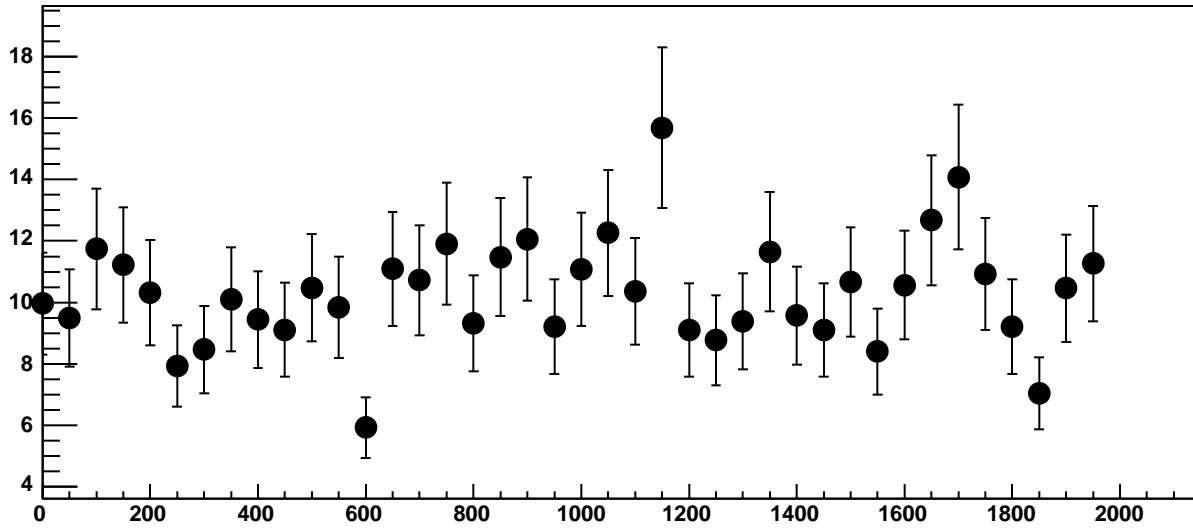


Chip 0, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC

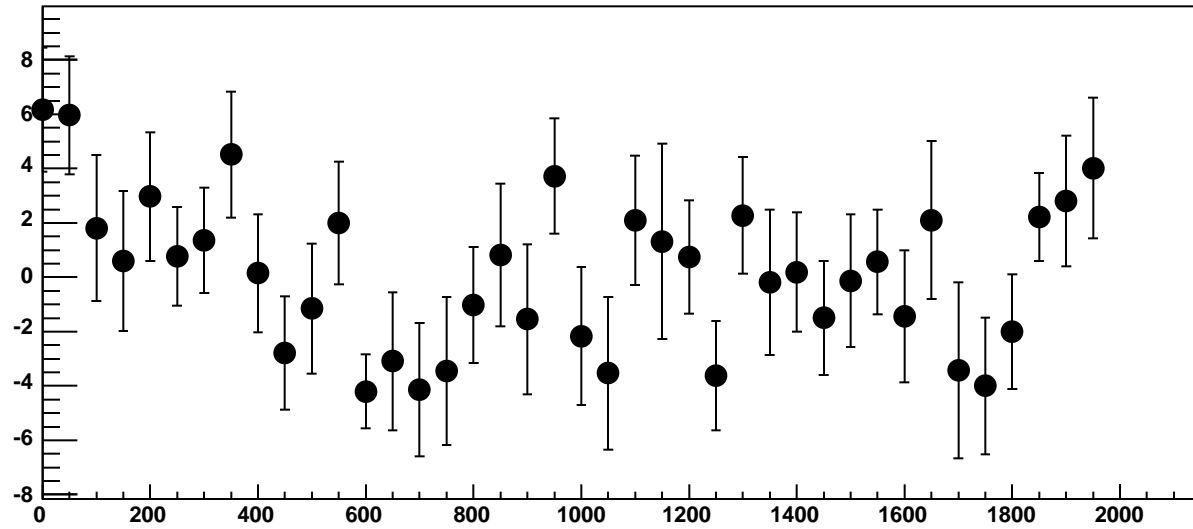


$\chi^2 / \text{ndf}$  11.45 / 11  
p0  $-1099 \pm 3.868$   
p1  $0.01044 \pm 0.003436$

Chip 0, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC

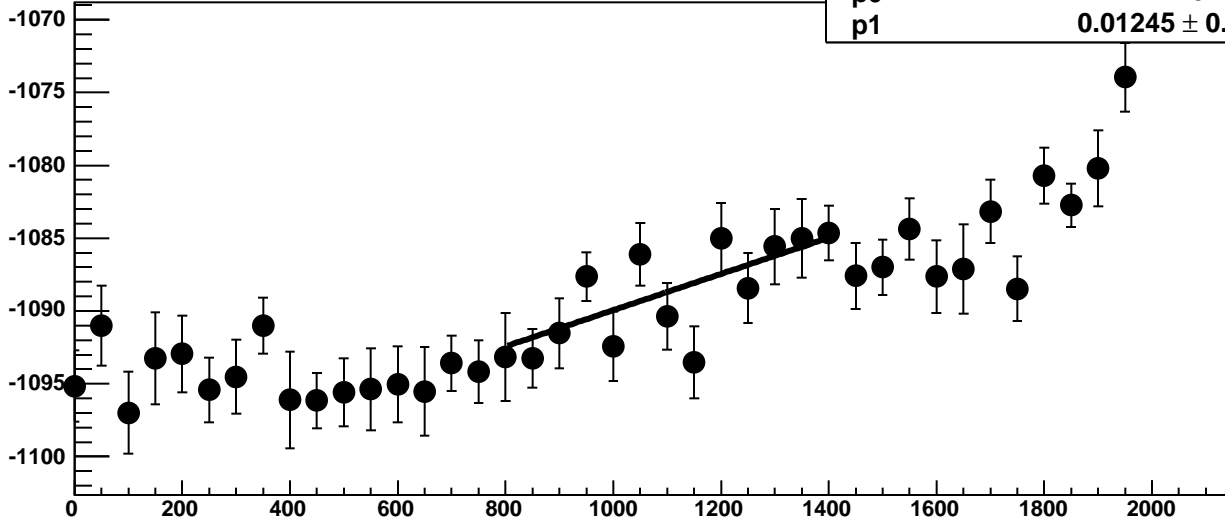


Chip 0, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC

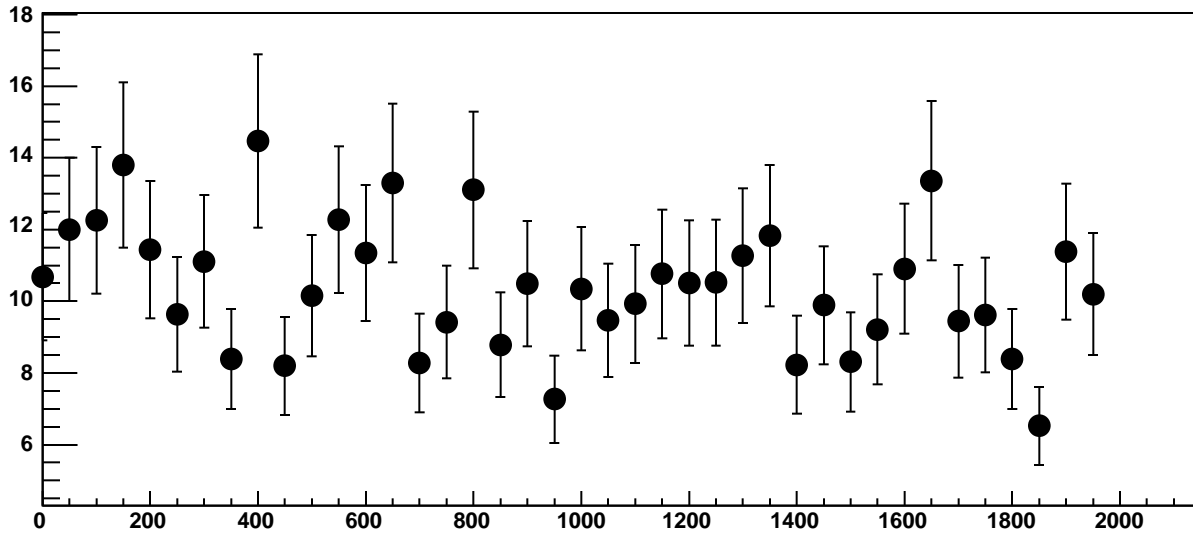




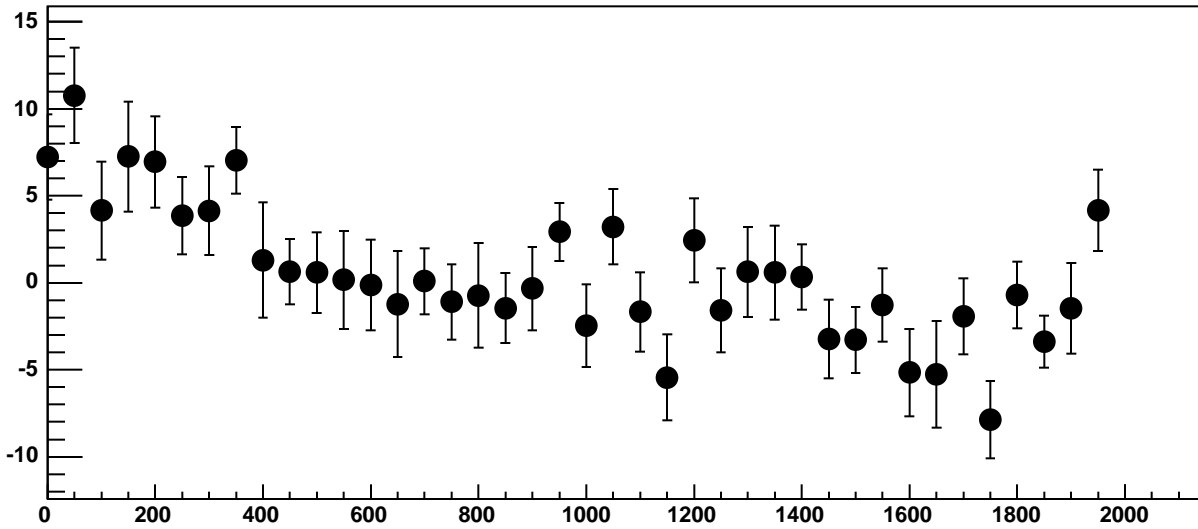
Chip 0, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC



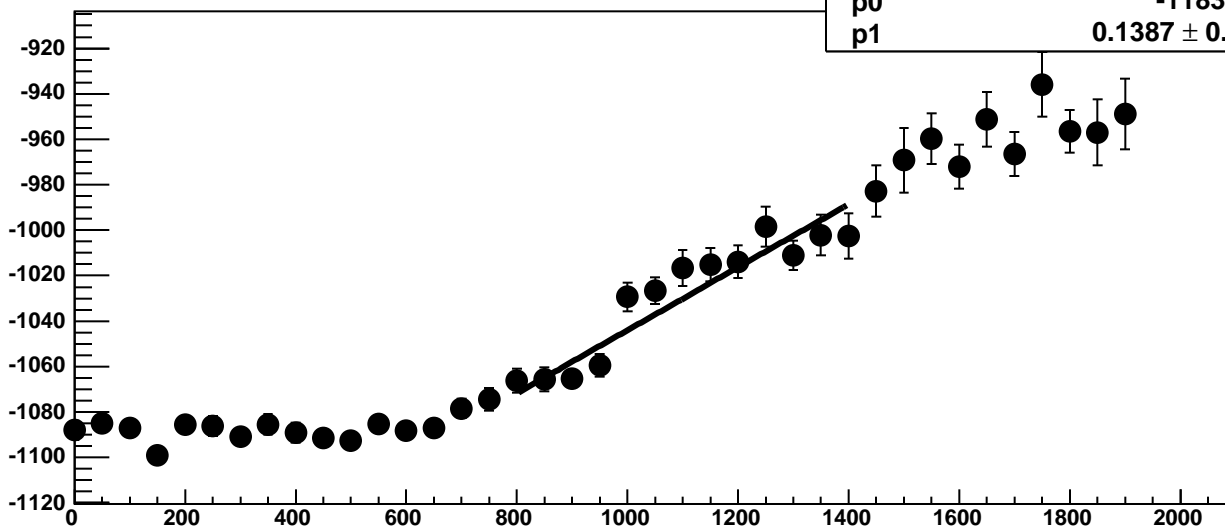
Chip 0, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC



Chip 0, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC

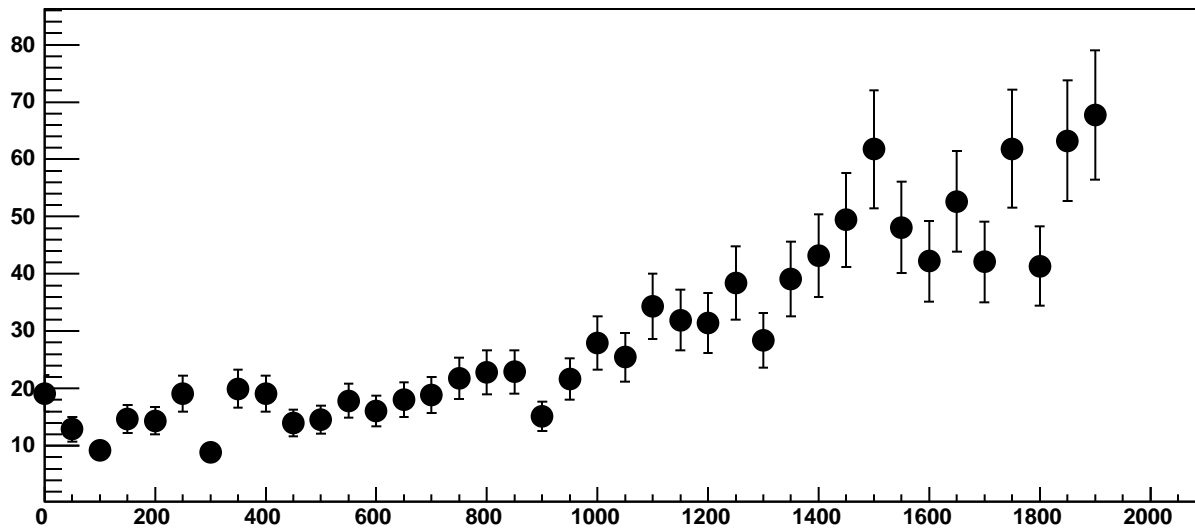


Chip 0, Channel 17, Enable 5, Hold=30, ADC Mean vs DAC

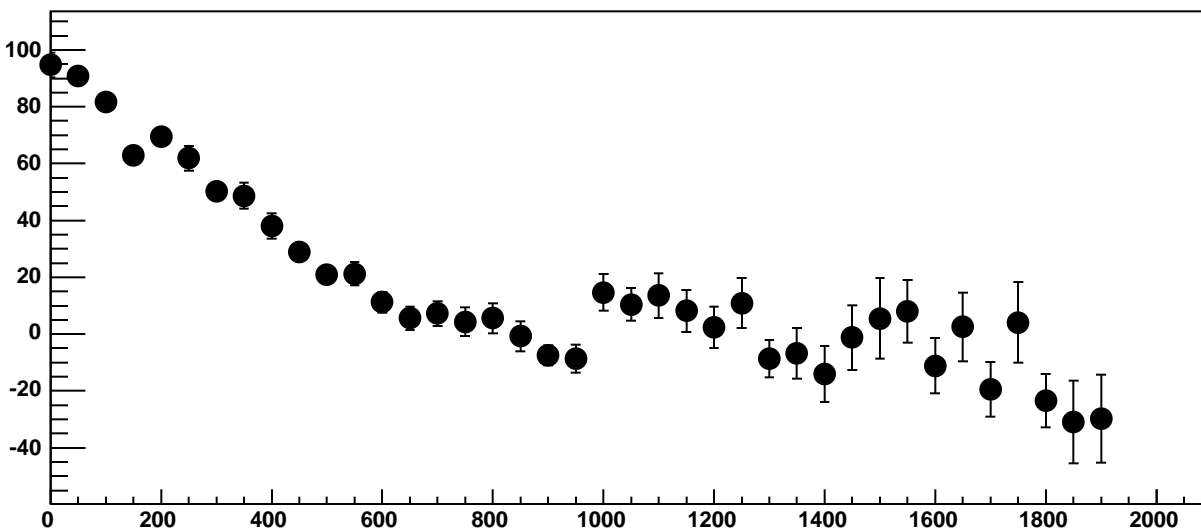


$\chi^2 / \text{ndf}$  27.45 / 11  
p0 -1183 ± 9.981  
p1 0.1387 ± 0.009699

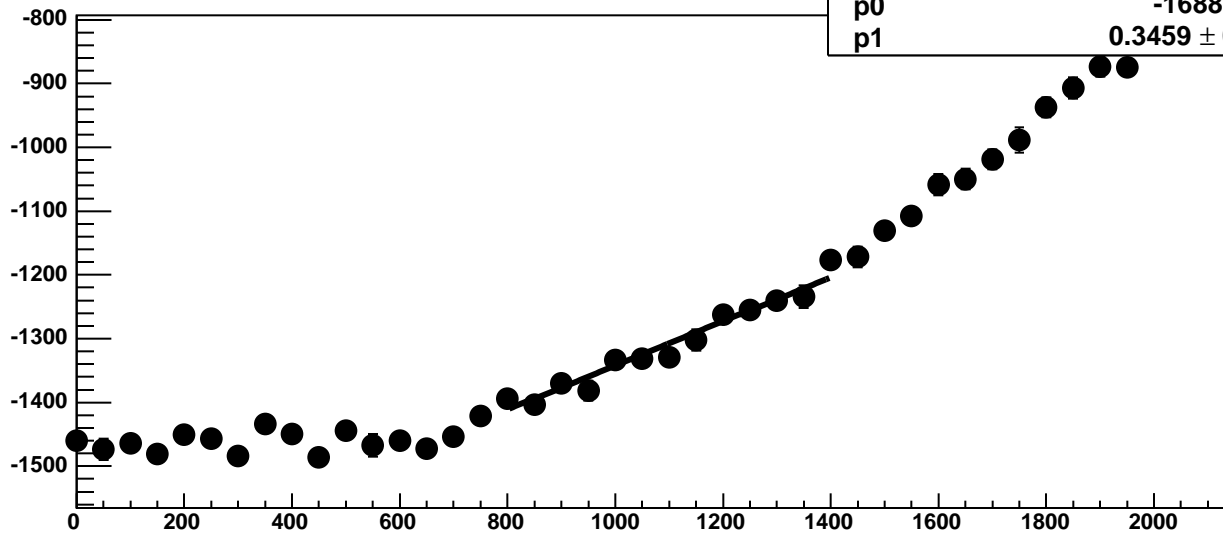
Chip 0, Channel 17, Enable 5, Hold=30, ADC Noise vs DAC



Chip 0, Channel 17, Enable 5, Hold=30, ADC Residuals vs DAC

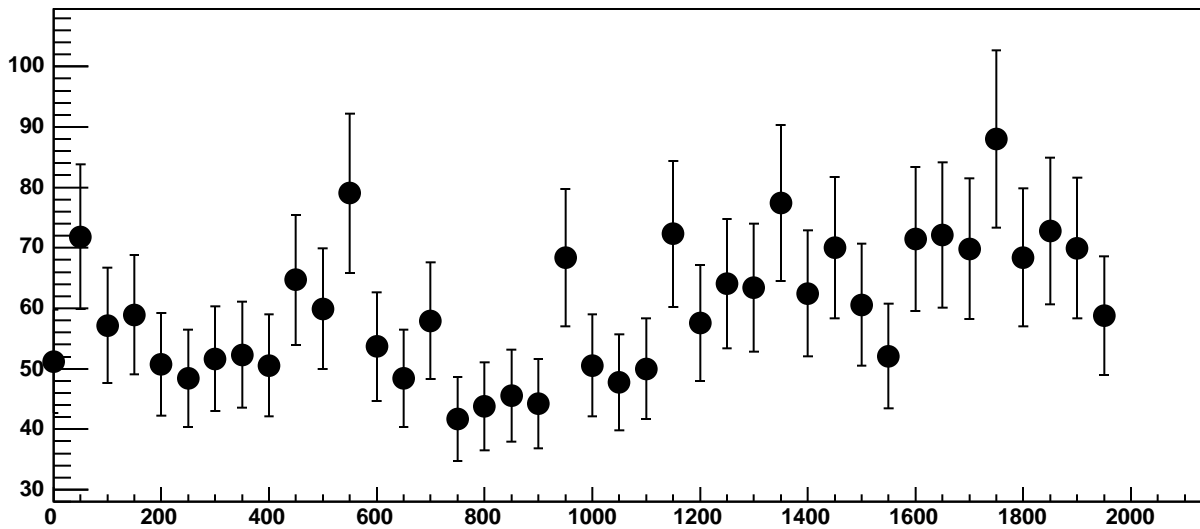


Chip 1, Channel 0, Enable 0, Hold=30, ADC Mean vs DAC

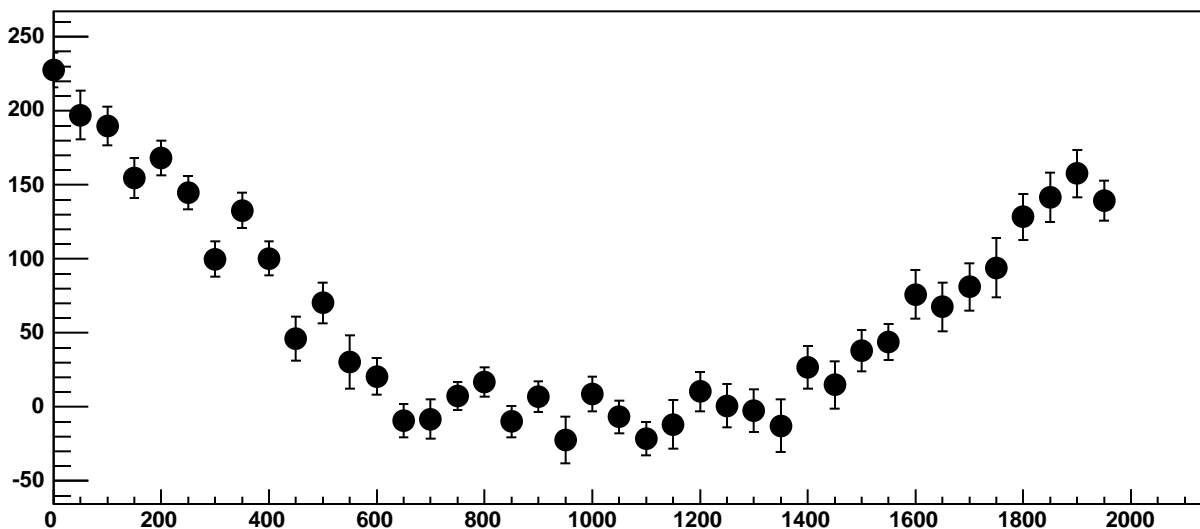


$\chi^2 / \text{ndf}$  15.79 / 11  
p0  $-1688 \pm 20.07$   
p1  $0.3459 \pm 0.01887$

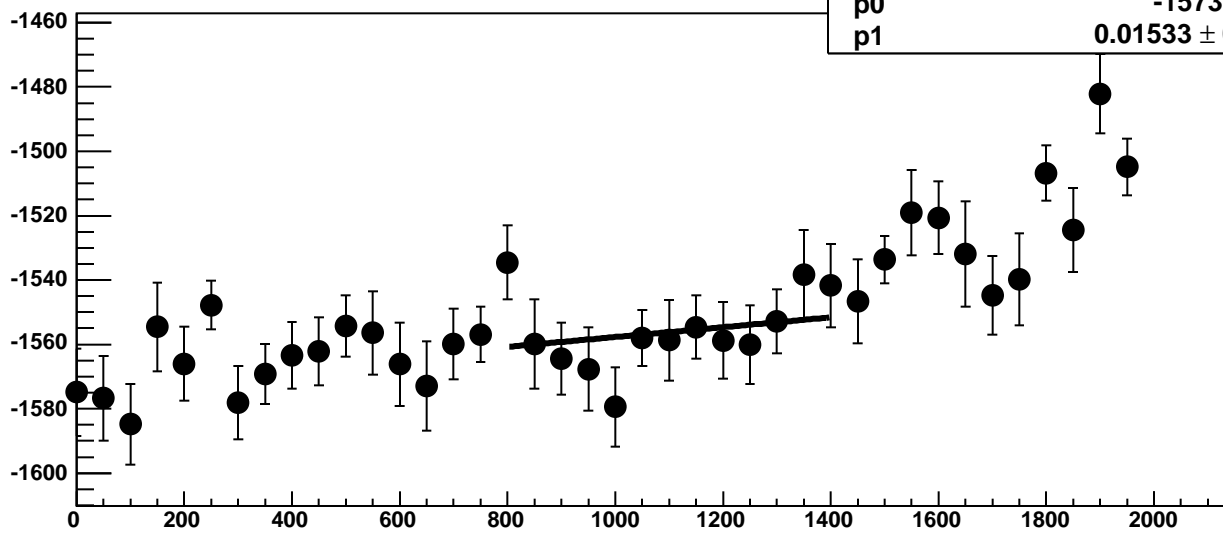
Chip 1, Channel 0, Enable 0, Hold=30, ADC Noise vs DAC



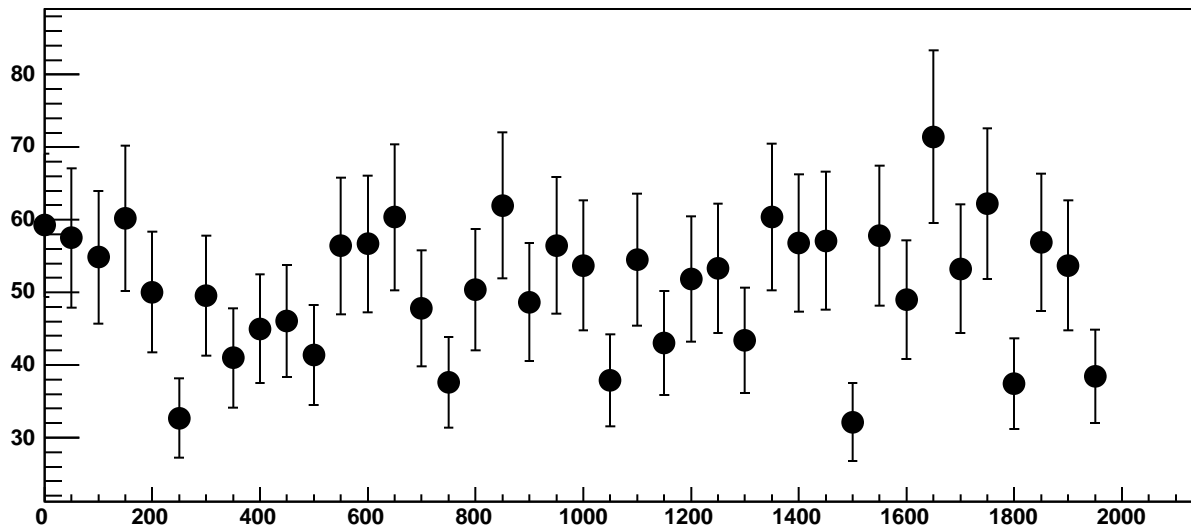
Chip 1, Channel 0, Enable 0, Hold=30, ADC Residuals vs DAC



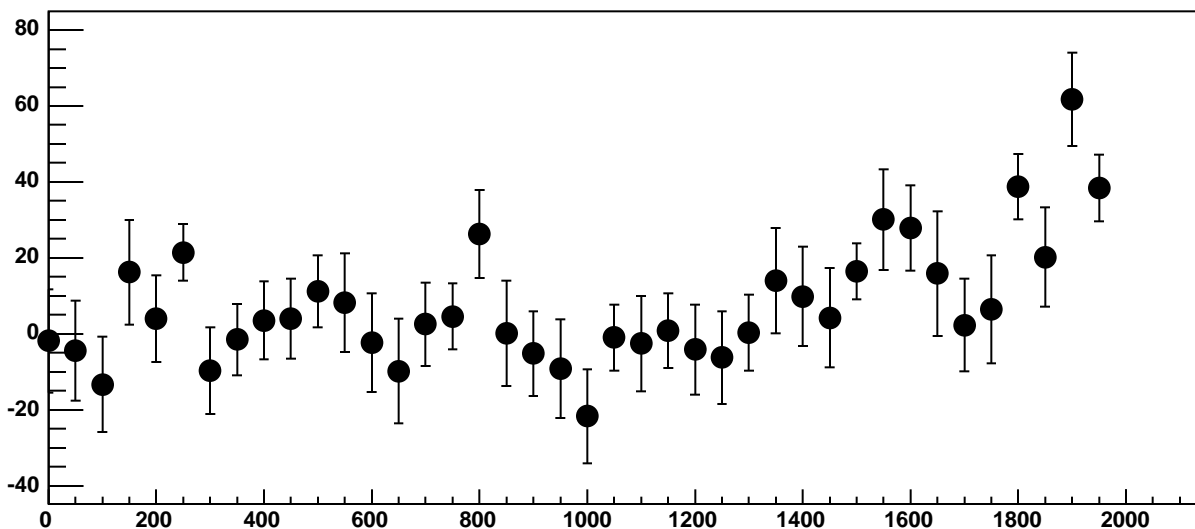
Chip 1, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC



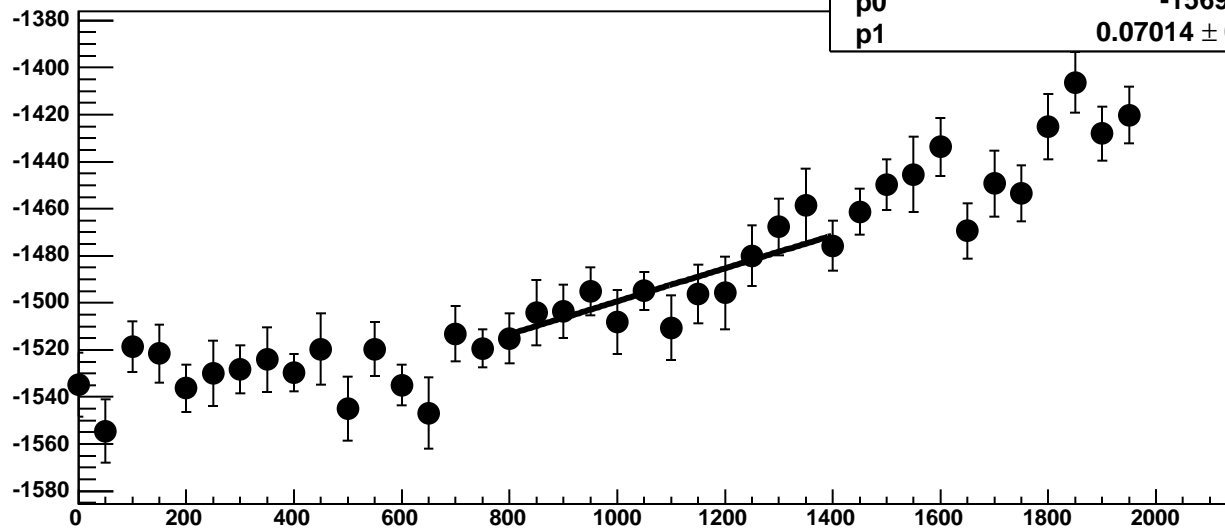
Chip 1, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC



Chip 1, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC

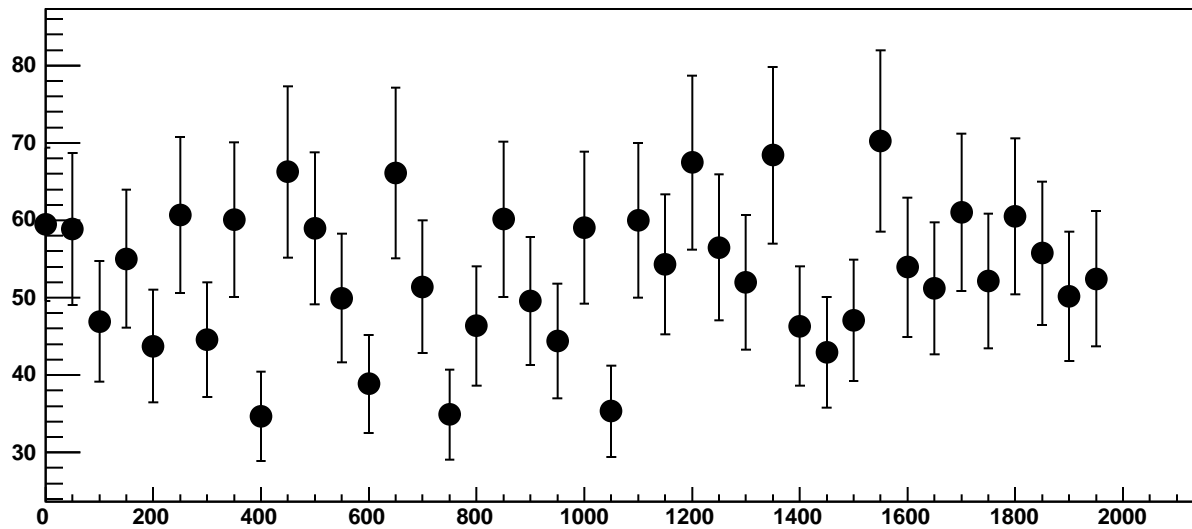


Chip 1, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC

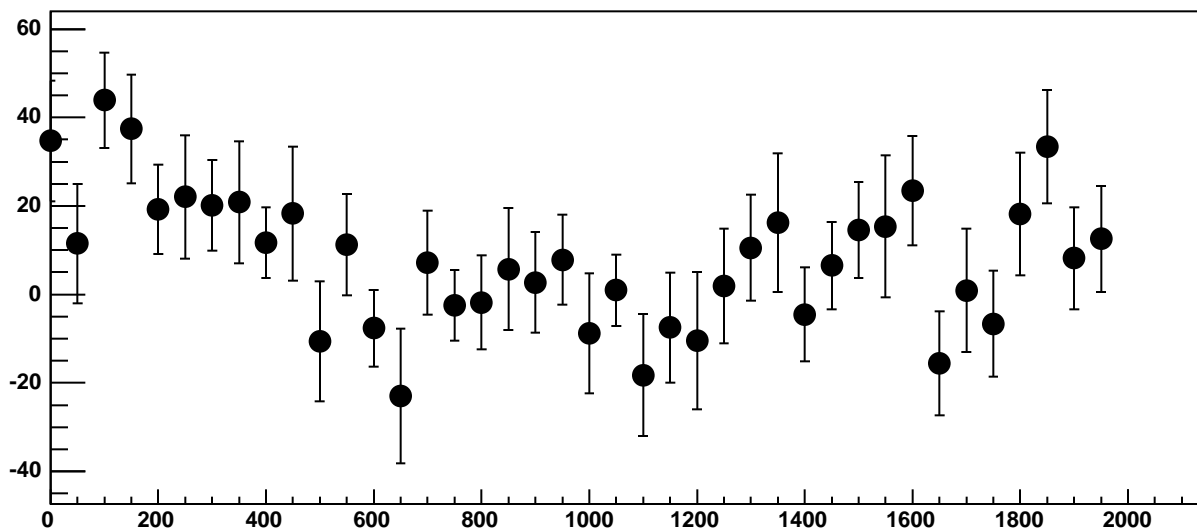


$\chi^2 / \text{ndf}$  5.909 / 11  
p0 -1569 ± 19.31  
p1 0.07014 ± 0.01757

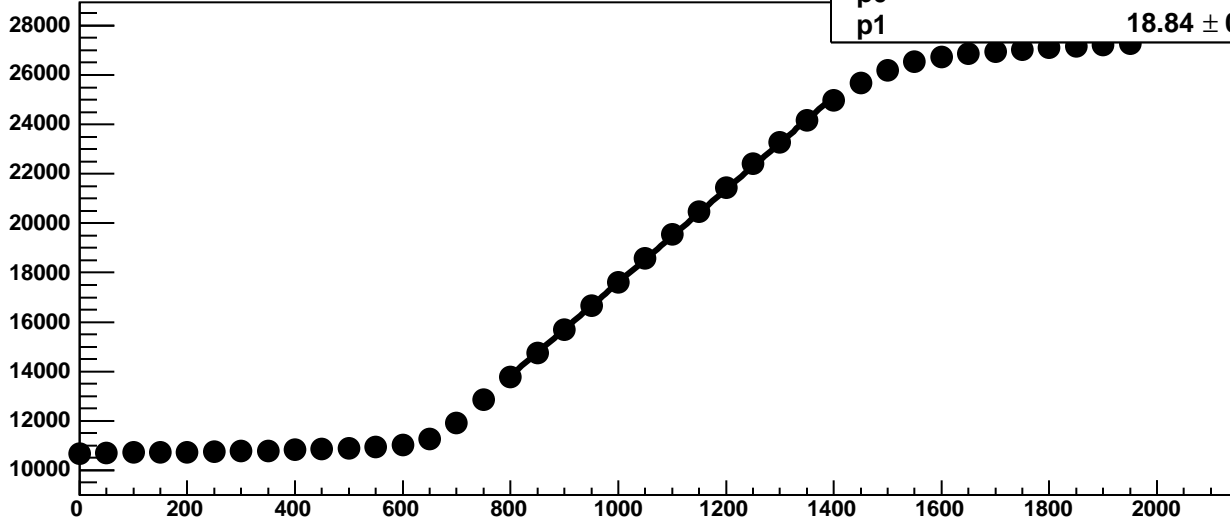
Chip 1, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC



Chip 1, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC

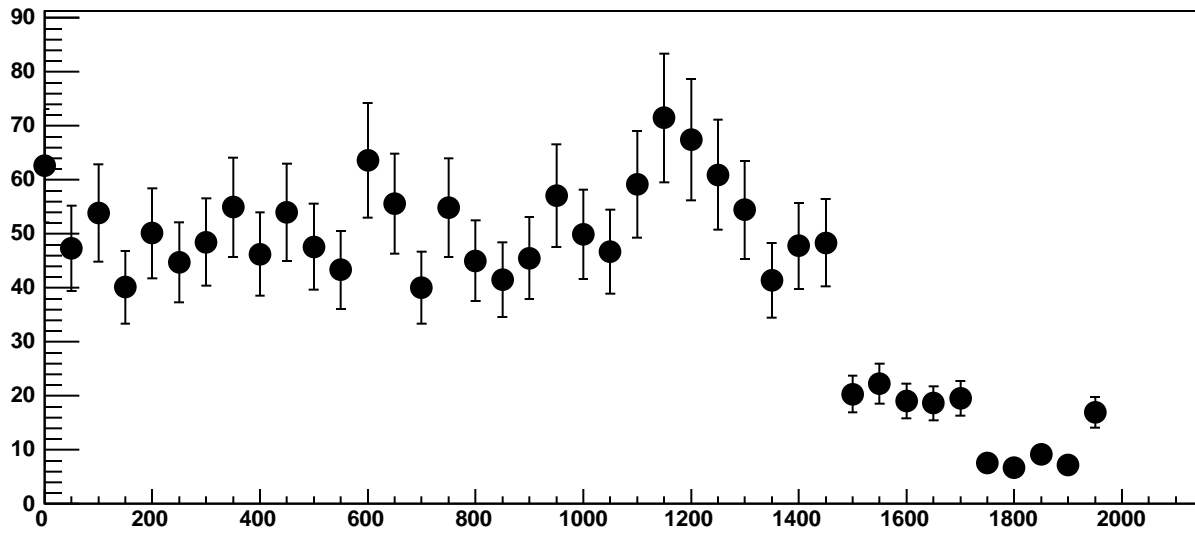


Chip 1, Channel 0, Enable 3!, Hold=30, ADC Mean vs DAC

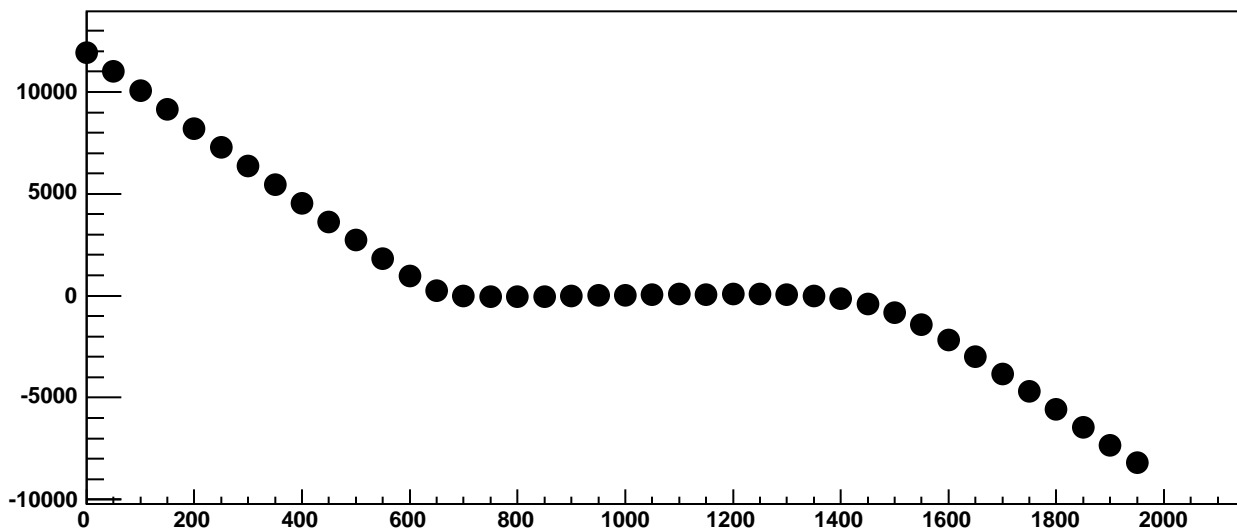


$\chi^2 / \text{ndf}$  353.5 / 11  
p0  $-1244 \pm 17.51$   
p1  $18.84 \pm 0.01588$

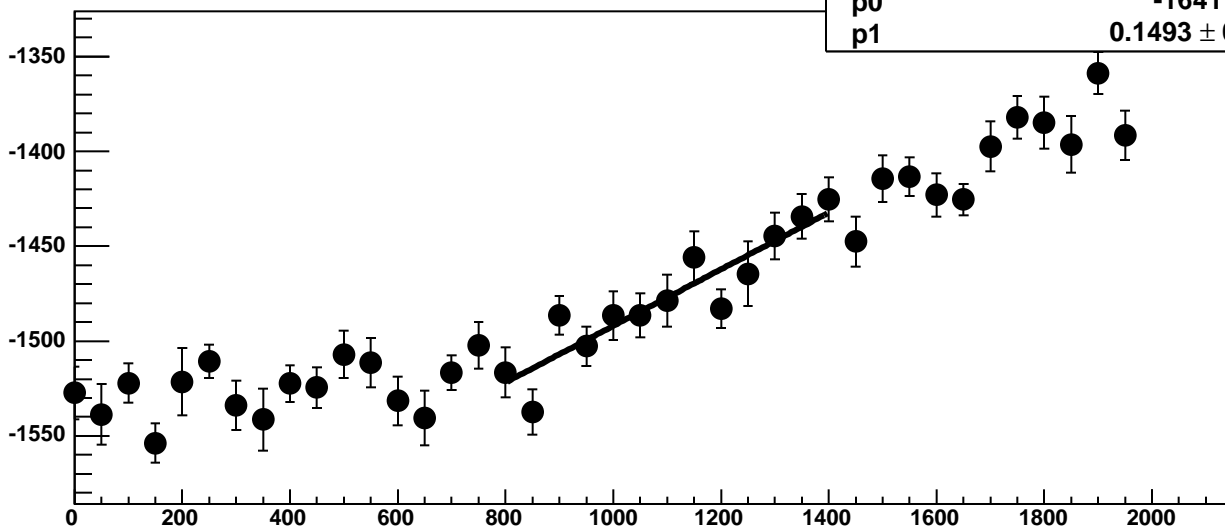
Chip 1, Channel 0, Enable 3!, Hold=30, ADC Noise vs DAC



Chip 1, Channel 0, Enable 3!, Hold=30, ADC Residuals vs DAC

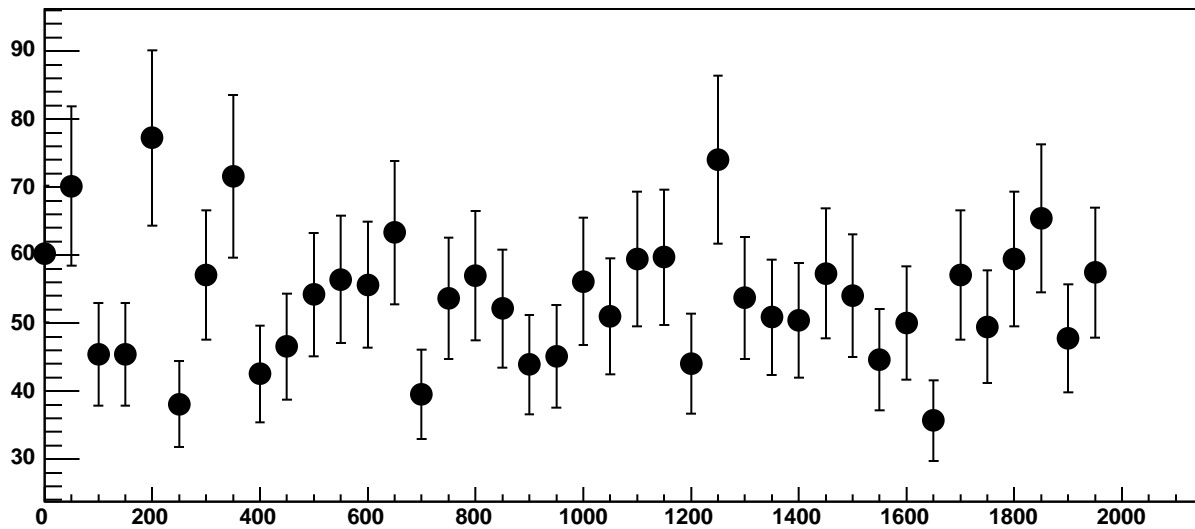


Chip 1, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC

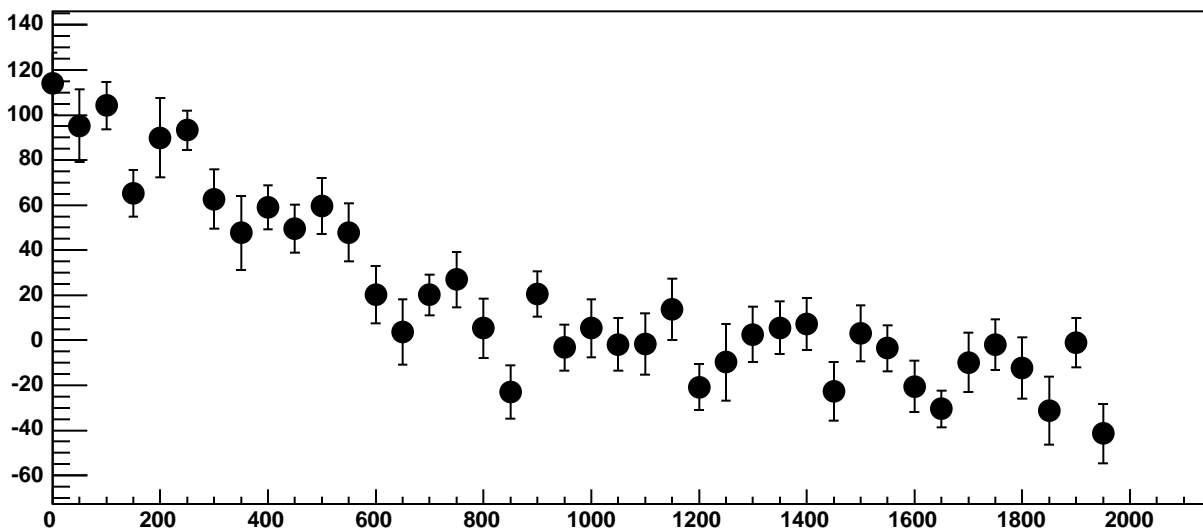


$\chi^2 / \text{ndf}$  14.54 / 11  
p0  $-1641 \pm 19.52$   
p1  $0.1493 \pm 0.01761$

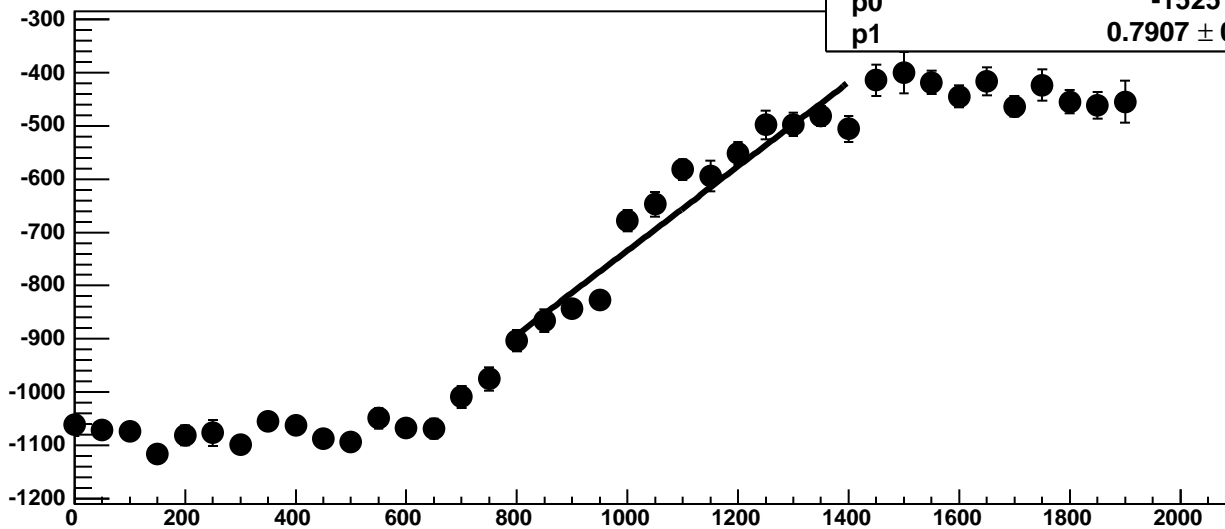
Chip 1, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC



Chip 1, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC

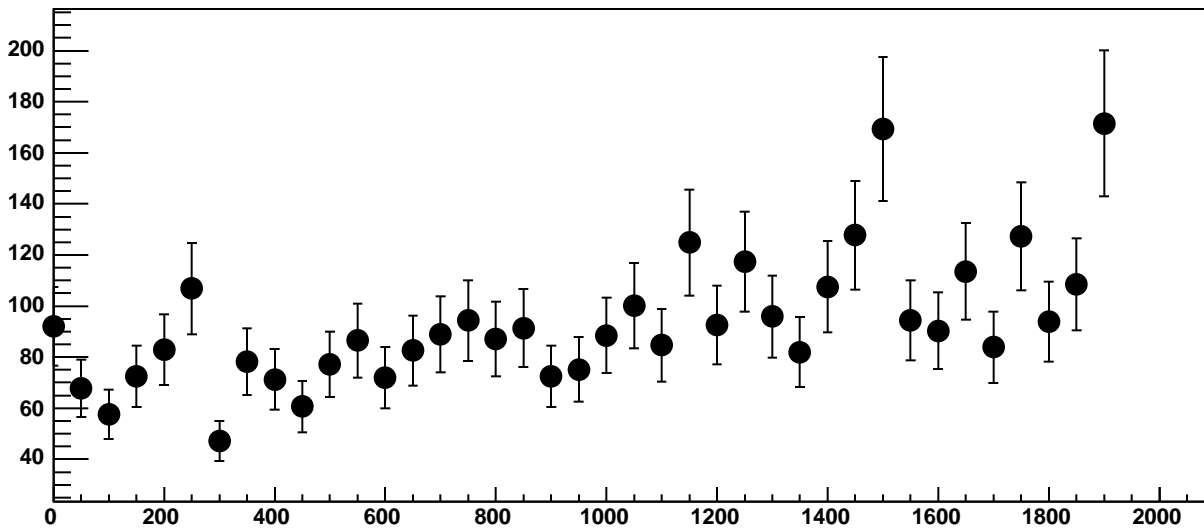


Chip 1, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC

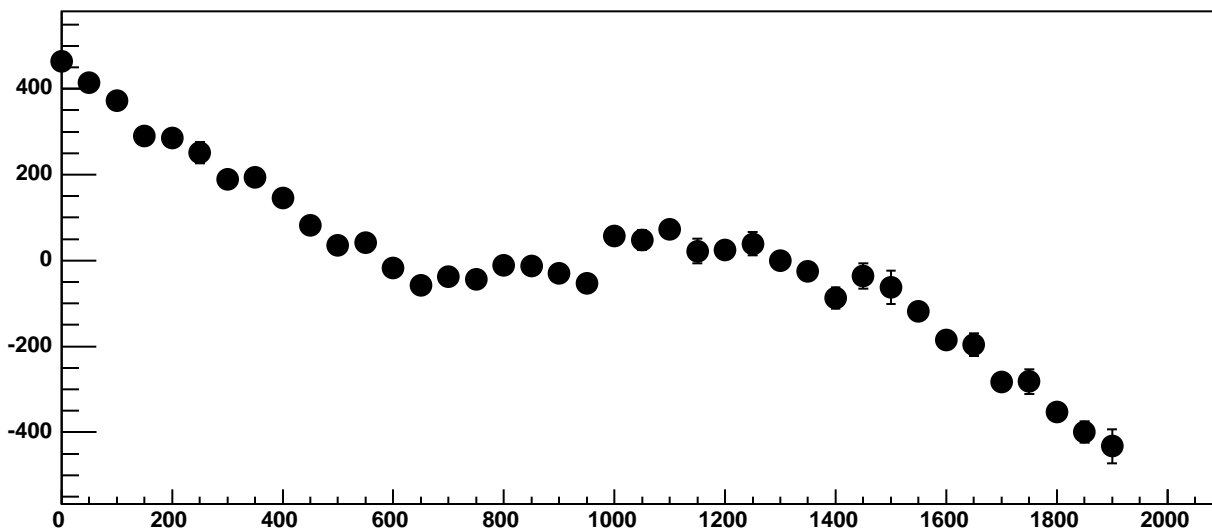


$\chi^2 / \text{ndf}$  58.04 / 11  
p0  $-1525 \pm 33.49$   
p1  $0.7907 \pm 0.03072$

Chip 1, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC

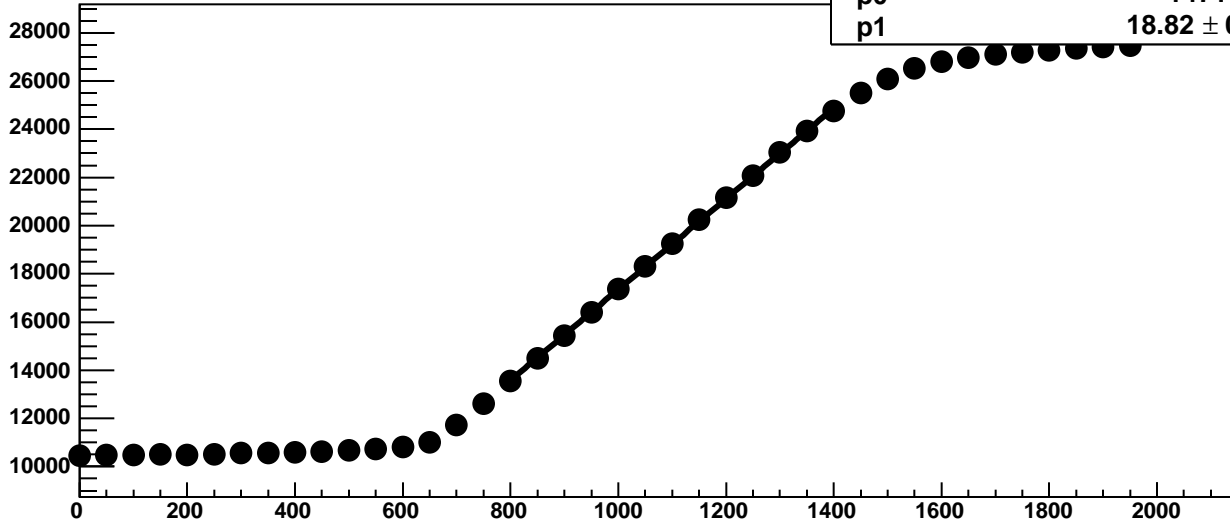


Chip 1, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC

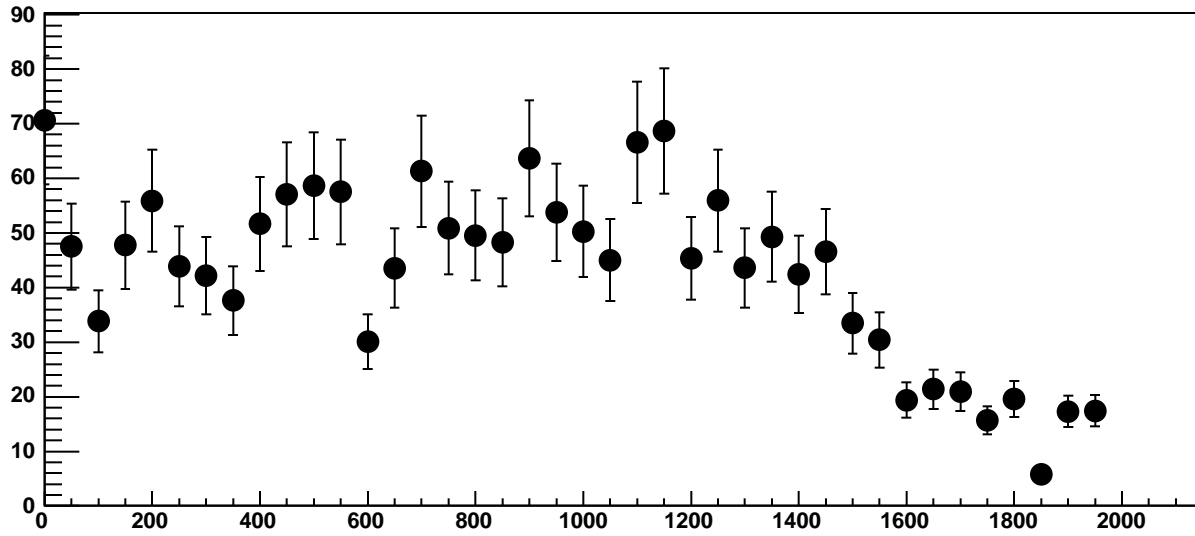




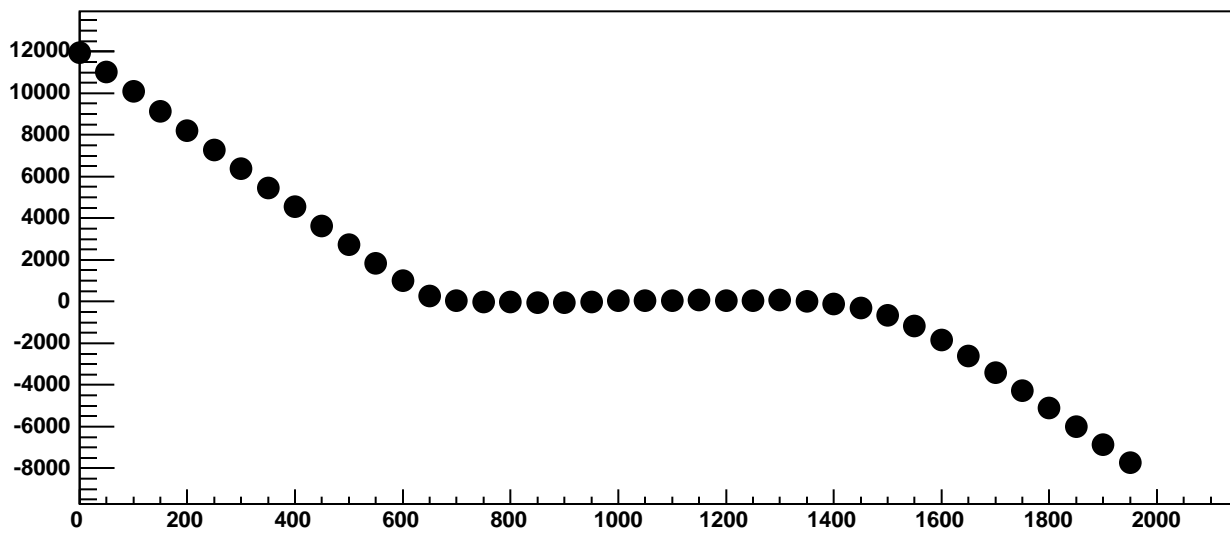
Chip 1, Channel 1, Enable 0!, Hold=30, ADC Mean vs DAC



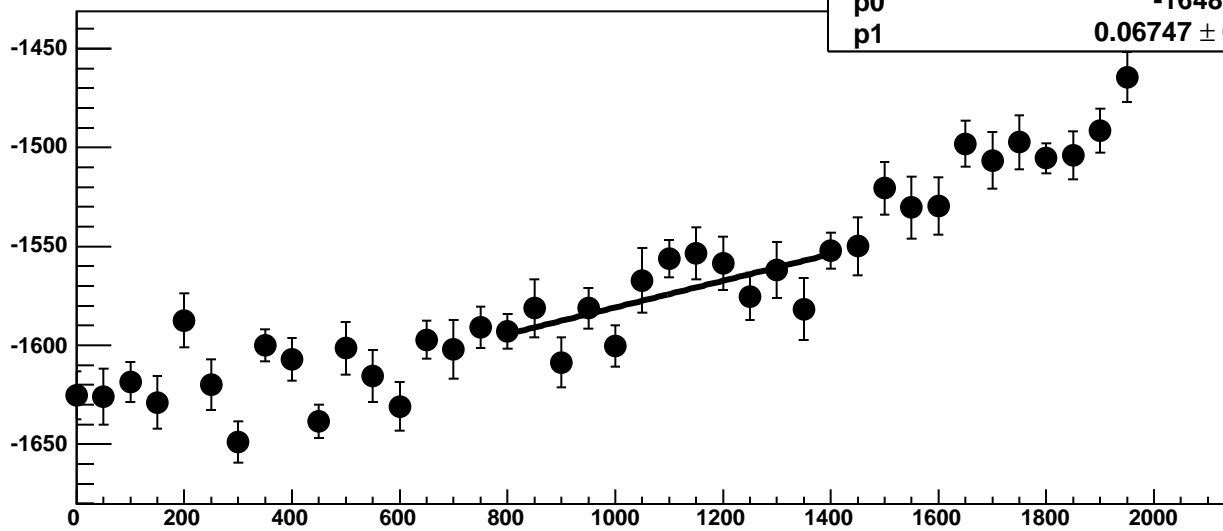
Chip 1, Channel 1, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 1, Channel 1, Enable 0!, Hold=30, ADC Residuals vs DAC

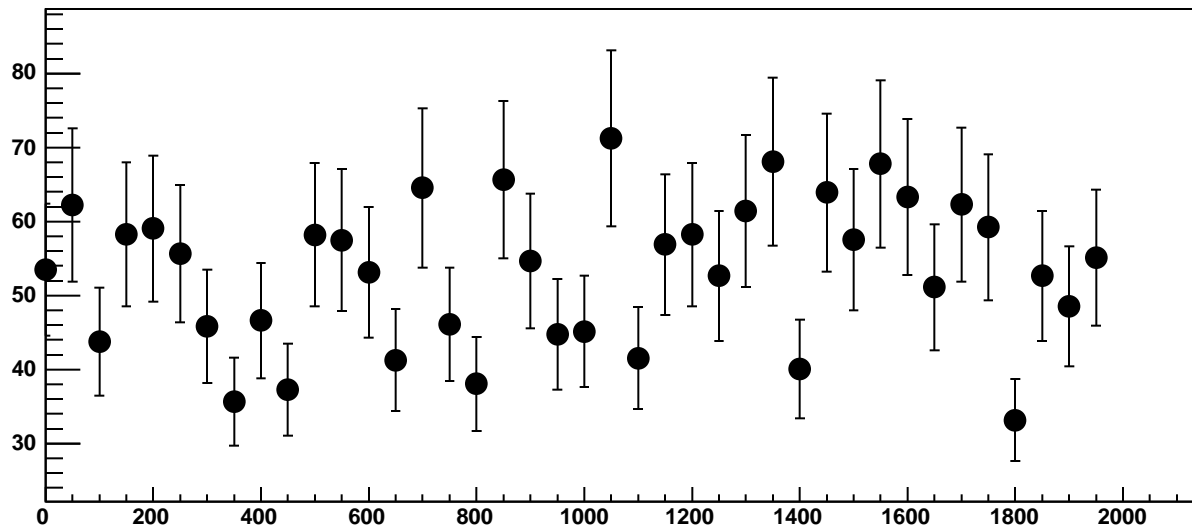


Chip 1, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC

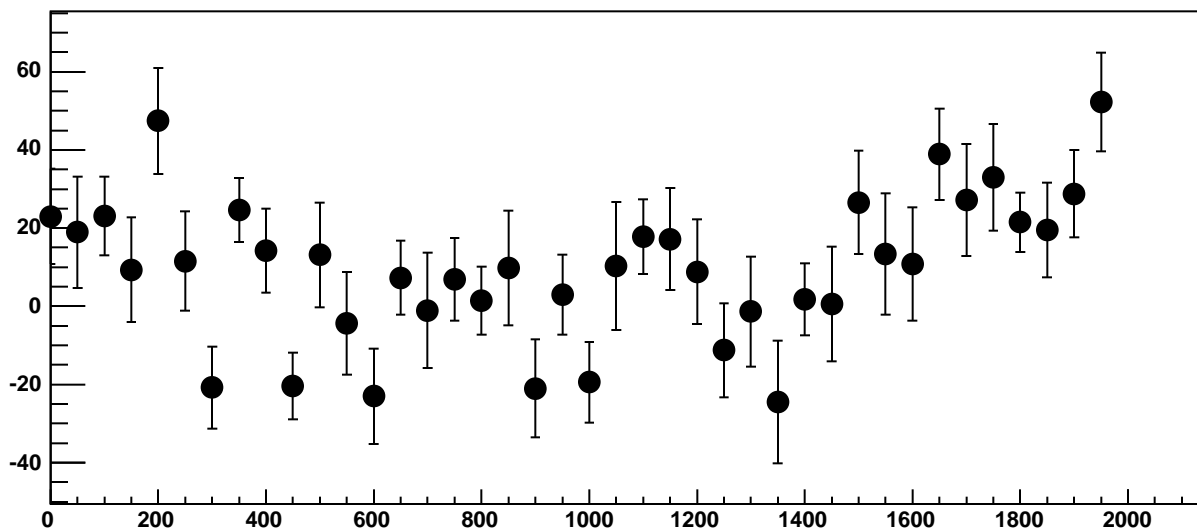


$\chi^2 / \text{ndf}$  16.34 / 11  
p0  $-1648 \pm 17.97$   
p1  $0.06747 \pm 0.01629$

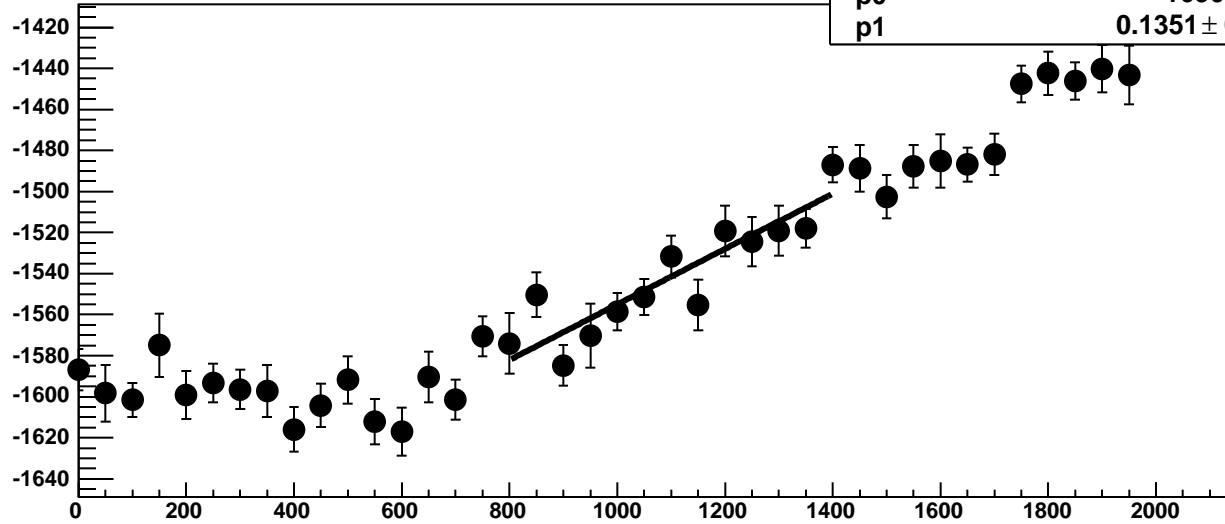
Chip 1, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC



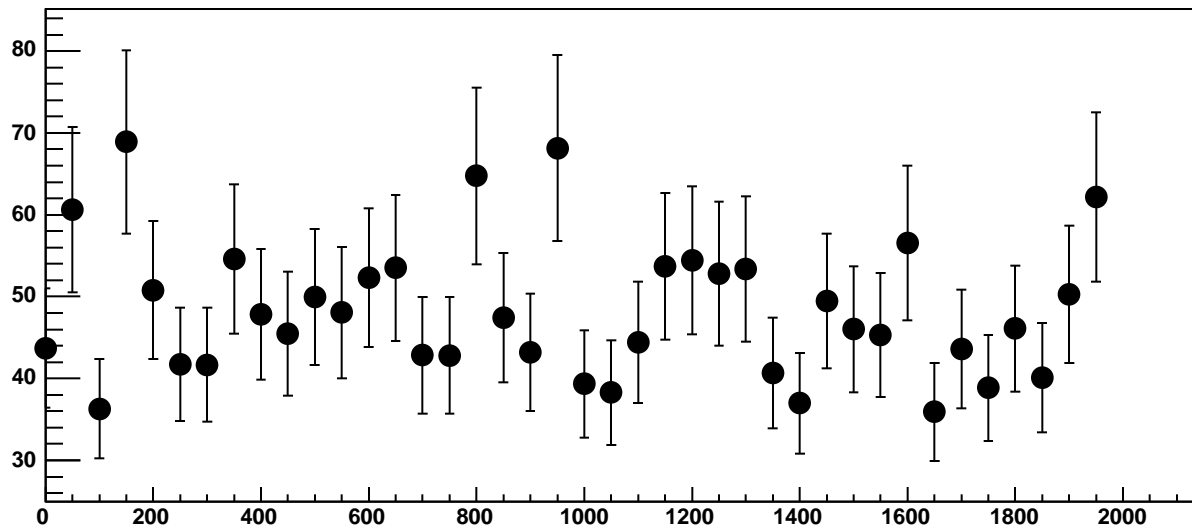
Chip 1, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC



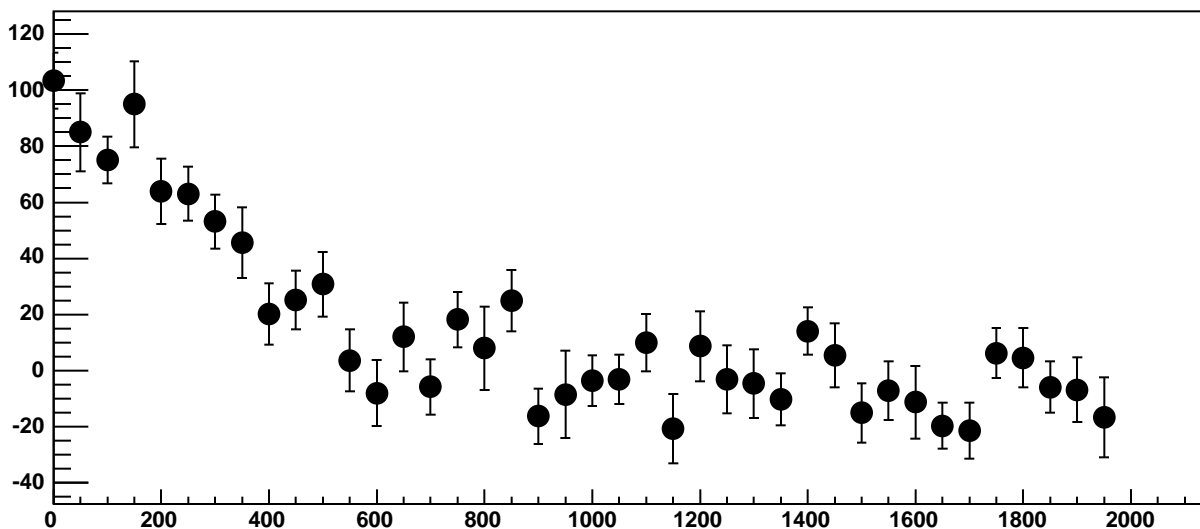
Chip 1, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC



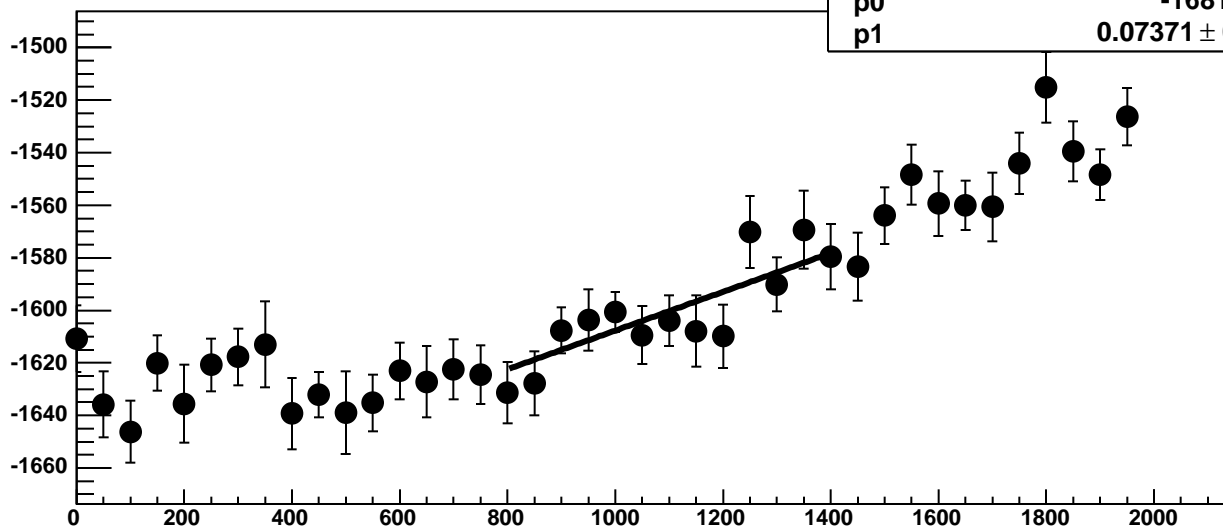
Chip 1, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC



Chip 1, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 1, Channel 1, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

9.108 / 11

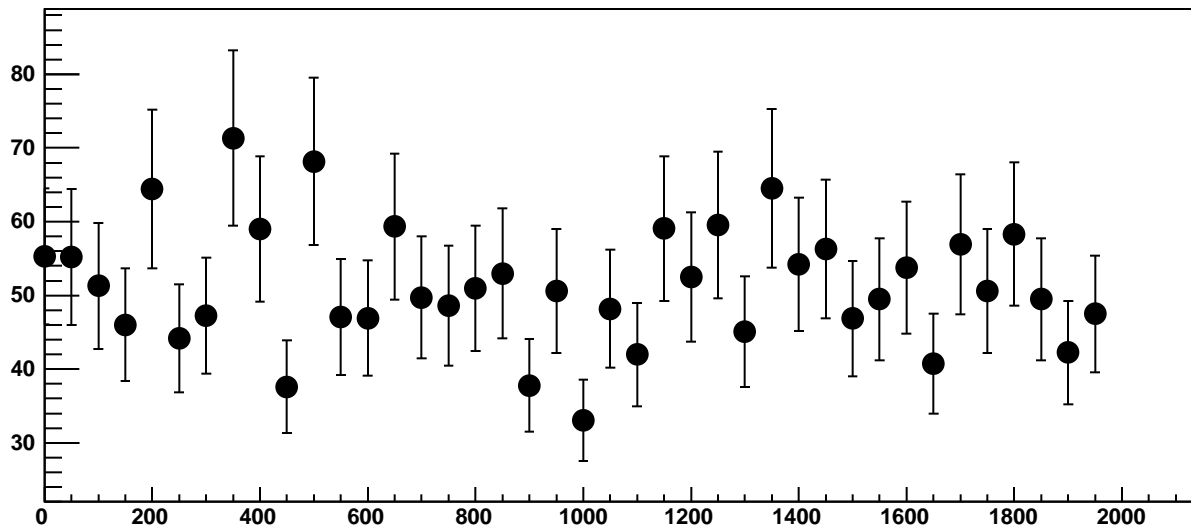
p0

$-1681 \pm 18.81$

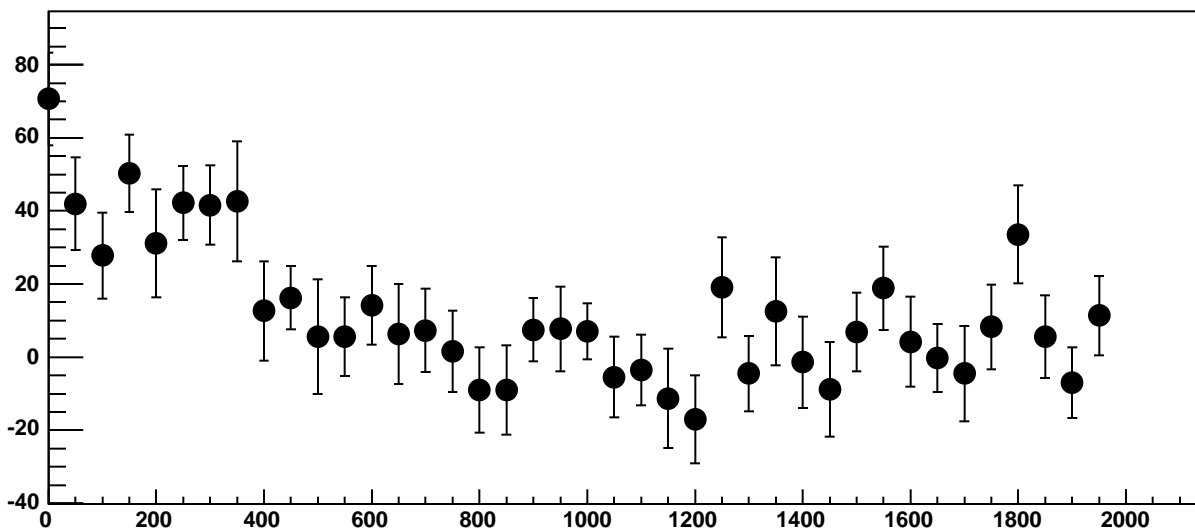
p1

$0.07371 \pm 0.01732$

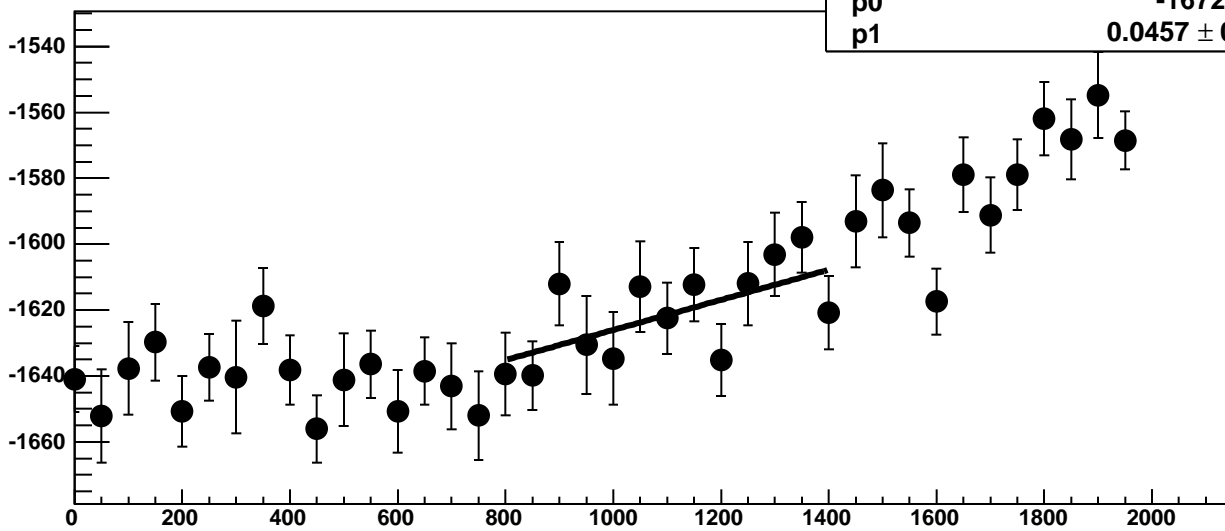
Chip 1, Channel 1, Enable 3, Hold=30, ADC Noise vs DAC



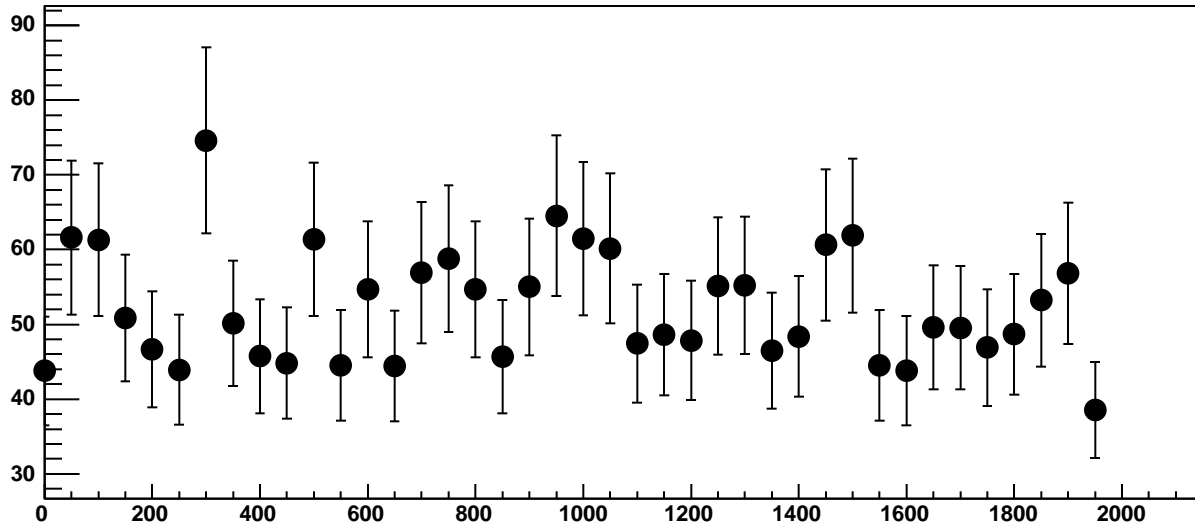
Chip 1, Channel 1, Enable 3, Hold=30, ADC Residuals vs DAC



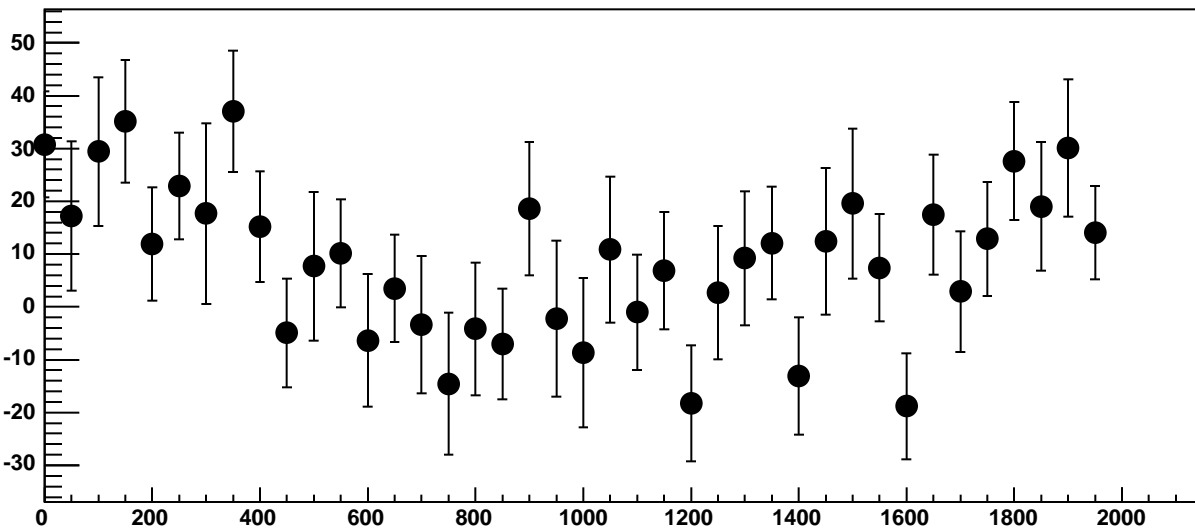
Chip 1, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC



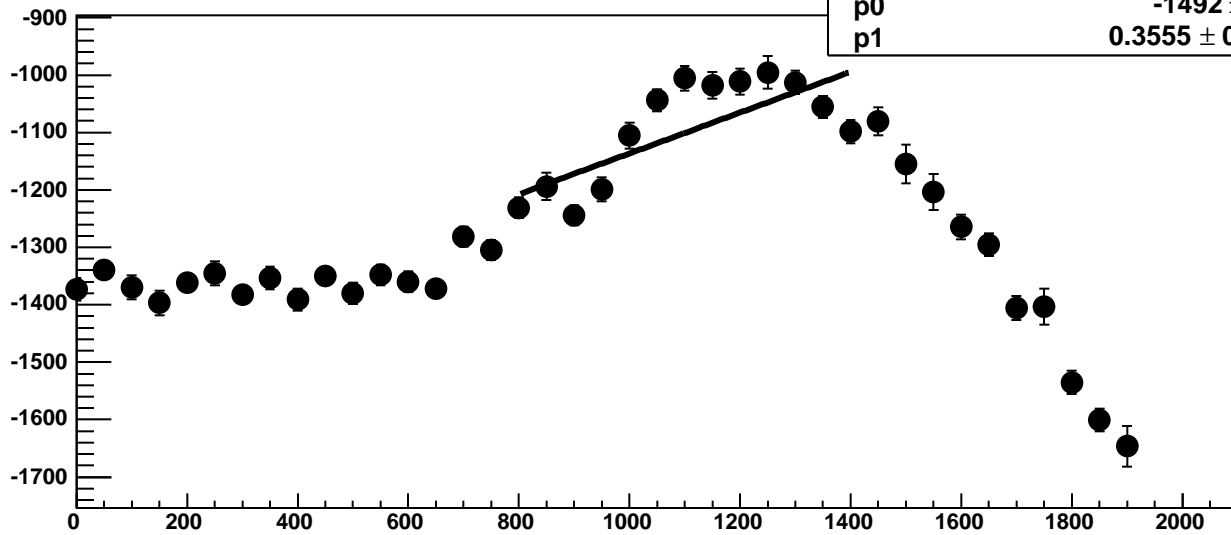
Chip 1, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC



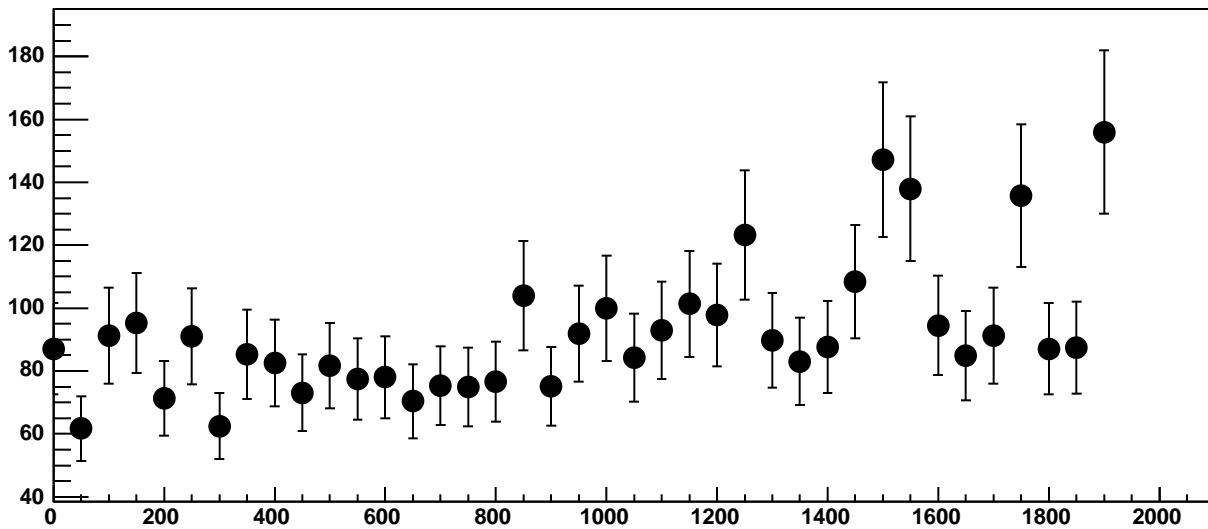
Chip 1, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC



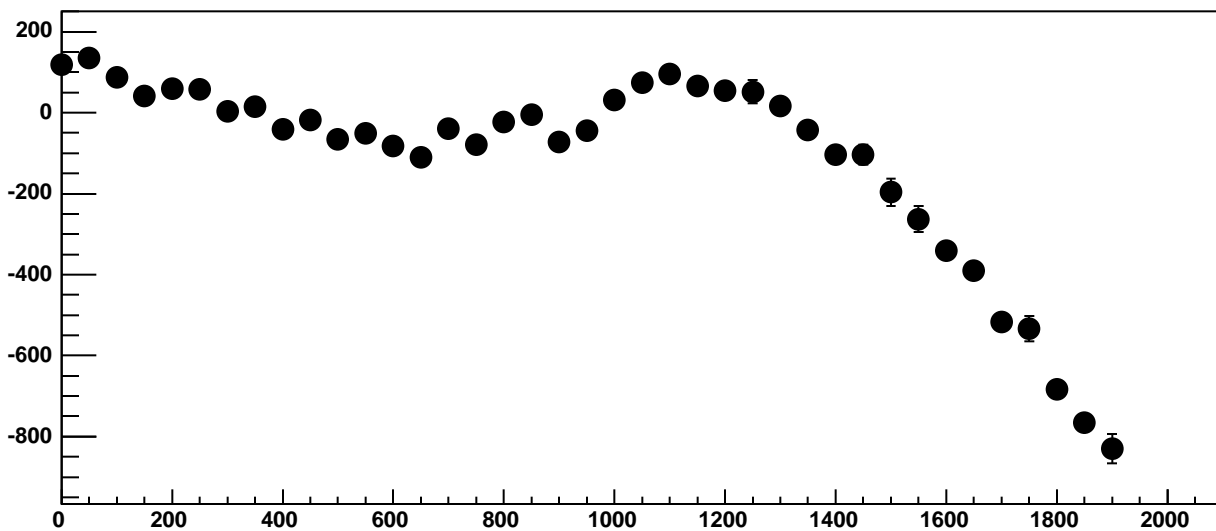
Chip 1, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC



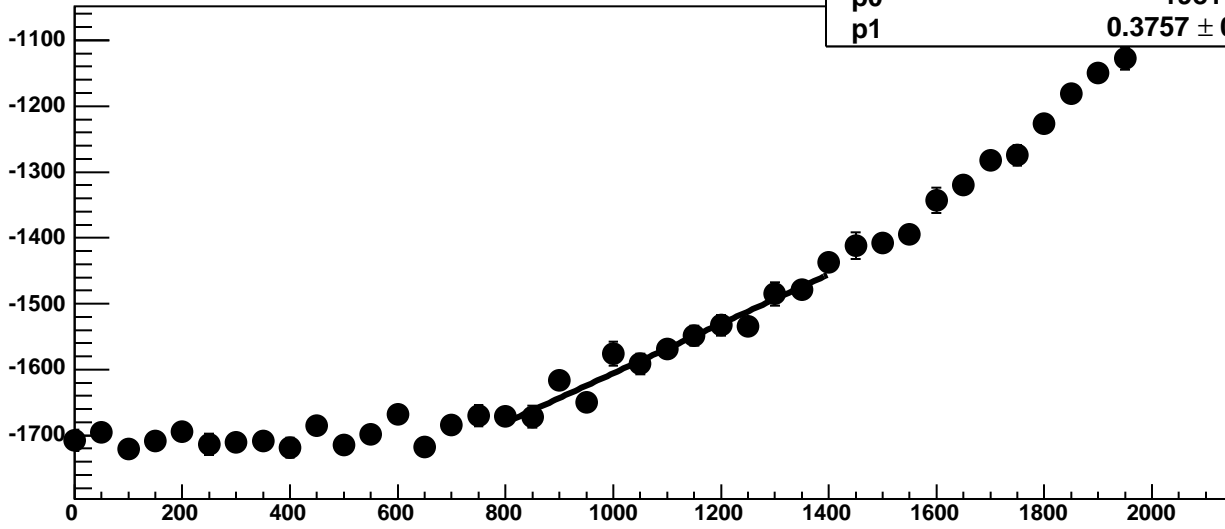
Chip 1, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC



Chip 1, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC

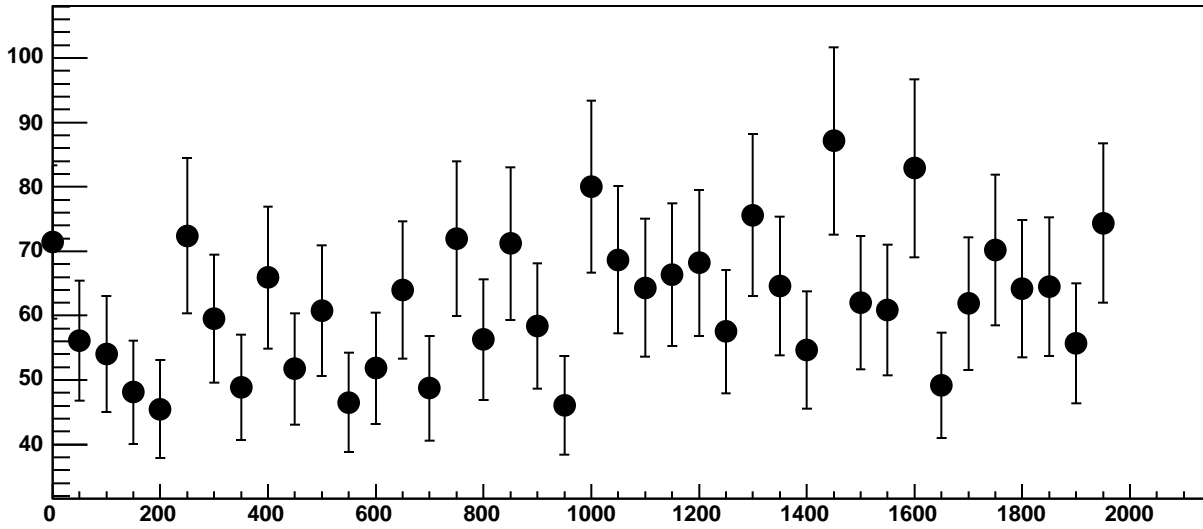


Chip 1, Channel 2, Enable 0, Hold=30, ADC Mean vs DAC

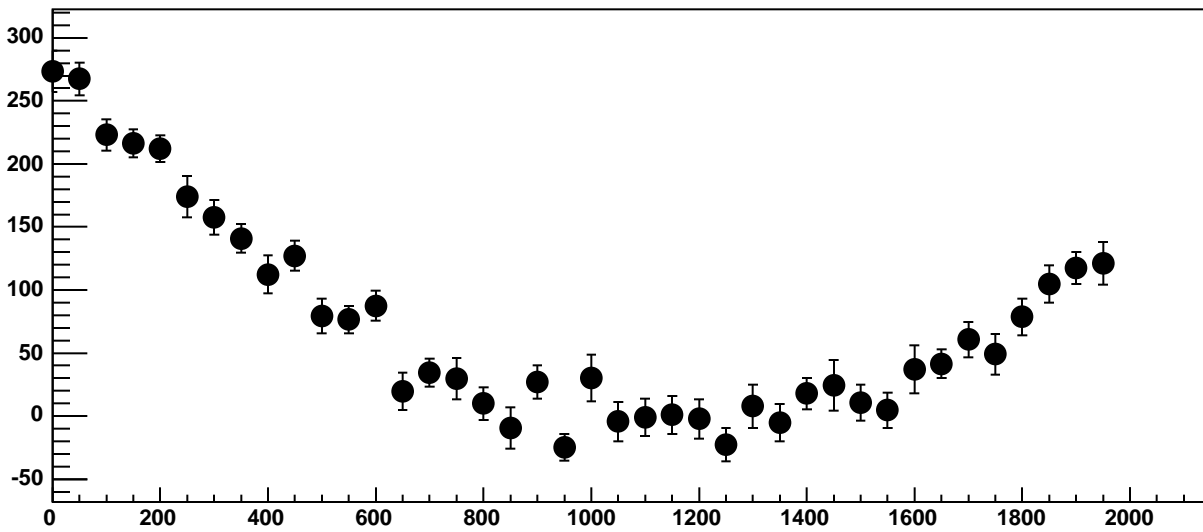


$\chi^2 / \text{ndf}$  18.64 / 11  
p0  $-1981 \pm 22.62$   
p1  $0.3757 \pm 0.02039$

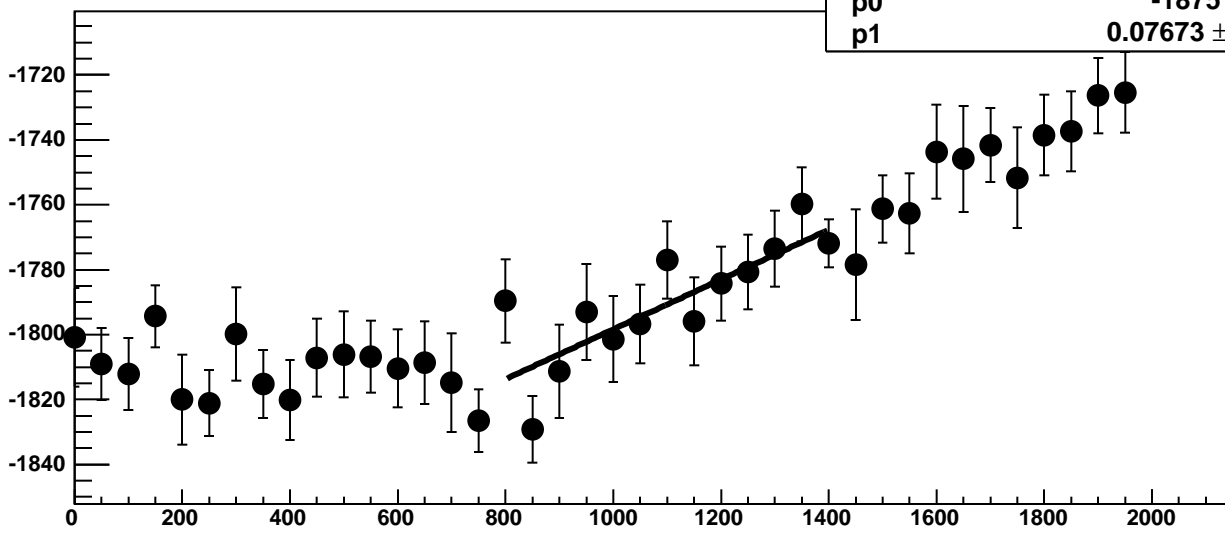
Chip 1, Channel 2, Enable 0, Hold=30, ADC Noise vs DAC



Chip 1, Channel 2, Enable 0, Hold=30, ADC Residuals vs DAC

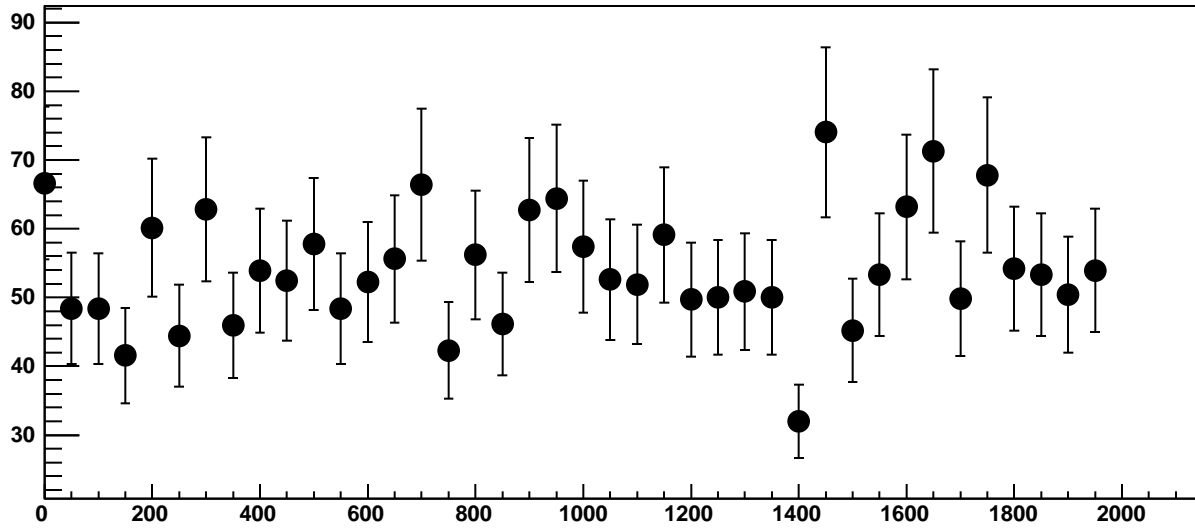


Chip 1, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC

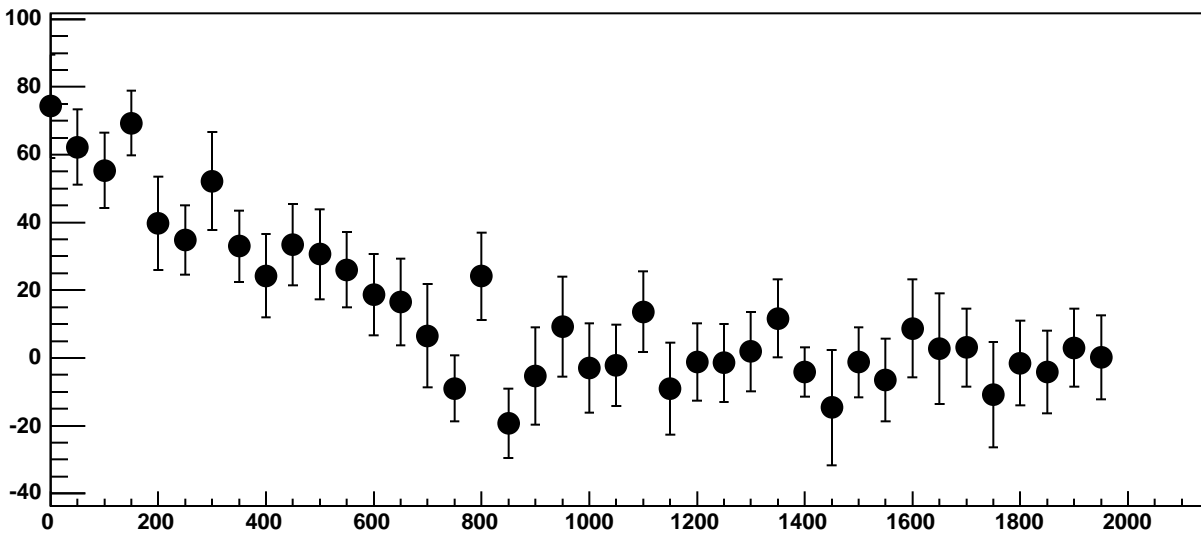


$\chi^2 / \text{ndf}$  10.76 / 11  
p0  $-1875 \pm 18.36$   
p1  $0.07673 \pm 0.0158$

Chip 1, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC

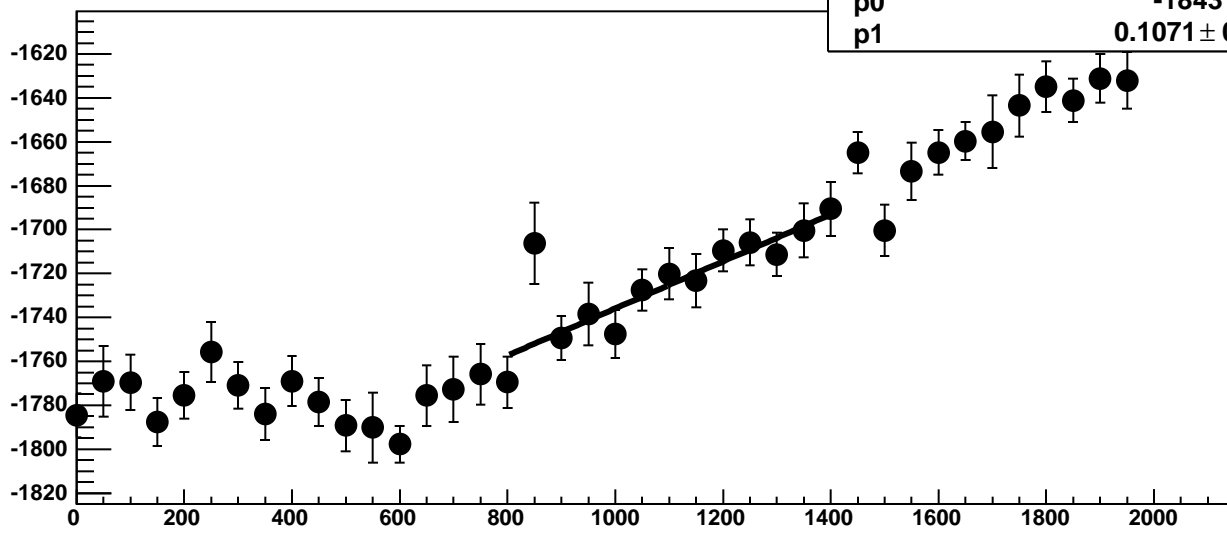


Chip 1, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC

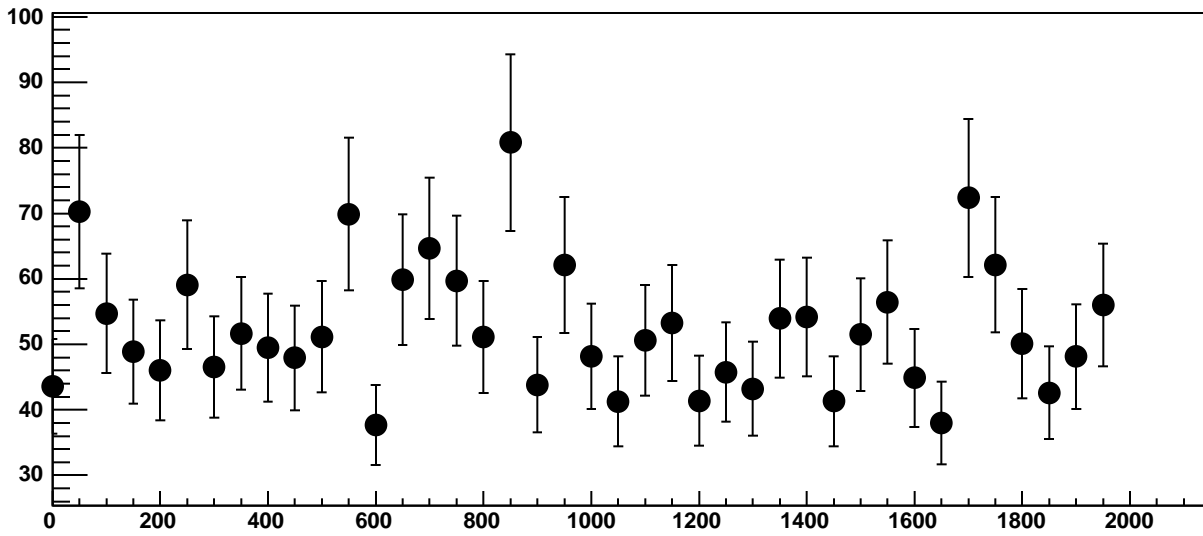




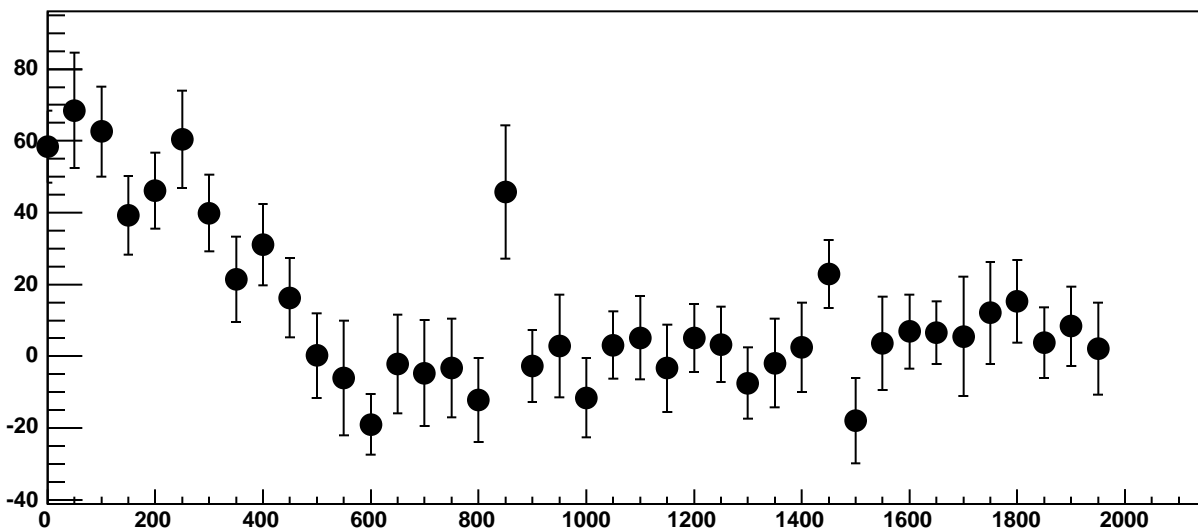
Chip 1, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC



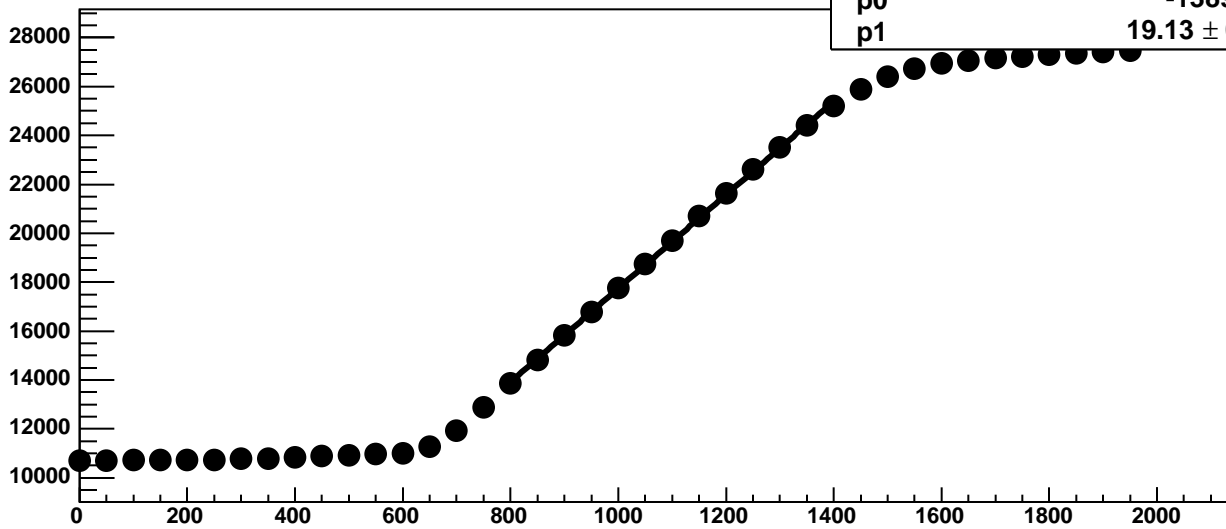
Chip 1, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC



Chip 1, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC

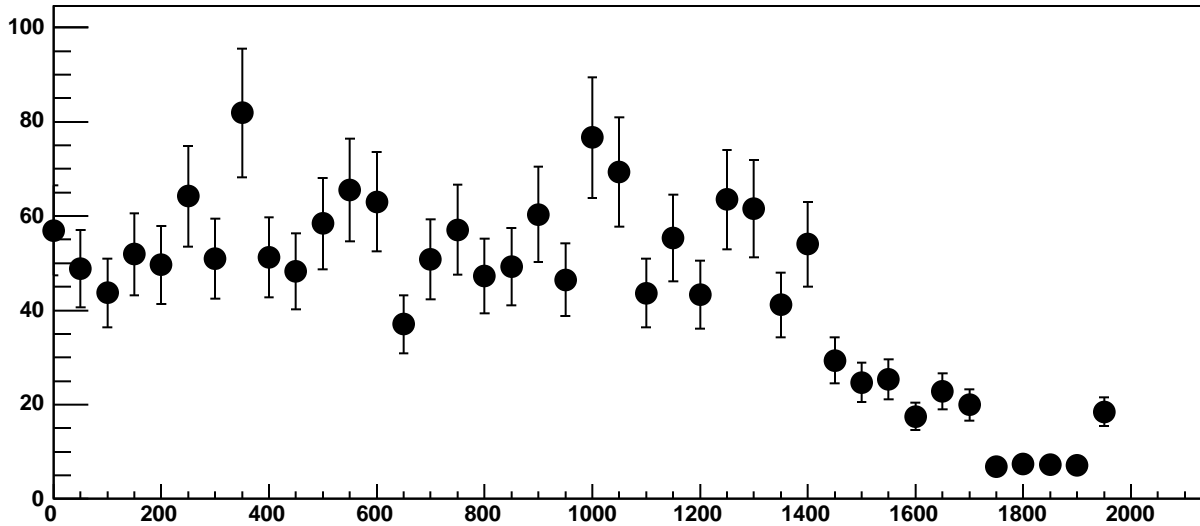


Chip 1, Channel 2, Enable 3!, Hold=30, ADC Mean vs DAC

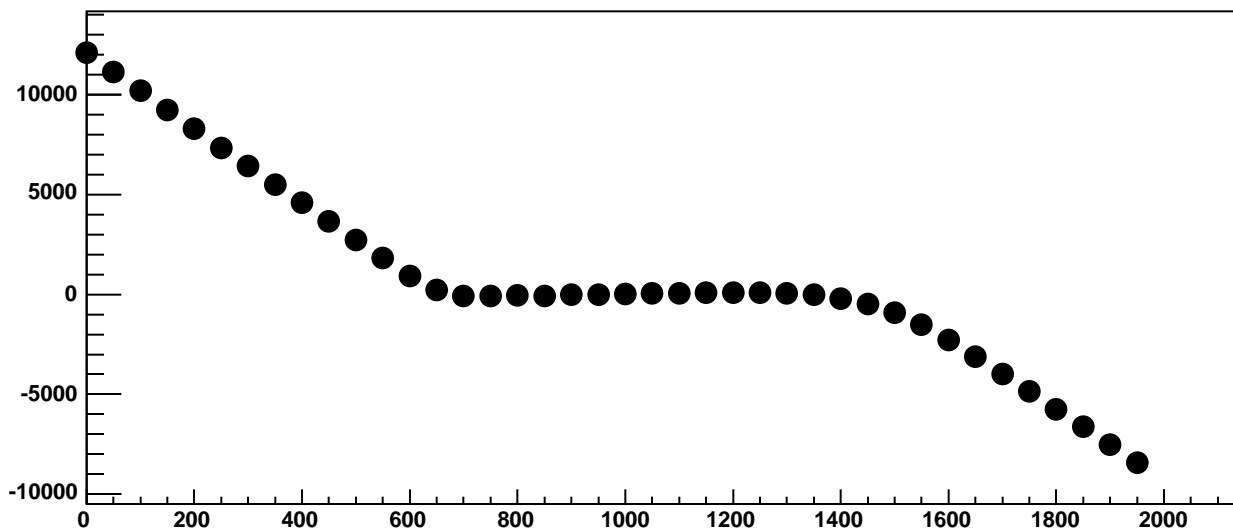


$\chi^2 / \text{ndf}$  513.4 / 11  
p0  $-1389 \pm 19.2$   
p1  $19.13 \pm 0.01713$

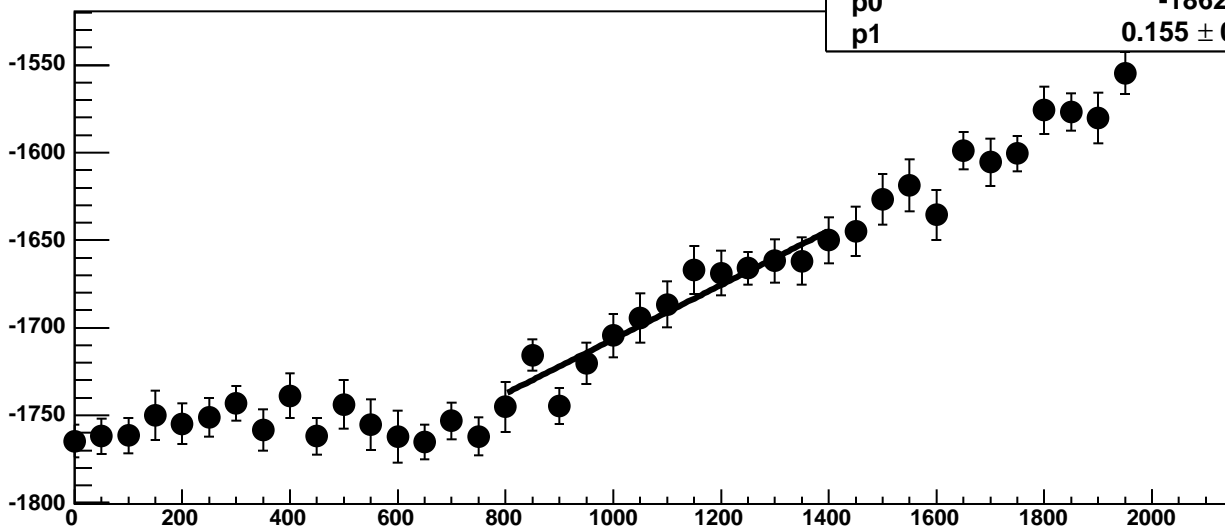
Chip 1, Channel 2, Enable 3!, Hold=30, ADC Noise vs DAC



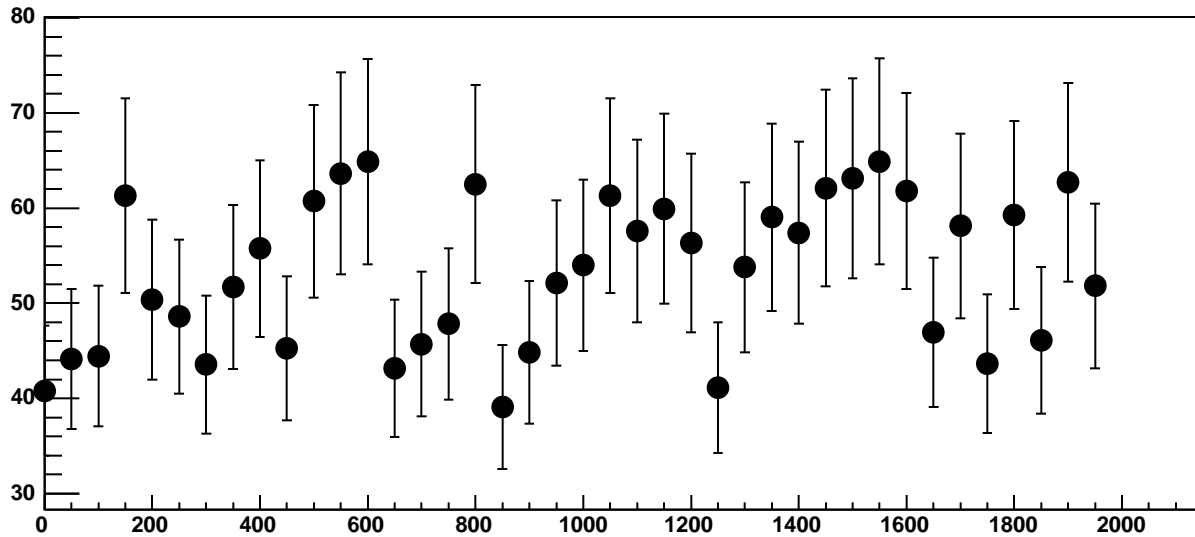
Chip 1, Channel 2, Enable 3!, Hold=30, ADC Residuals vs DAC



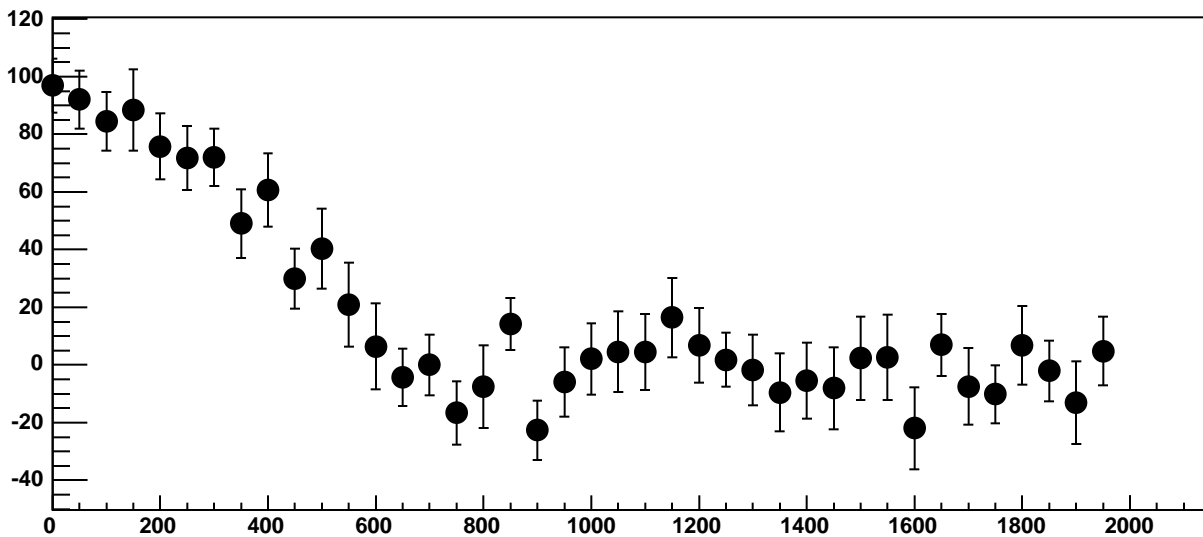
Chip 1, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC



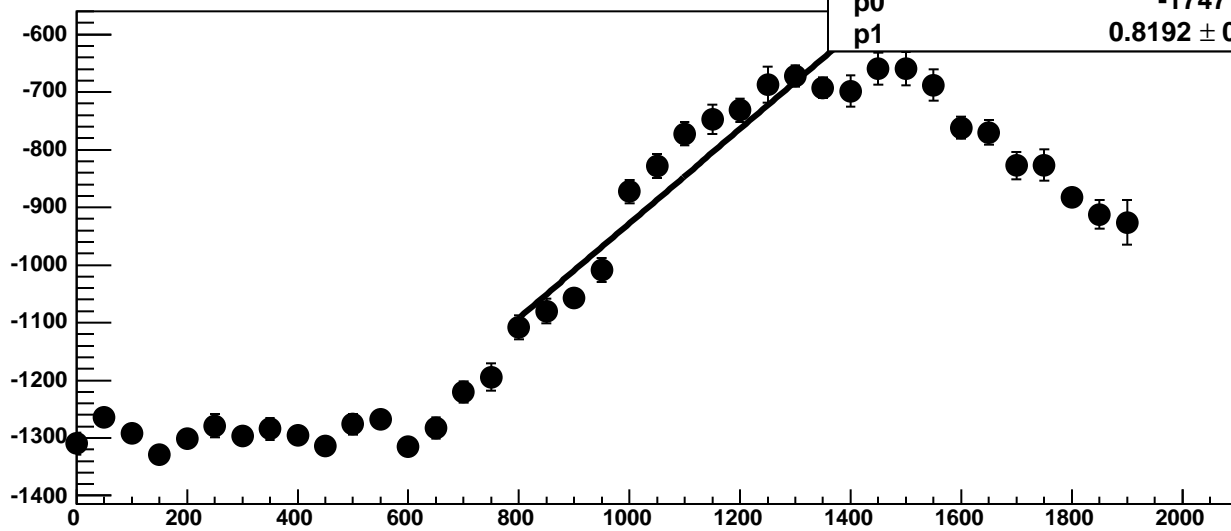
Chip 1, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC



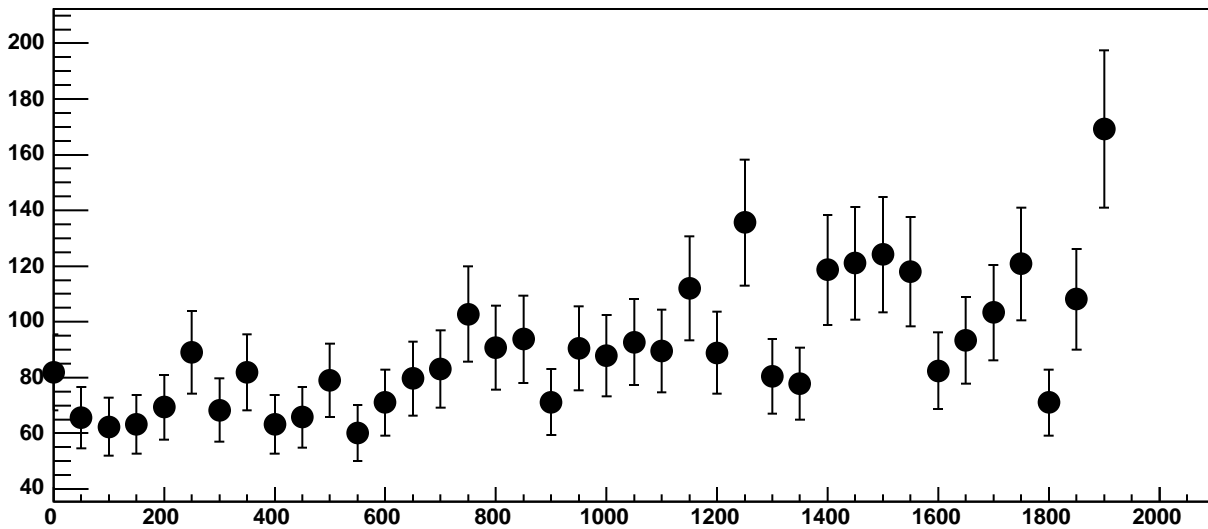
Chip 1, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC



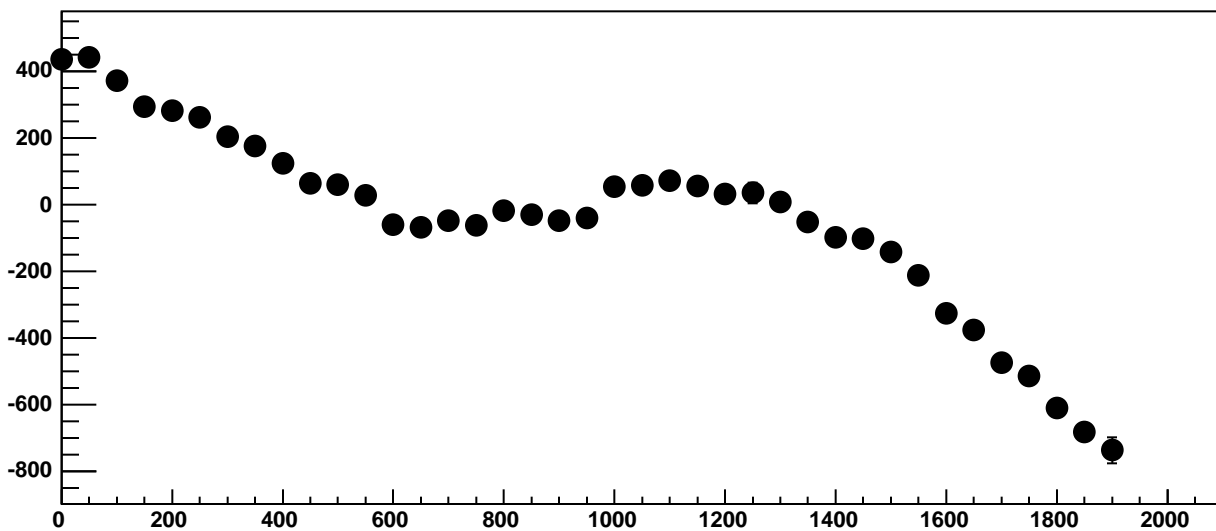
Chip 1, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC



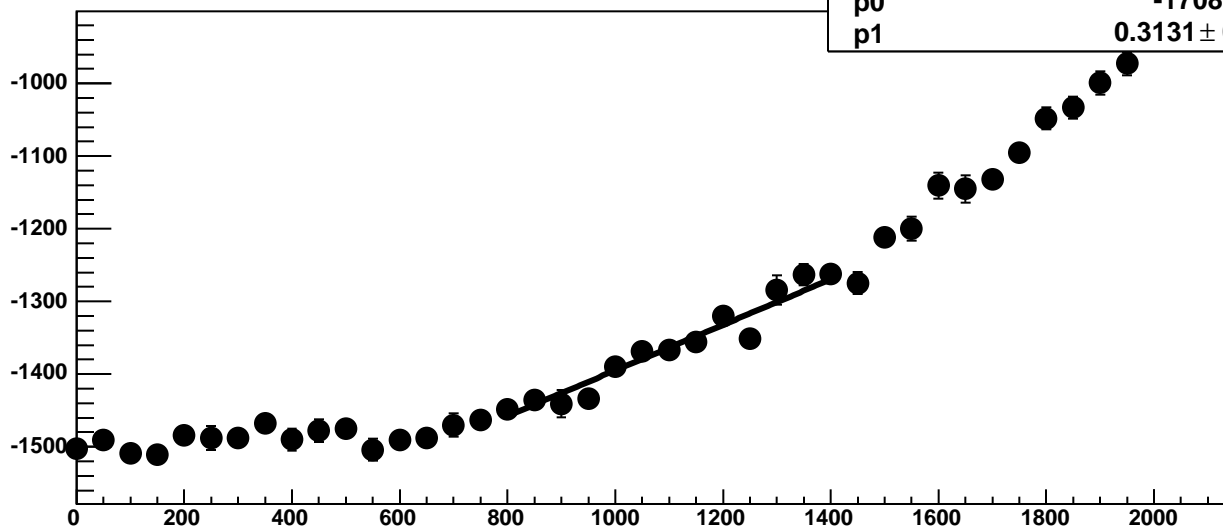
Chip 1, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC



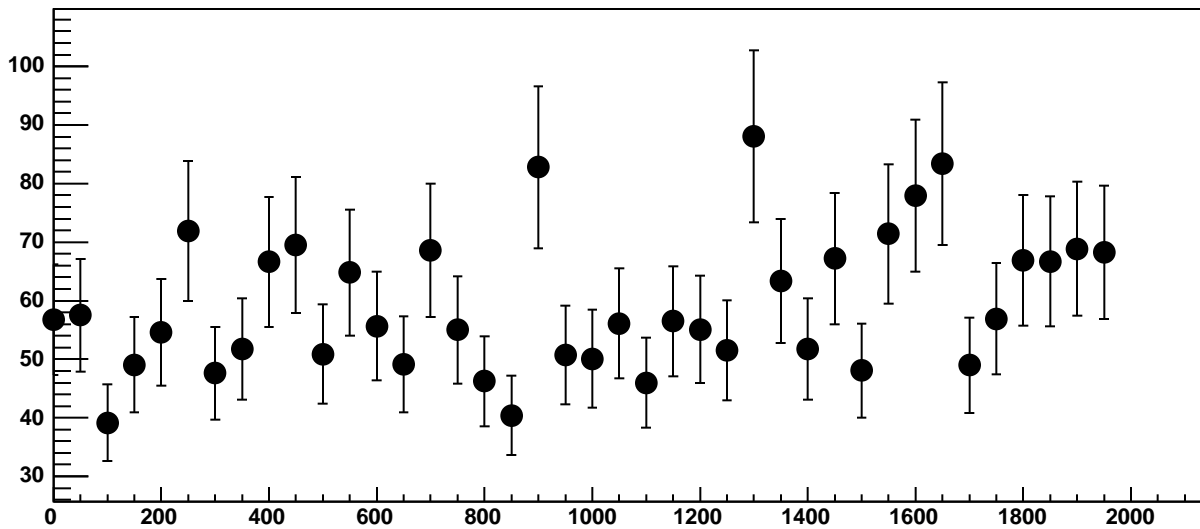
Chip 1, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC



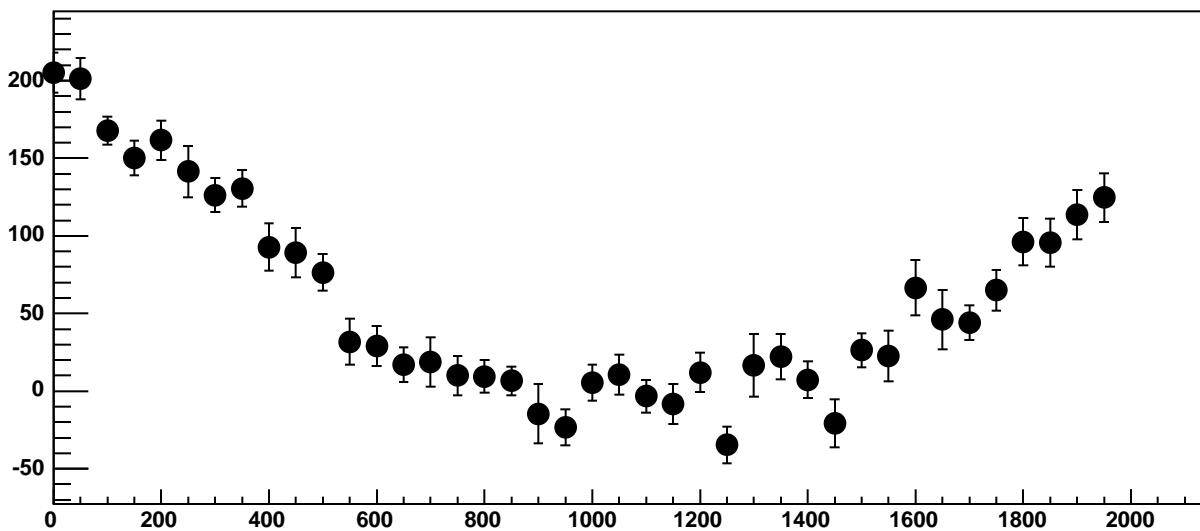
Chip 1, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC



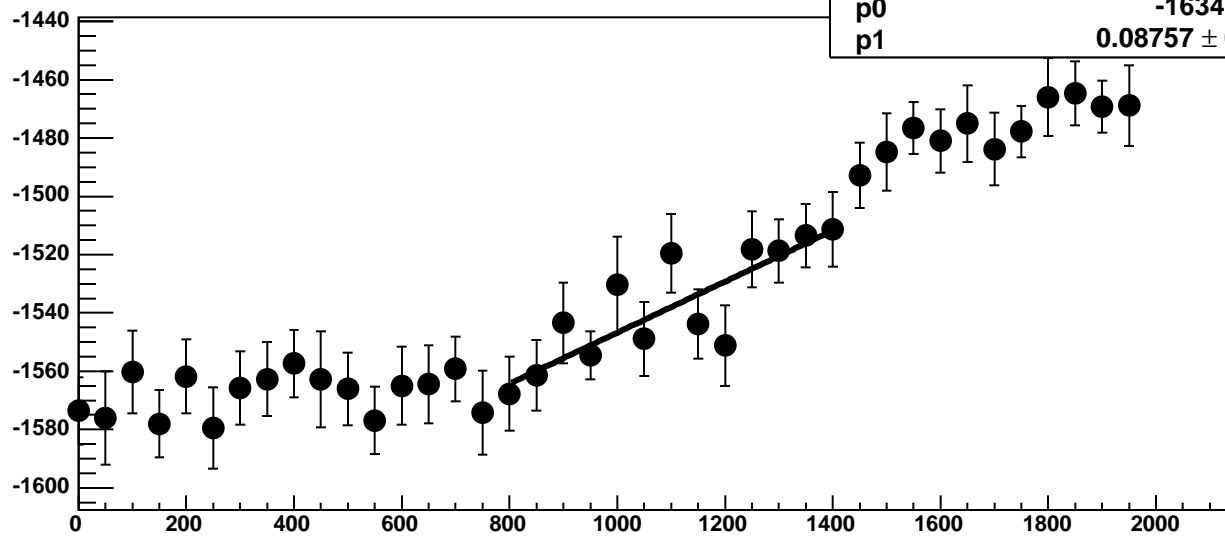
Chip 1, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC



Chip 1, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 1, Channel 3, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

7.729 / 11

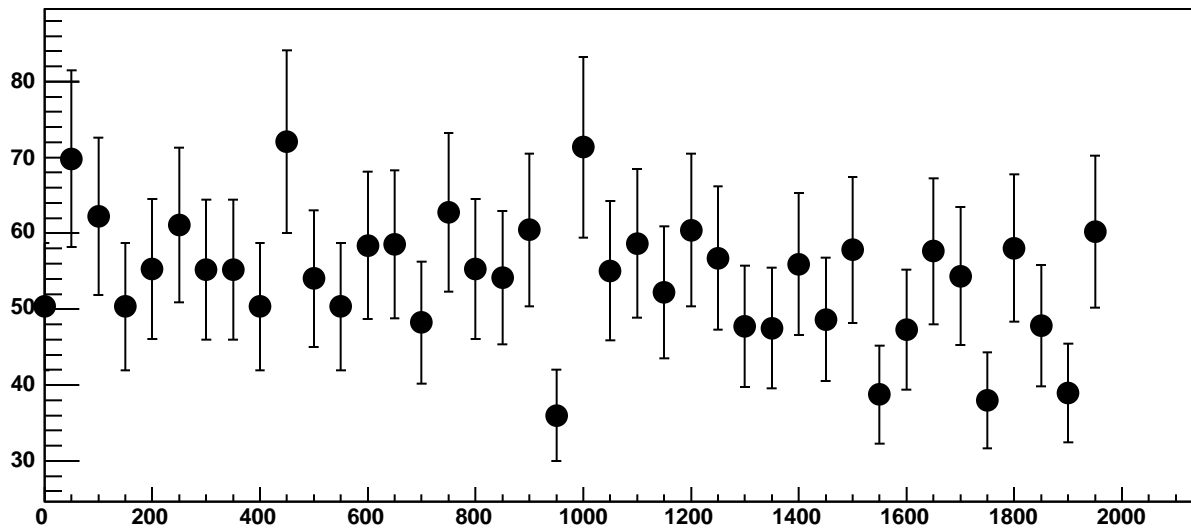
p0

$-1634 \pm 19.75$

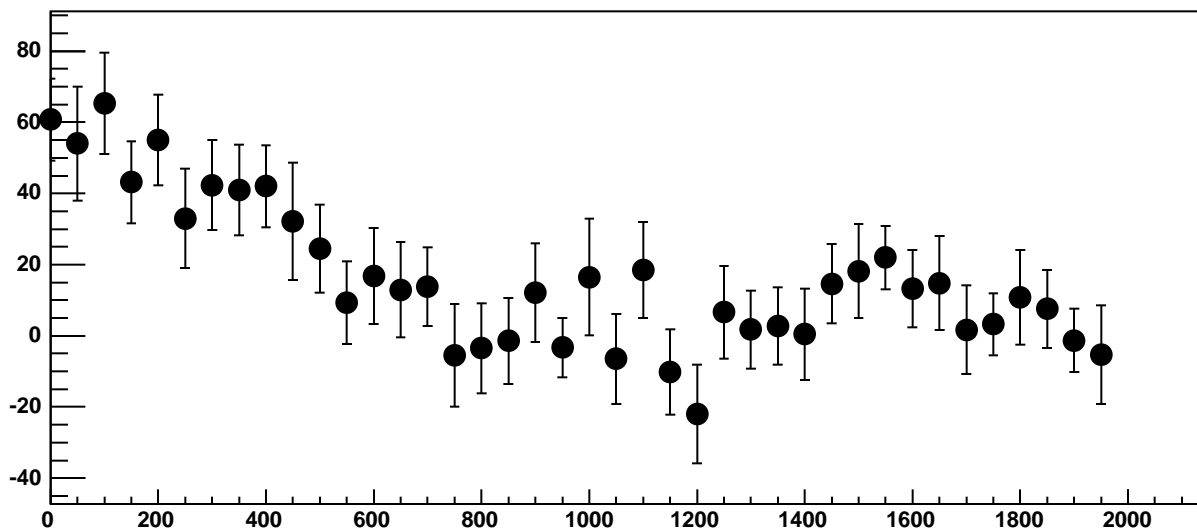
p1

$0.08757 \pm 0.01772$

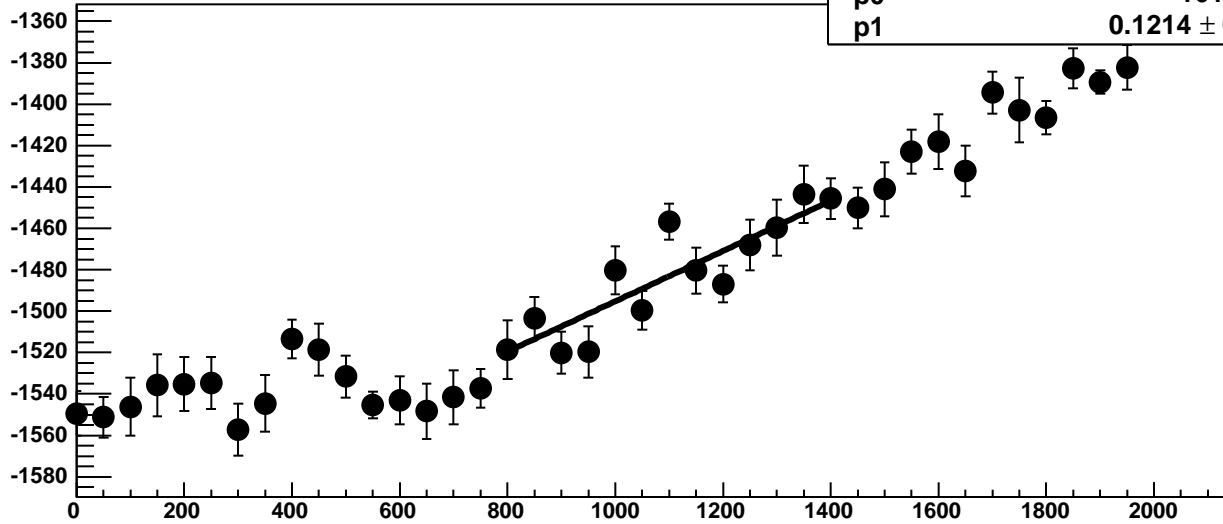
Chip 1, Channel 3, Enable 1, Hold=30, ADC Noise vs DAC



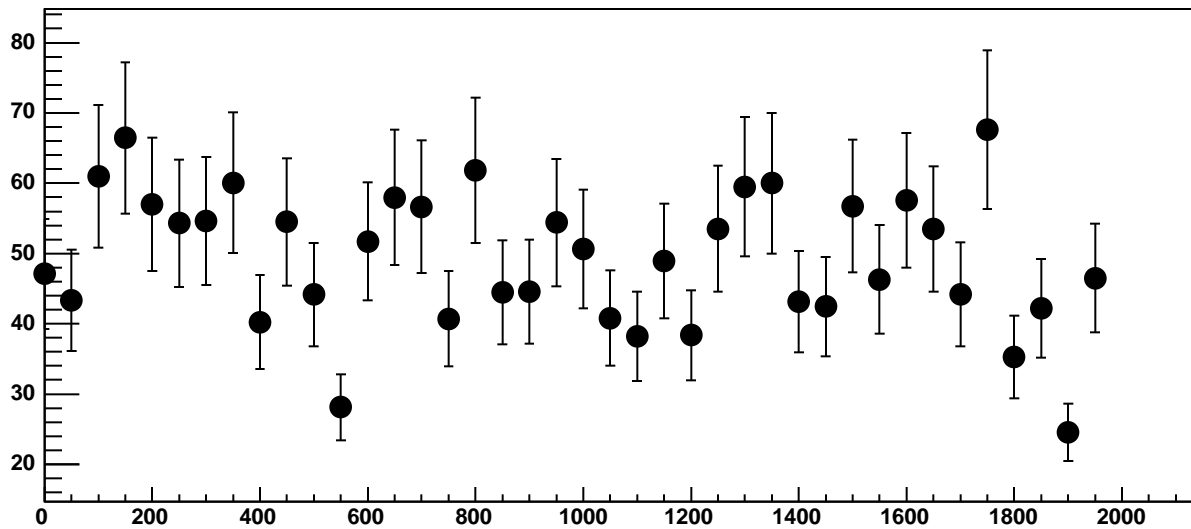
Chip 1, Channel 3, Enable 1, Hold=30, ADC Residuals vs DAC



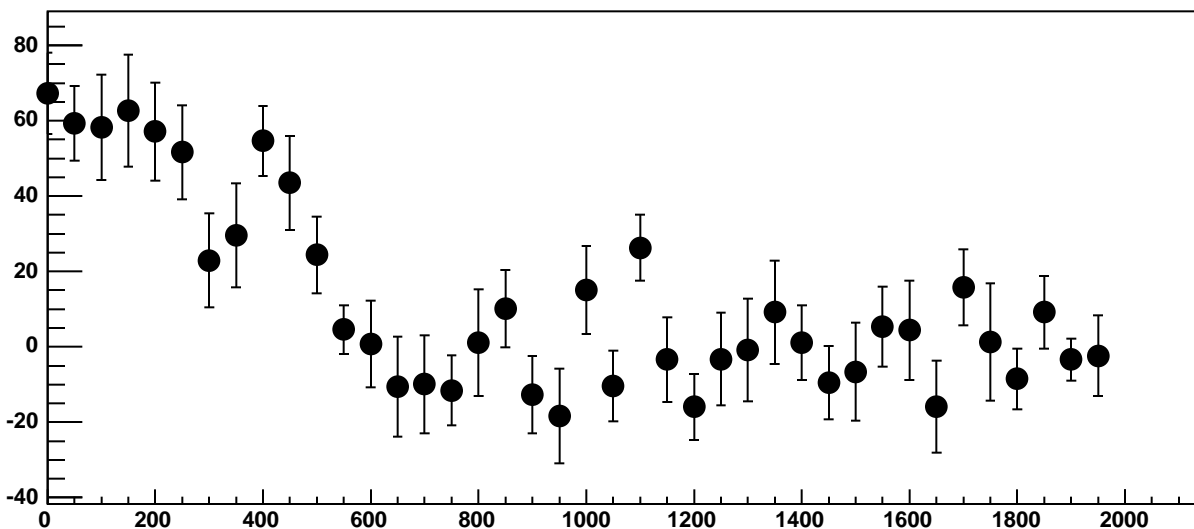
Chip 1, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC



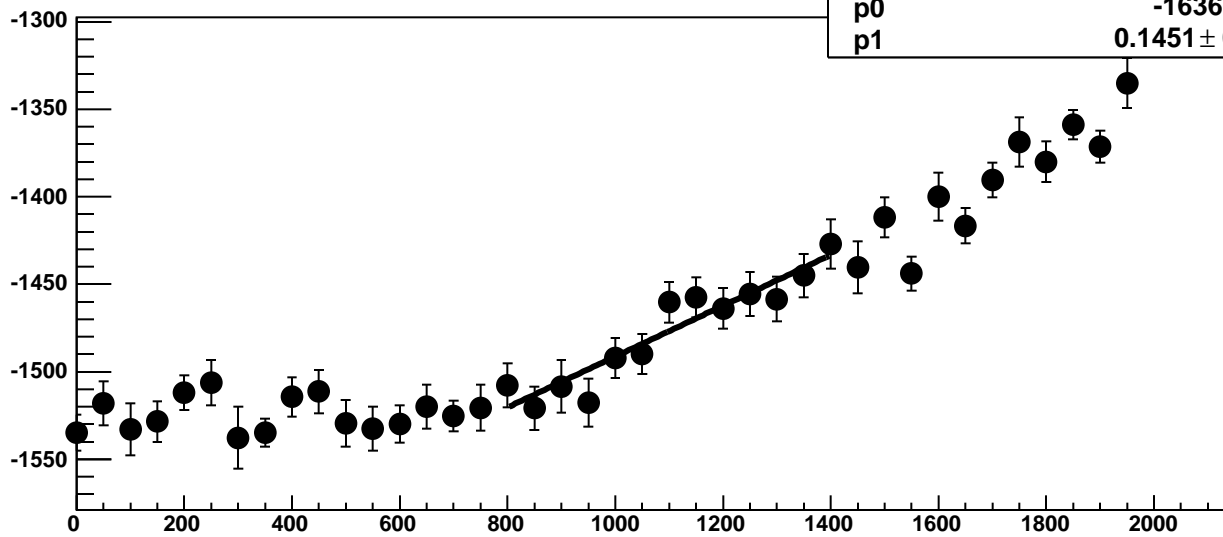
Chip 1, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC



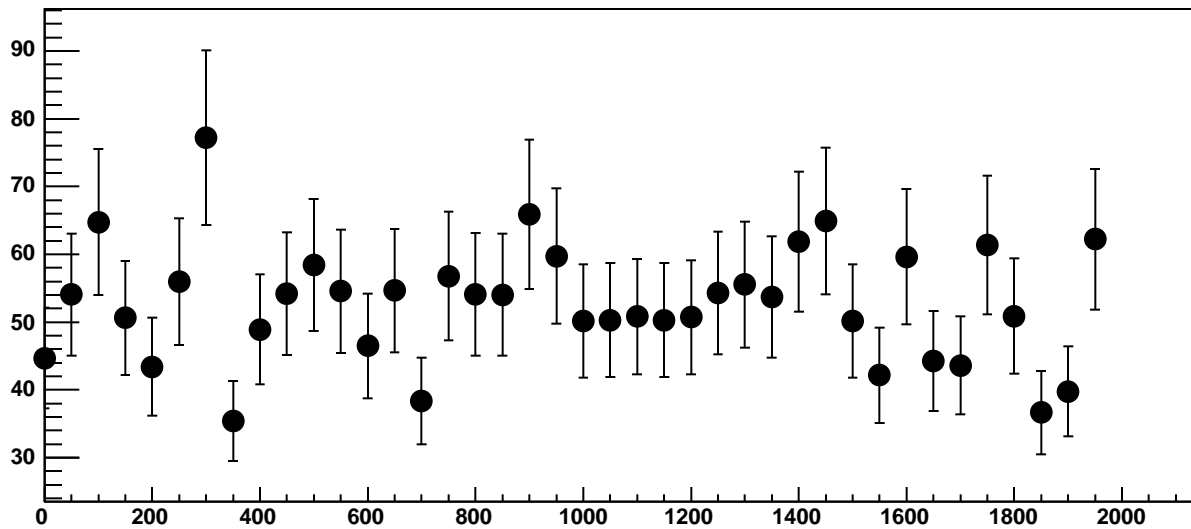
Chip 1, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC



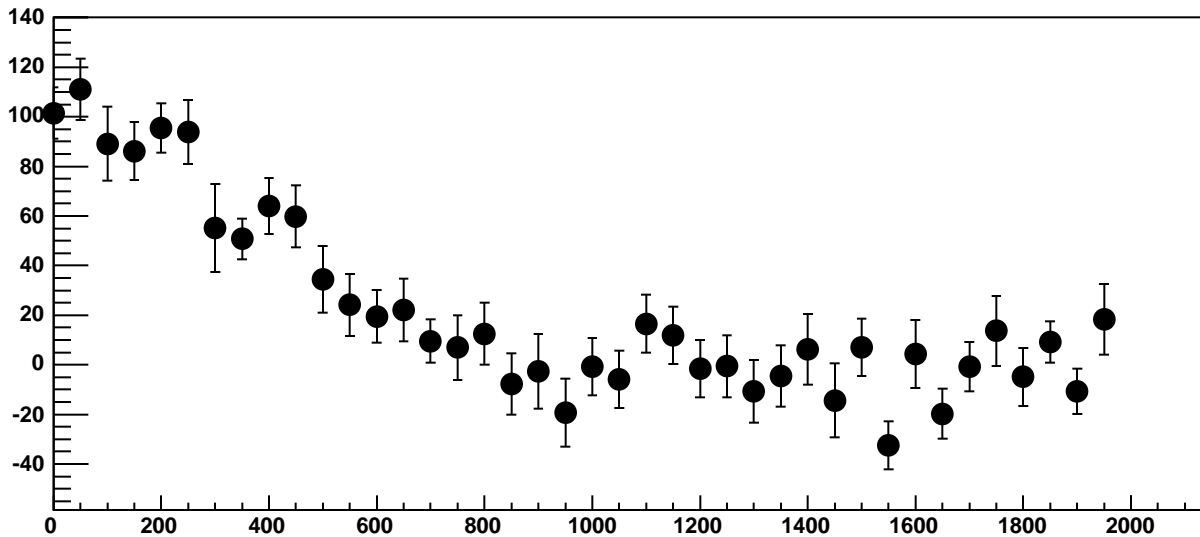
Chip 1, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC



Chip 1, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC

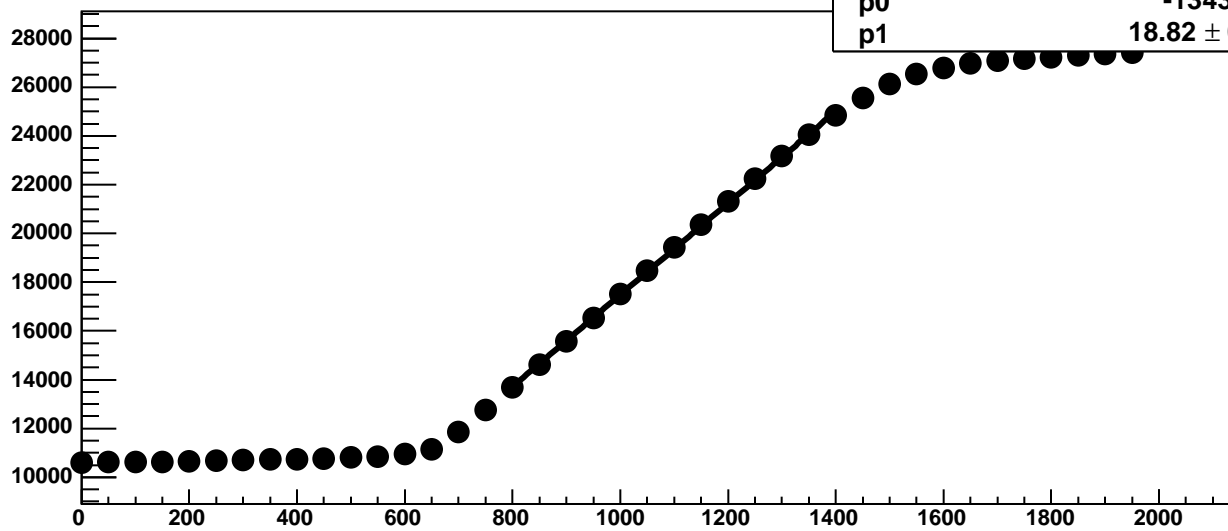


Chip 1, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC

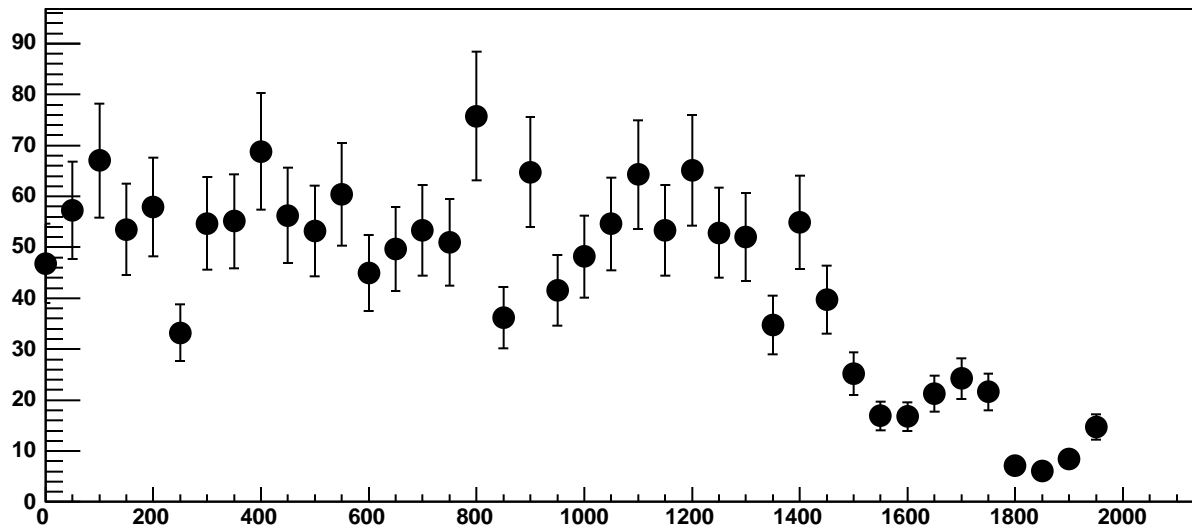




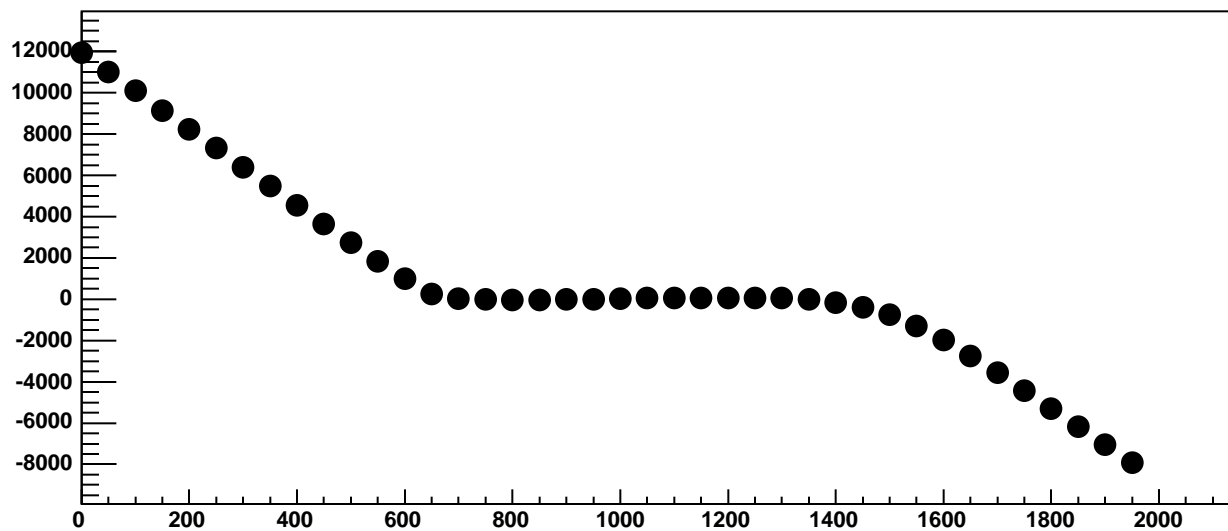
Chip 1, Channel 3, Enable 4!, Hold=30, ADC Mean vs DAC



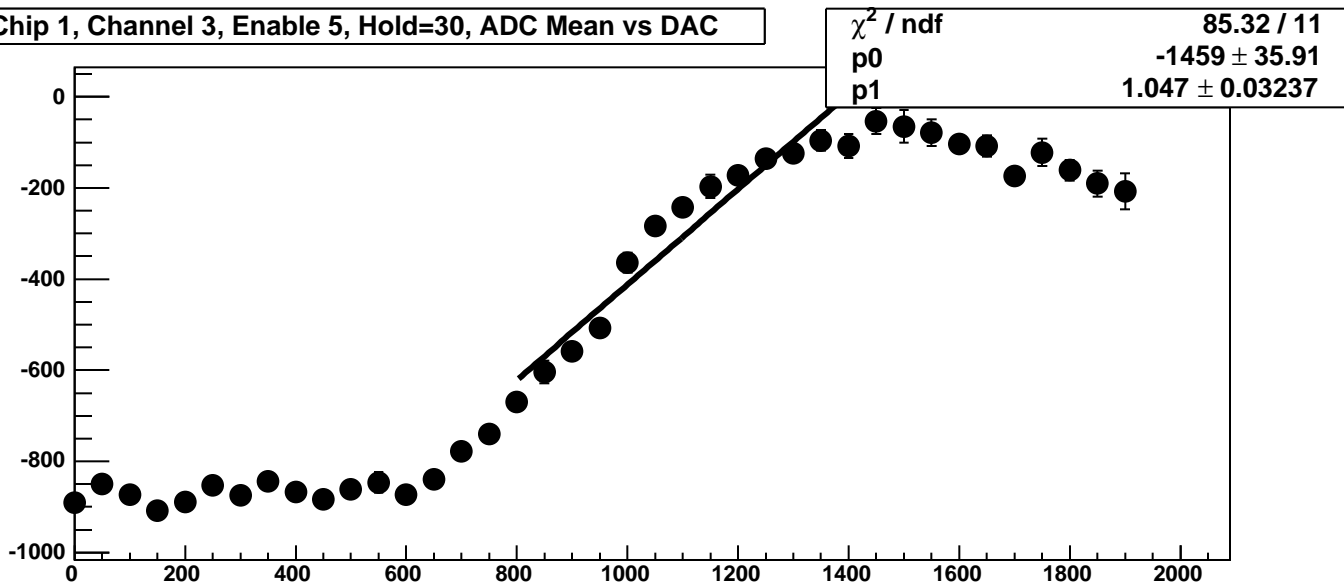
Chip 1, Channel 3, Enable 4!, Hold=30, ADC Noise vs DAC



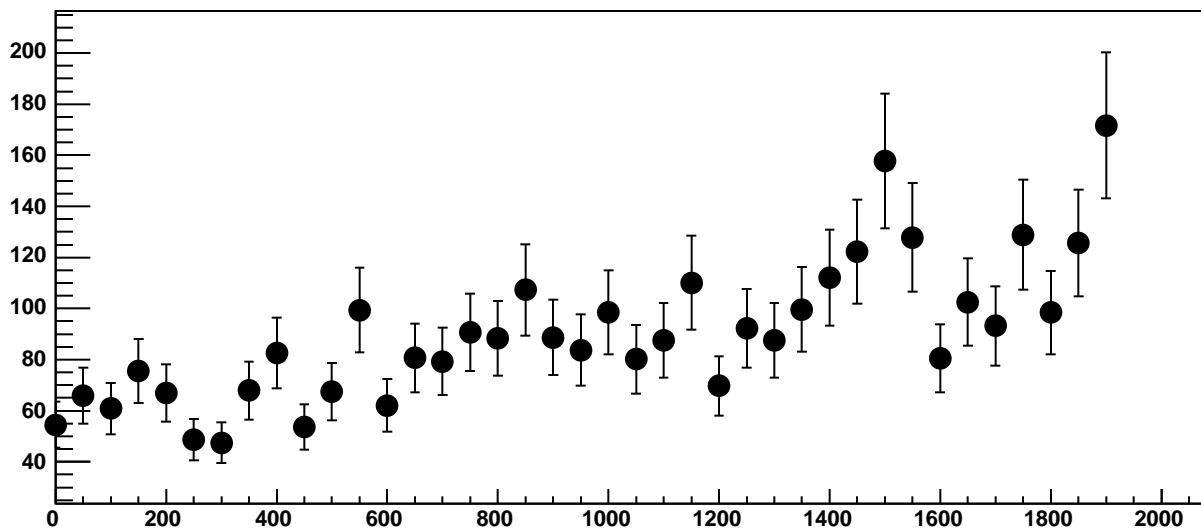
Chip 1, Channel 3, Enable 4!, Hold=30, ADC Residuals vs DAC



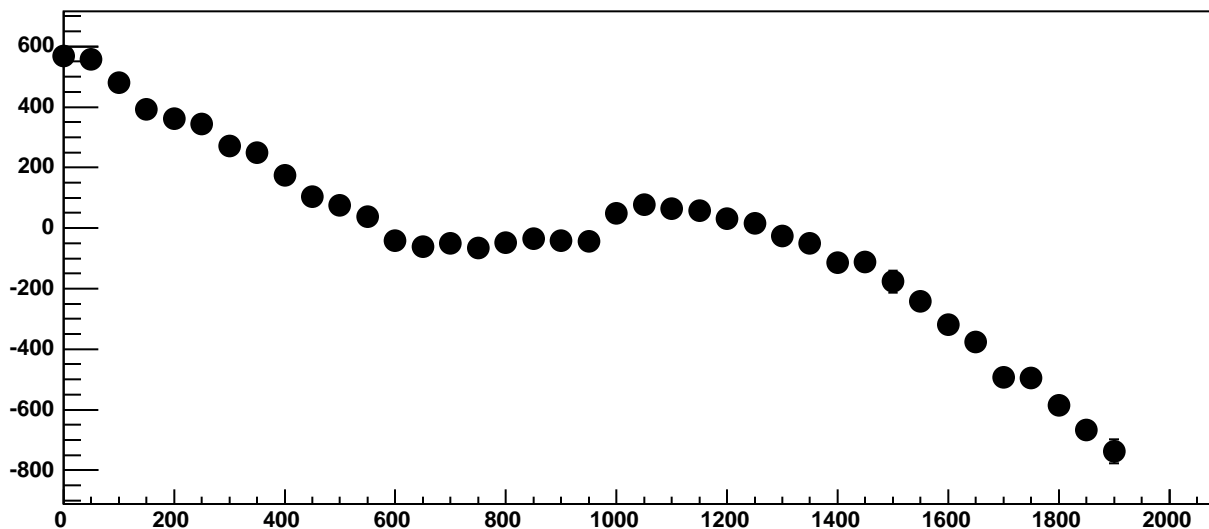
Chip 1, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC



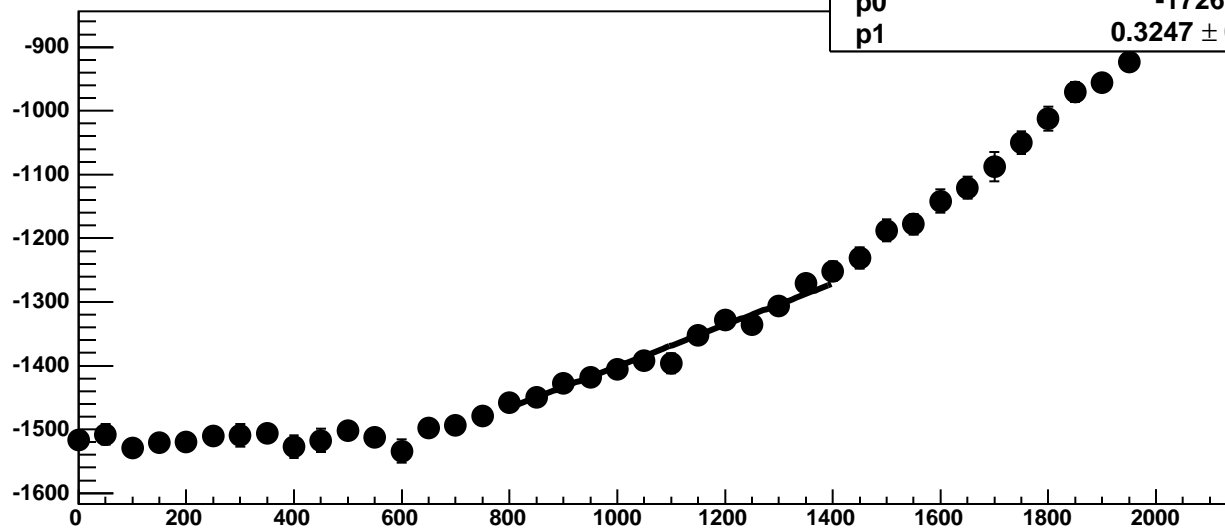
Chip 1, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC



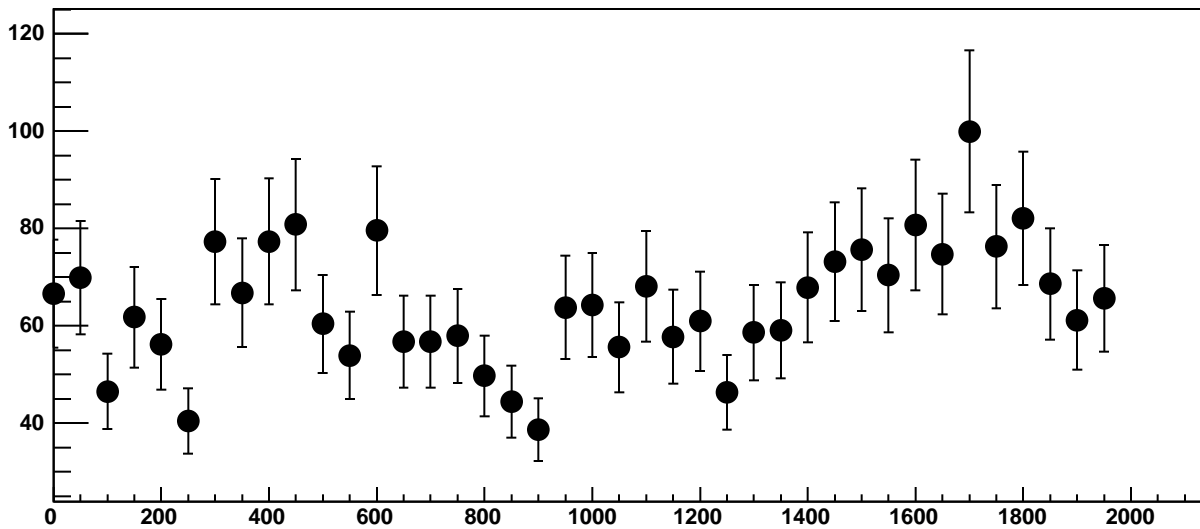
Chip 1, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC



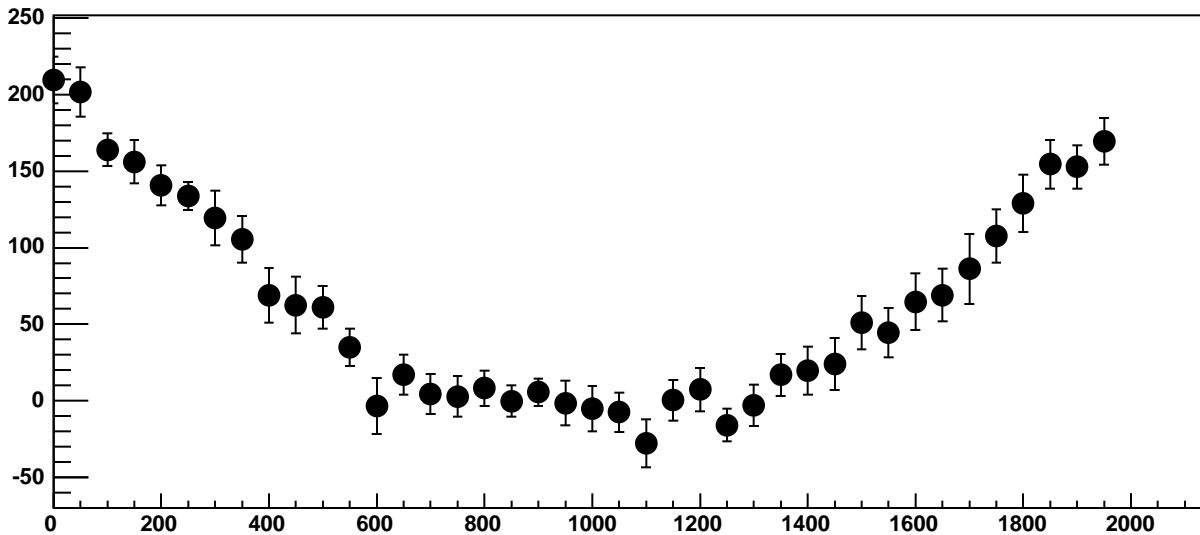
Chip 1, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC



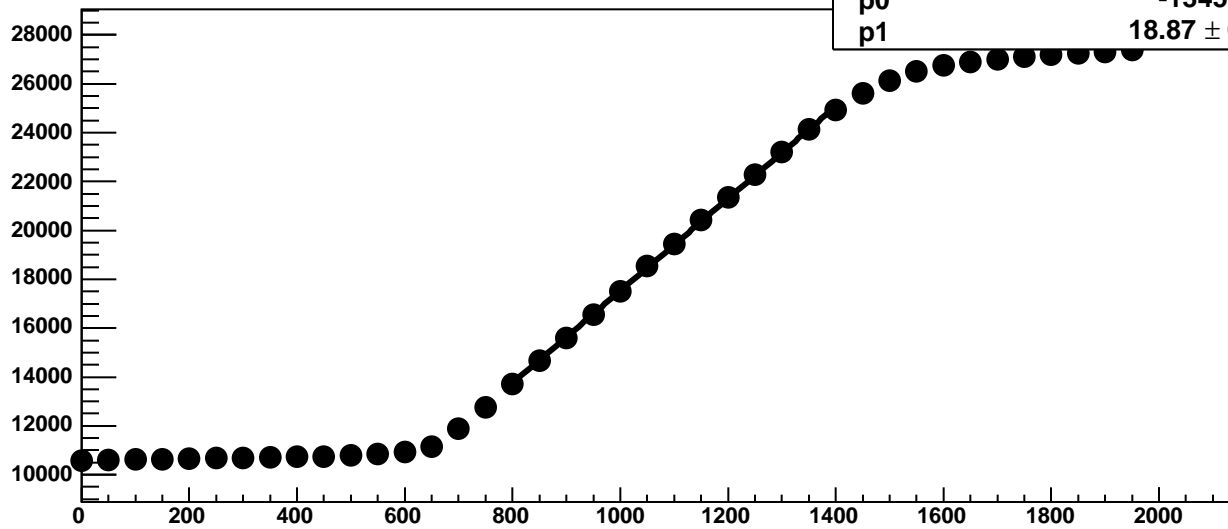
Chip 1, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC



Chip 1, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC

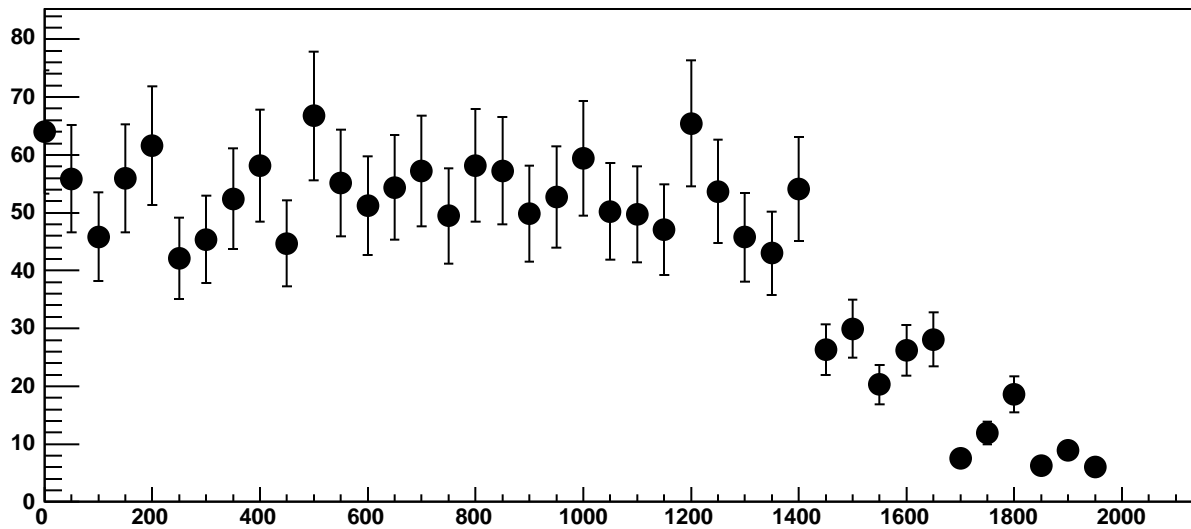


Chip 1, Channel 4, Enable 1!, Hold=30, ADC Mean vs DAC

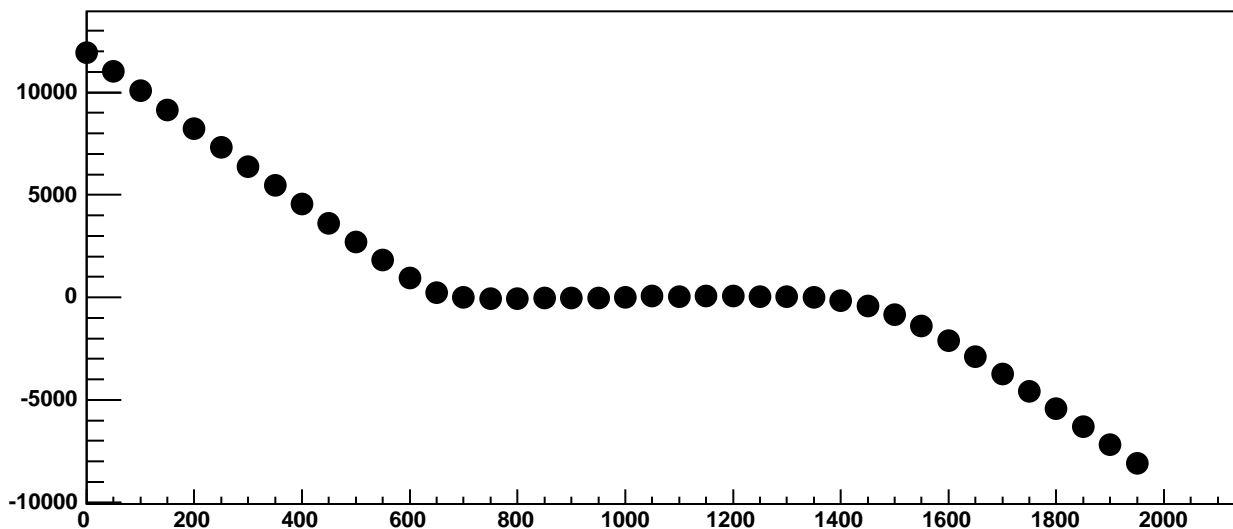


$\chi^2 / \text{ndf}$  289.6 / 11  
p0  $-1345 \pm 20.02$   
p1  $18.87 \pm 0.01769$

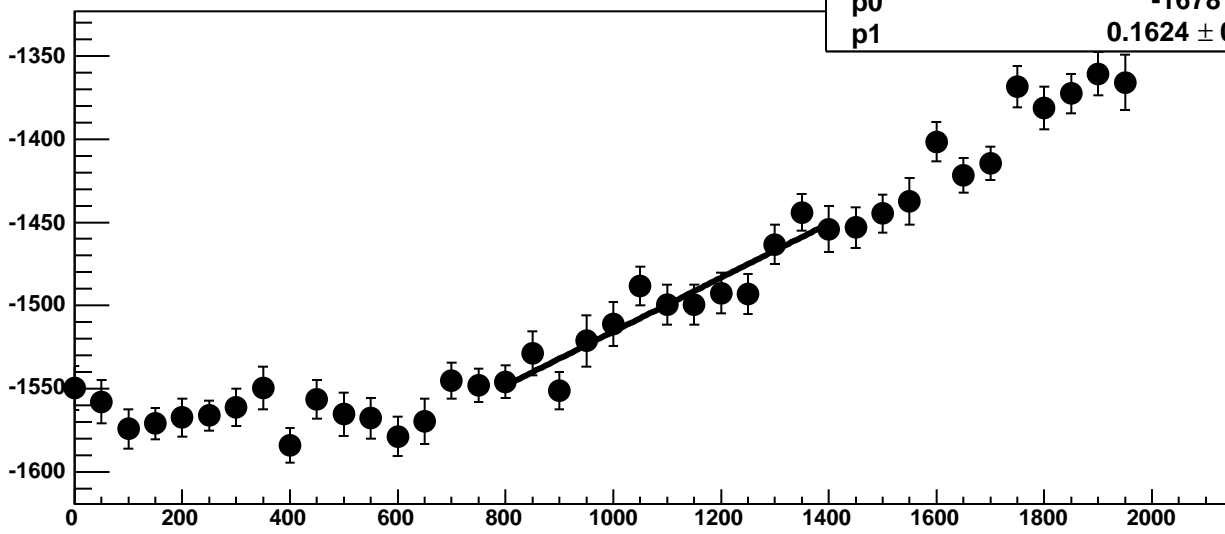
Chip 1, Channel 4, Enable 1!, Hold=30, ADC Noise vs DAC



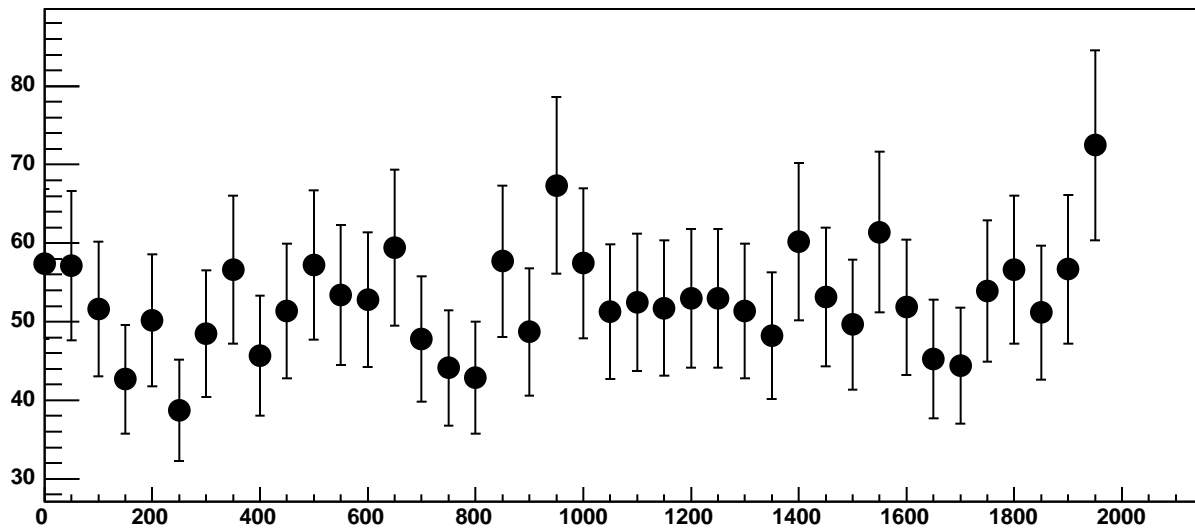
Chip 1, Channel 4, Enable 1!, Hold=30, ADC Residuals vs DAC



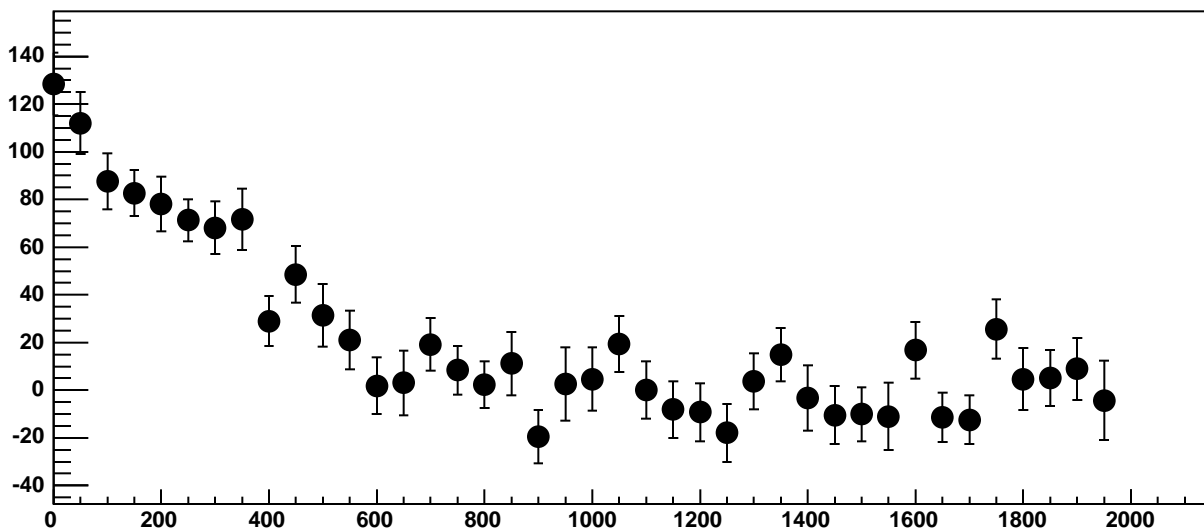
Chip 1, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC



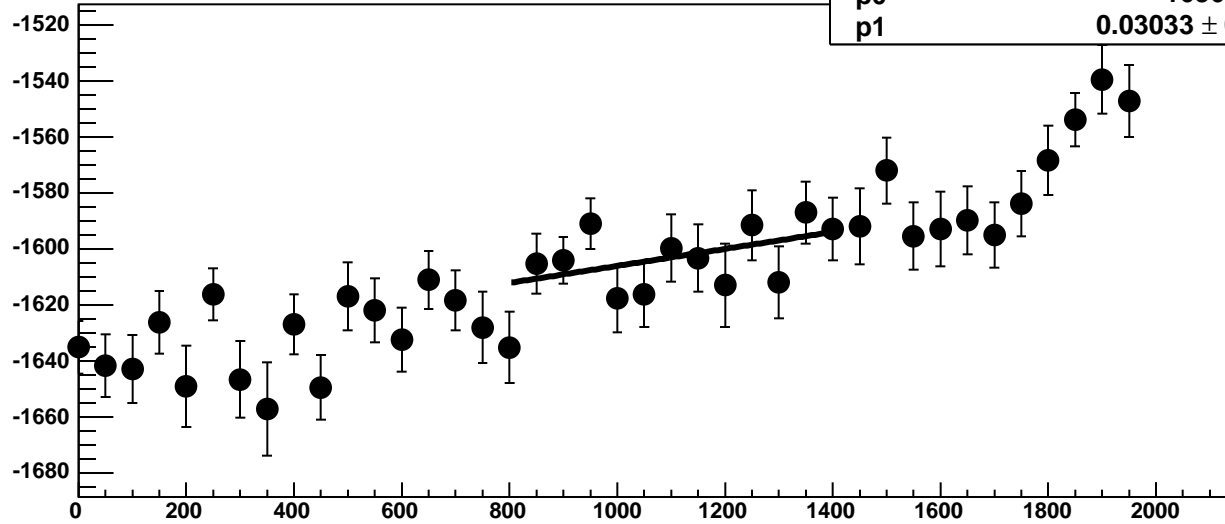
Chip 1, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC



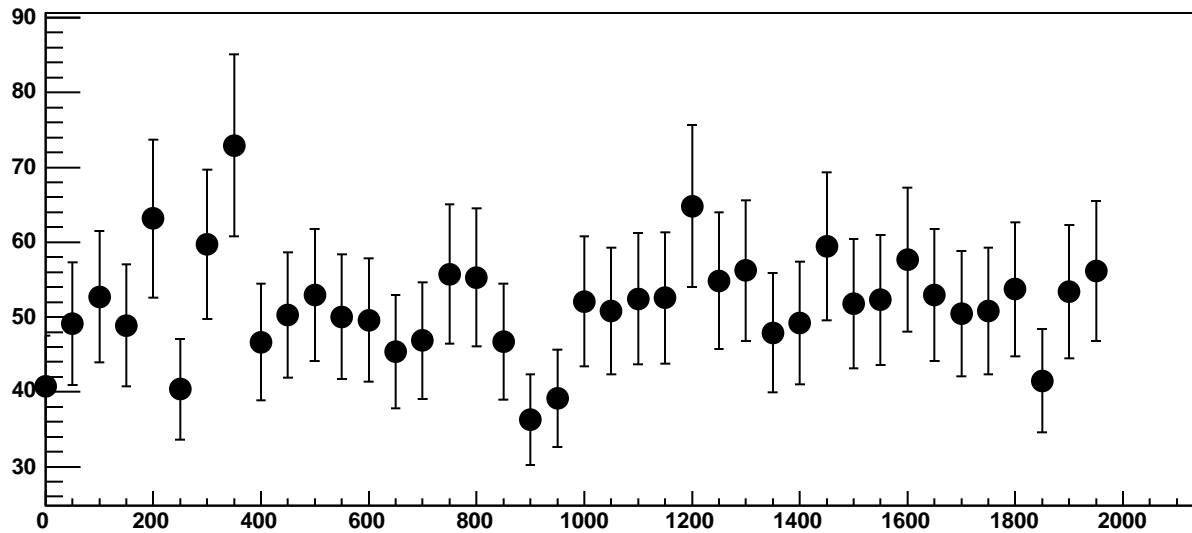
Chip 1, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC



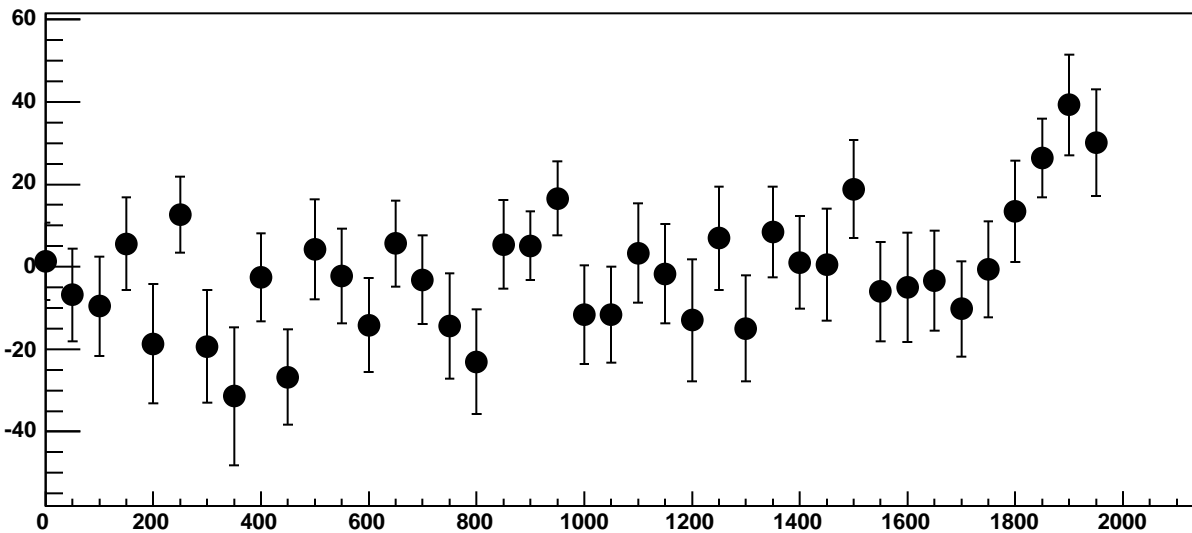
Chip 1, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC



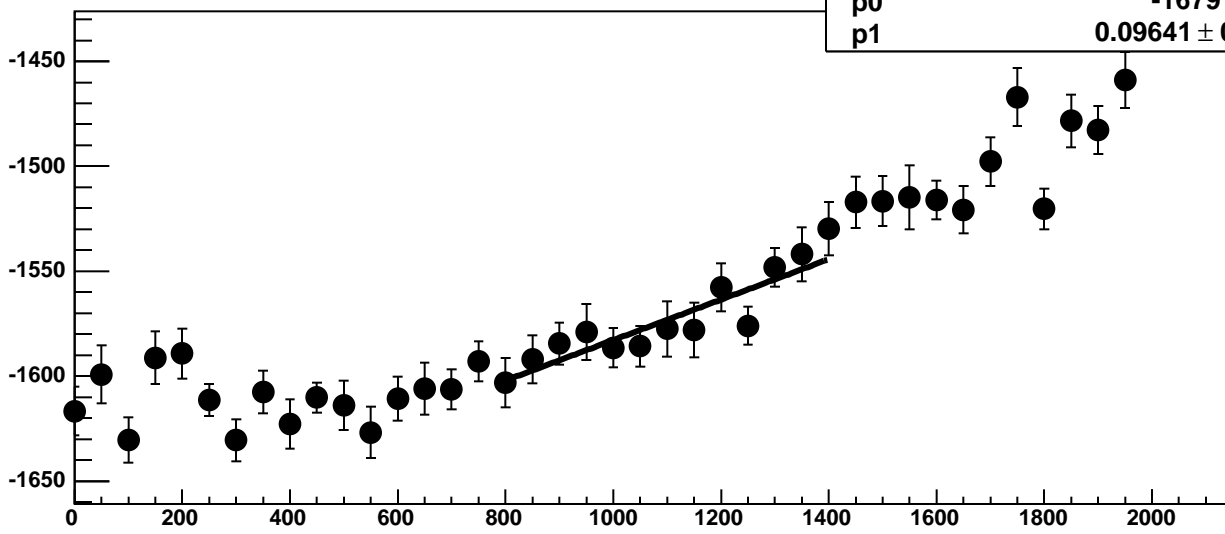
Chip 1, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC



Chip 1, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC

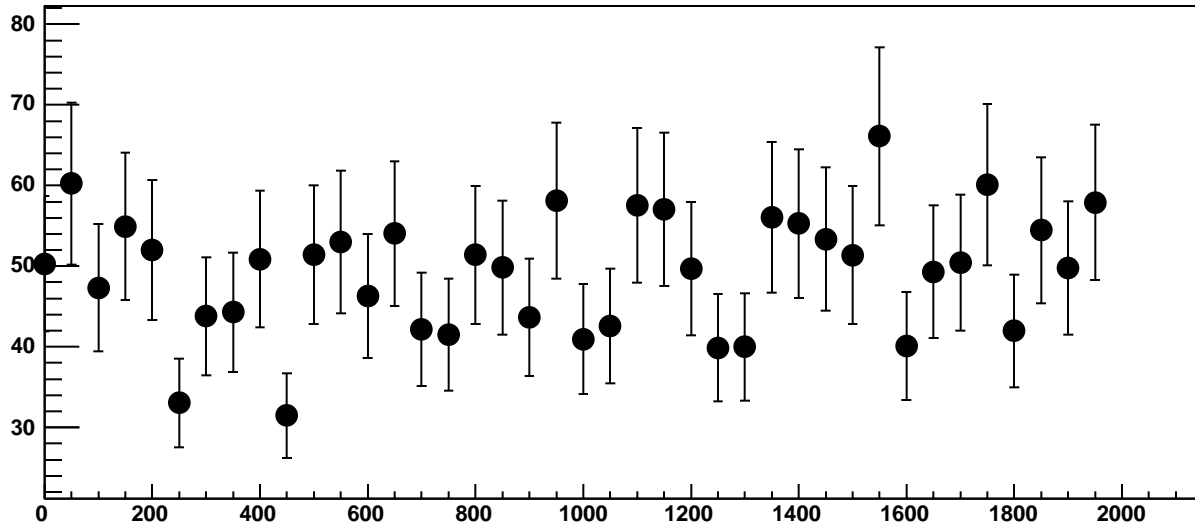


Chip 1, Channel 4, Enable 4, Hold=30, ADC Mean vs DAC

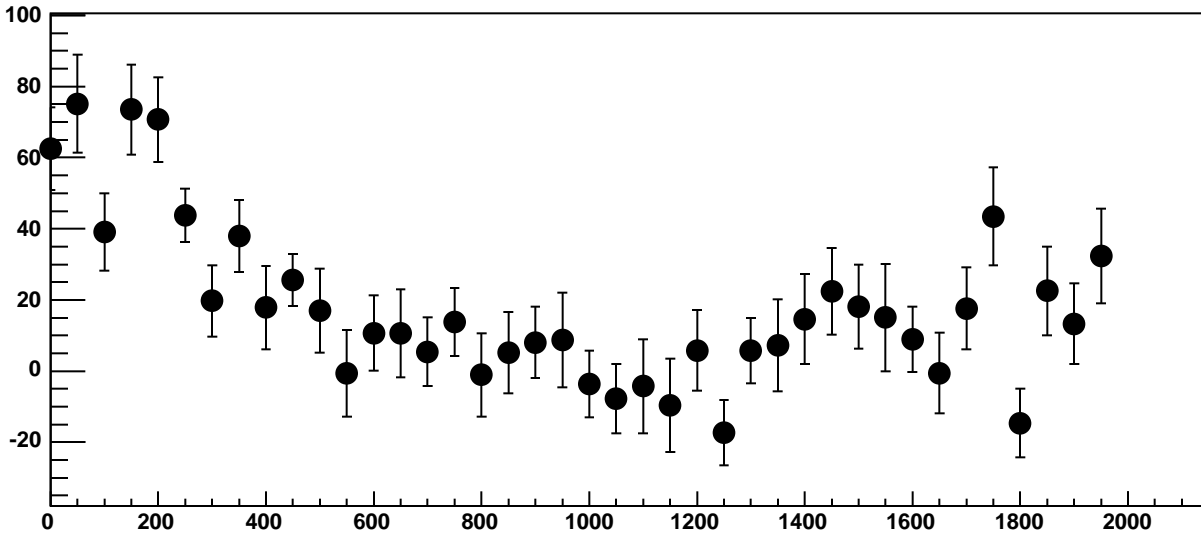


$\chi^2 / \text{ndf}$  8.599 / 11  
p0  $-1679 \pm 18.69$   
p1  $0.09641 \pm 0.01675$

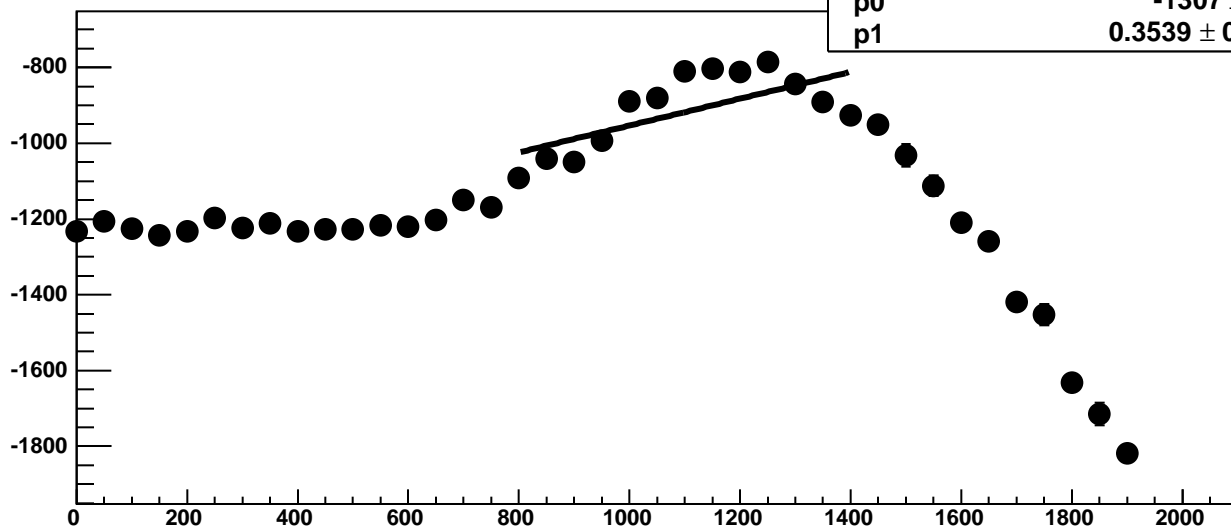
Chip 1, Channel 4, Enable 4, Hold=30, ADC Noise vs DAC



Chip 1, Channel 4, Enable 4, Hold=30, ADC Residuals vs DAC



Chip 1, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

168.2 / 11

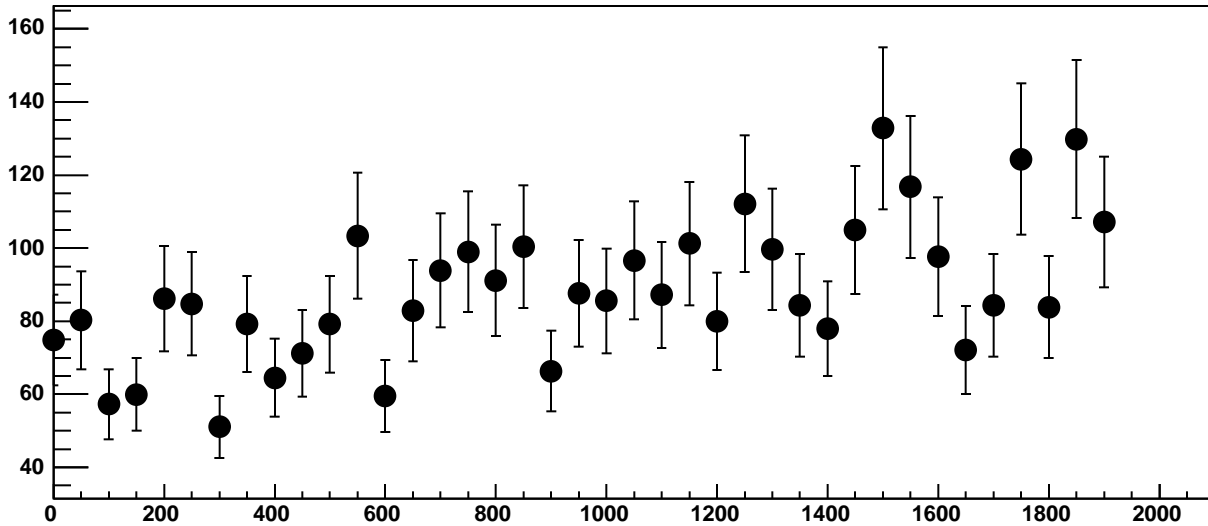
p0

$-1307 \pm 32.42$

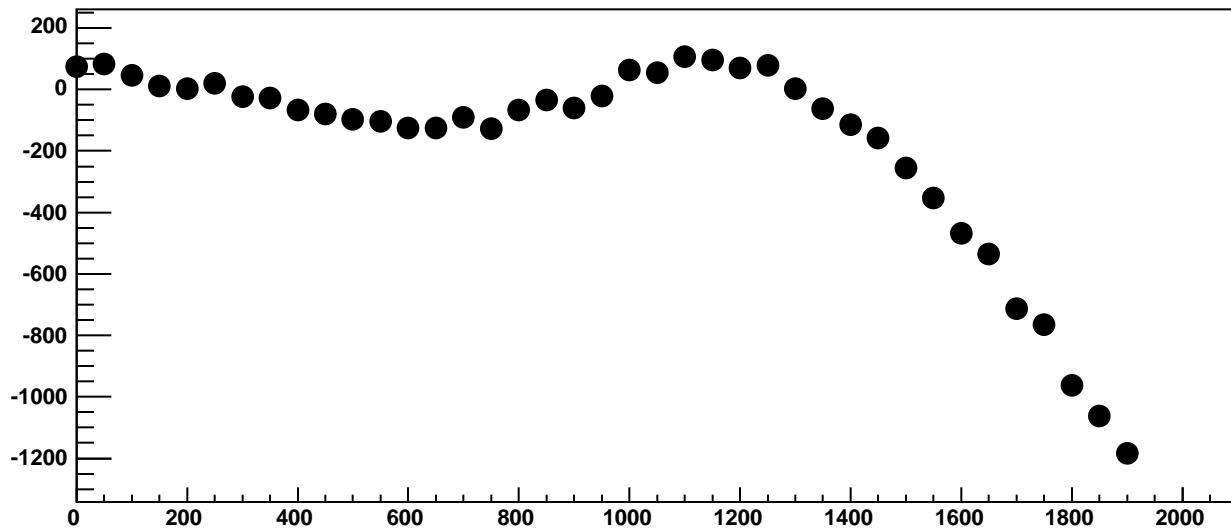
p1

$0.3539 \pm 0.02916$

Chip 1, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC

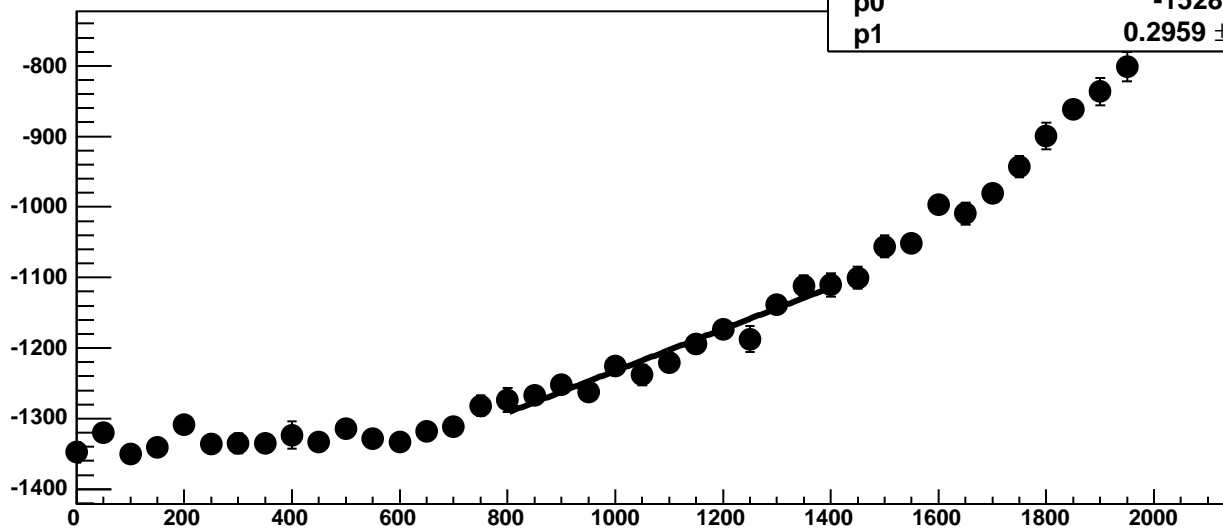


Chip 1, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC

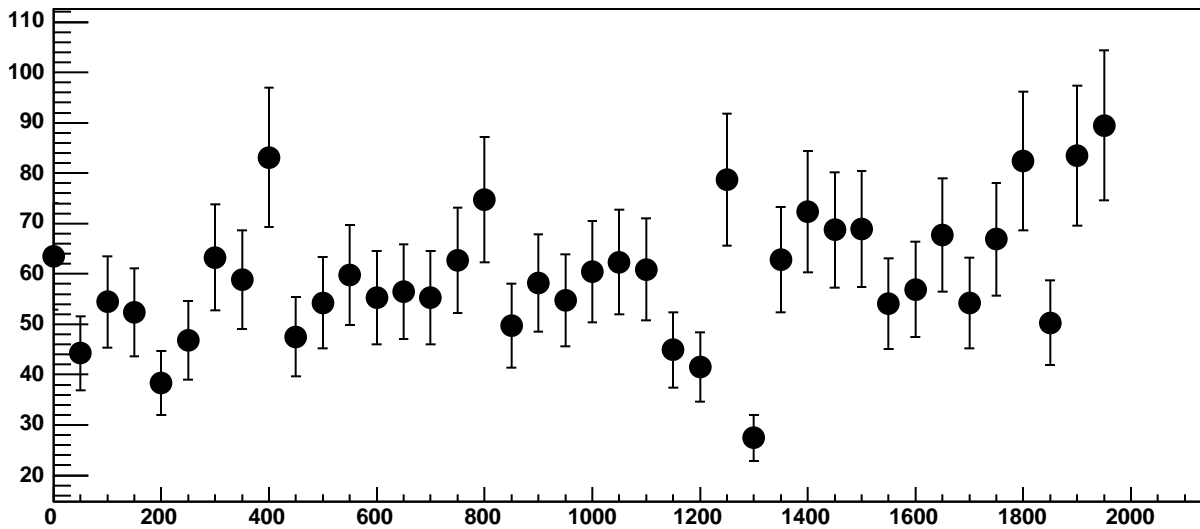




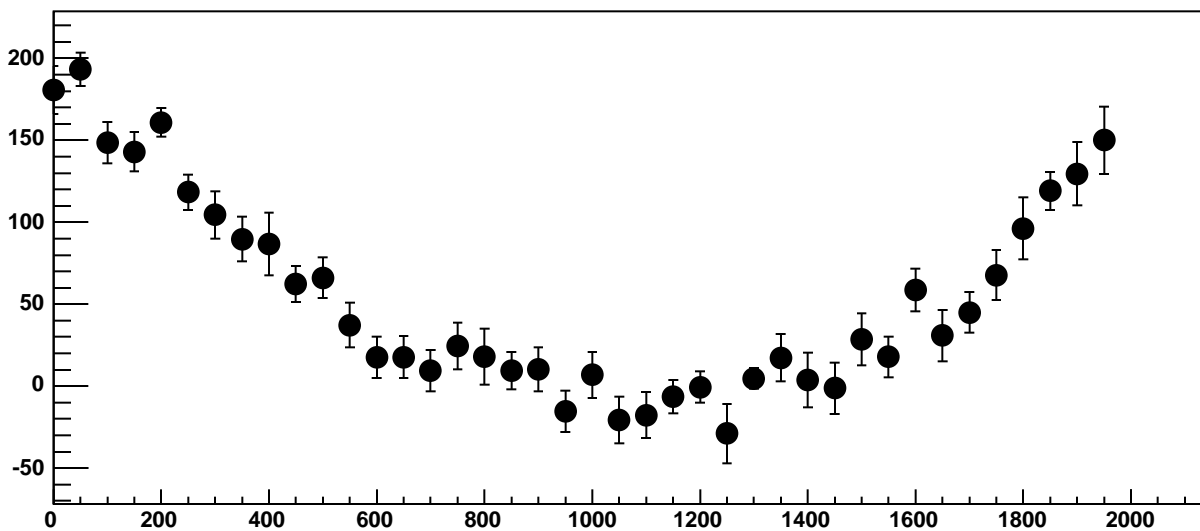
Chip 1, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC



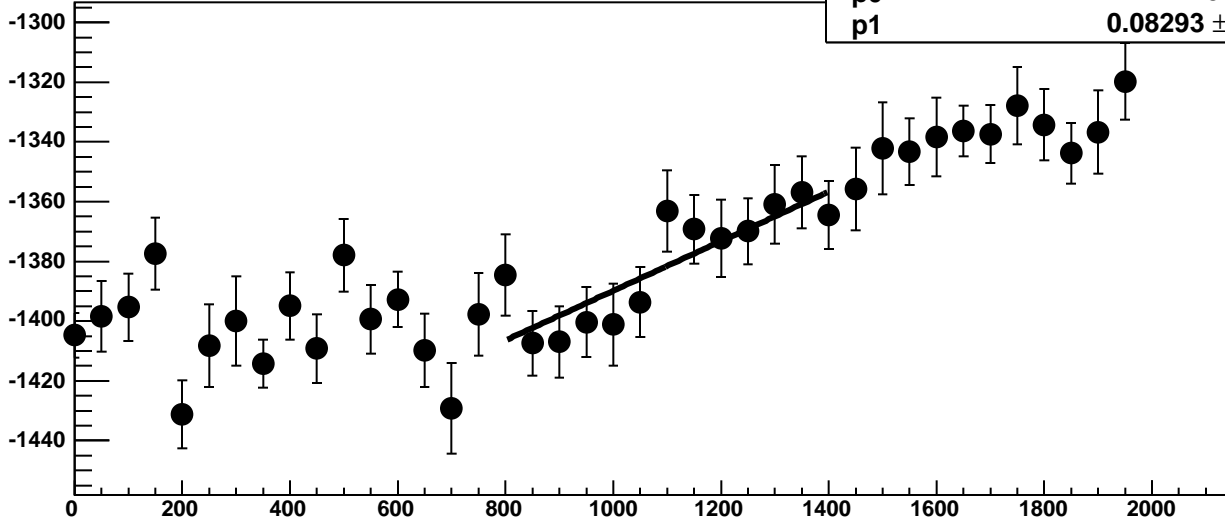
Chip 1, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC



Chip 1, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC

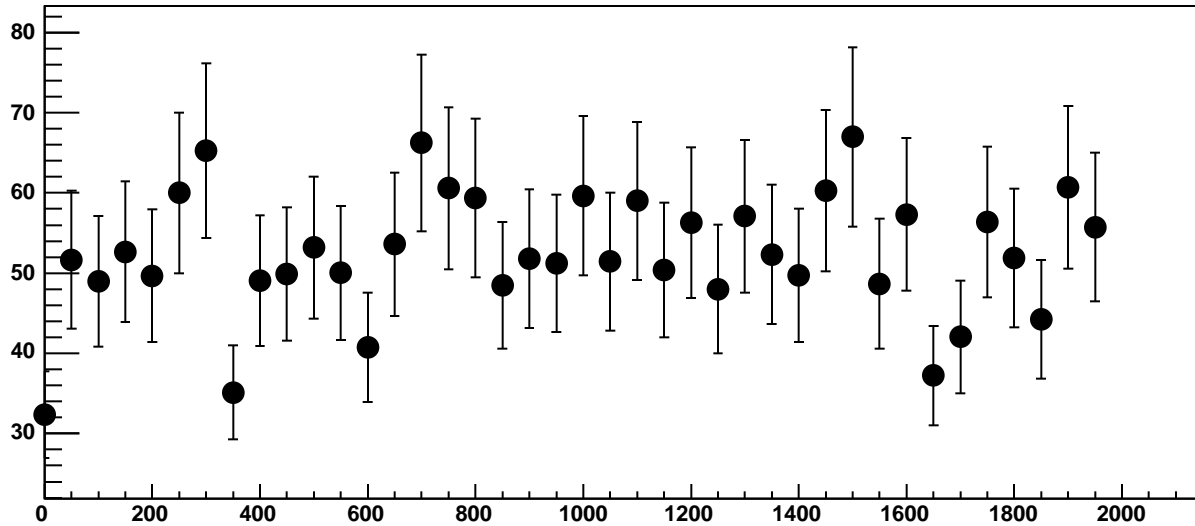


Chip 1, Channel 5, Enable 1, Hold=30, ADC Mean vs DAC

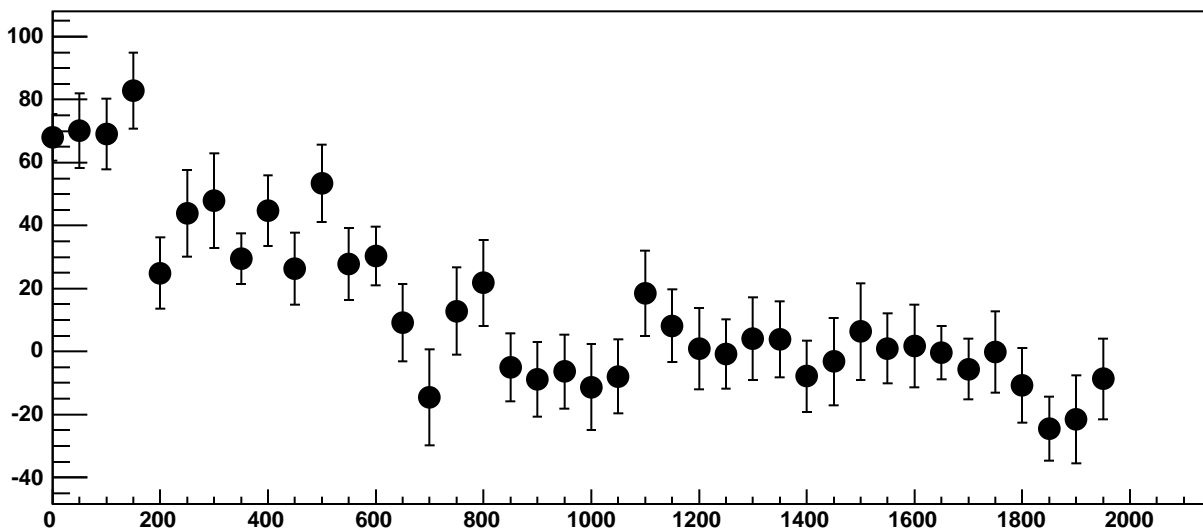


$\chi^2 / \text{ndf}$  7.788 / 11  
p0  $-1473 \pm 19.93$   
p1  $0.08293 \pm 0.0178$

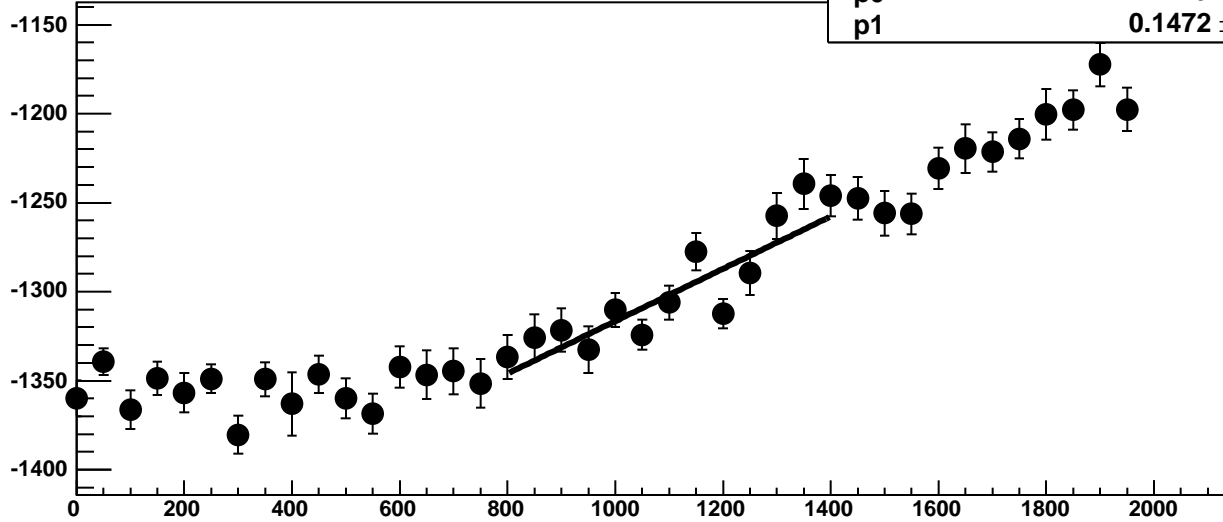
Chip 1, Channel 5, Enable 1, Hold=30, ADC Noise vs DAC



Chip 1, Channel 5, Enable 1, Hold=30, ADC Residuals vs DAC

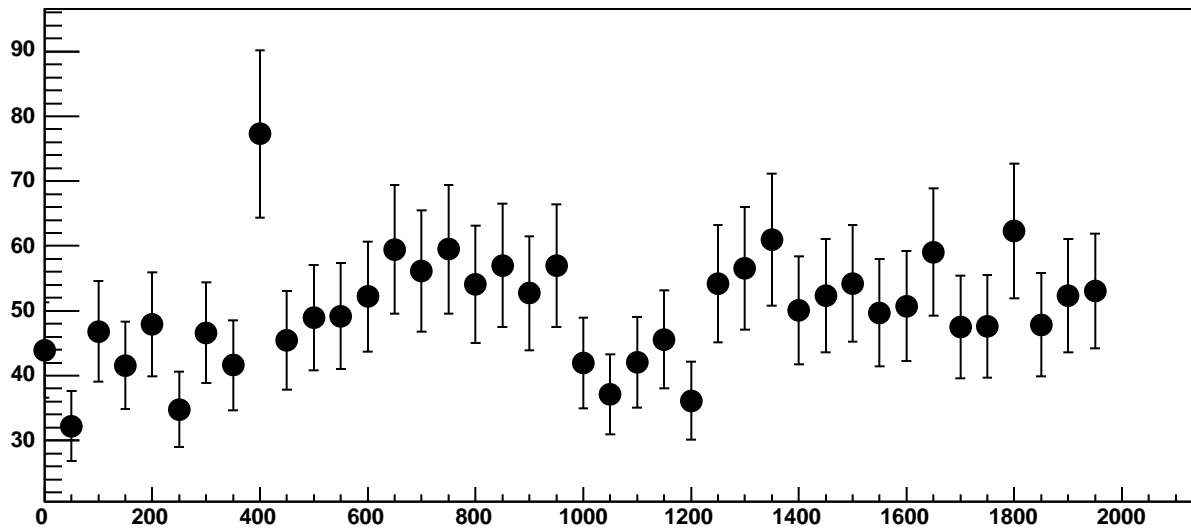


Chip 1, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC

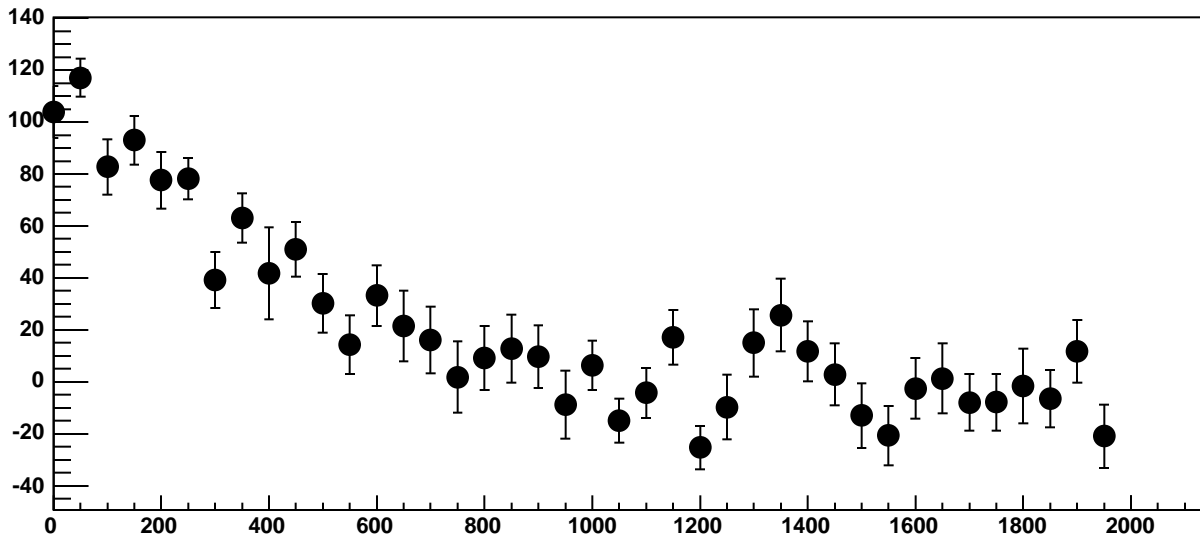


$\chi^2 / \text{ndf}$  24.67 / 11  
p0  $-1464 \pm 20.17$   
p1  $0.1472 \pm 0.0181$

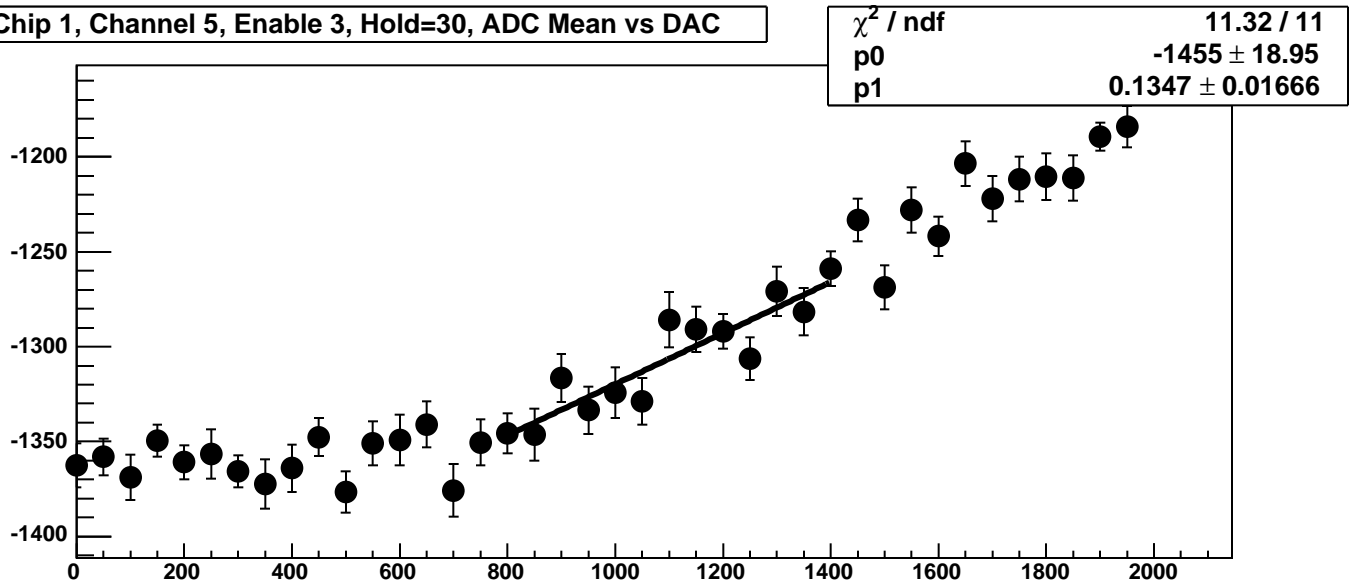
Chip 1, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC



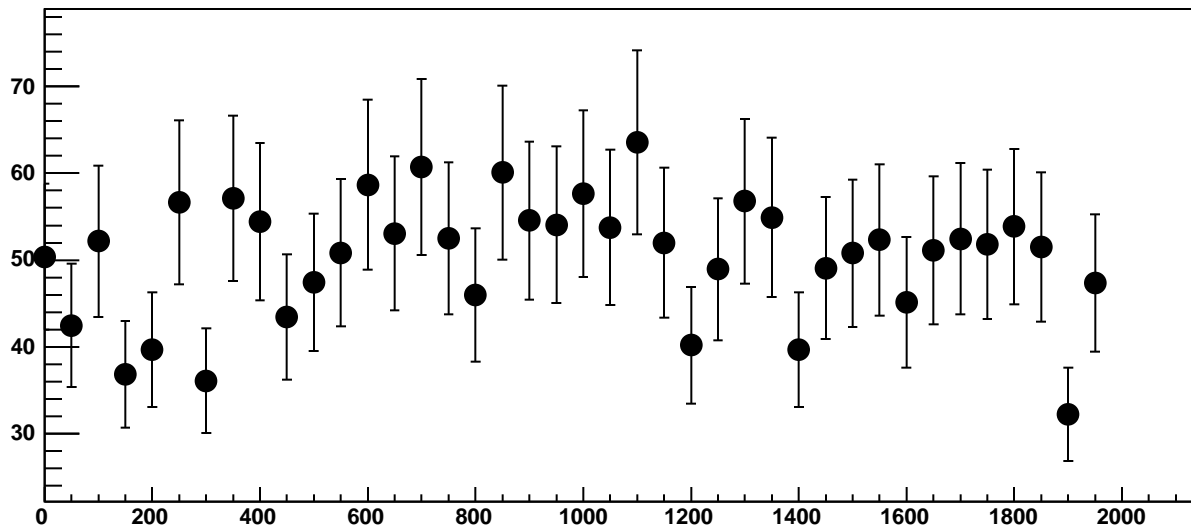
Chip 1, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC



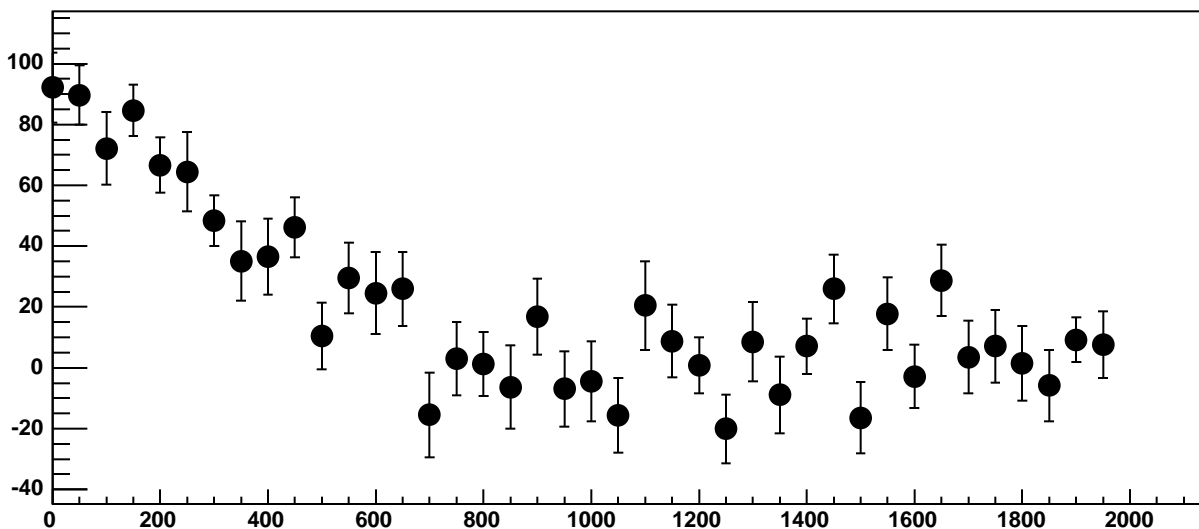
Chip 1, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC



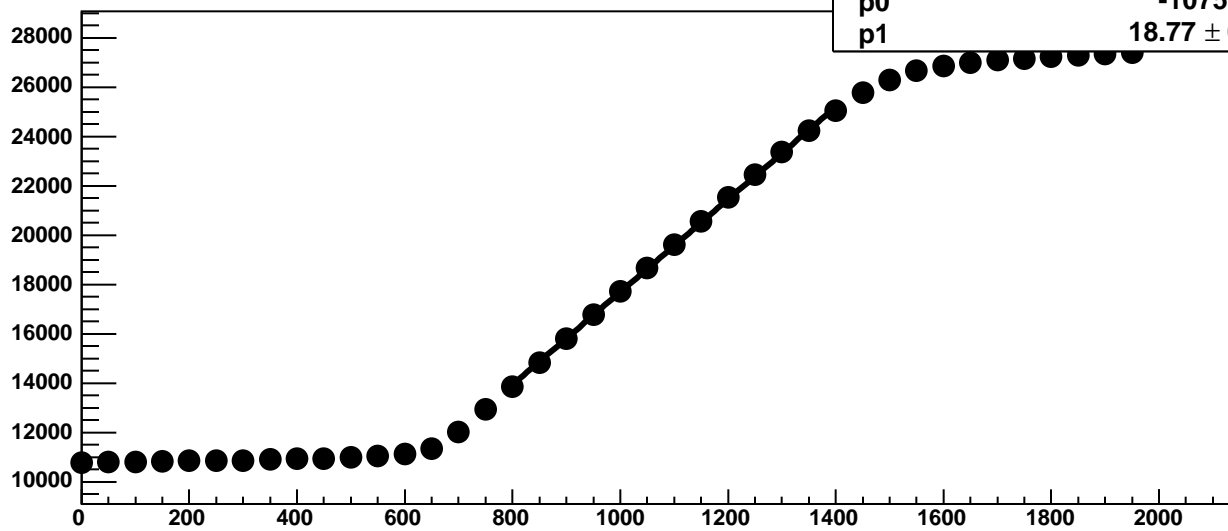
Chip 1, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC



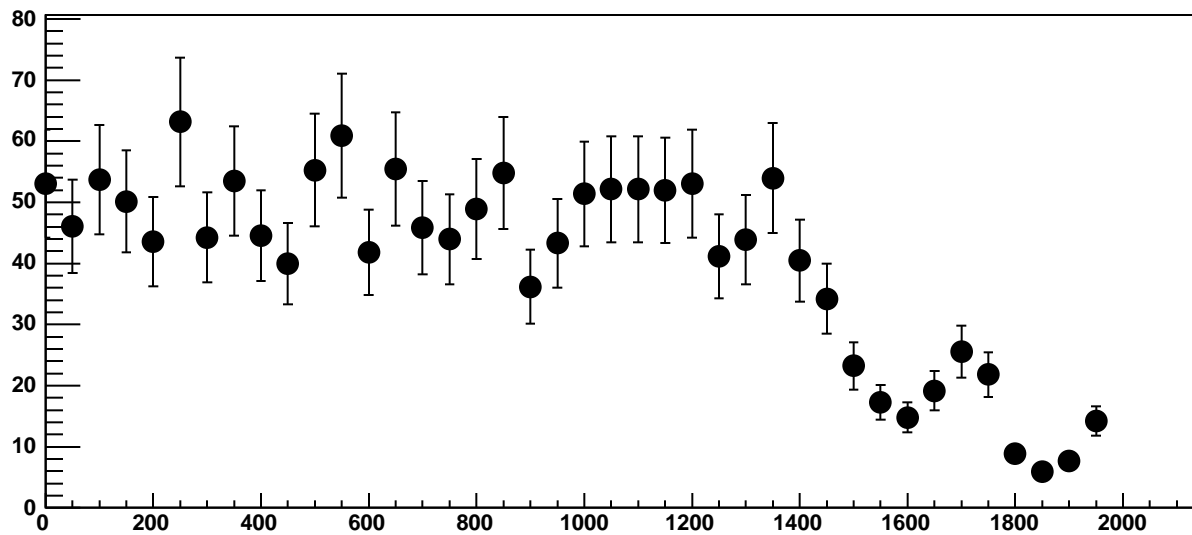
Chip 1, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC



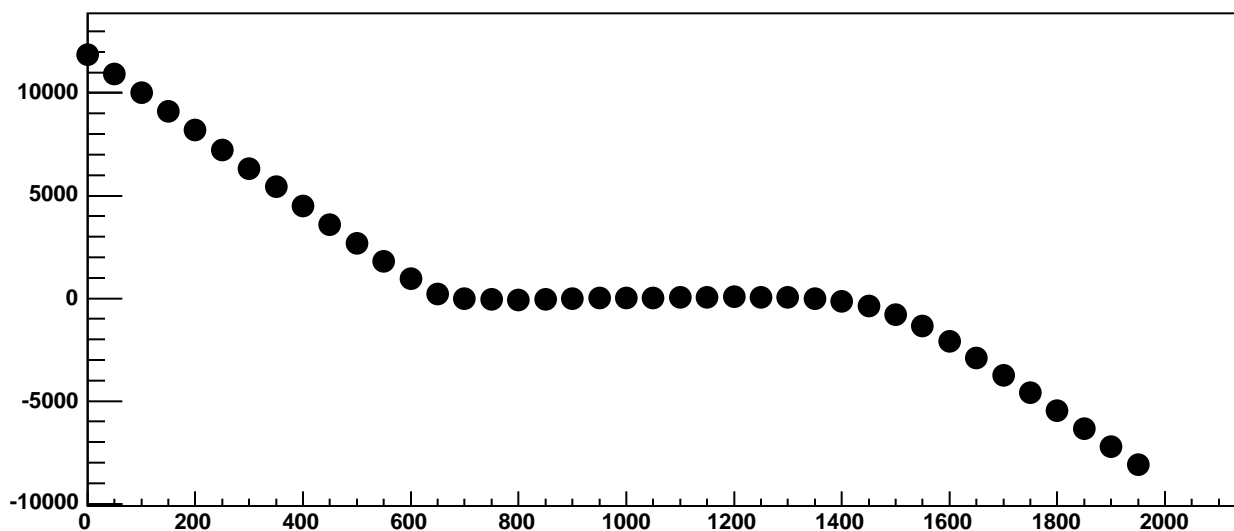
Chip 1, Channel 5, Enable 4!, Hold=30, ADC Mean vs DAC



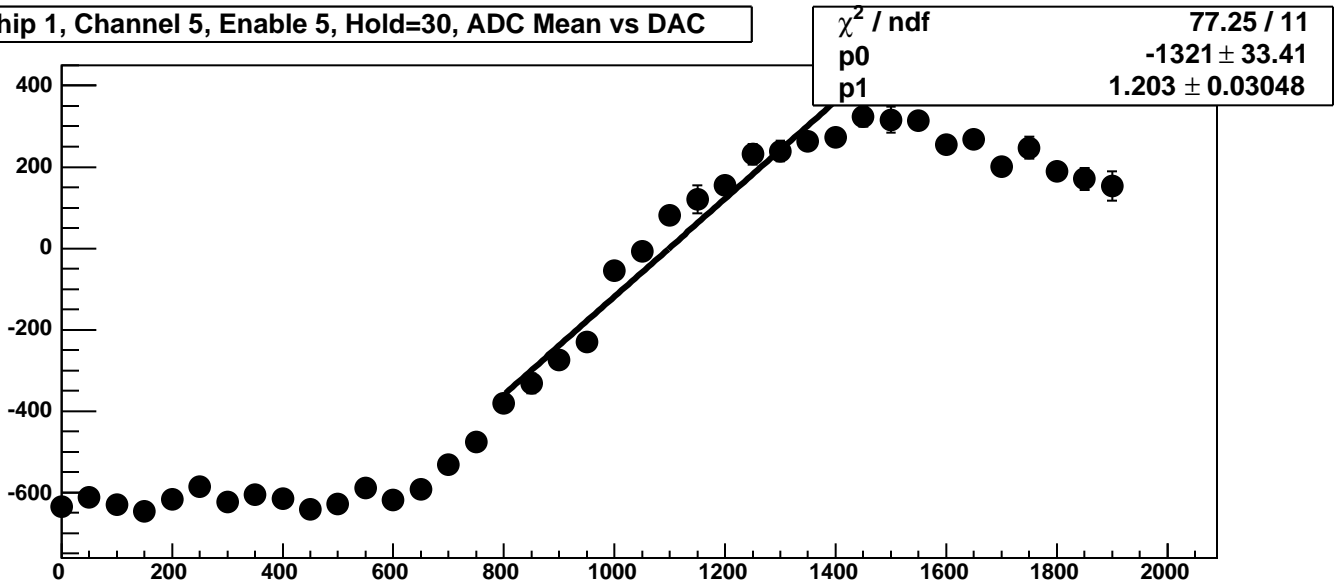
Chip 1, Channel 5, Enable 4!, Hold=30, ADC Noise vs DAC



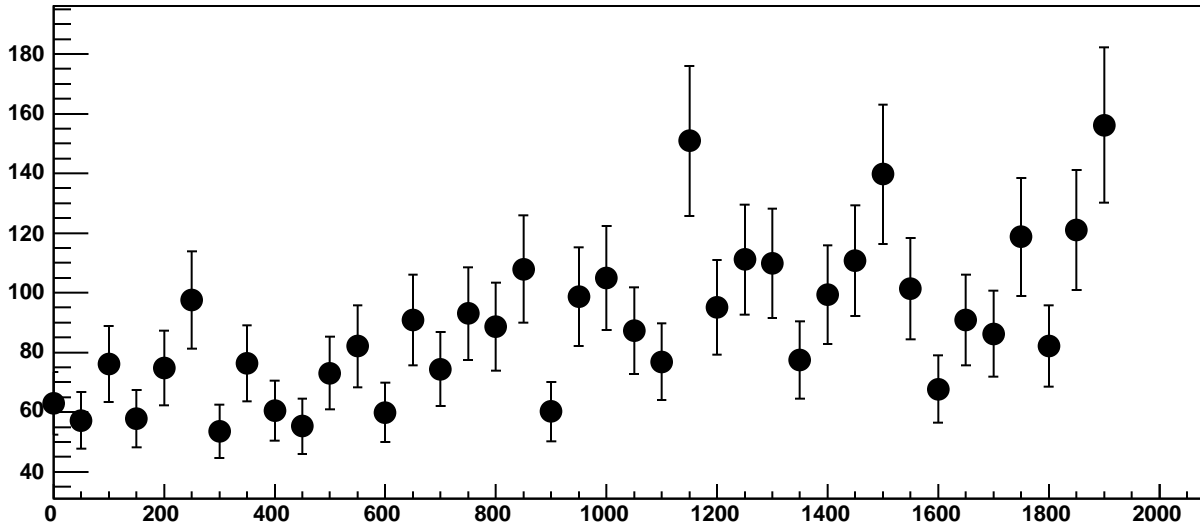
Chip 1, Channel 5, Enable 4!, Hold=30, ADC Residuals vs DAC



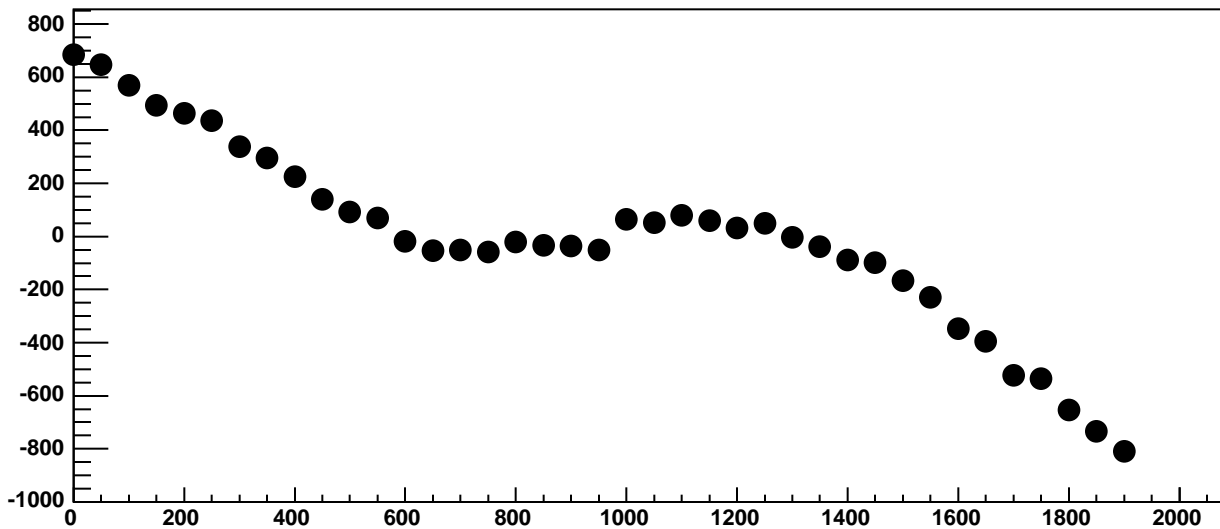
Chip 1, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC



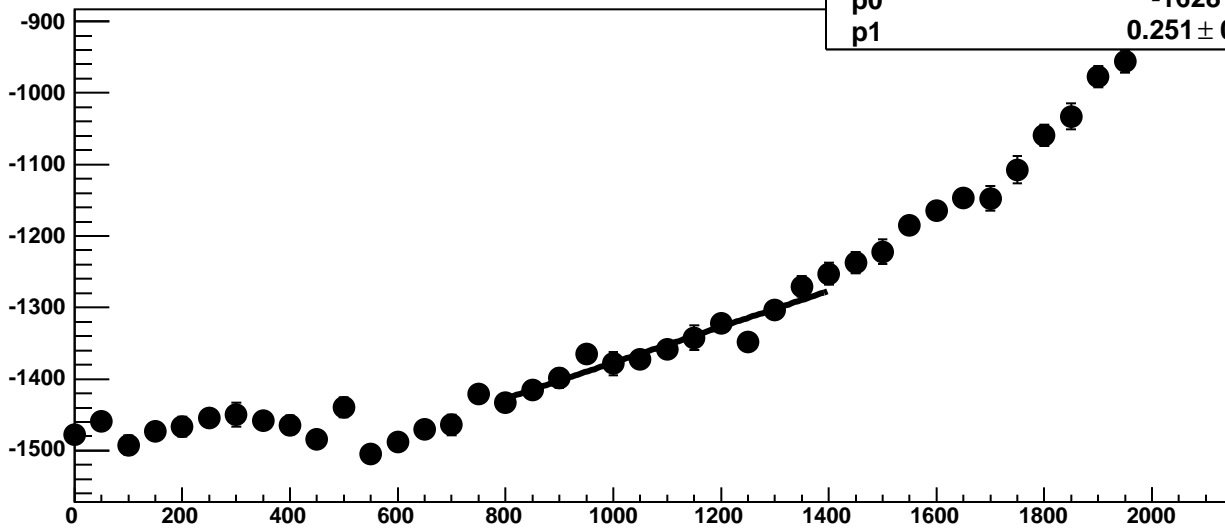
Chip 1, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC



Chip 1, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC

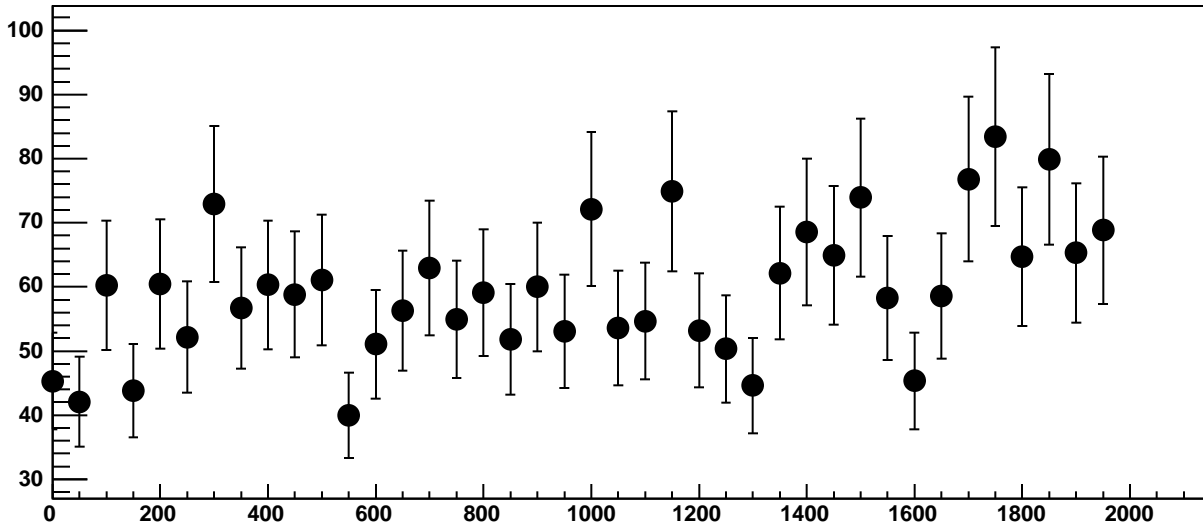


Chip 1, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC

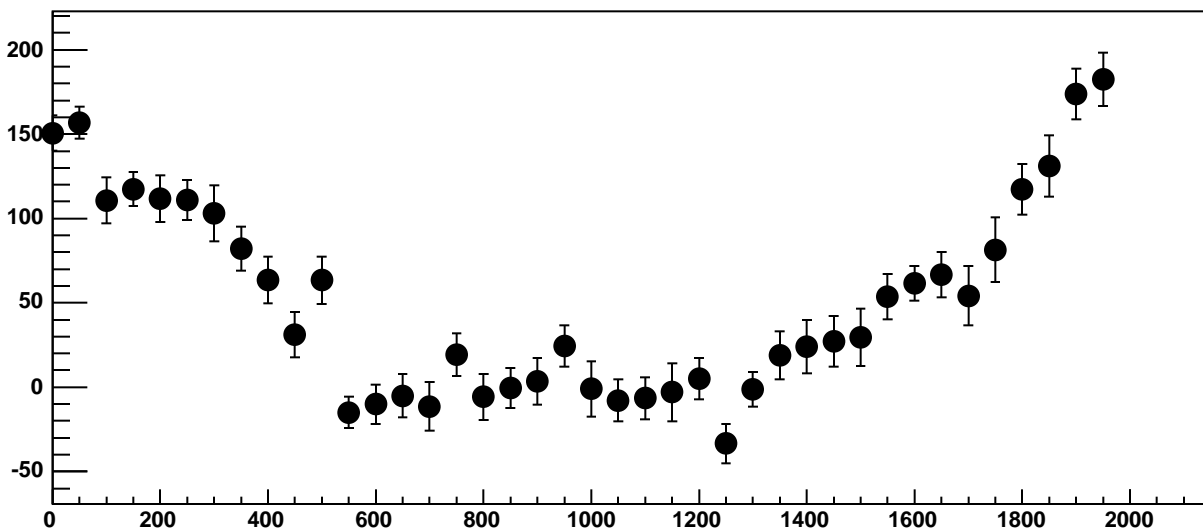


$\chi^2 / \text{ndf}$  17.6 / 11  
p0  $-1628 \pm 21.73$   
p1  $0.251 \pm 0.01945$

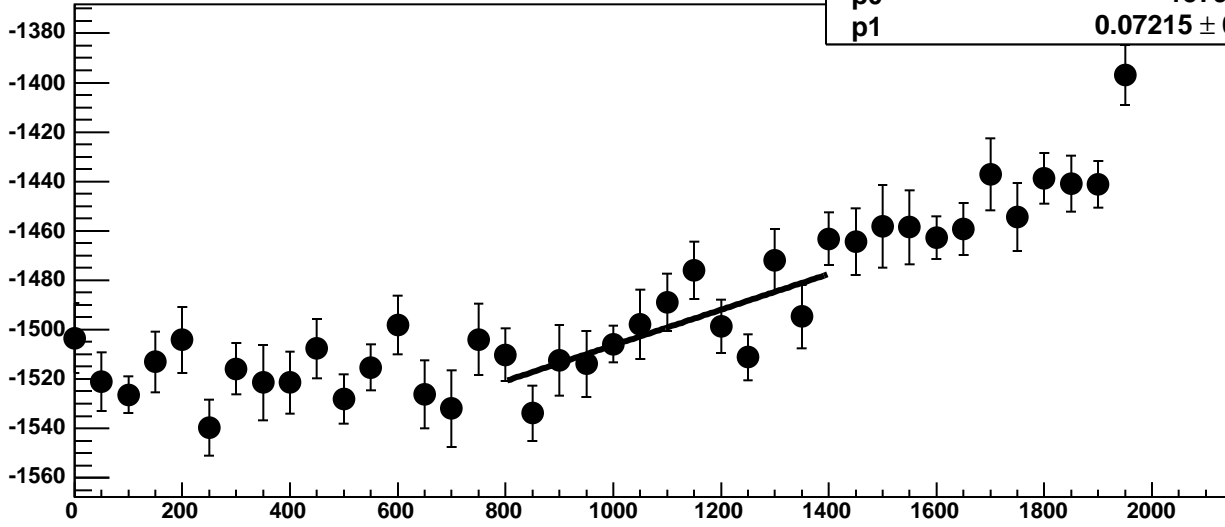
Chip 1, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC



Chip 1, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 1, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

17.5 / 11

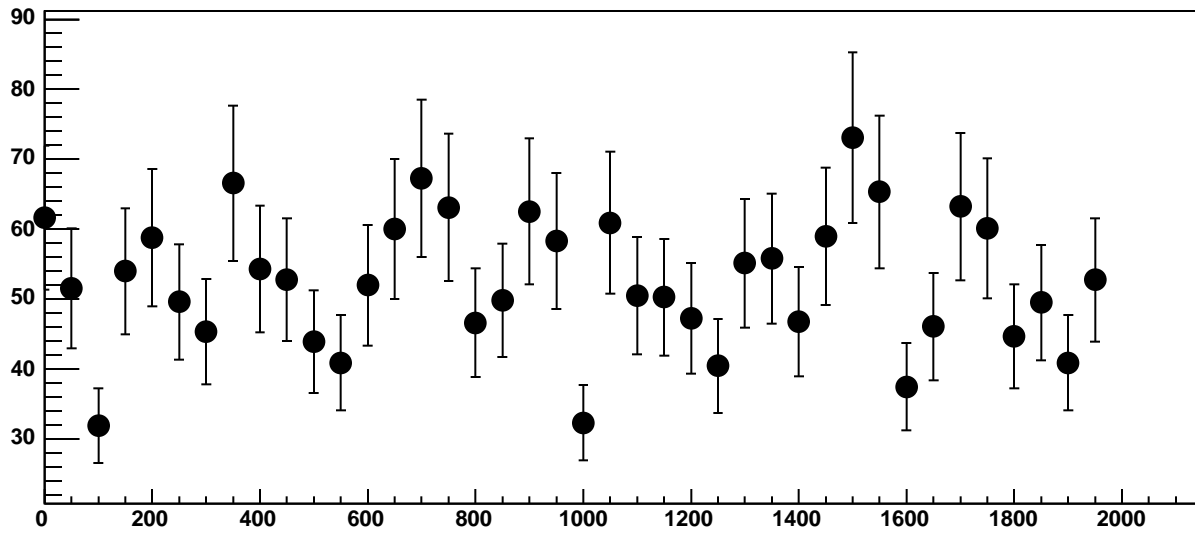
p0

$-1579 \pm 18.6$

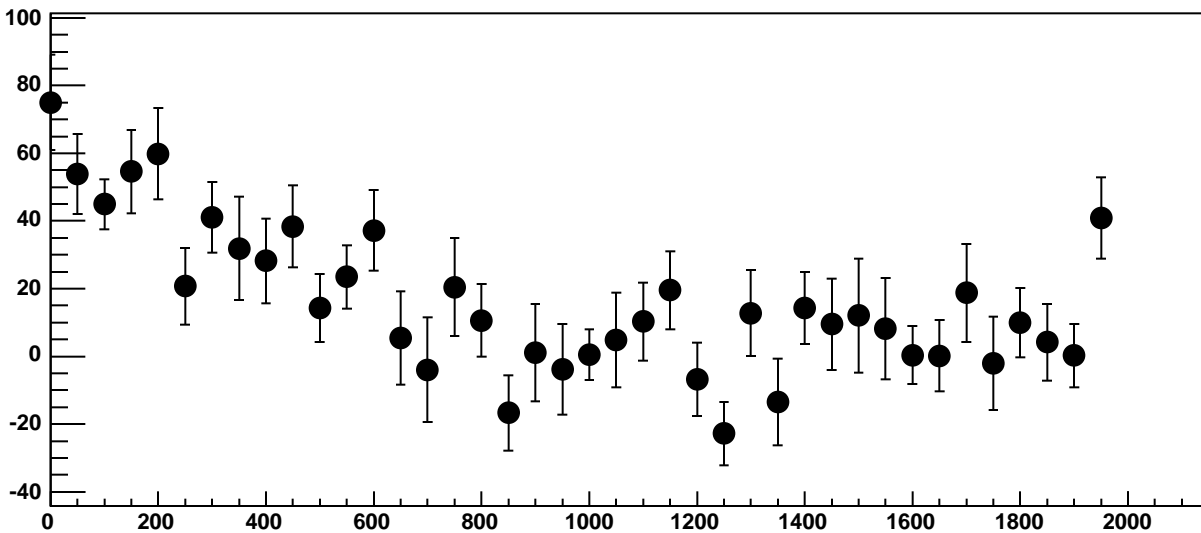
p1

$0.07215 \pm 0.01671$

Chip 1, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC

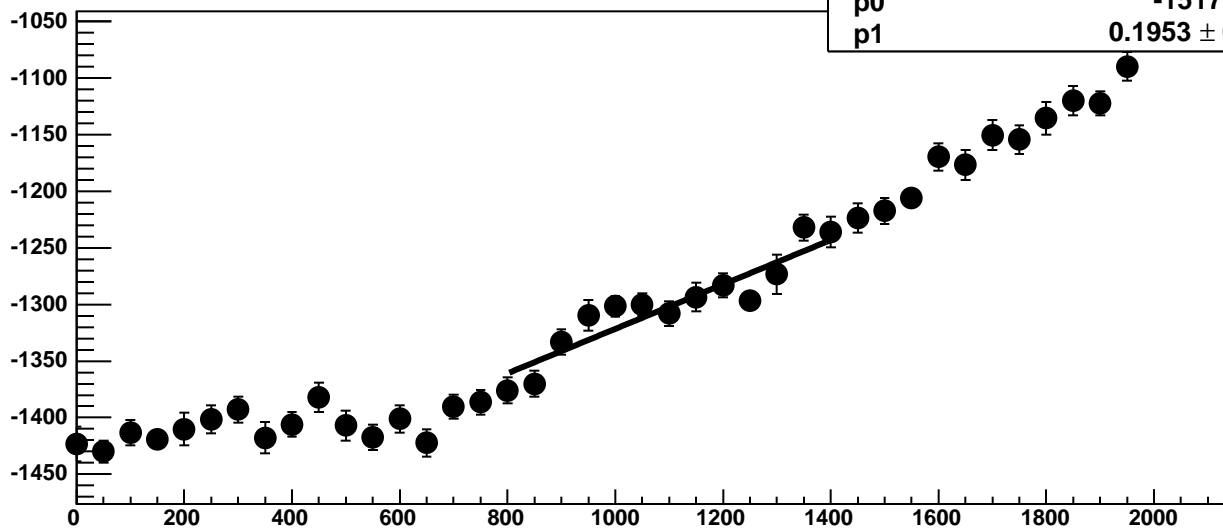


Chip 1, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC

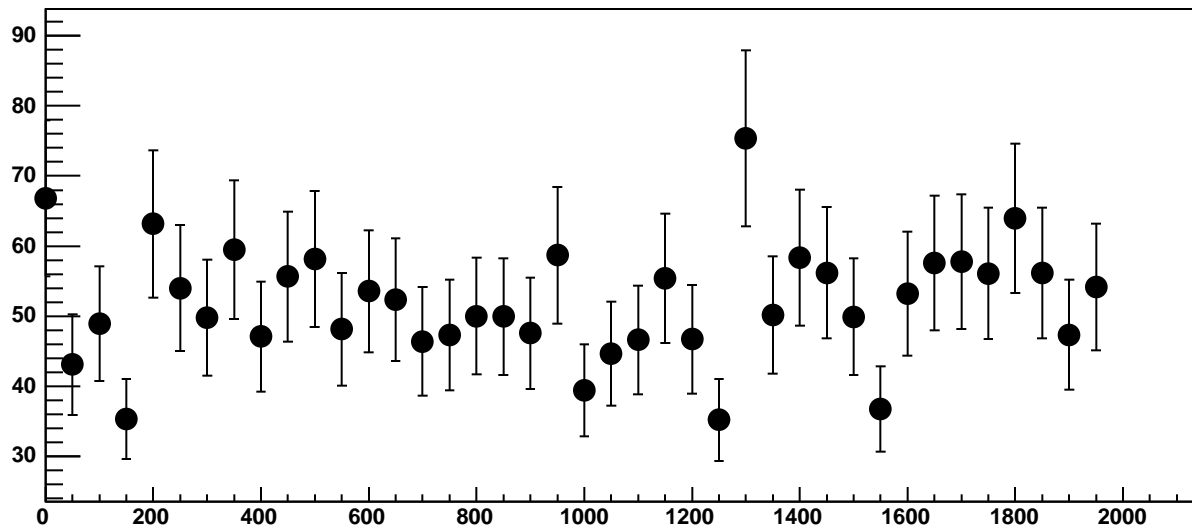




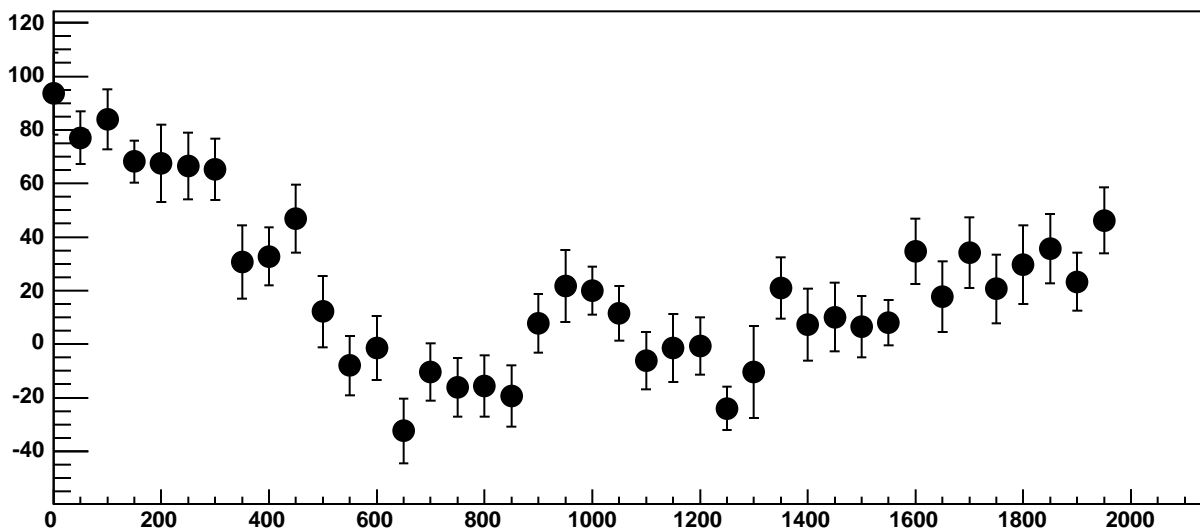
Chip 1, Channel 6, Enable 2, Hold=30, ADC Mean vs DAC



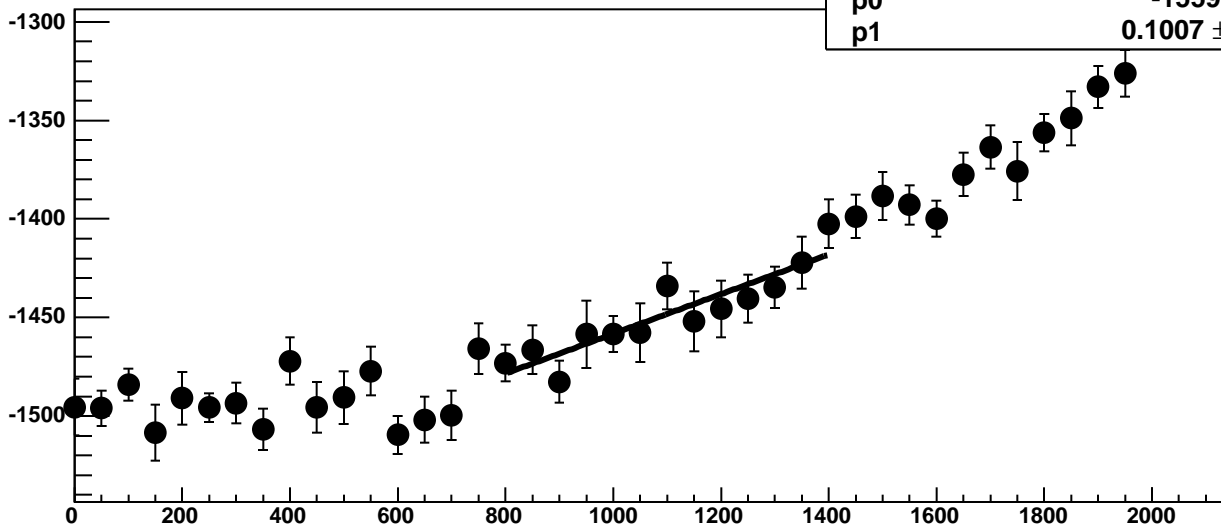
Chip 1, Channel 6, Enable 2, Hold=30, ADC Noise vs DAC



Chip 1, Channel 6, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 1, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

7.033 / 11

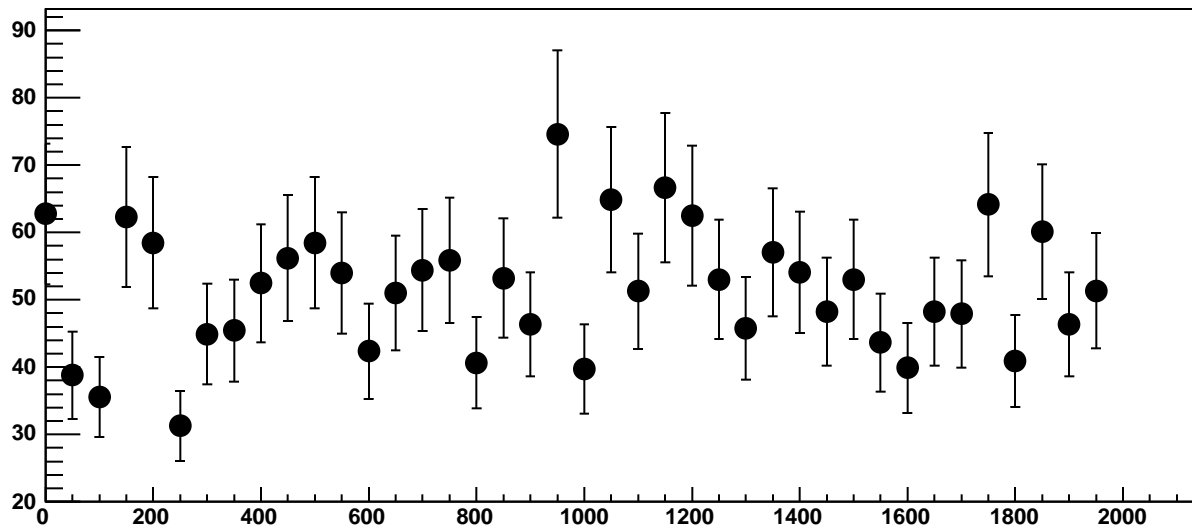
p0

$-1559 \pm 18.54$

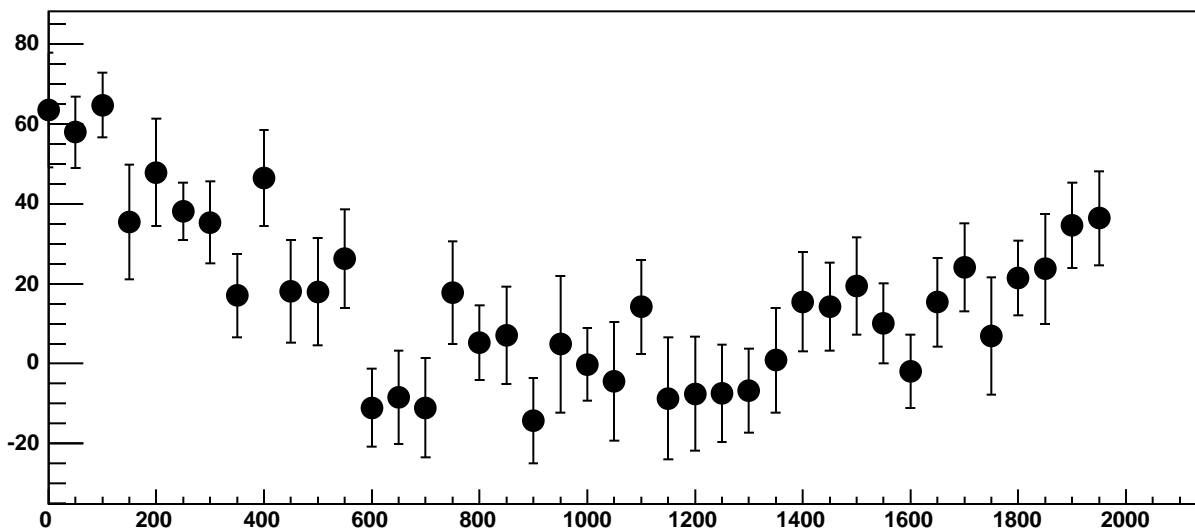
p1

$0.1007 \pm 0.0169$

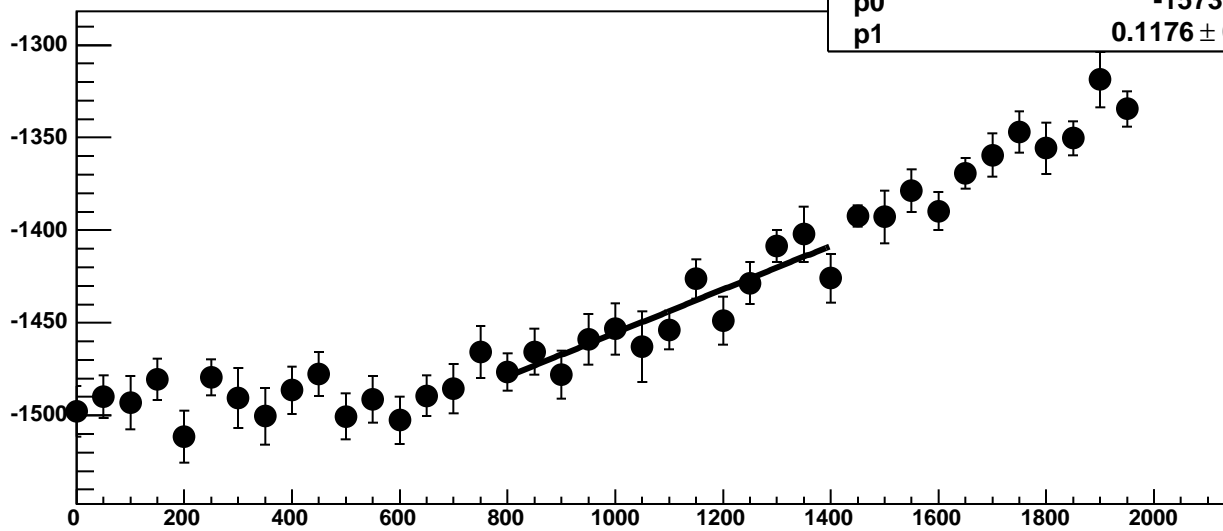
Chip 1, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC



Chip 1, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC

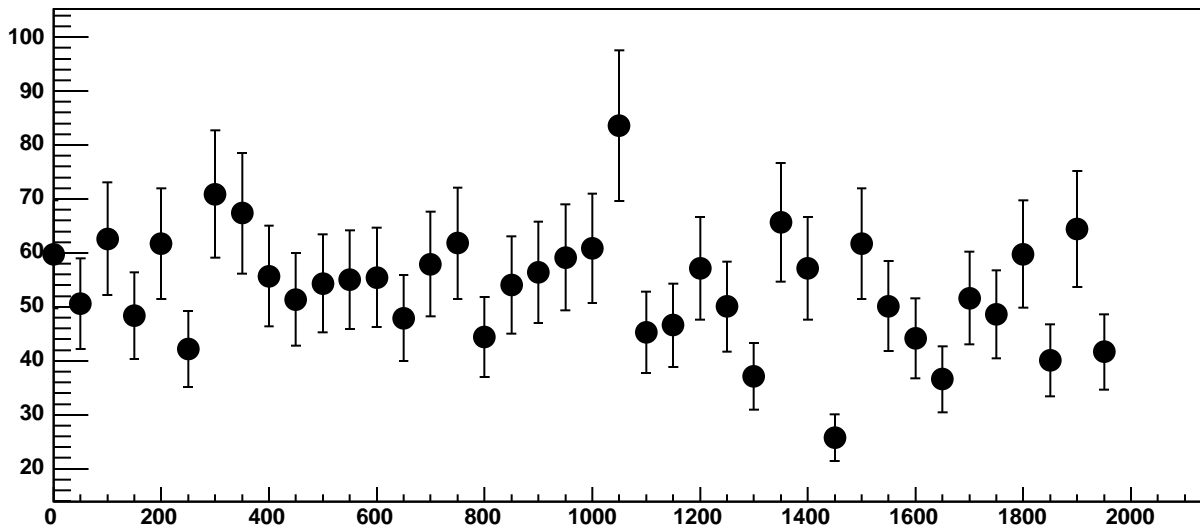


Chip 1, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC

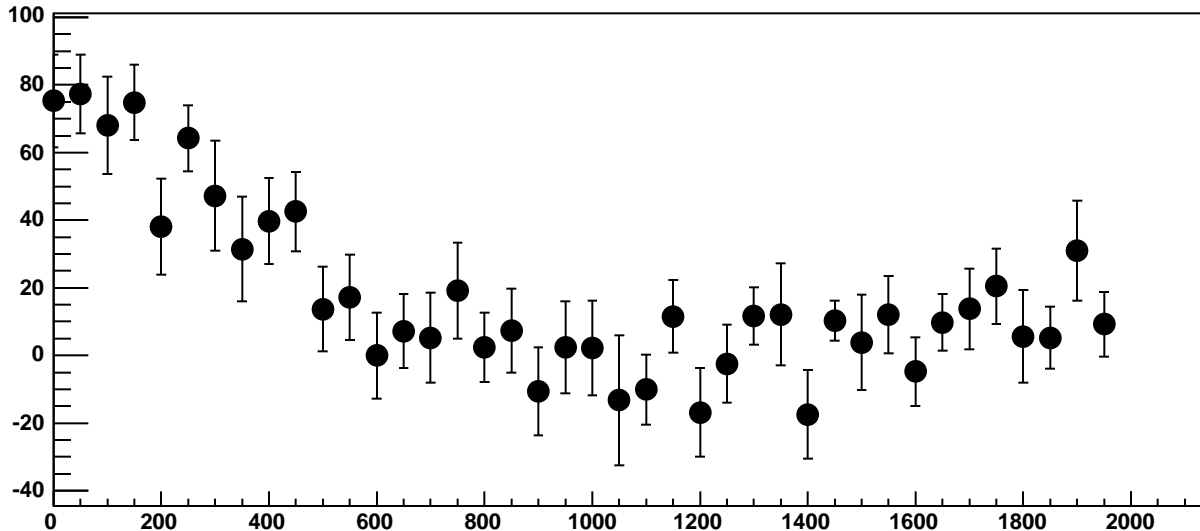


$\chi^2 / \text{ndf}$  9.709 / 11  
p0  $-1573 \pm 19.57$   
p1  $0.1176 \pm 0.01743$

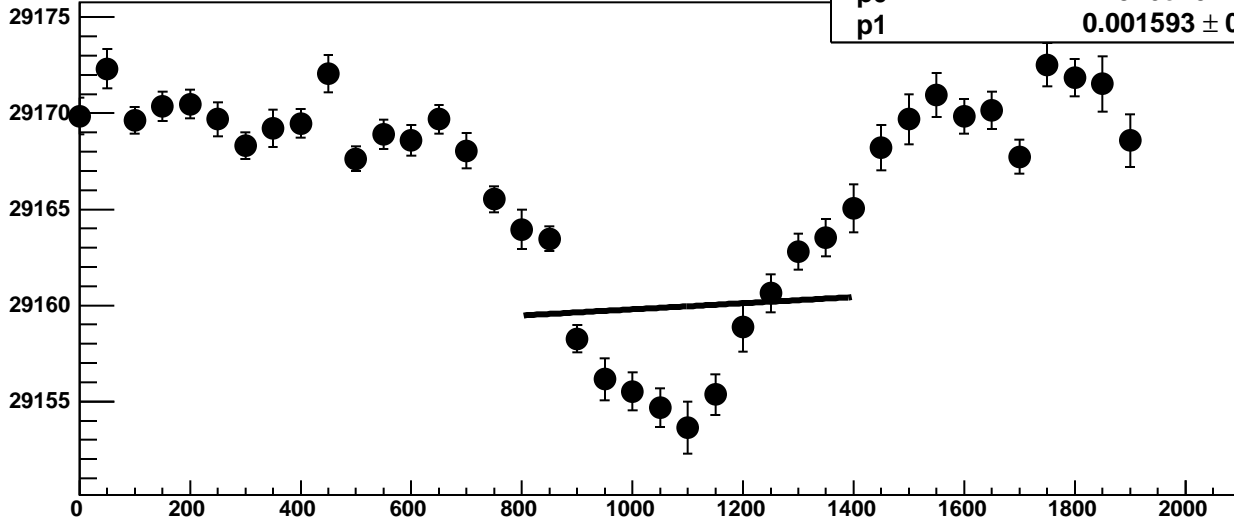
Chip 1, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC



Chip 1, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC

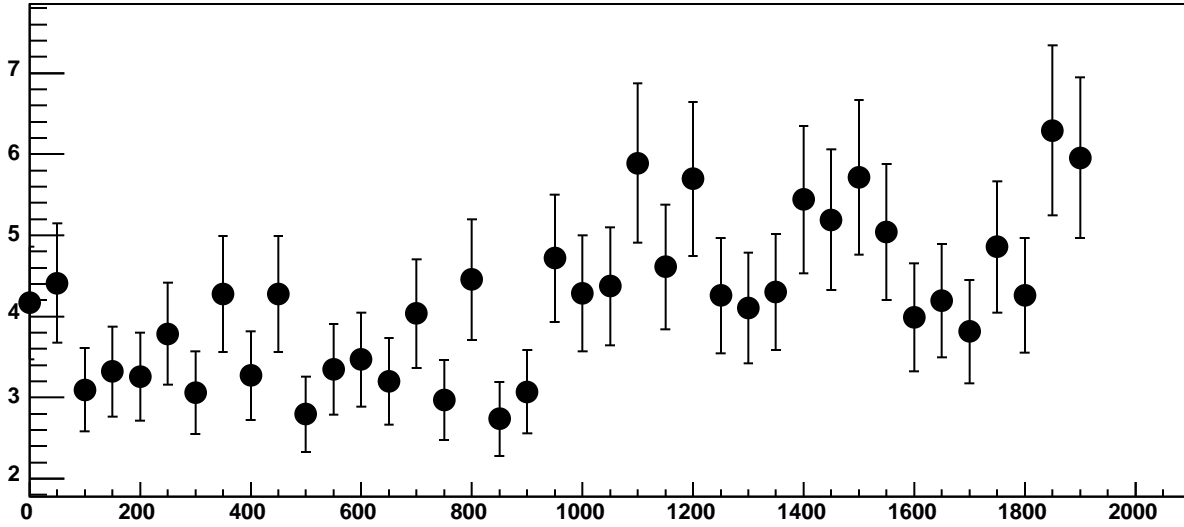


Chip 1, Channel 6, Enable 5!, Hold=30, ADC Mean vs DAC

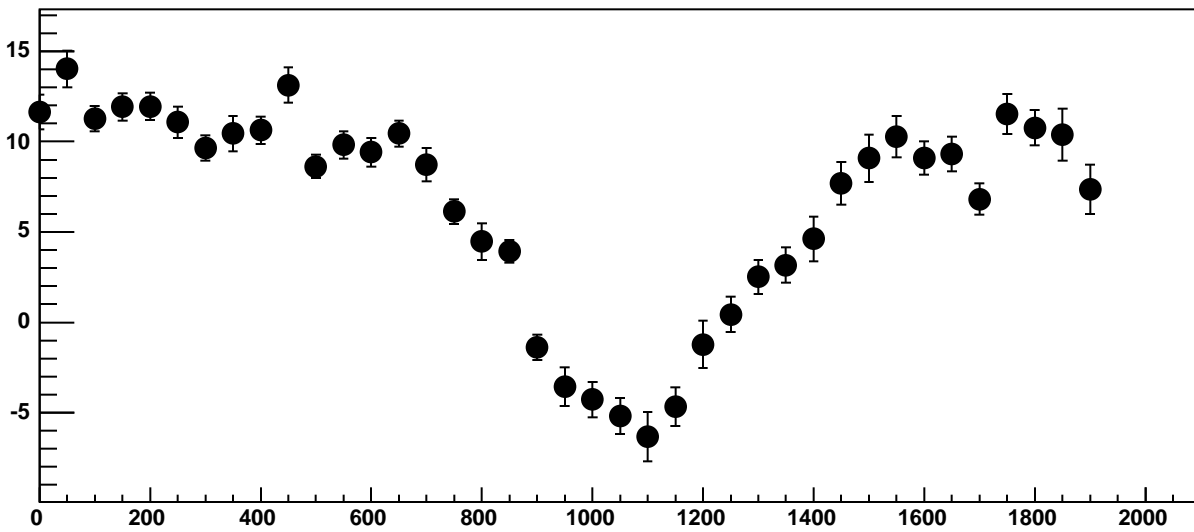


$\chi^2 / \text{ndf}$  192 / 11  
p0  $2.916\text{e}+04 \pm 1.477$   
p1  $0.001593 \pm 0.00138$

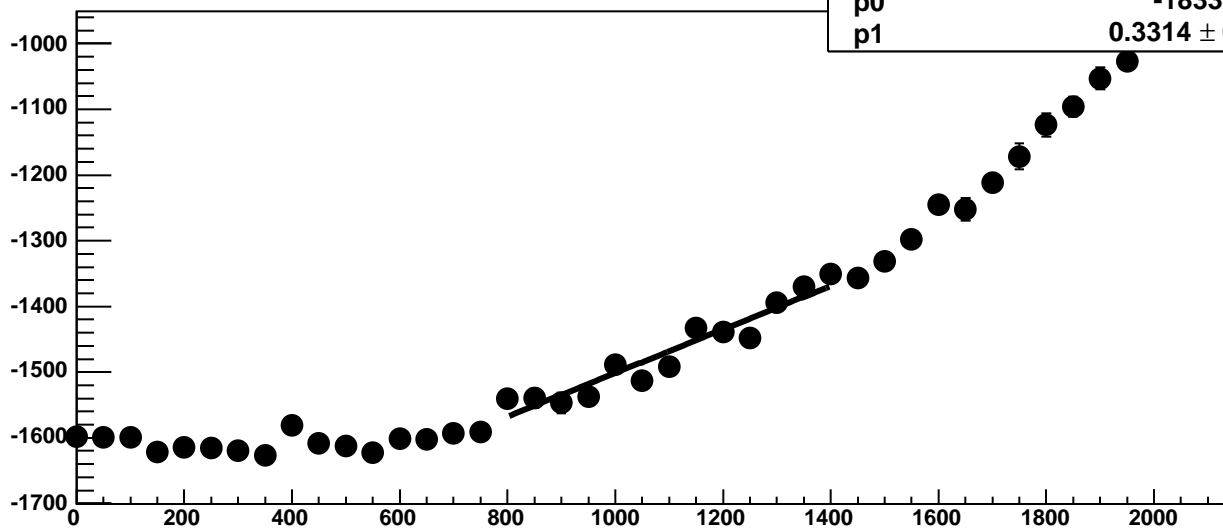
Chip 1, Channel 6, Enable 5!, Hold=30, ADC Noise vs DAC



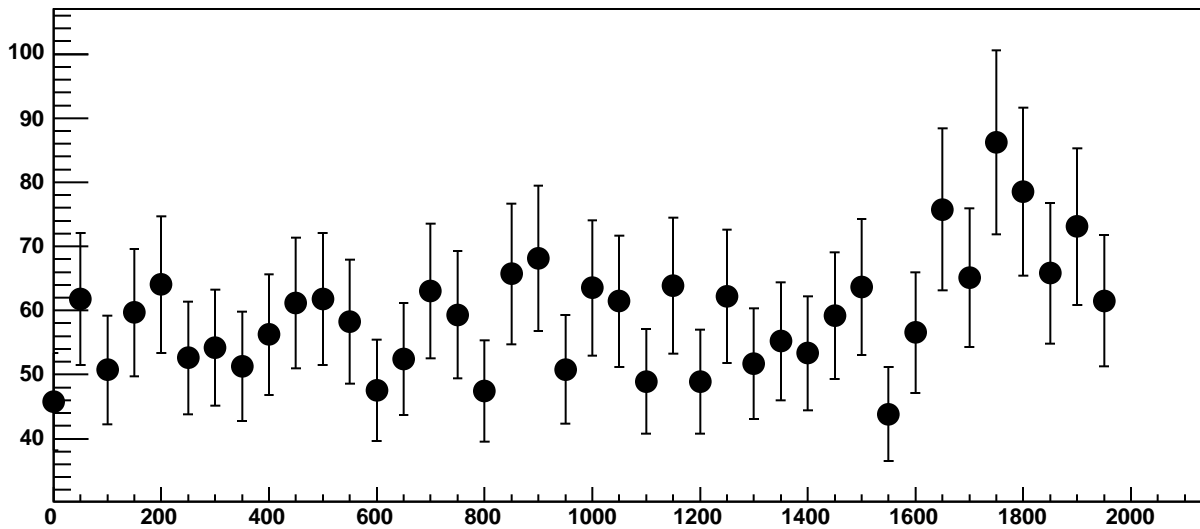
Chip 1, Channel 6, Enable 5!, Hold=30, ADC Residuals vs DAC



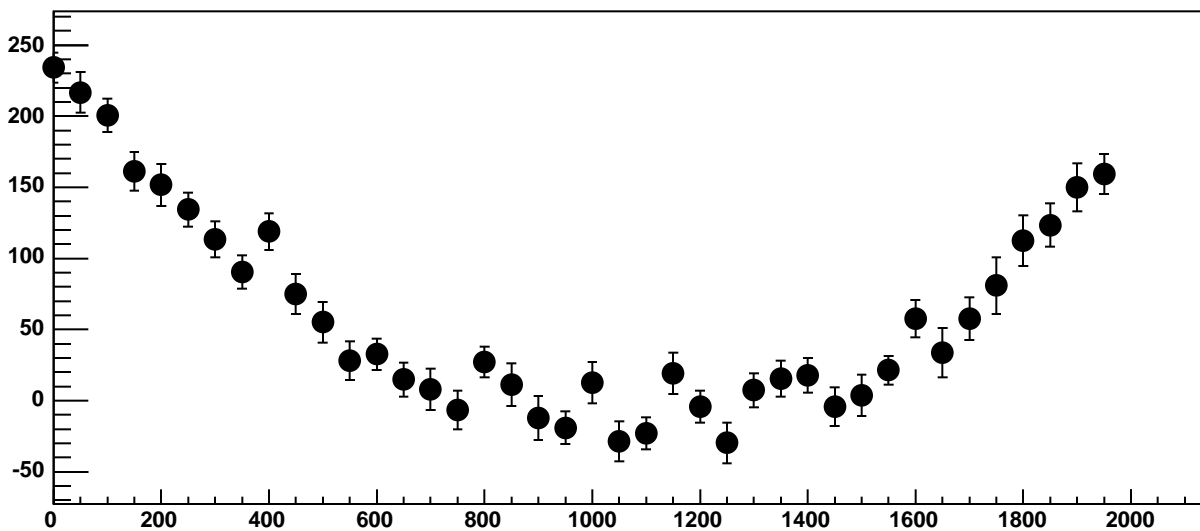
Chip 1, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC



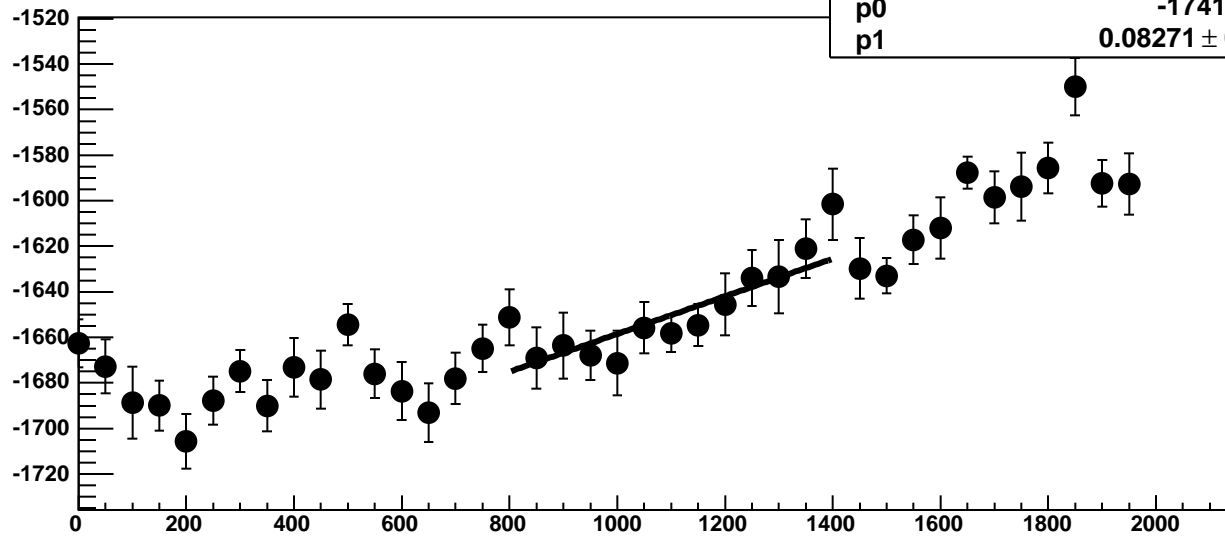
Chip 1, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC



Chip 1, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 1, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

9.58 / 11

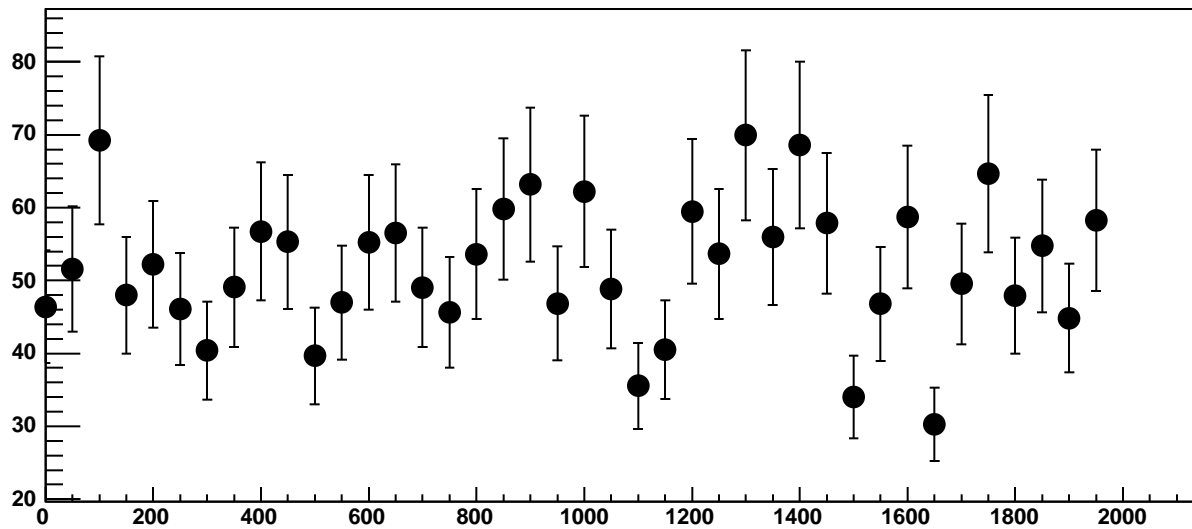
p0

$-1741 \pm 21.98$

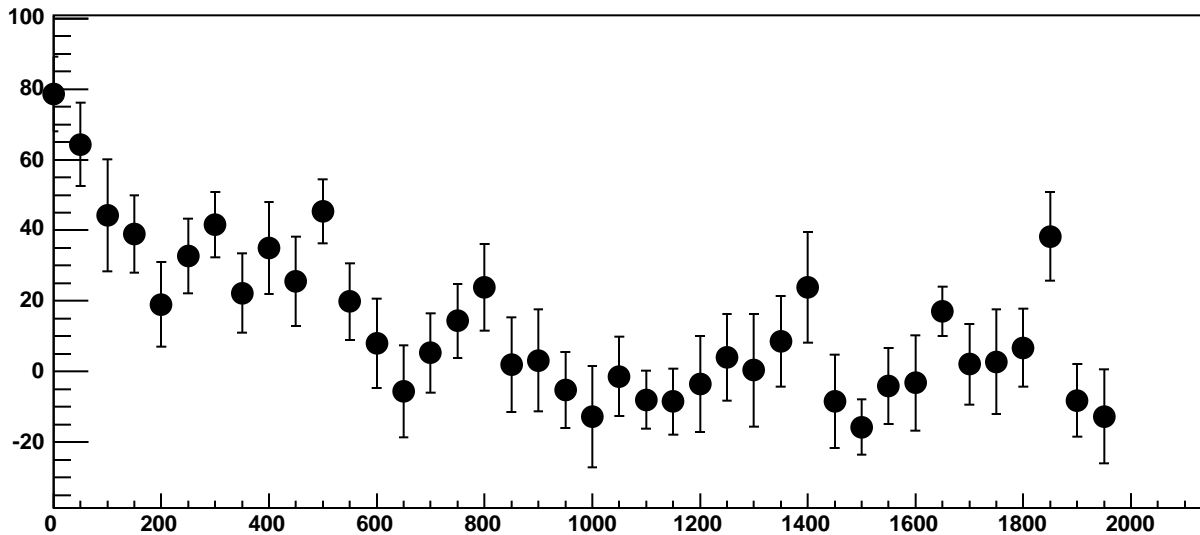
p1

$0.08271 \pm 0.01993$

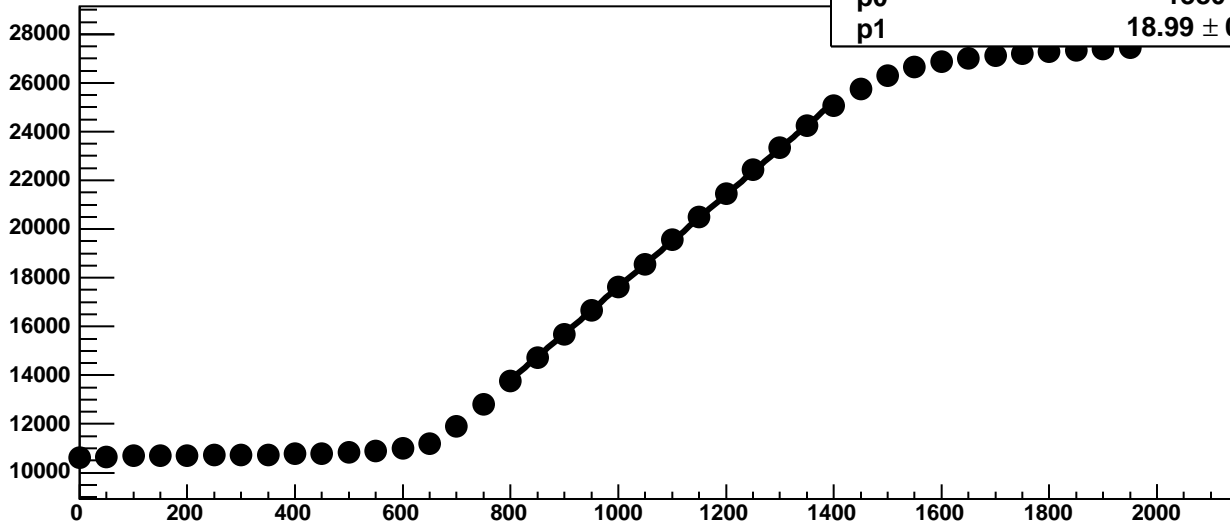
Chip 1, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC



Chip 1, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC

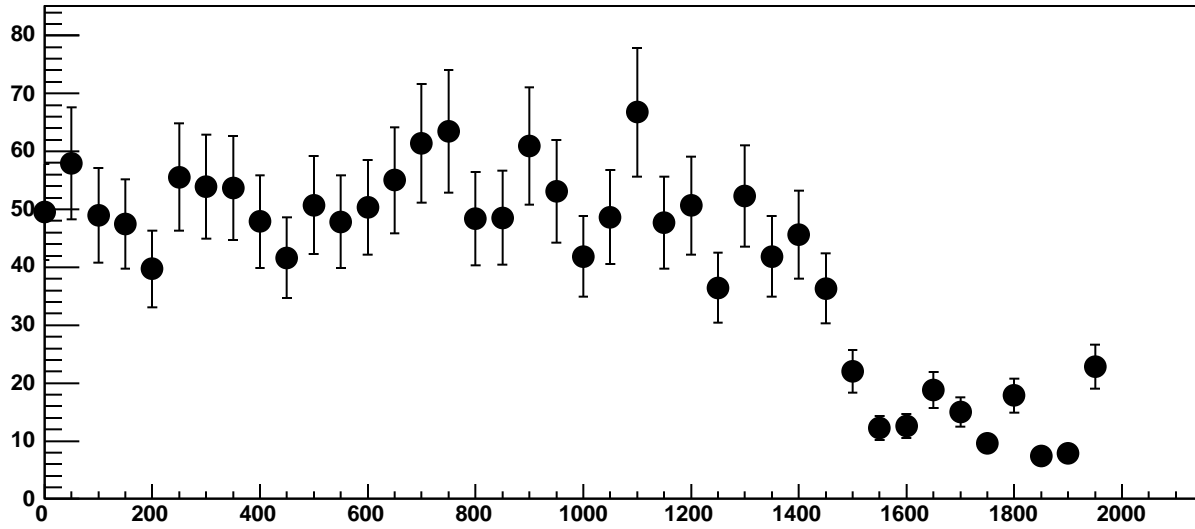


Chip 1, Channel 7, Enable 2!, Hold=30, ADC Mean vs DAC

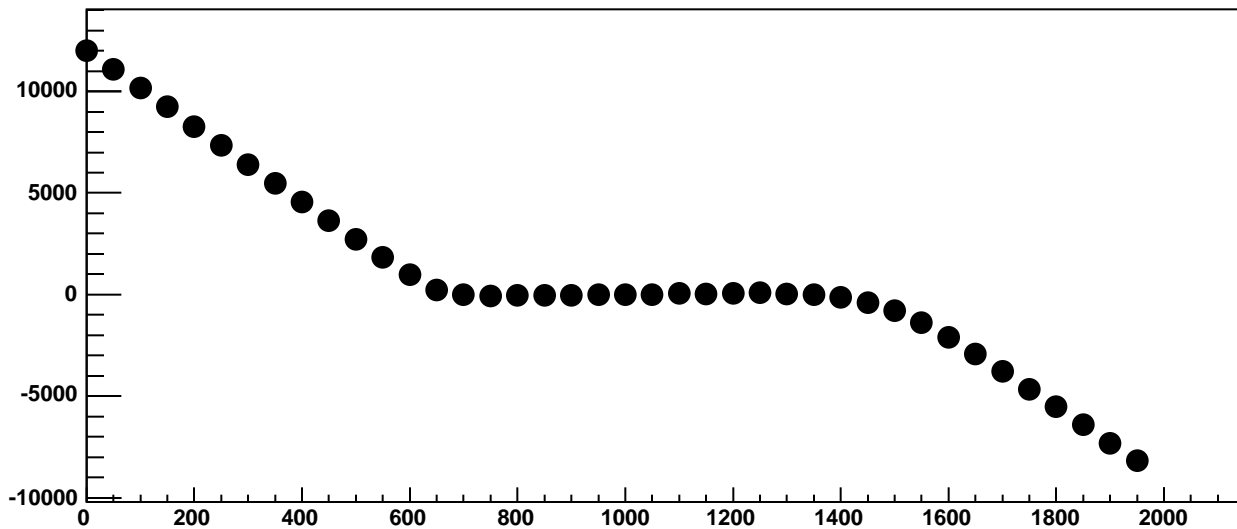


$\chi^2 / \text{ndf}$	383.7 / 11
p0	-1380 ± 18.26
p1	18.99 ± 0.01608

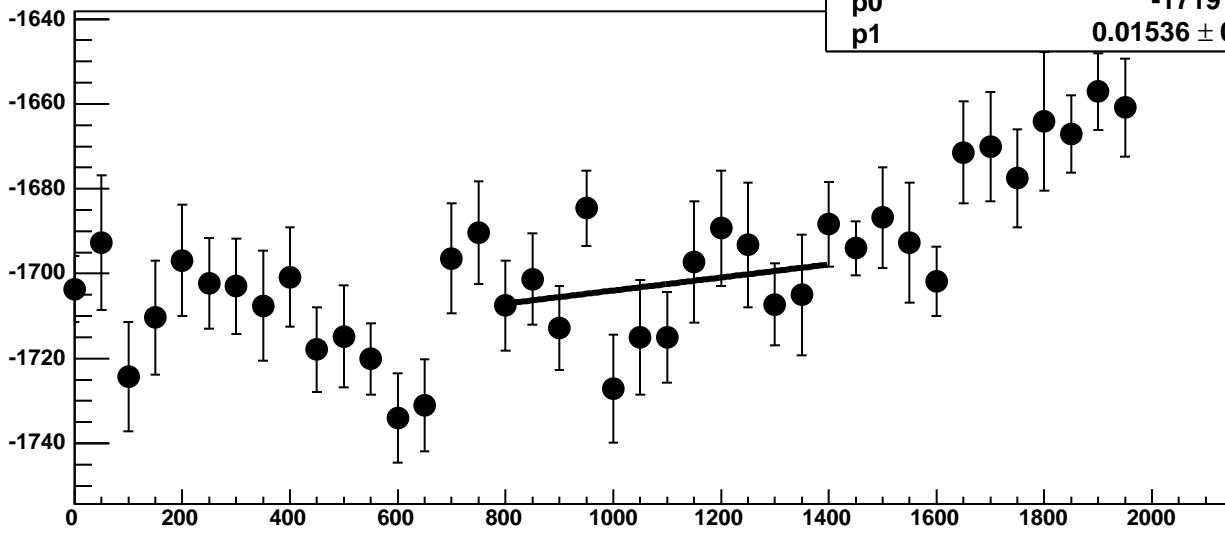
Chip 1, Channel 7, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 1, Channel 7, Enable 2!, Hold=30, ADC Residuals vs DAC

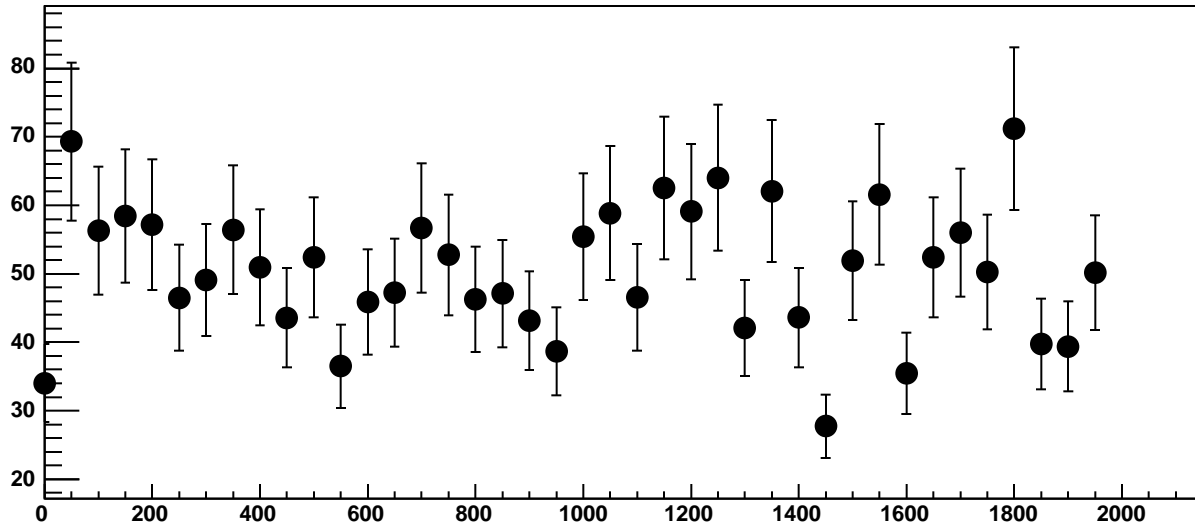


Chip 1, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC

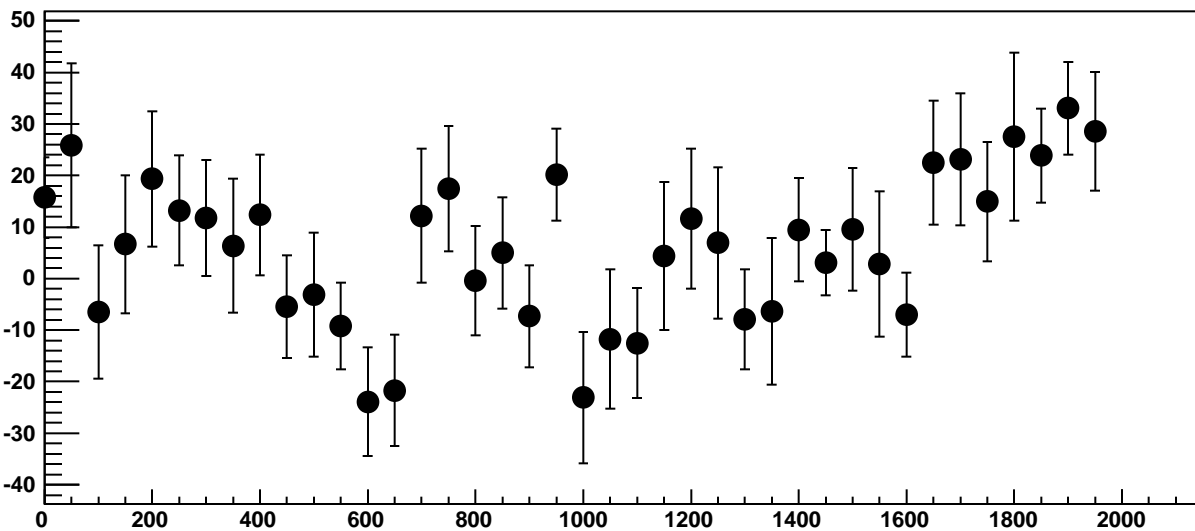


$\chi^2 / \text{ndf}$  14.15 / 11  
p0  $-1719 \pm 17.66$   
p1  $0.01536 \pm 0.01606$

Chip 1, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC

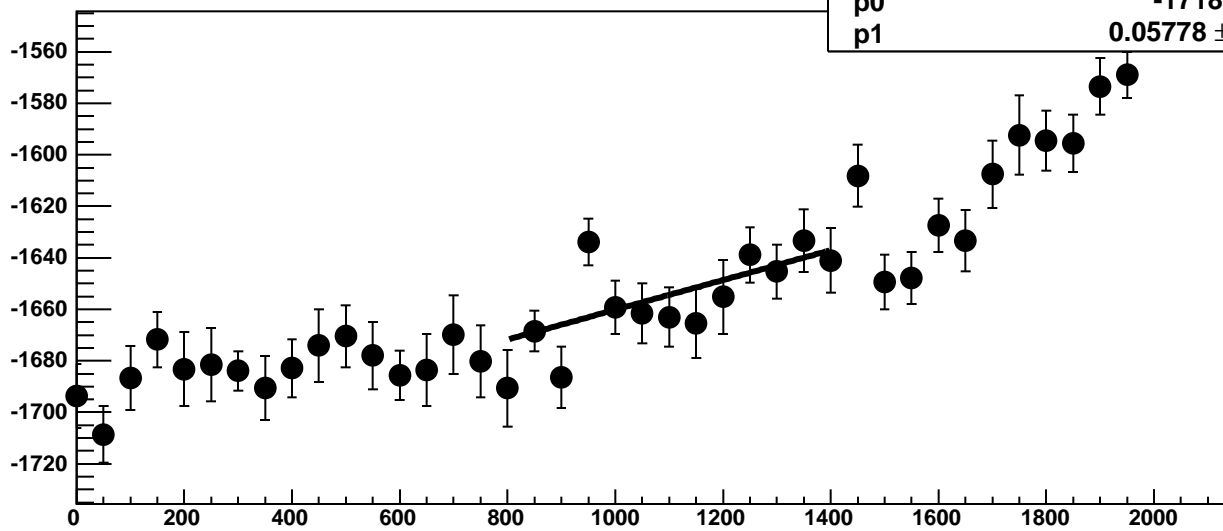


Chip 1, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC





Chip 1, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

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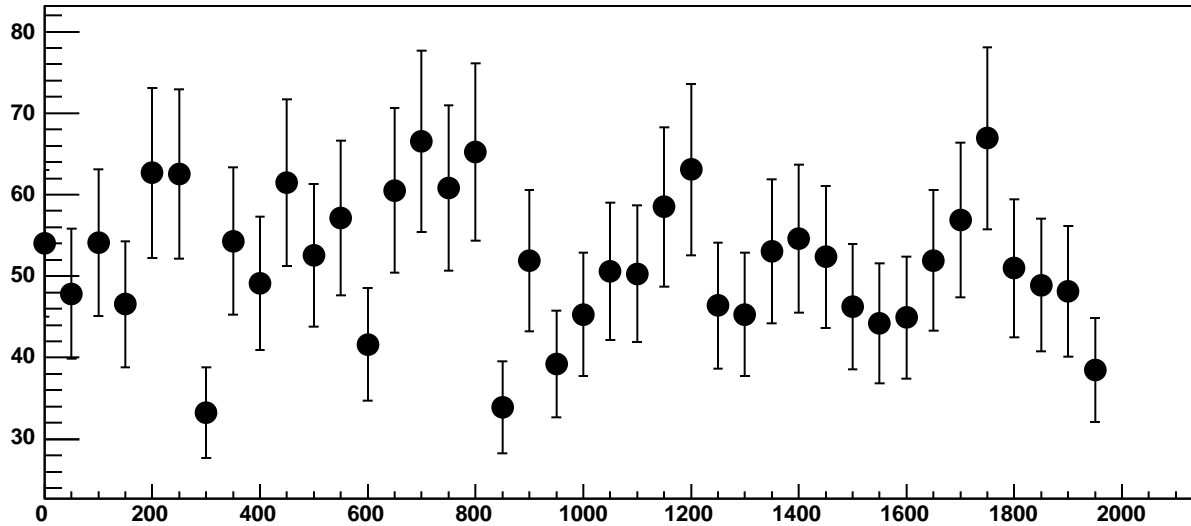
p0

$-1718 \pm 18.02$

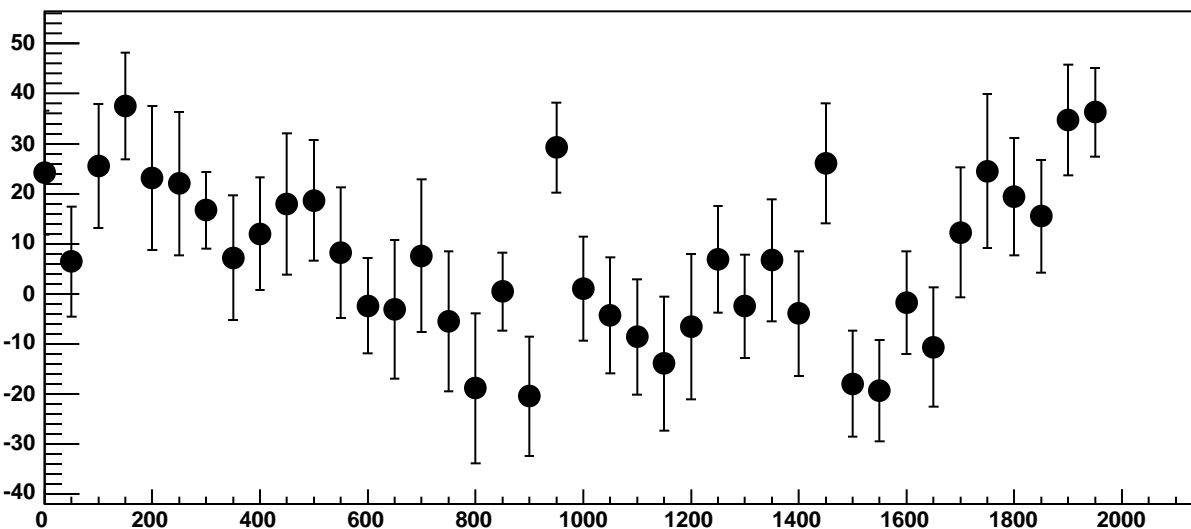
p1

$0.05778 \pm 0.0165$

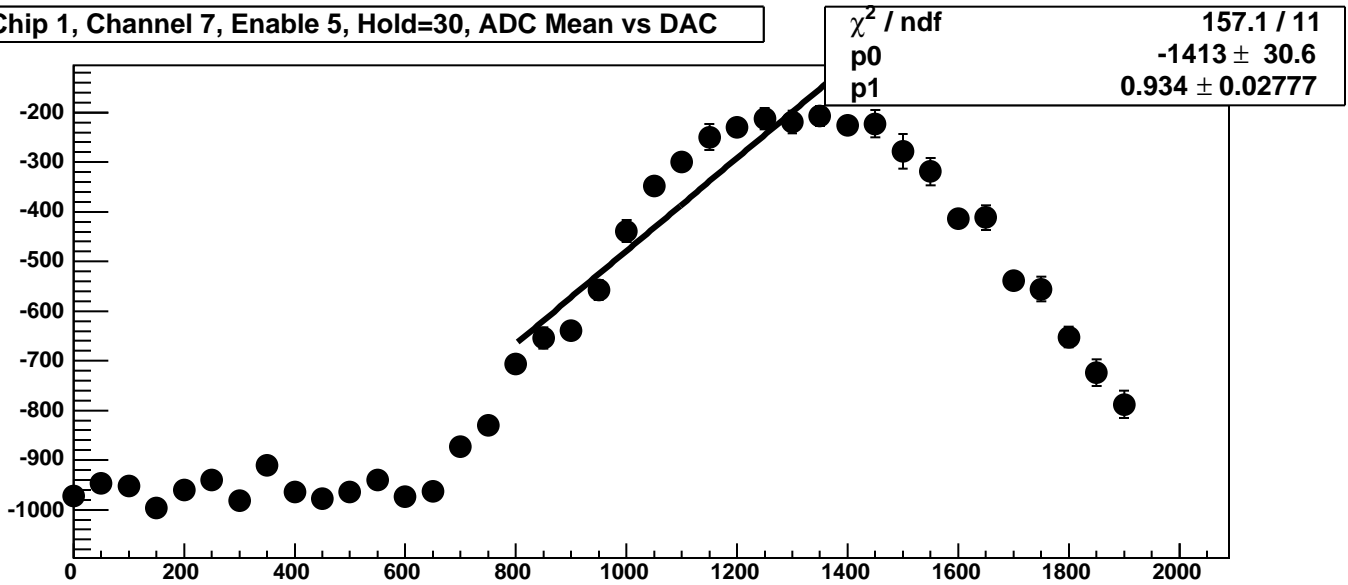
Chip 1, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC



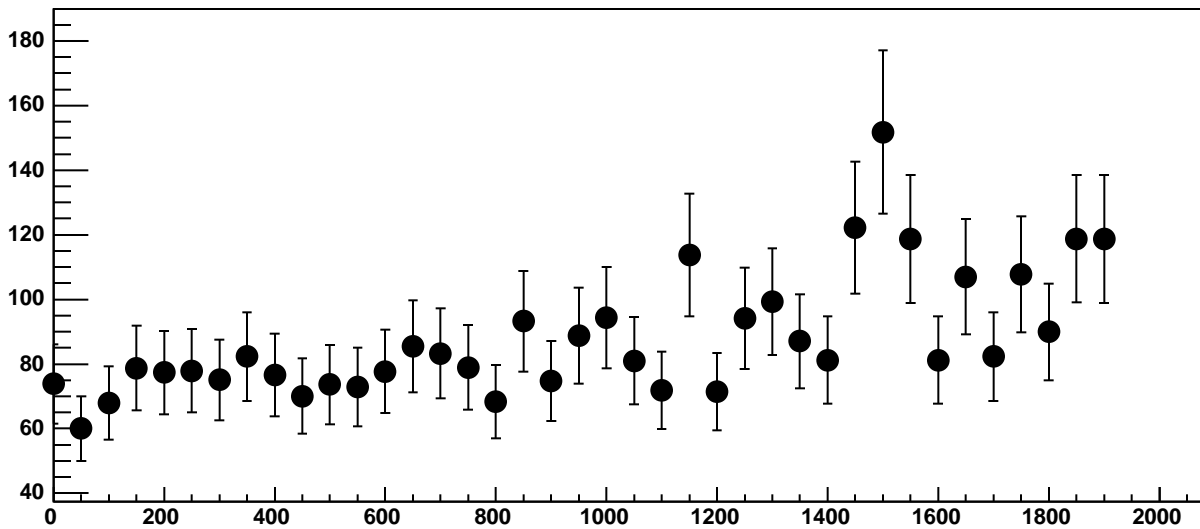
Chip 1, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC



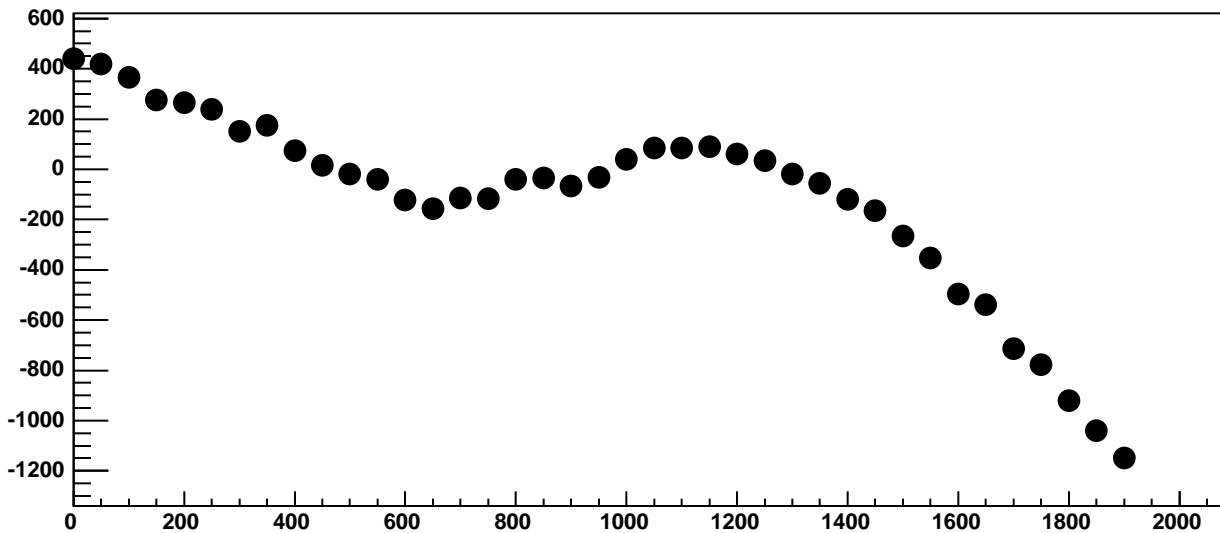
Chip 1, Channel 7, Enable 5, Hold=30, ADC Mean vs DAC



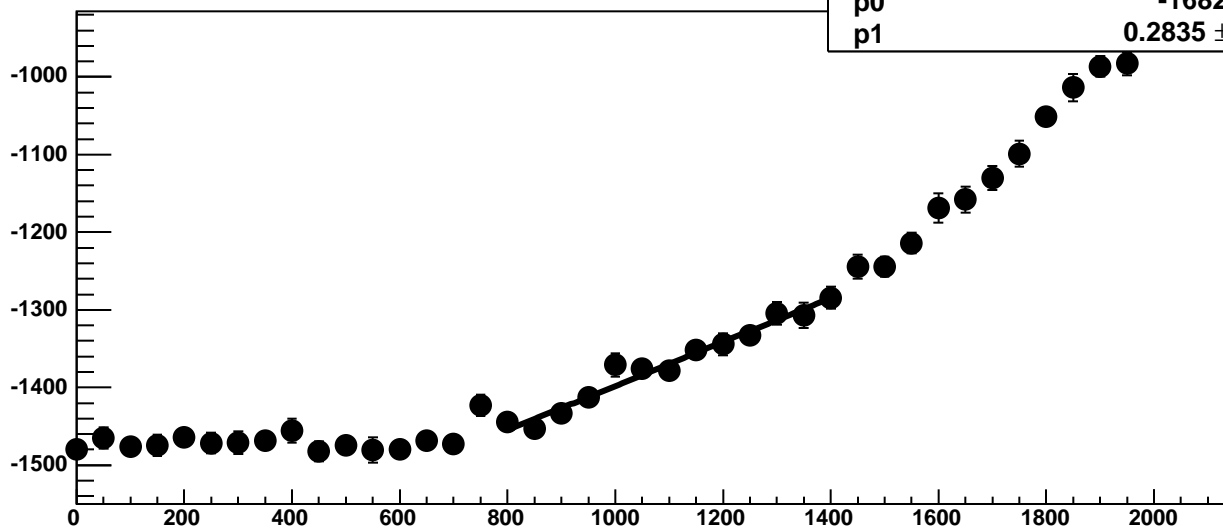
Chip 1, Channel 7, Enable 5, Hold=30, ADC Noise vs DAC



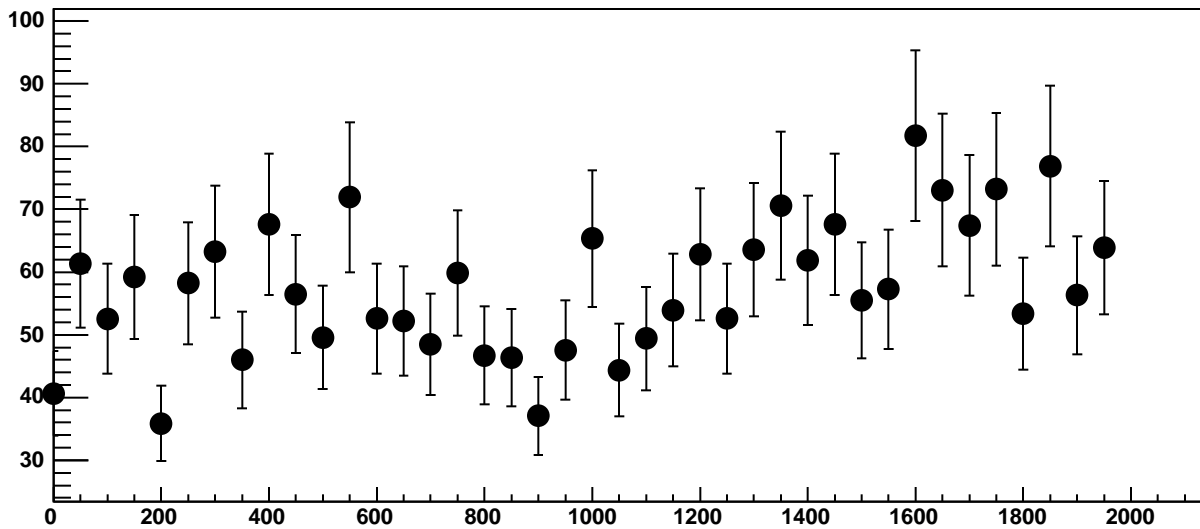
Chip 1, Channel 7, Enable 5, Hold=30, ADC Residuals vs DAC



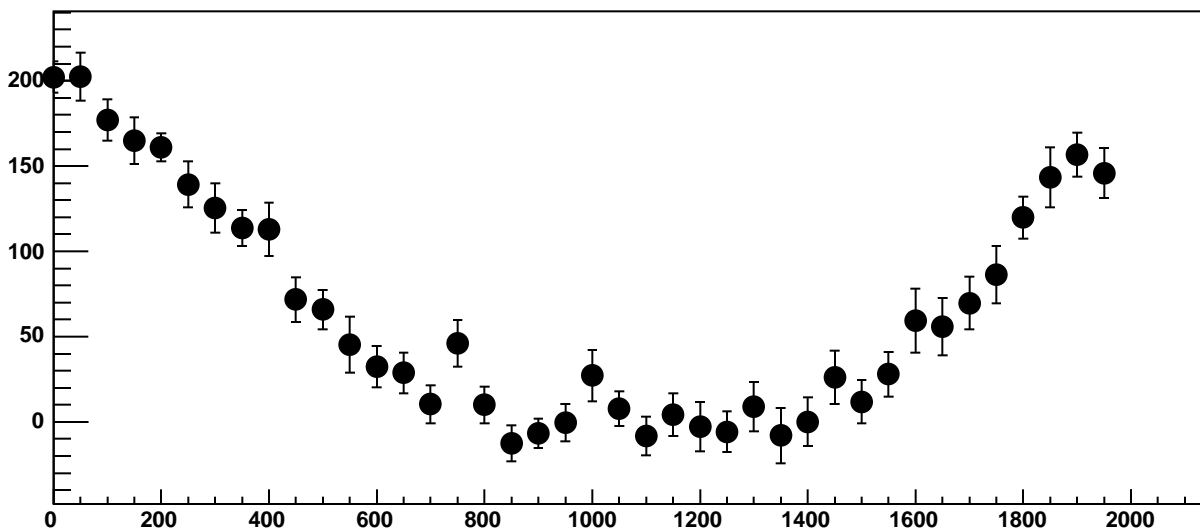
Chip 1, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC



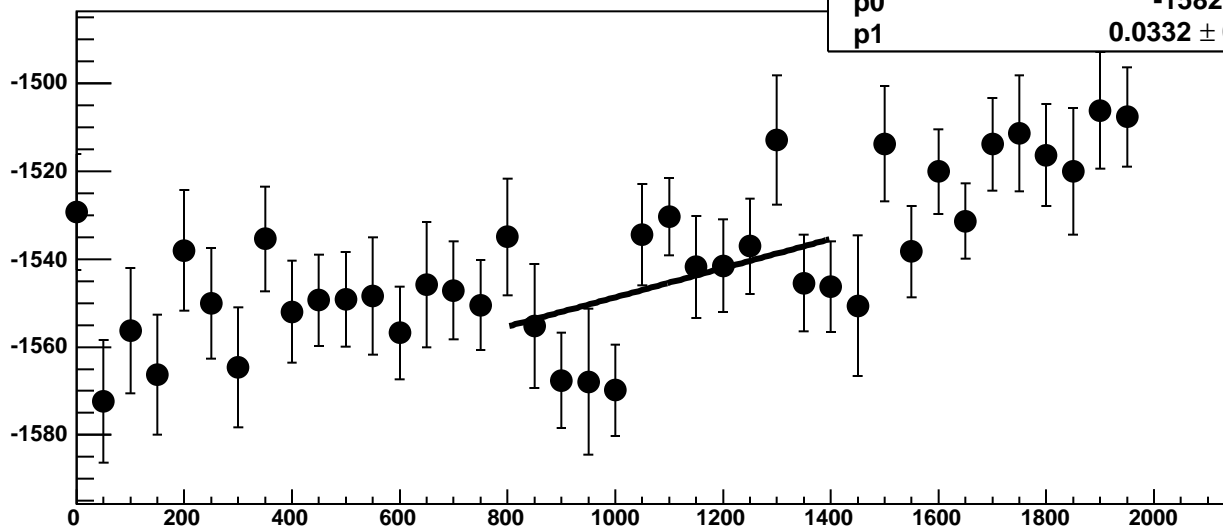
Chip 1, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC



Chip 1, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC

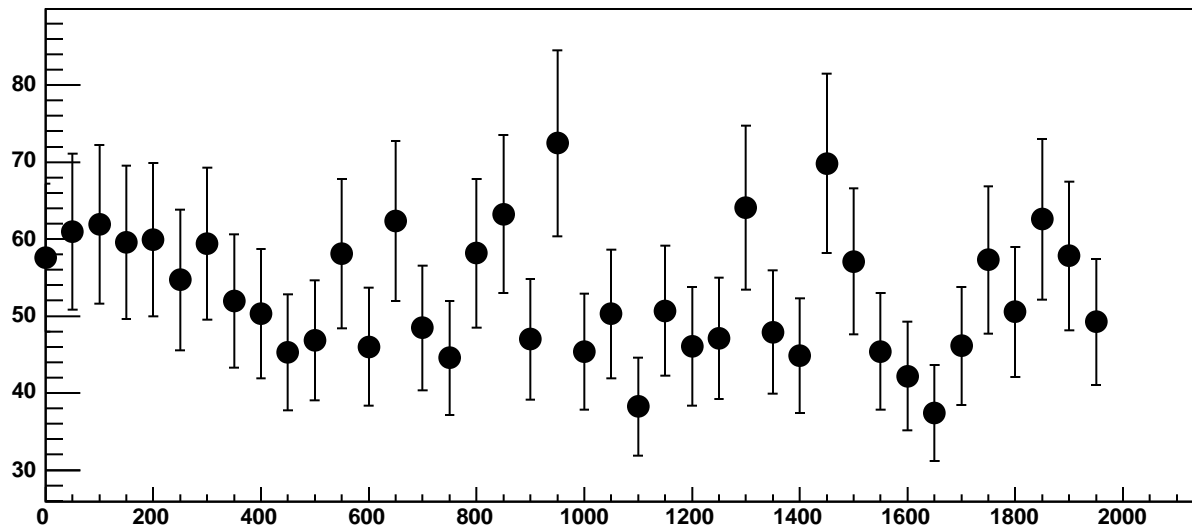


Chip 1, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC

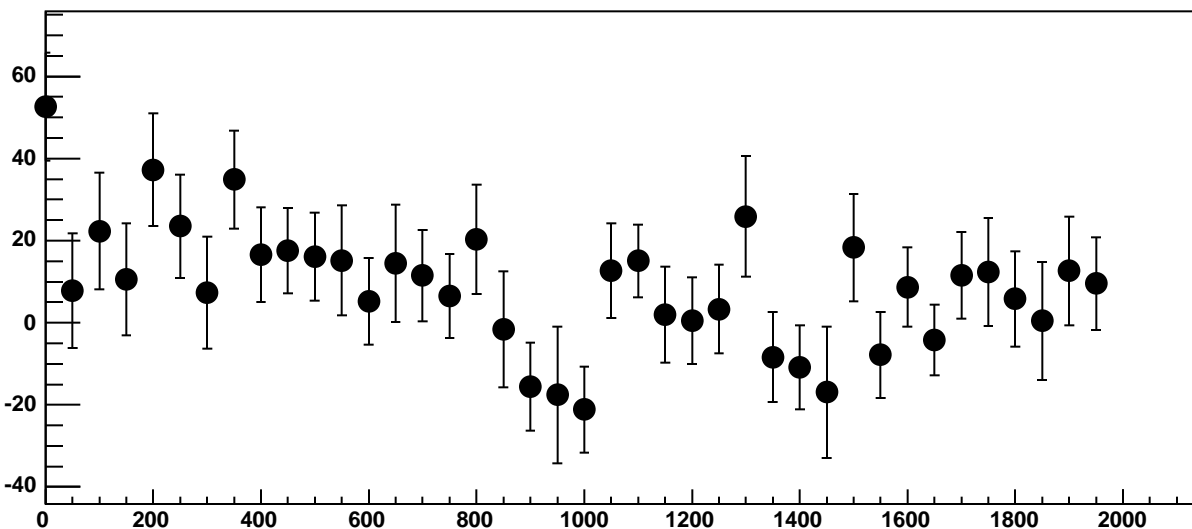


$\chi^2 / \text{ndf}$  18.74 / 11  
p0  $-1582 \pm 20.12$   
p1  $0.0332 \pm 0.01776$

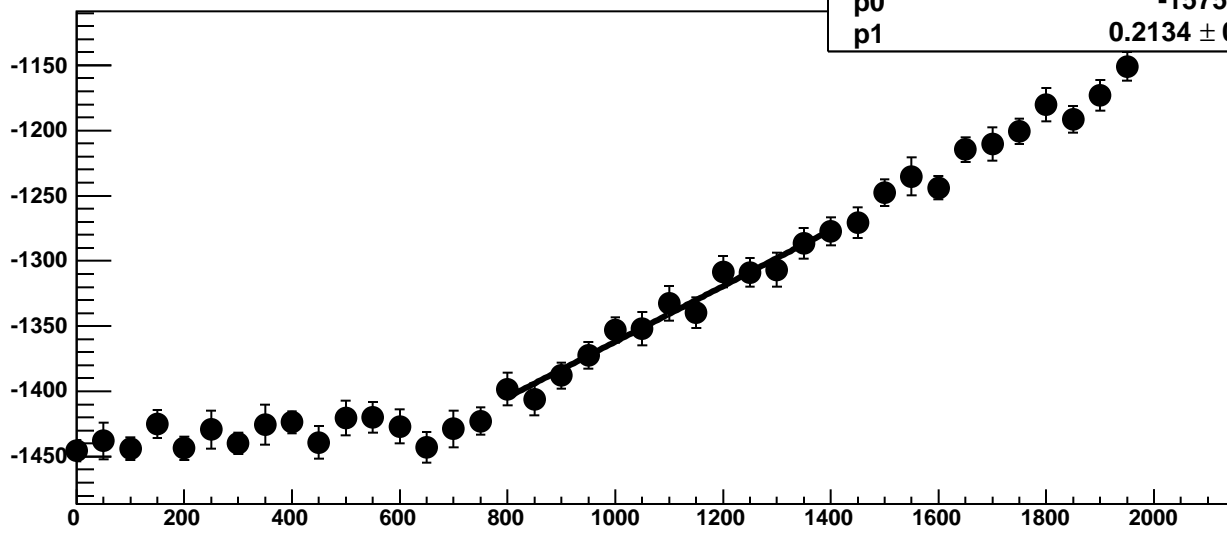
Chip 1, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC



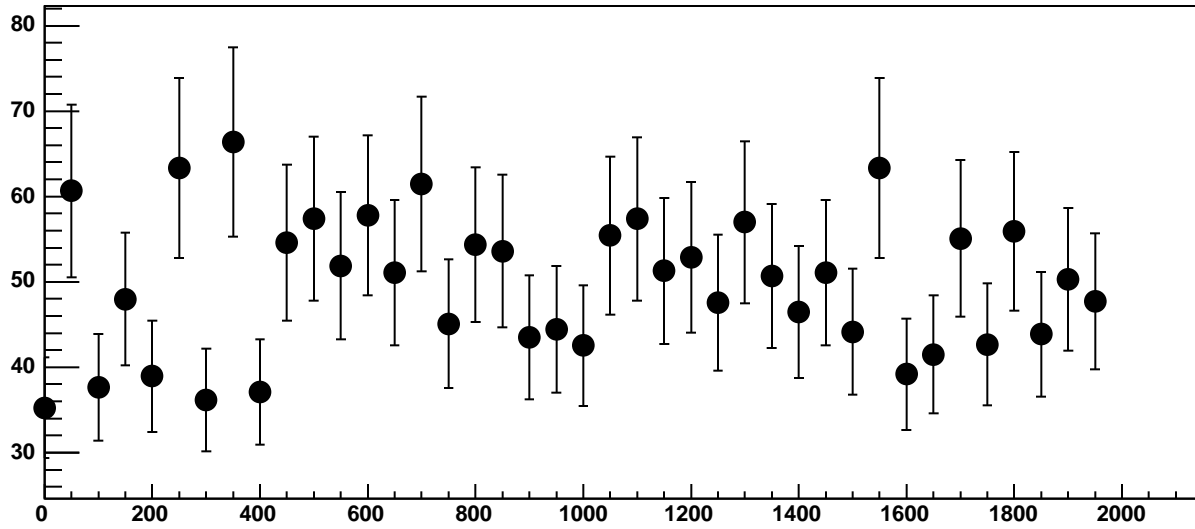
Chip 1, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC



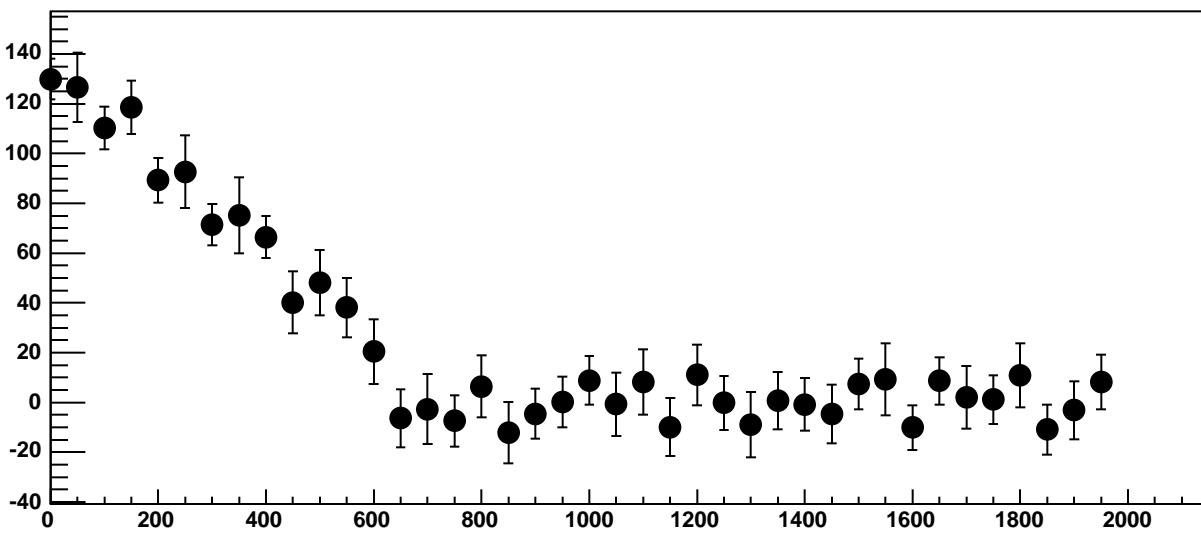
Chip 1, Channel 8, Enable 2, Hold=30, ADC Mean vs DAC



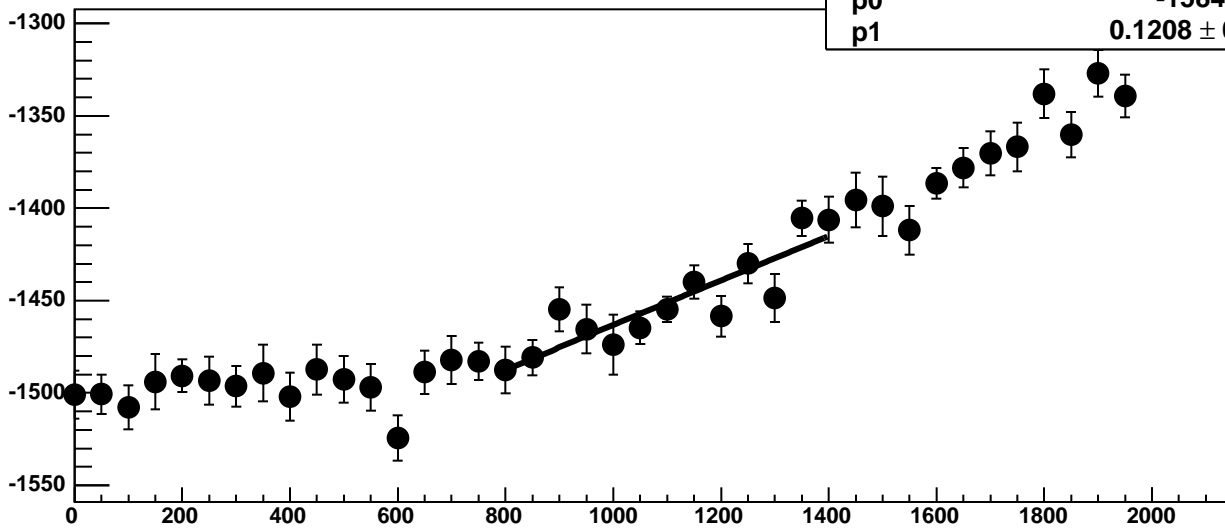
Chip 1, Channel 8, Enable 2, Hold=30, ADC Noise vs DAC



Chip 1, Channel 8, Enable 2, Hold=30, ADC Residuals vs DAC

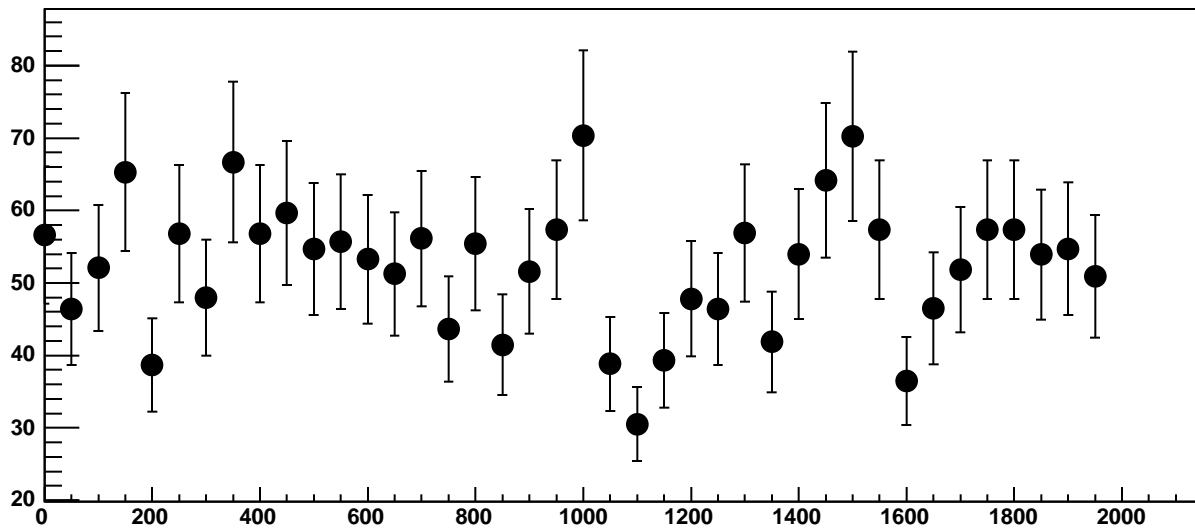


Chip 1, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC

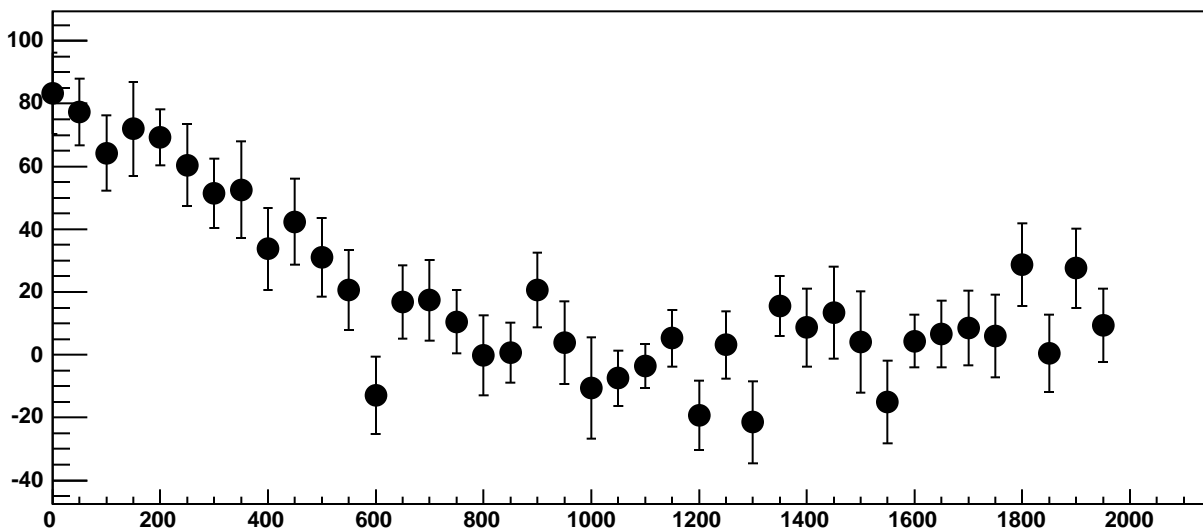


$\chi^2 / \text{ndf}$  13.87 / 11  
p0  $-1584 \pm 18.81$   
p1  $0.1208 \pm 0.01681$

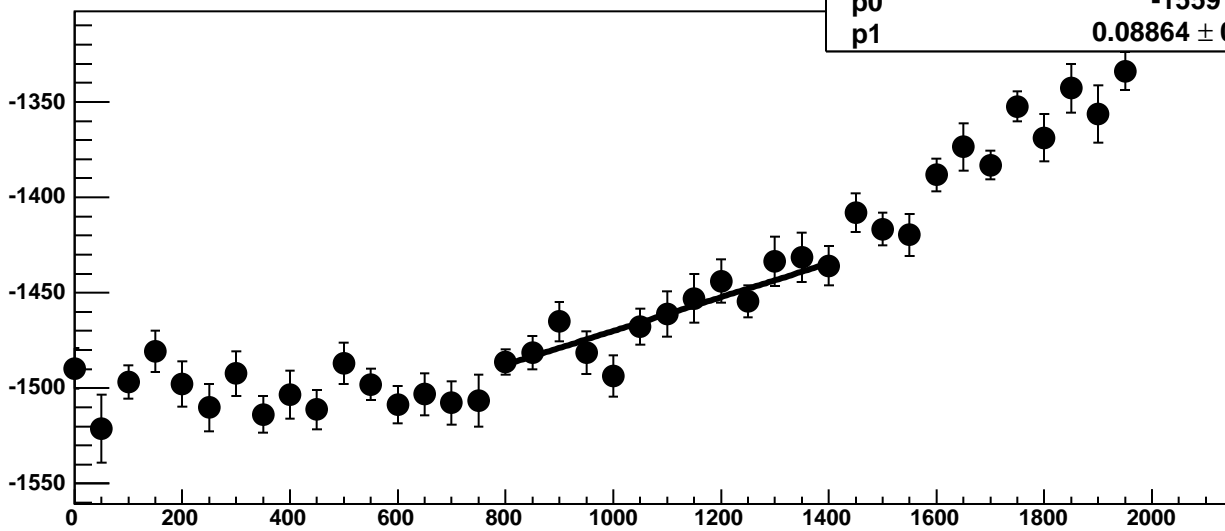
Chip 1, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC



Chip 1, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC

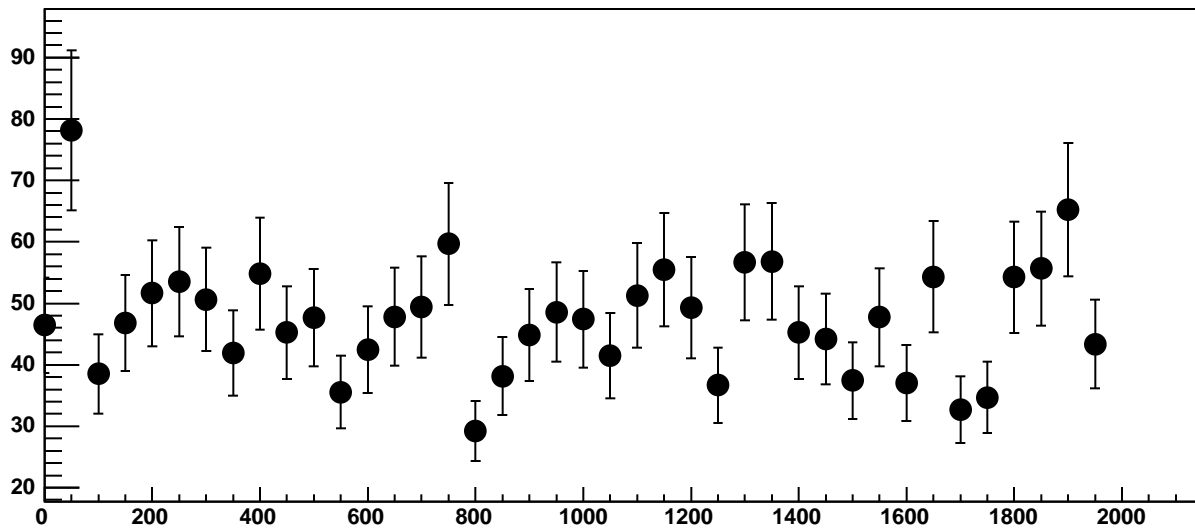


Chip 1, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC

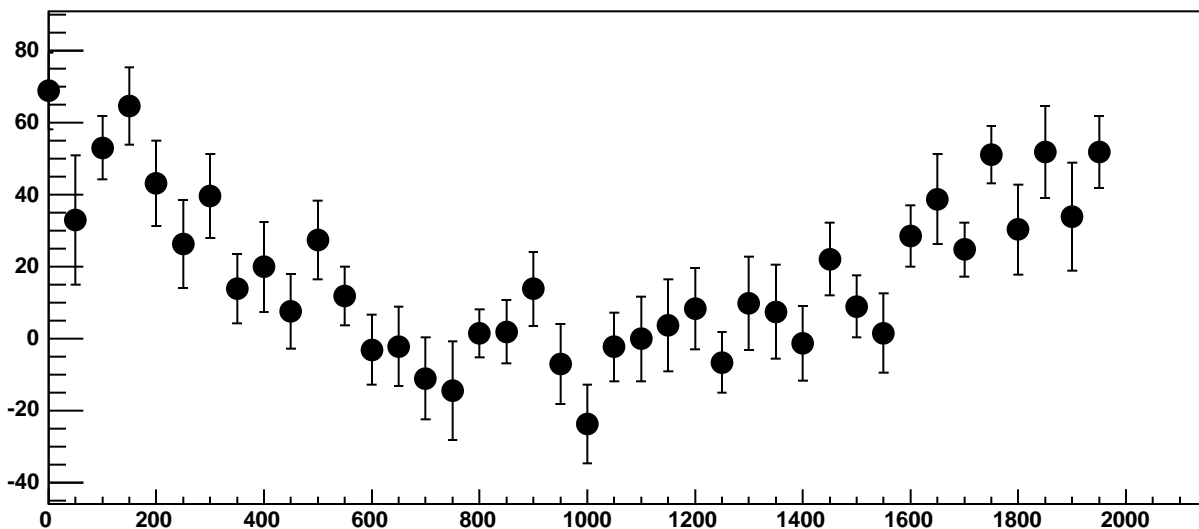


$\chi^2 / \text{ndf}$  9.263 / 11  
p0  $-1559 \pm 15.06$   
p1  $0.08864 \pm 0.01403$

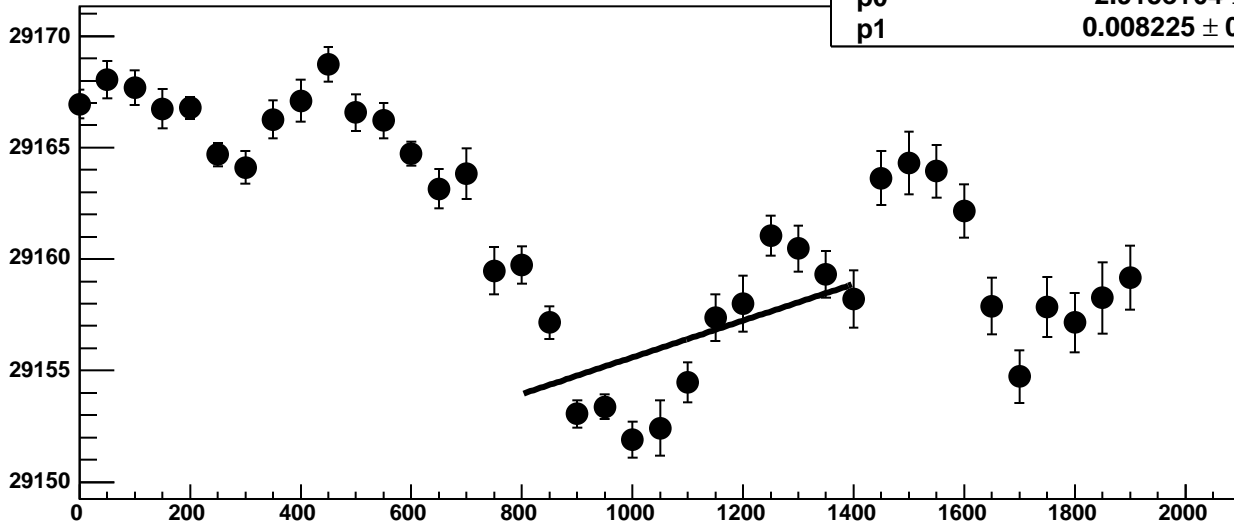
Chip 1, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC



Chip 1, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC



Chip 1, Channel 8, Enable 5!, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

134.9 / 11

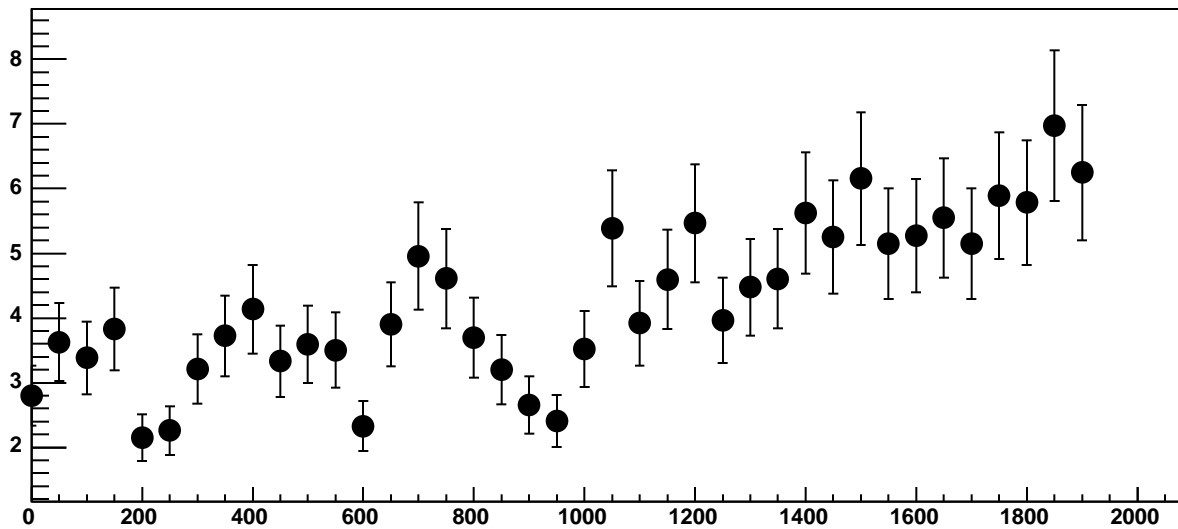
p0

$2.915e+04 \pm 1.423$

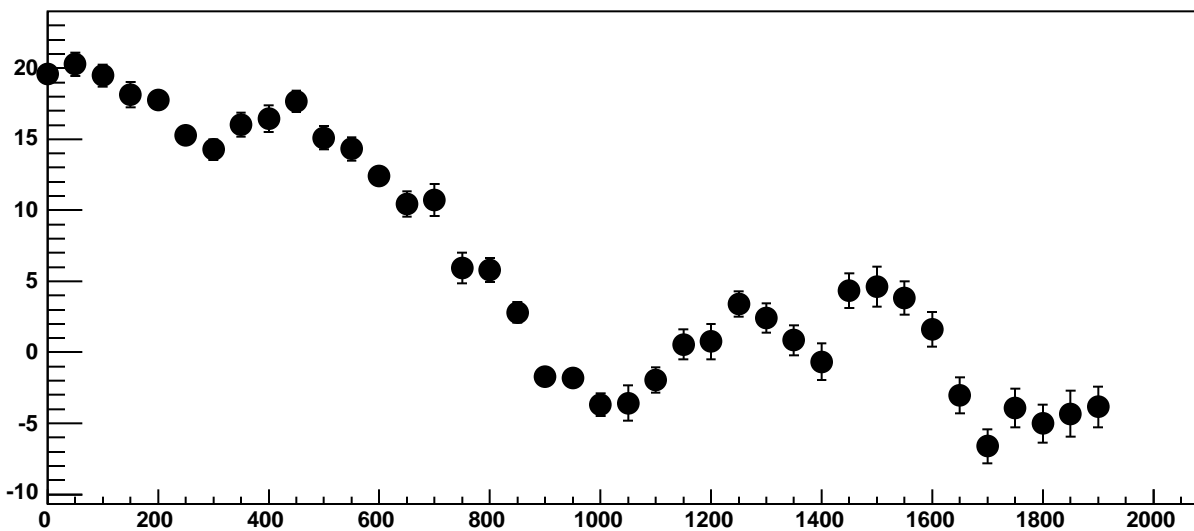
p1

$0.008225 \pm 0.00136$

Chip 1, Channel 8, Enable 5!, Hold=30, ADC Noise vs DAC

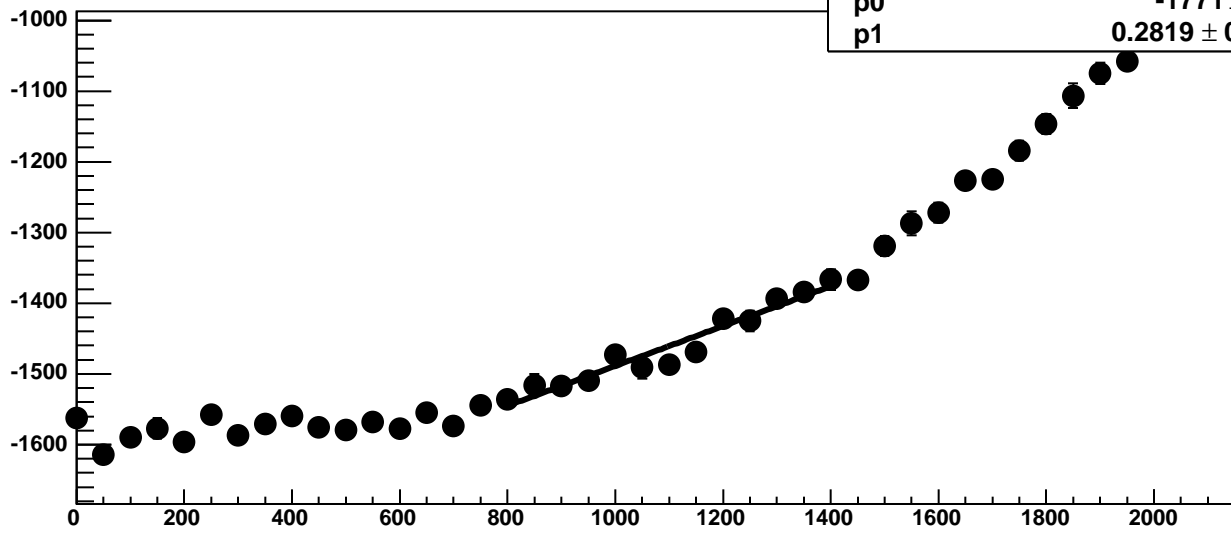


Chip 1, Channel 8, Enable 5!, Hold=30, ADC Residuals vs DAC

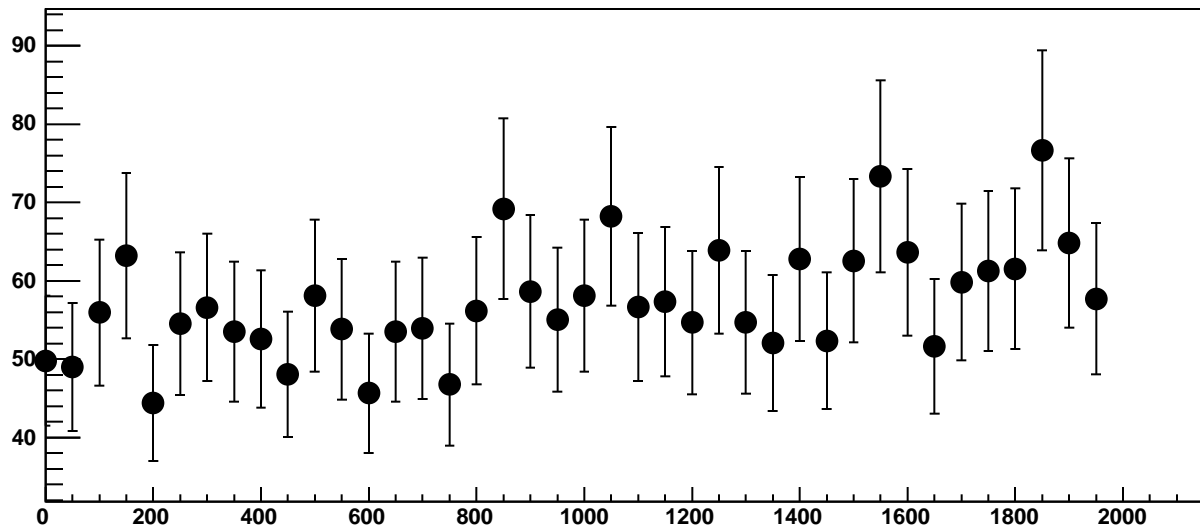




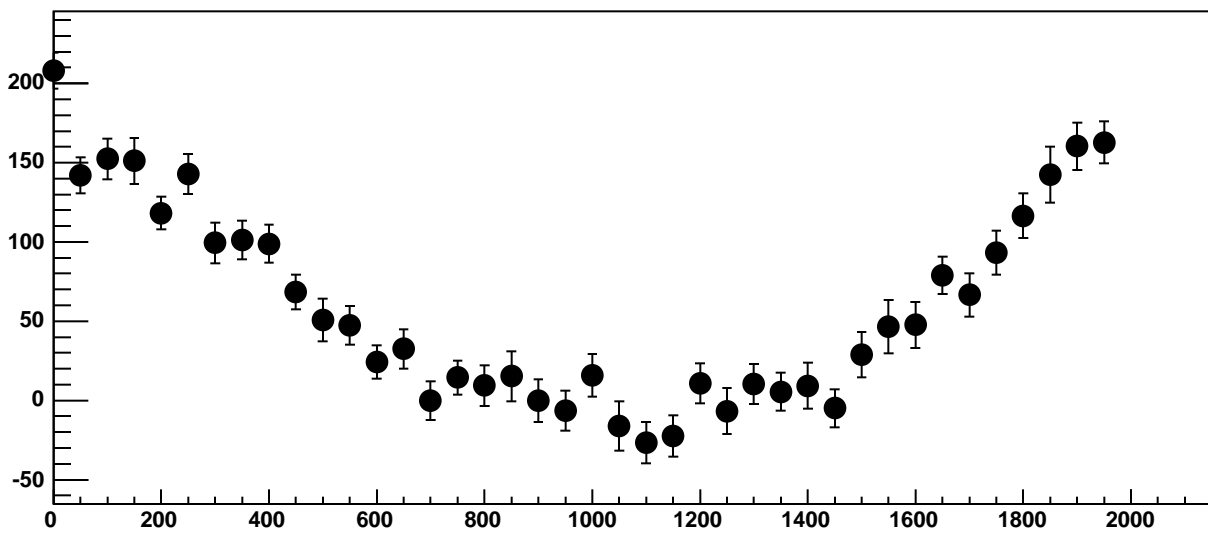
Chip 1, Channel 9, Enable 0, Hold=30, ADC Mean vs DAC



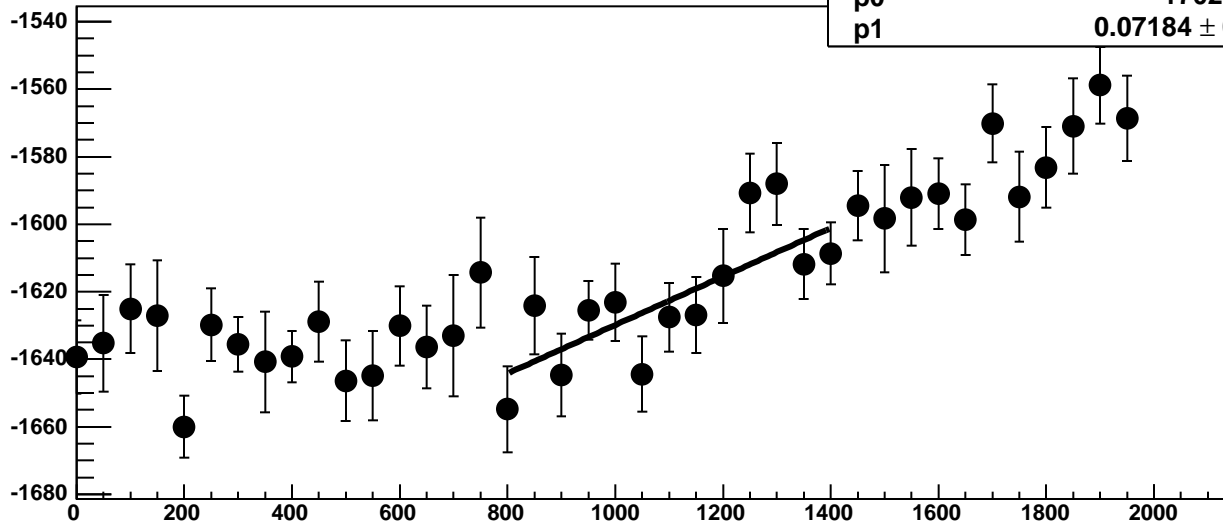
Chip 1, Channel 9, Enable 0, Hold=30, ADC Noise vs DAC



Chip 1, Channel 9, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 1, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

14.16 / 11

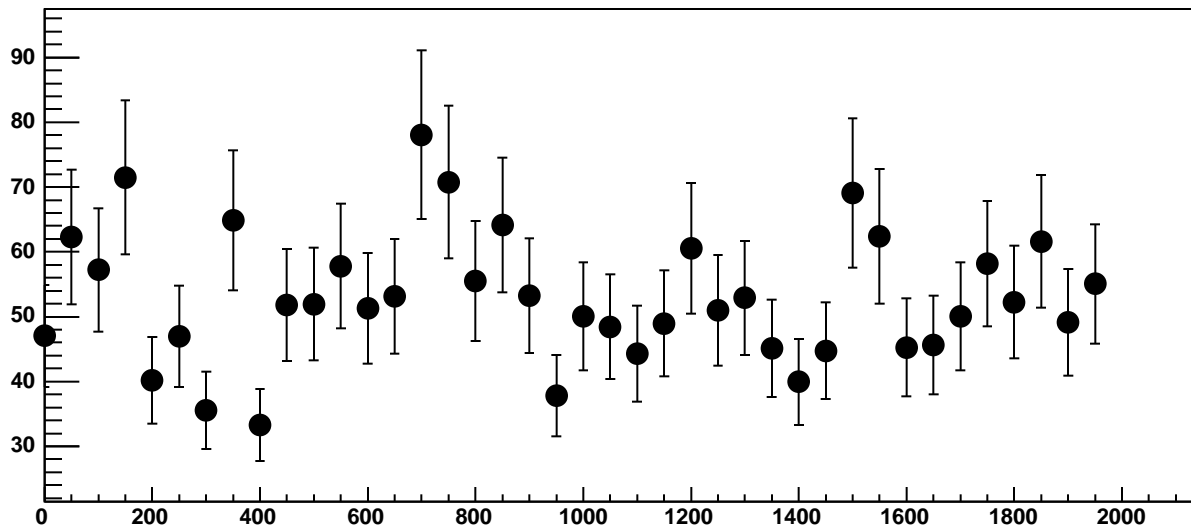
p0

$-1702 \pm 18.72$

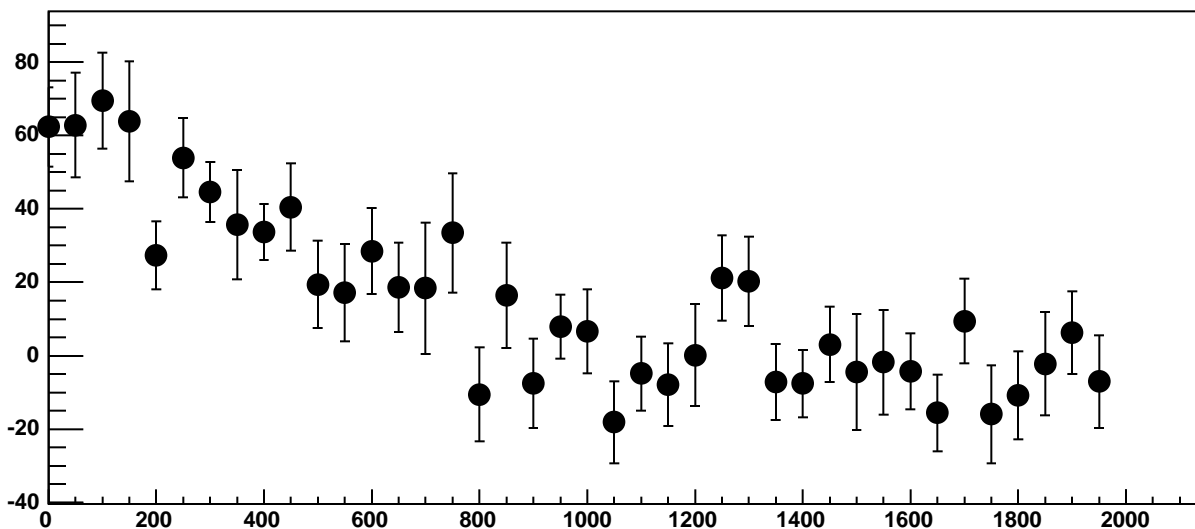
p1

$0.07184 \pm 0.01654$

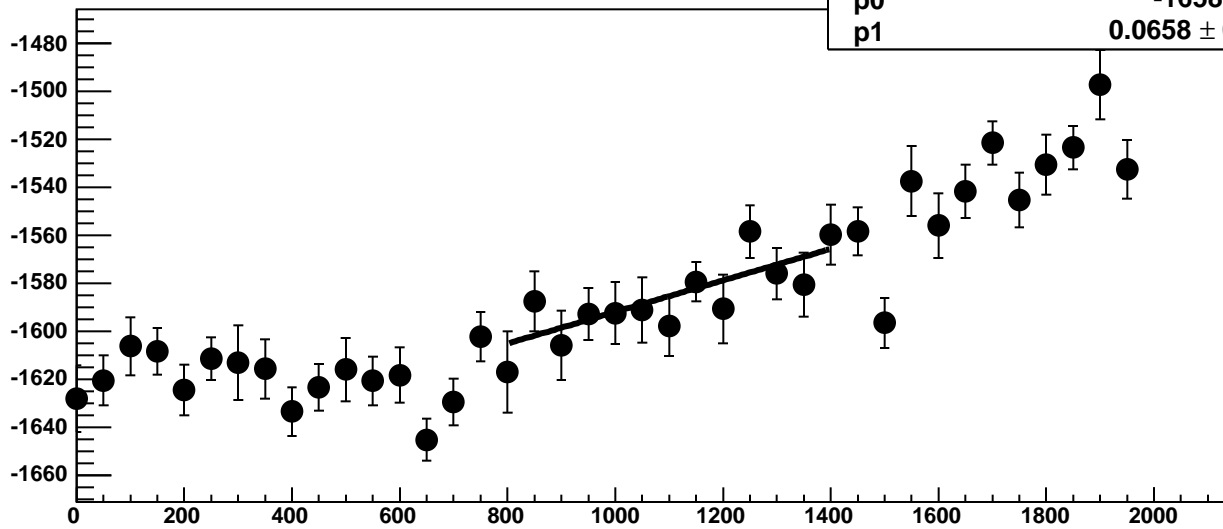
Chip 1, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC



Chip 1, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC

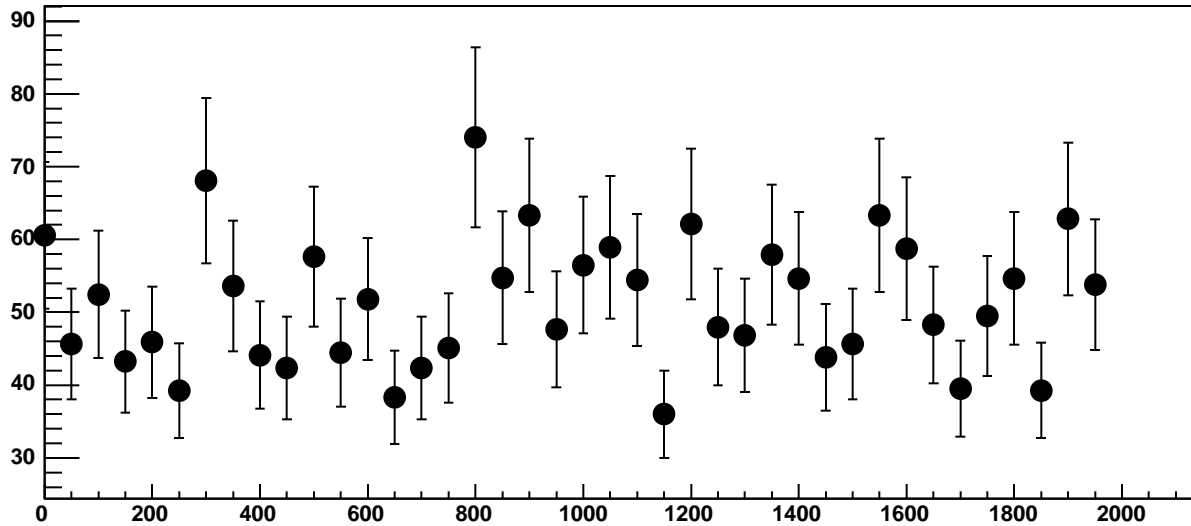


Chip 1, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC

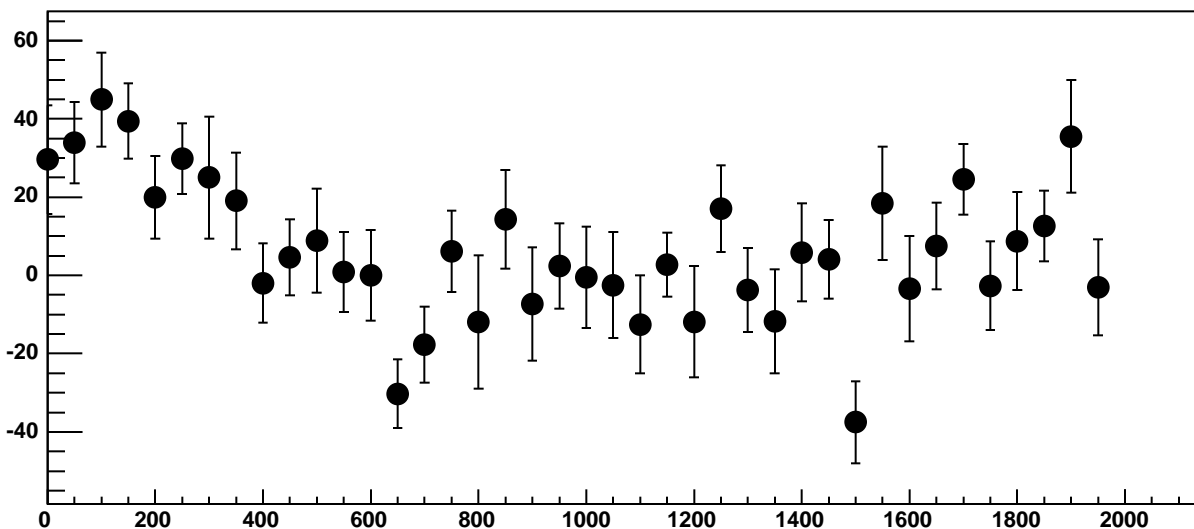


$\chi^2 / \text{ndf}$  7.461 / 11  
p0  $-1658 \pm 21.95$   
p1  $0.0658 \pm 0.01937$

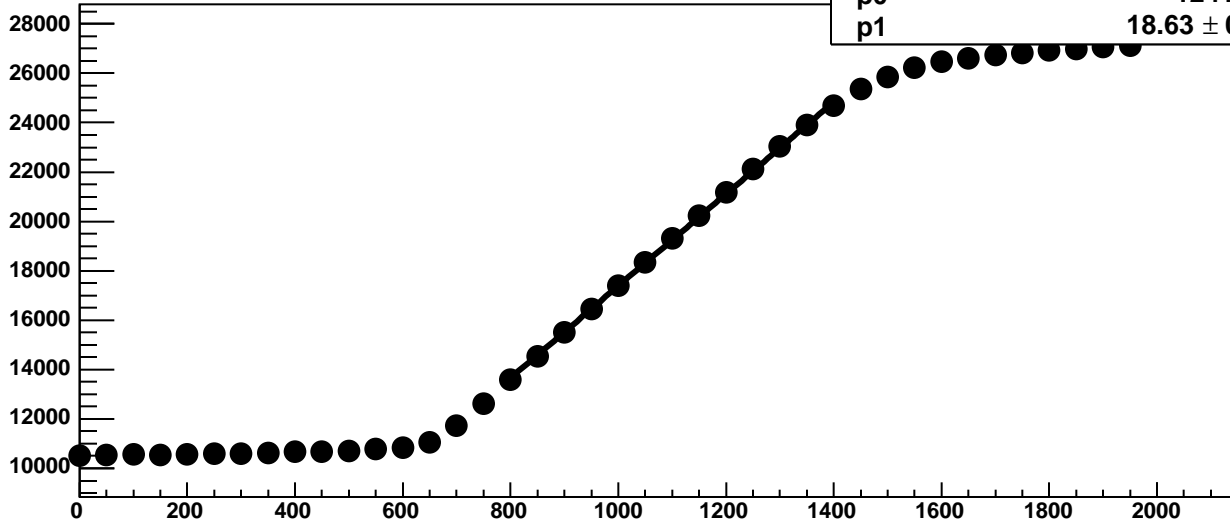
Chip 1, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC



Chip 1, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC

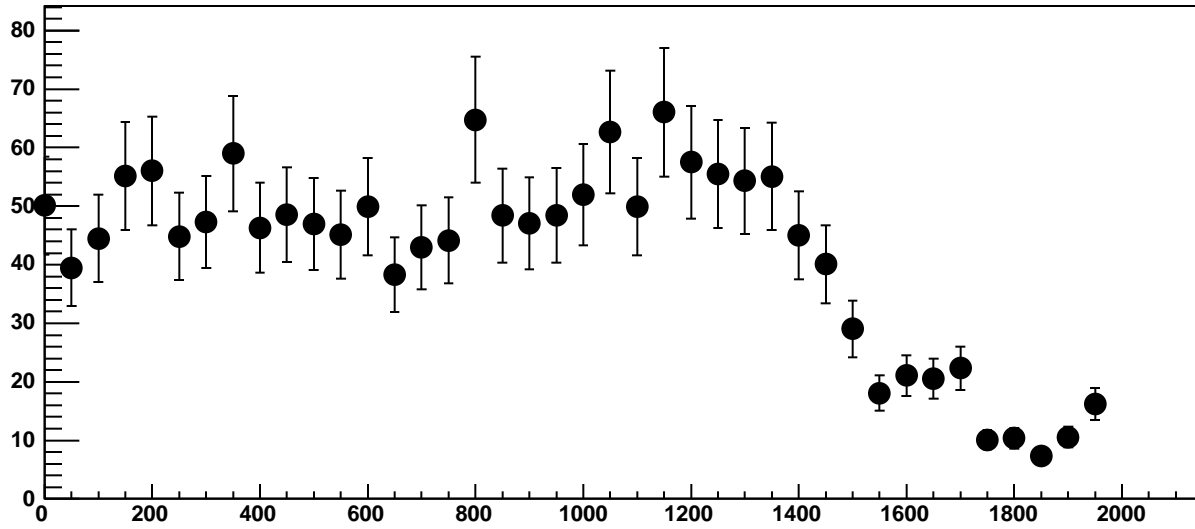


Chip 1, Channel 9, Enable 3!, Hold=30, ADC Mean vs DAC

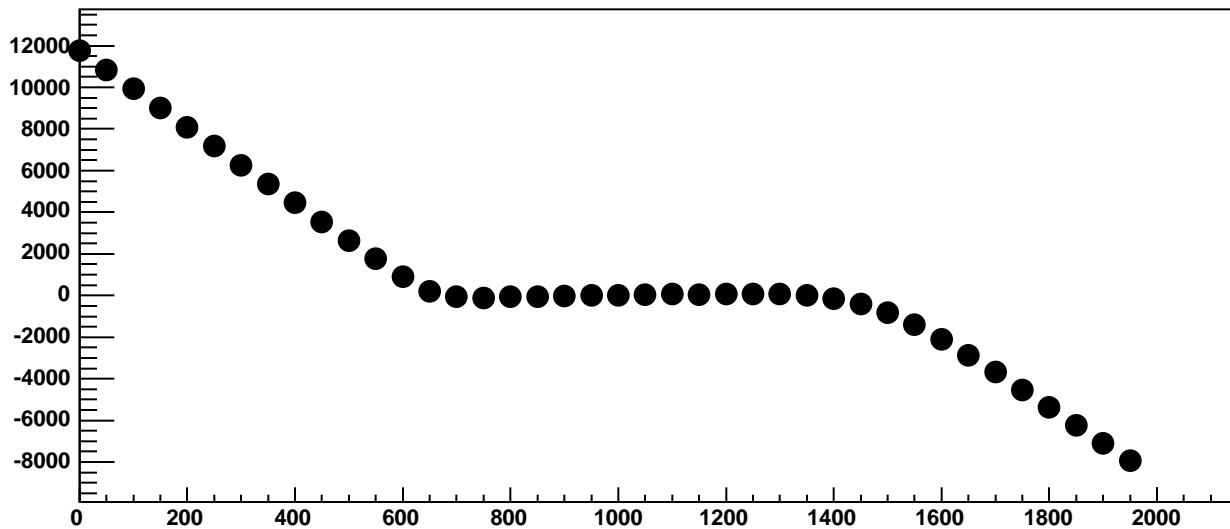


$\chi^2 / \text{ndf}$  380.5 / 11  
p0  $-1241 \pm 19.7$   
p1  $18.63 \pm 0.01762$

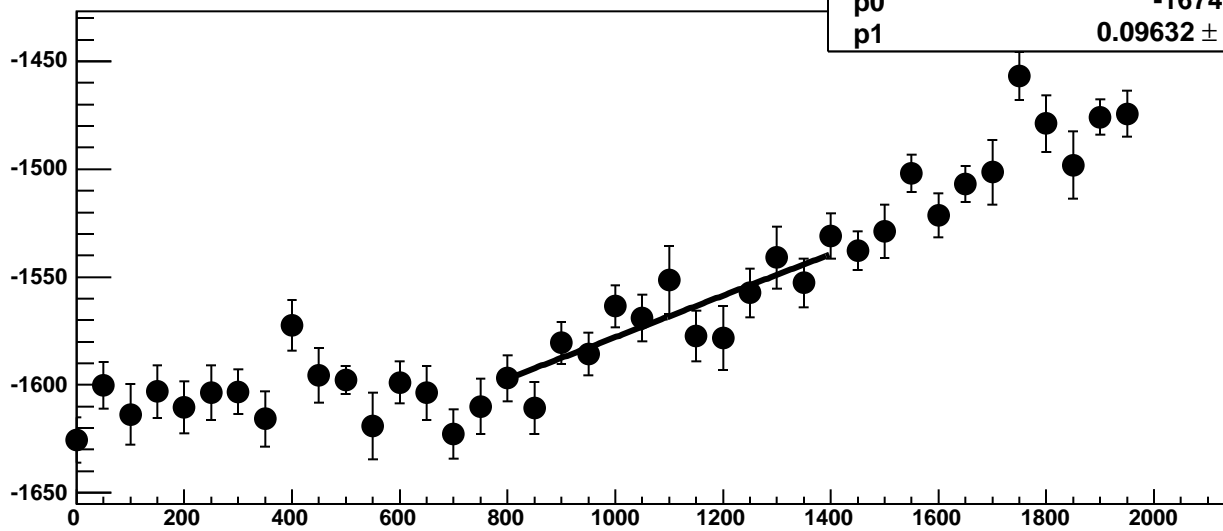
Chip 1, Channel 9, Enable 3!, Hold=30, ADC Noise vs DAC



Chip 1, Channel 9, Enable 3!, Hold=30, ADC Residuals vs DAC

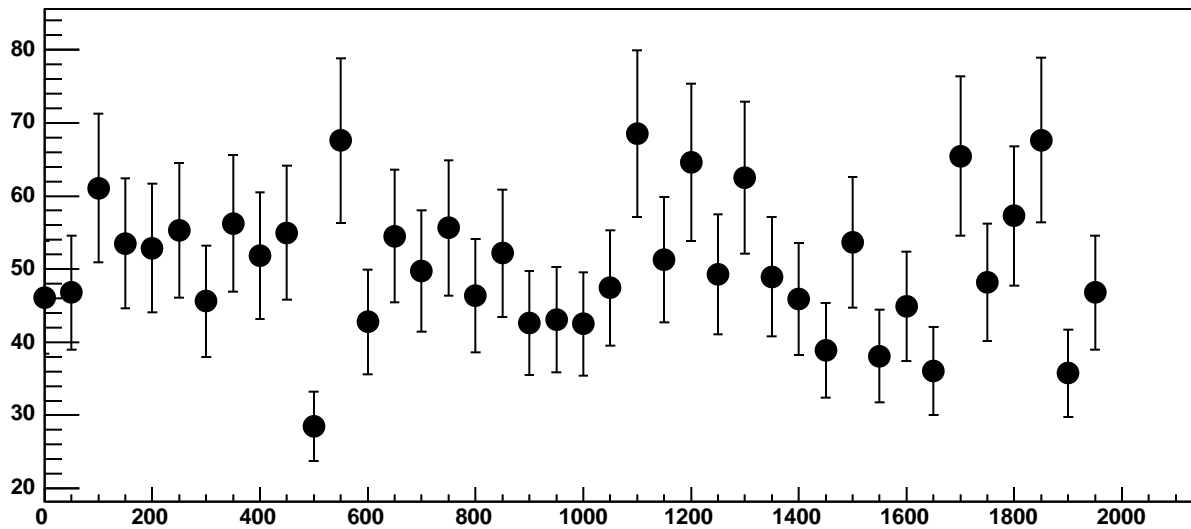


Chip 1, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC

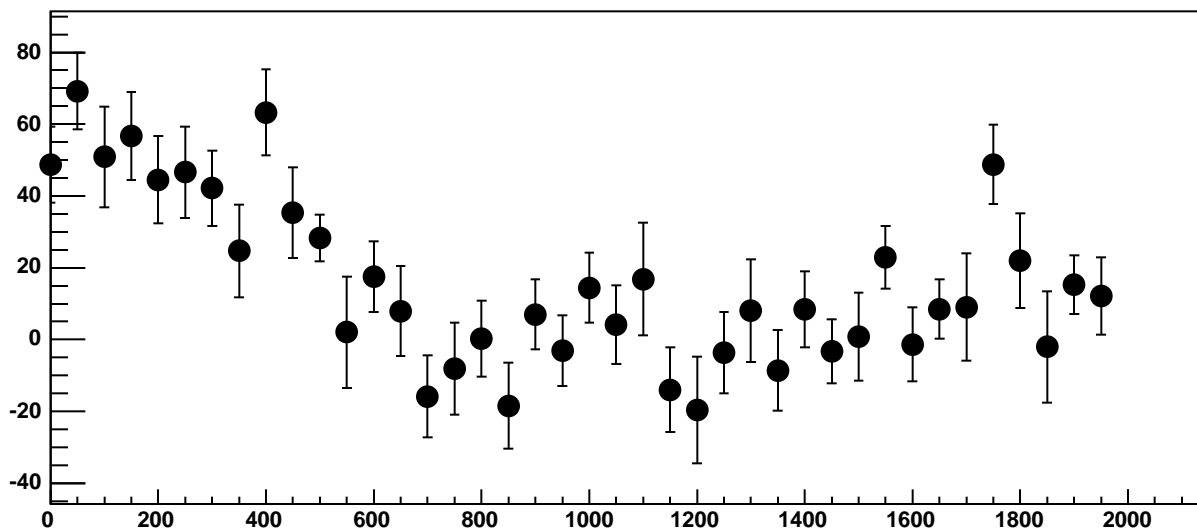


$\chi^2 / \text{ndf}$  11.25 / 11  
p0  $-1674 \pm 18.03$   
p1  $0.09632 \pm 0.01641$

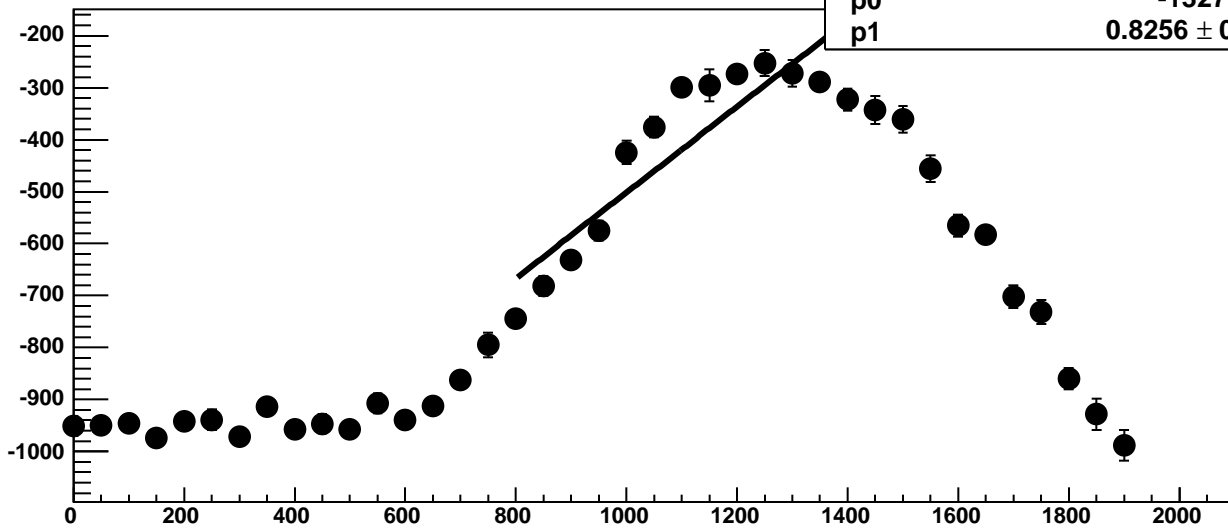
Chip 1, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC



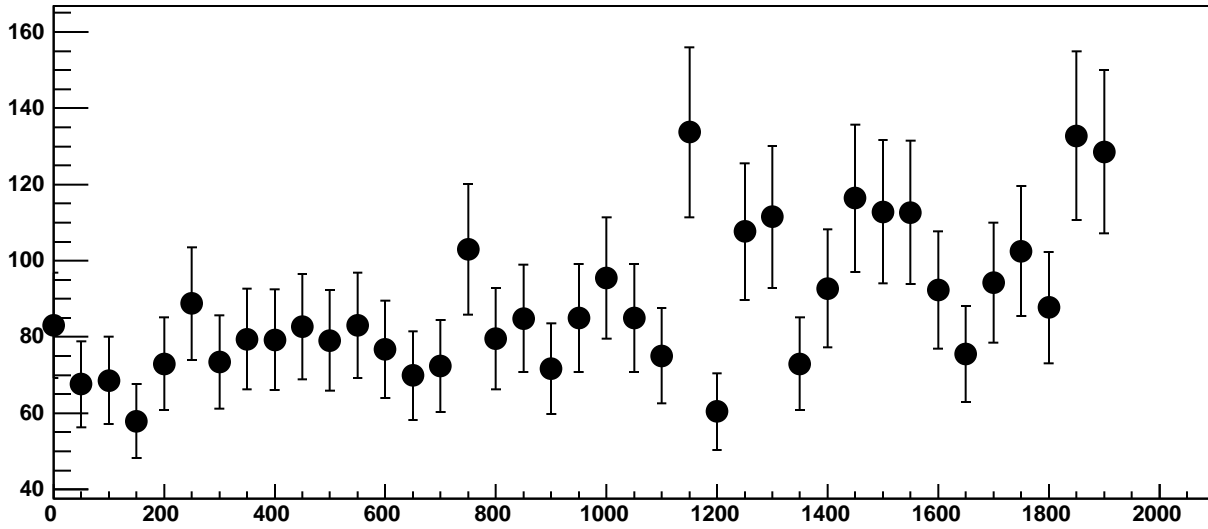
Chip 1, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC



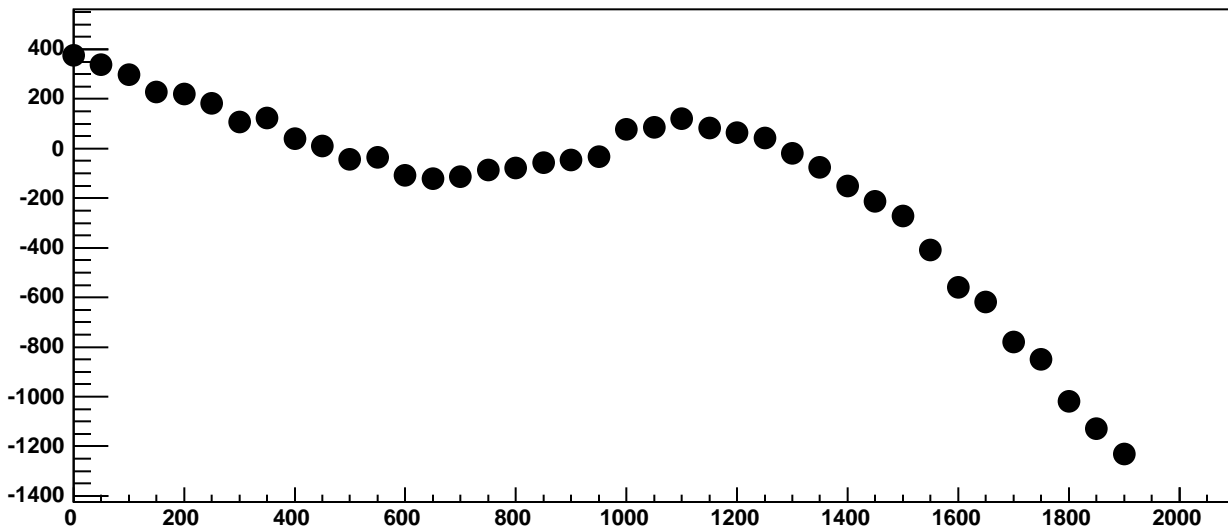
Chip 1, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC



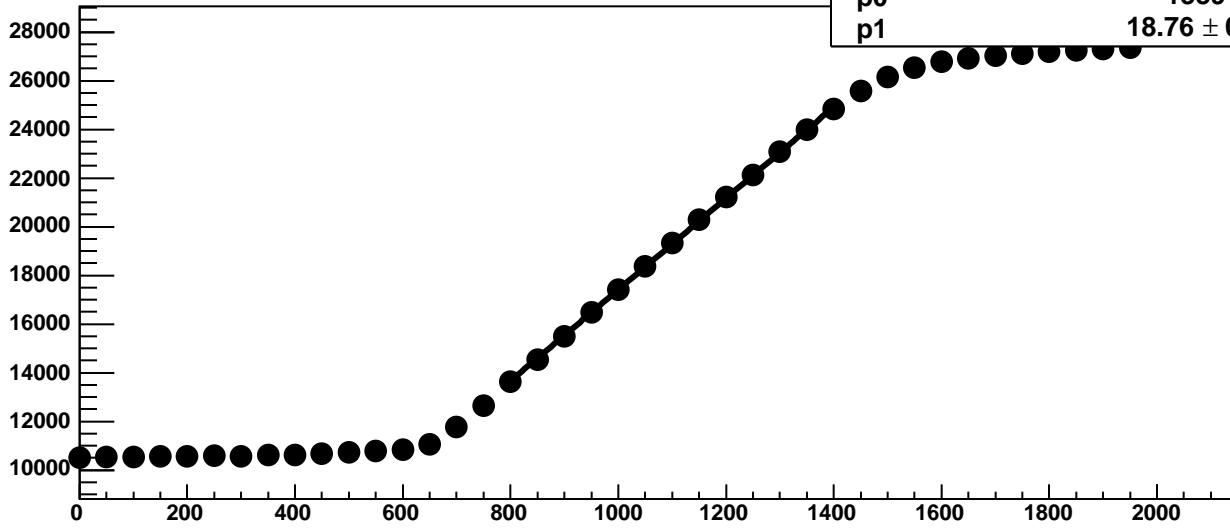
Chip 1, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC



Chip 1, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC

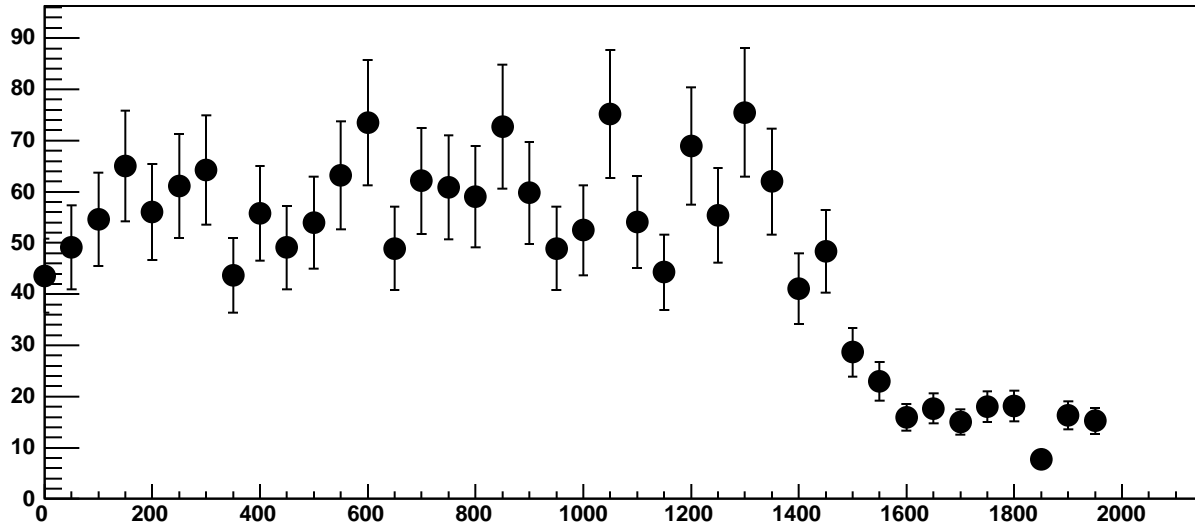


Chip 1, Channel 10, Enable 0!, Hold=30, ADC Mean vs DAC

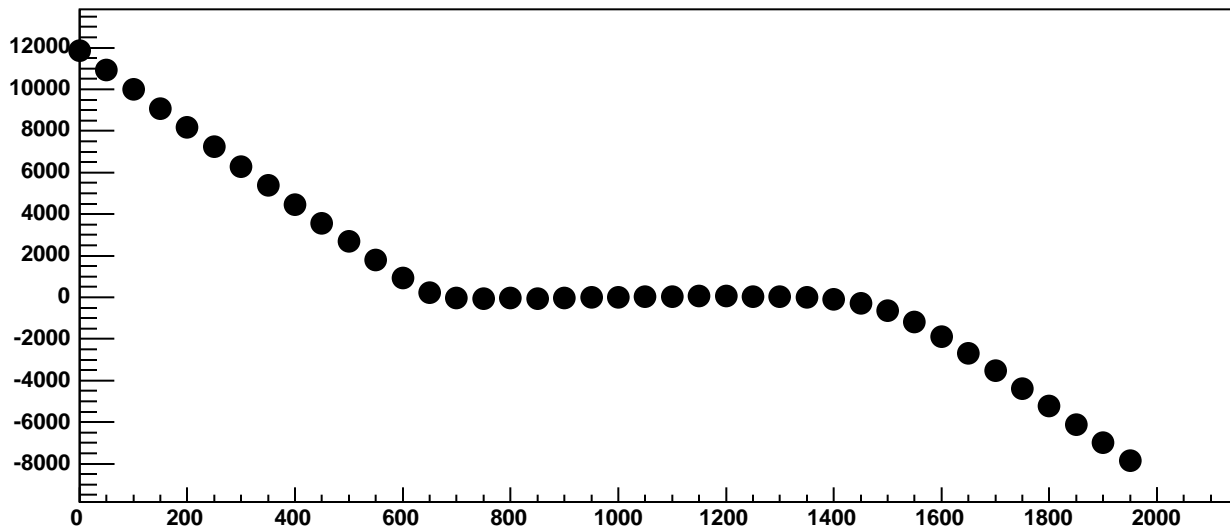


$\chi^2 / \text{ndf}$  211.5 / 11  
p0  $-1339 \pm 21.37$   
p1  $18.76 \pm 0.01884$

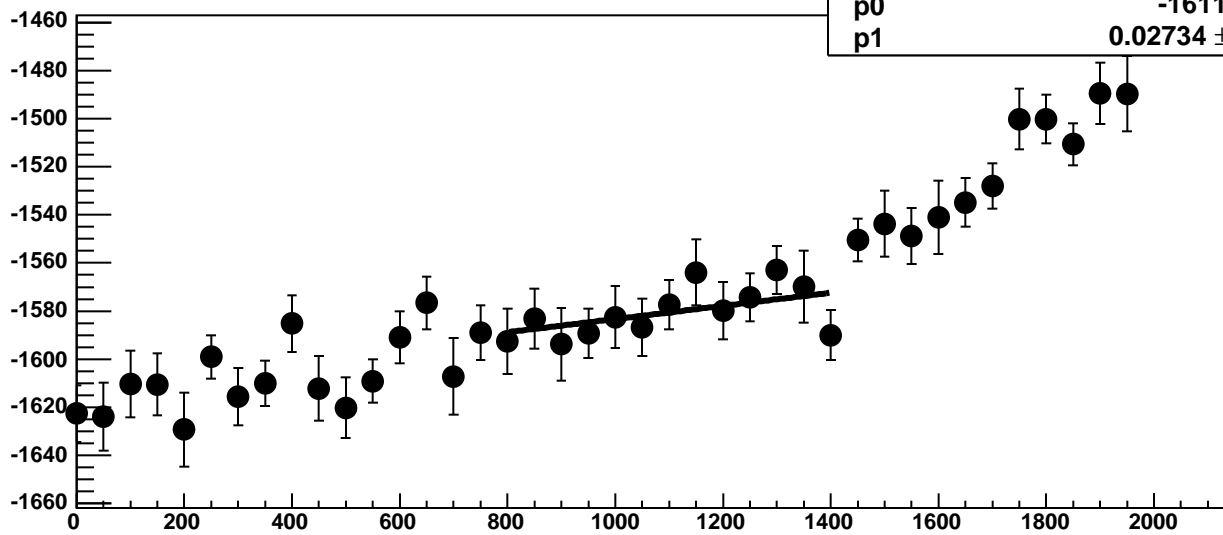
Chip 1, Channel 10, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 1, Channel 10, Enable 0!, Hold=30, ADC Residuals vs DAC

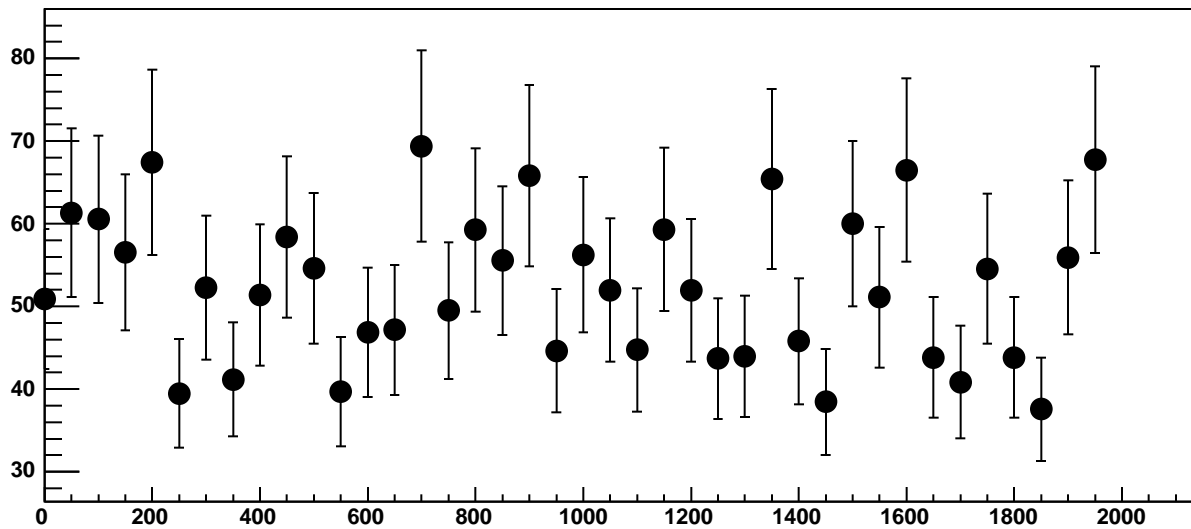


Chip 1, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC

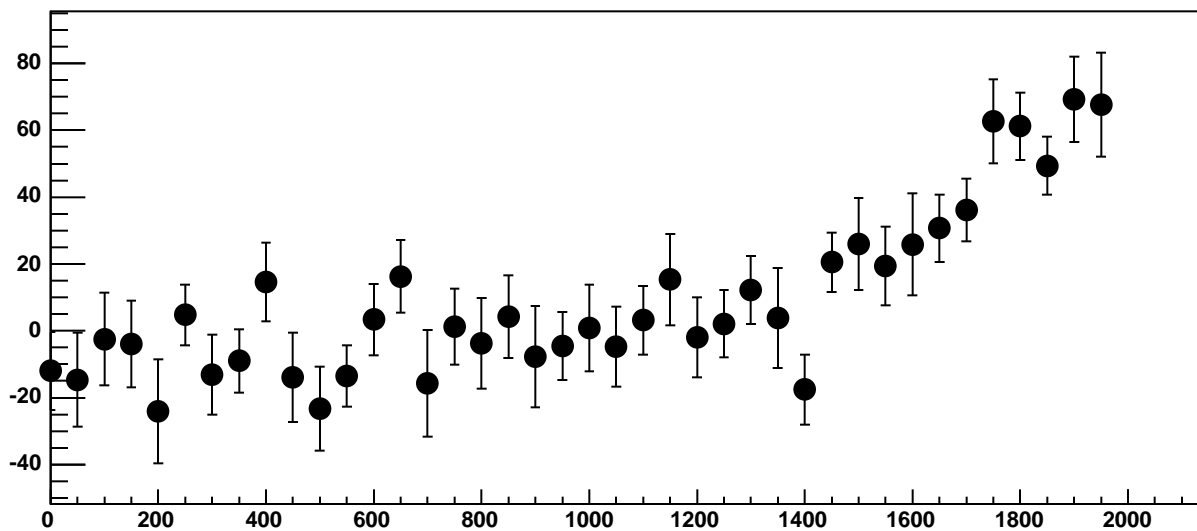


$\chi^2 / \text{ndf}$  6.568 / 11  
p0  $-1611 \pm 20.29$   
p1  $0.02734 \pm 0.0179$

Chip 1, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC

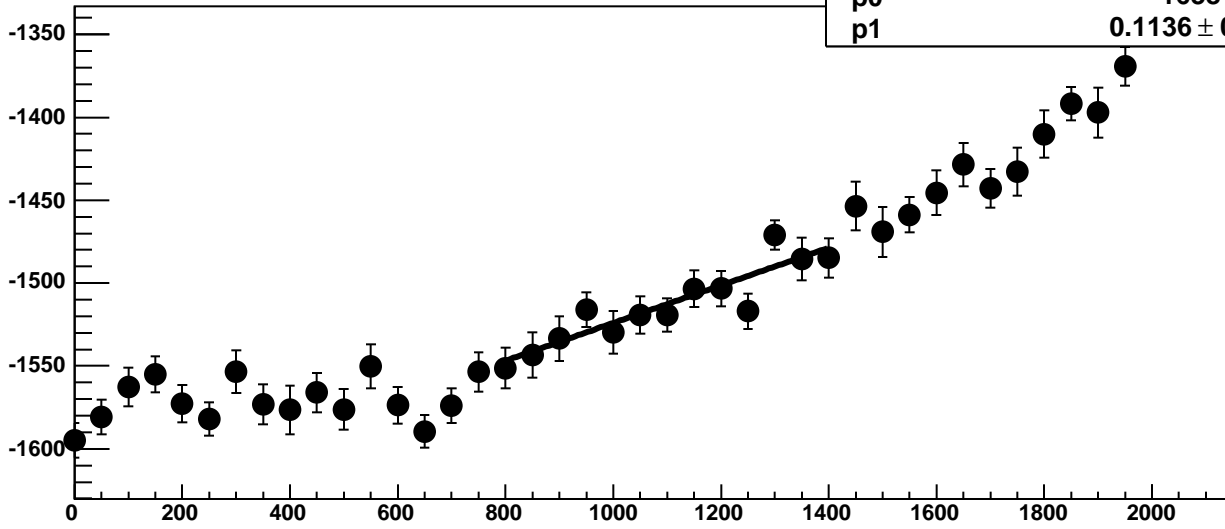


Chip 1, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC



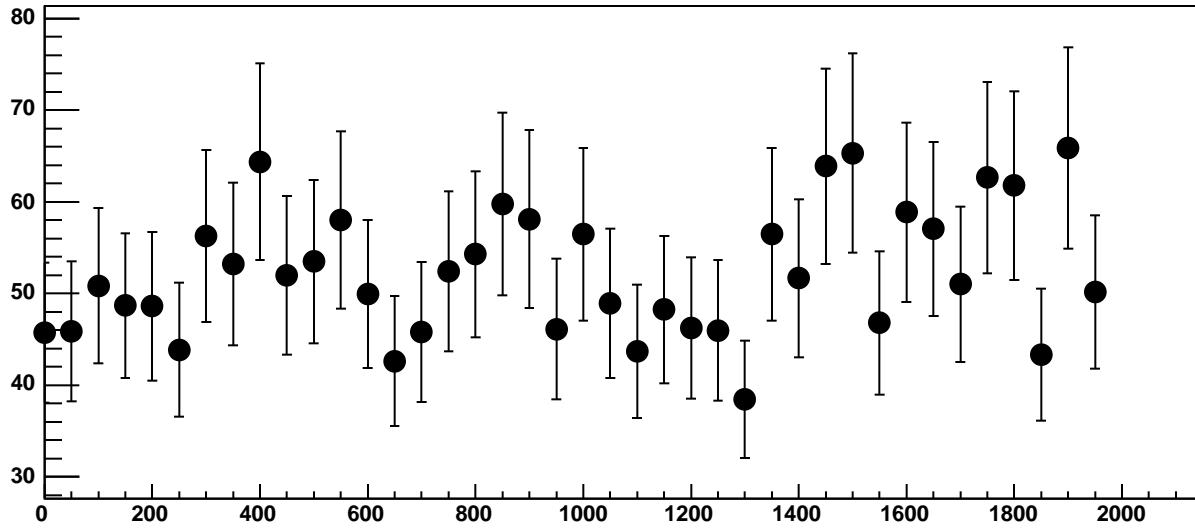


Chip 1, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC

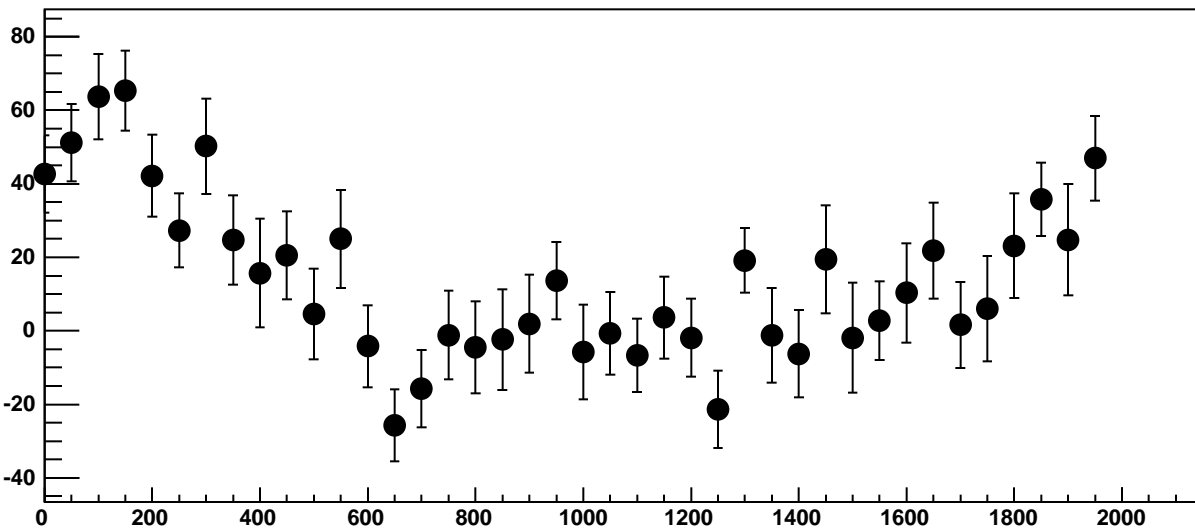


$\chi^2 / \text{ndf}$  11.71 / 11  
p0  $-1638 \pm 20.02$   
p1  $0.1136 \pm 0.01764$

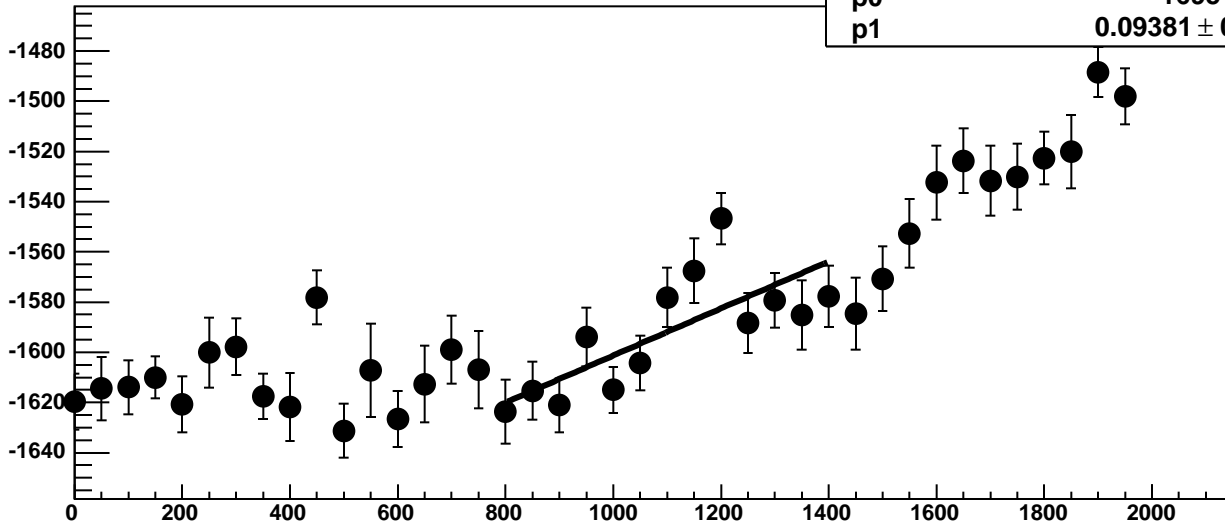
Chip 1, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC



Chip 1, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC

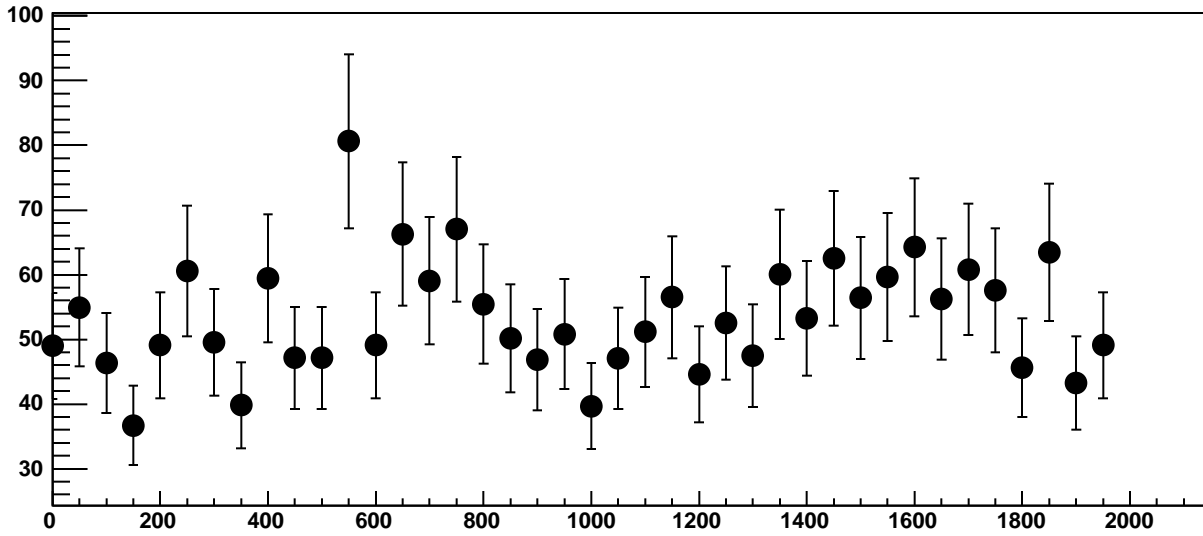


Chip 1, Channel 10, Enable 3, Hold=30, ADC Mean vs DAC

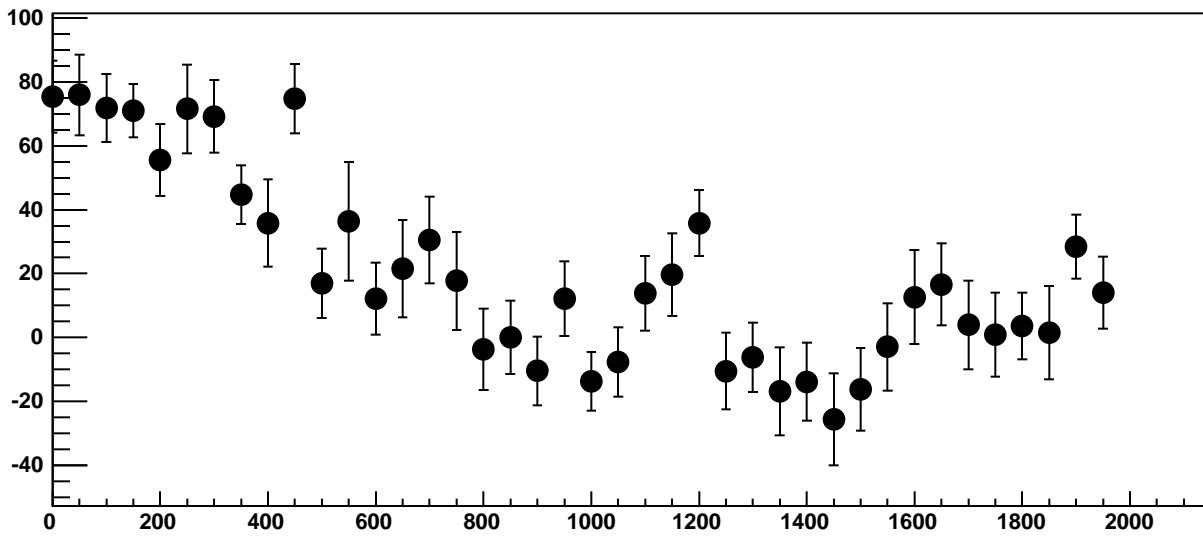


$\chi^2 / \text{ndf}$  24.66 / 11  
p0  $-1695 \pm 19.52$   
p1  $0.09381 \pm 0.01765$

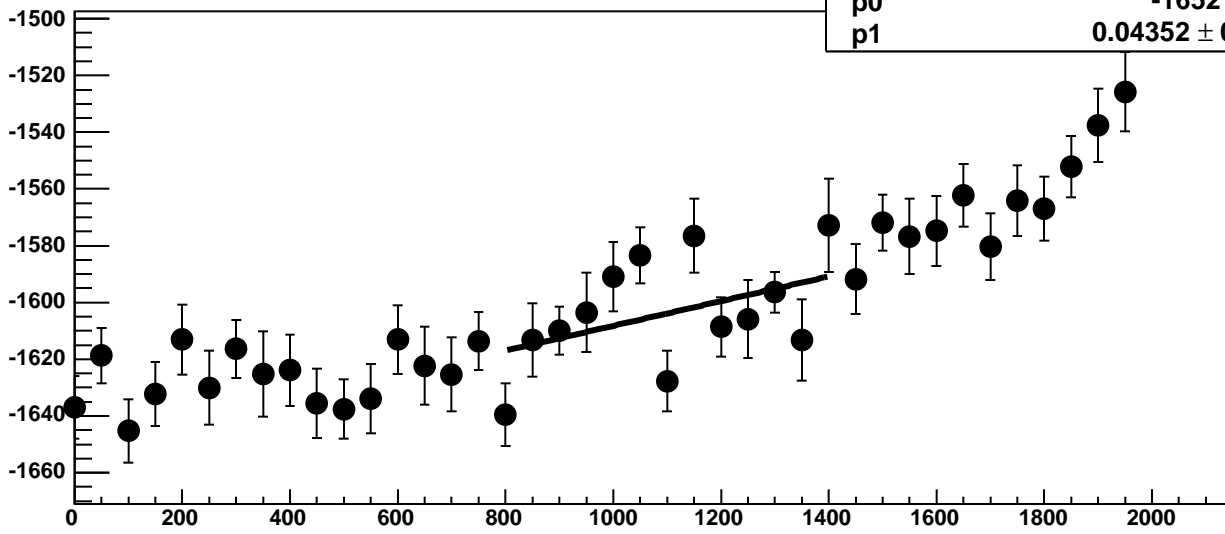
Chip 1, Channel 10, Enable 3, Hold=30, ADC Noise vs DAC



Chip 1, Channel 10, Enable 3, Hold=30, ADC Residuals vs DAC



Chip 1, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

24.74 / 11

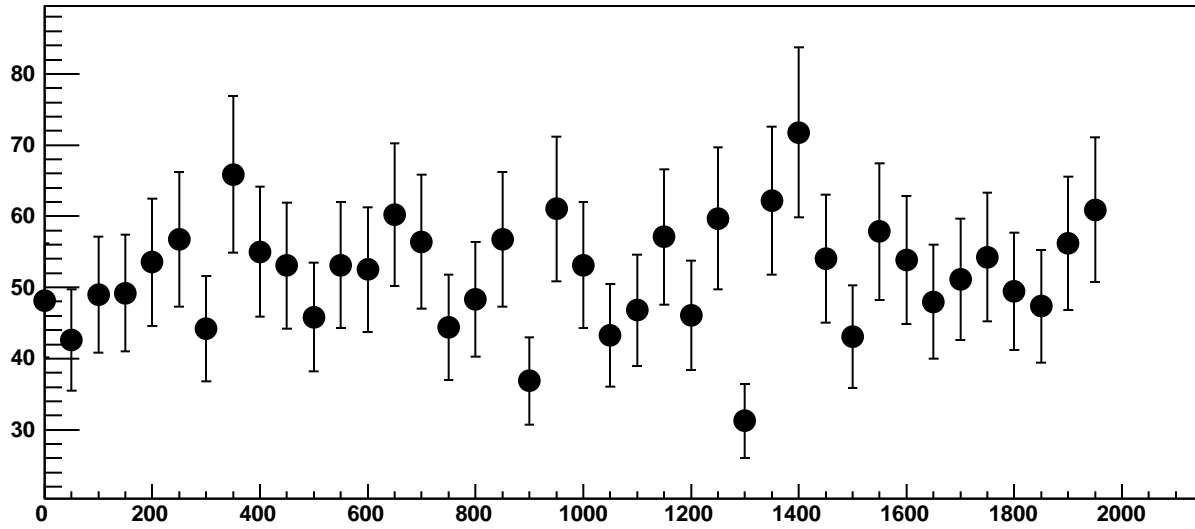
p0

$-1652 \pm 18.76$

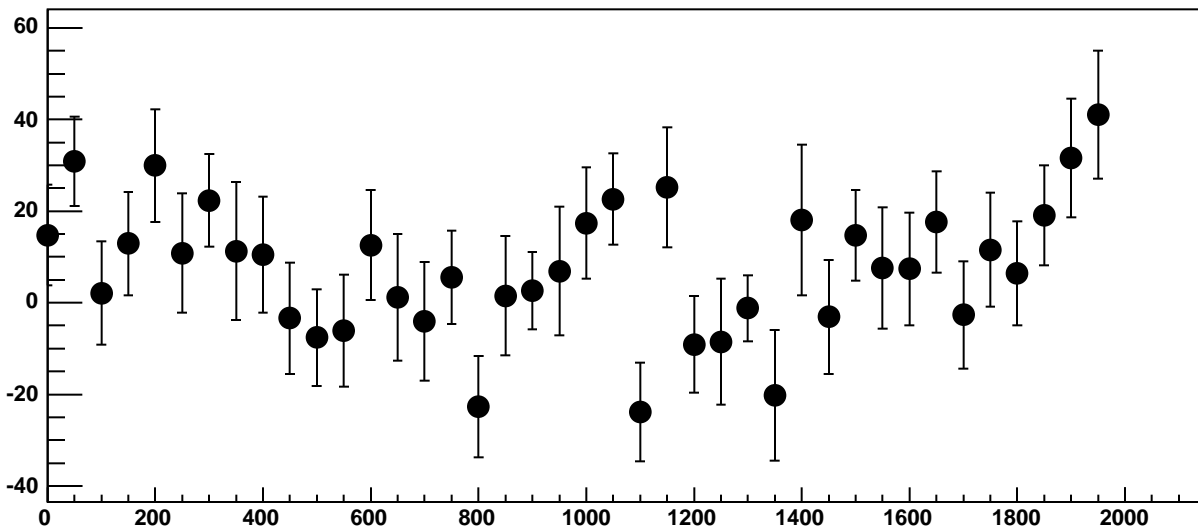
p1

$0.04352 \pm 0.01689$

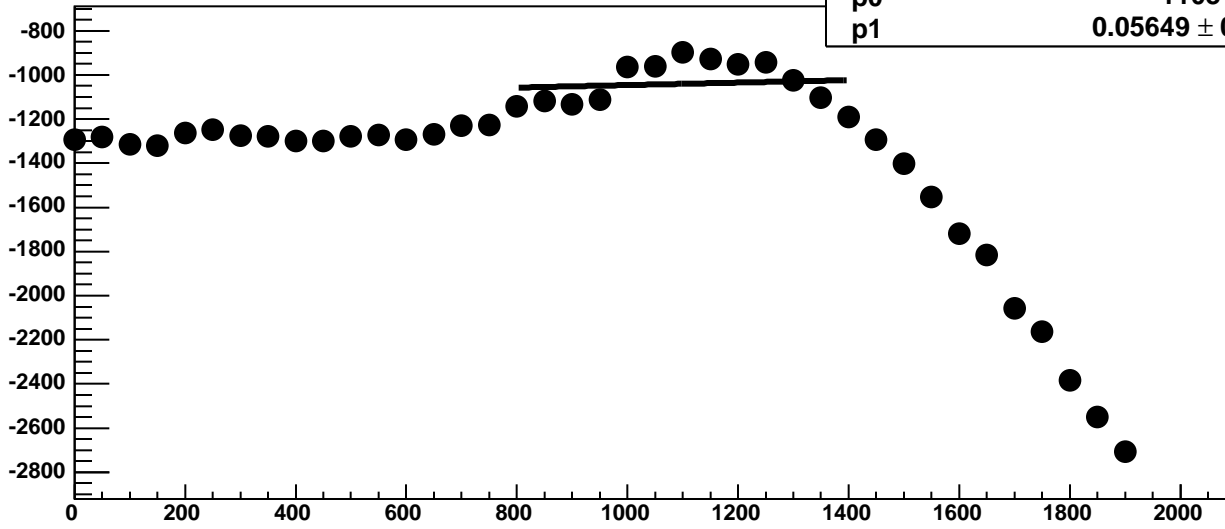
Chip 1, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC



Chip 1, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC



Chip 1, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

297.3 / 11

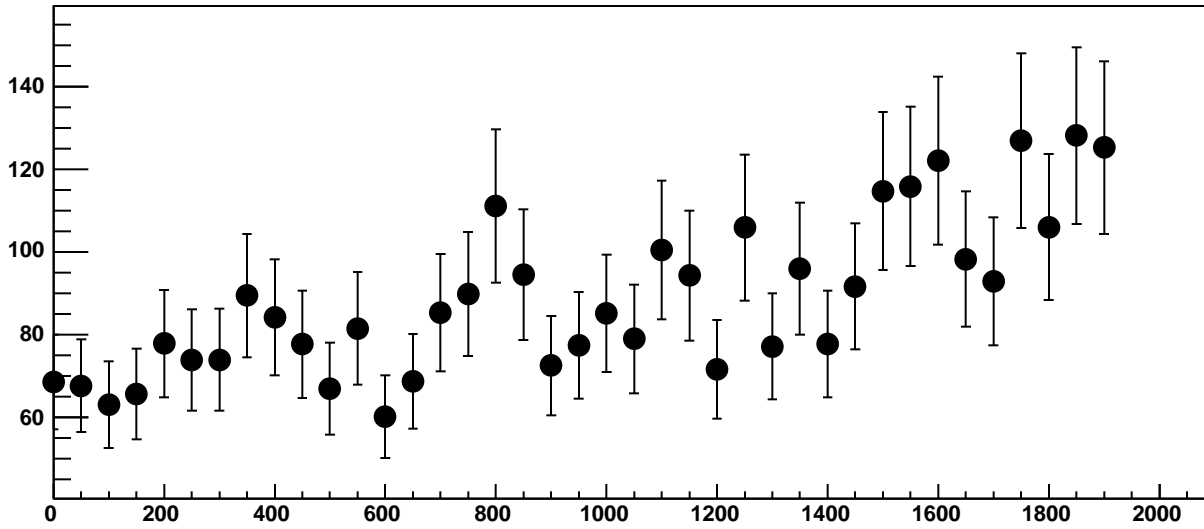
p0

$-1103 \pm 33.23$

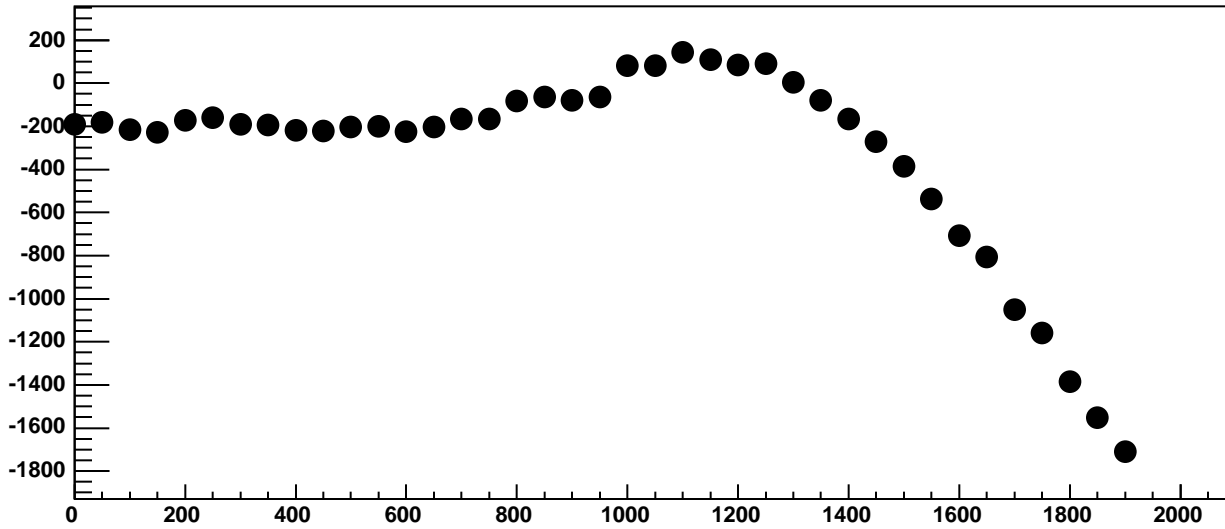
p1

$0.05649 \pm 0.02963$

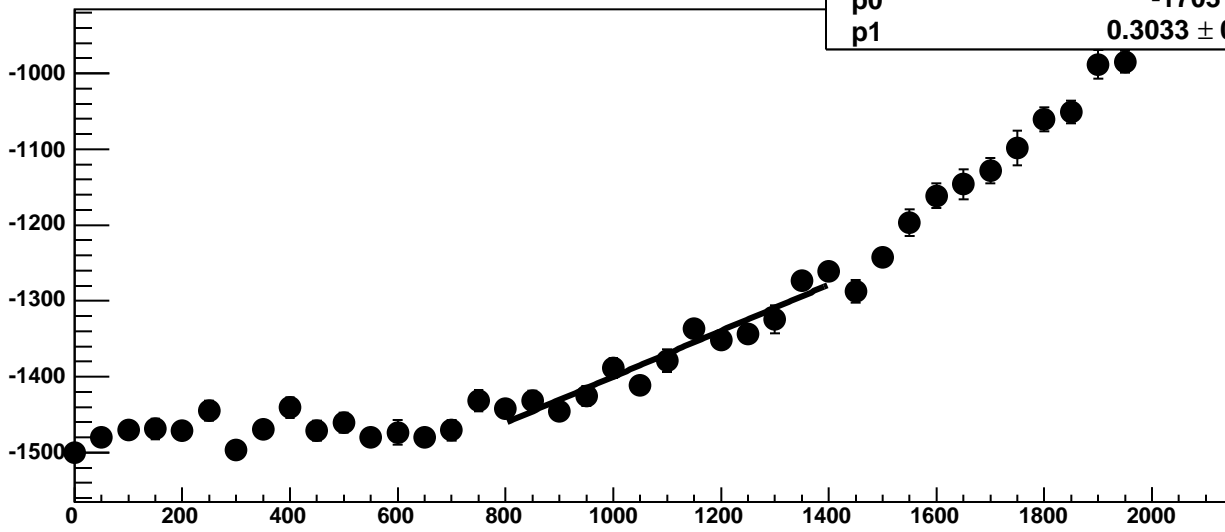
Chip 1, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC



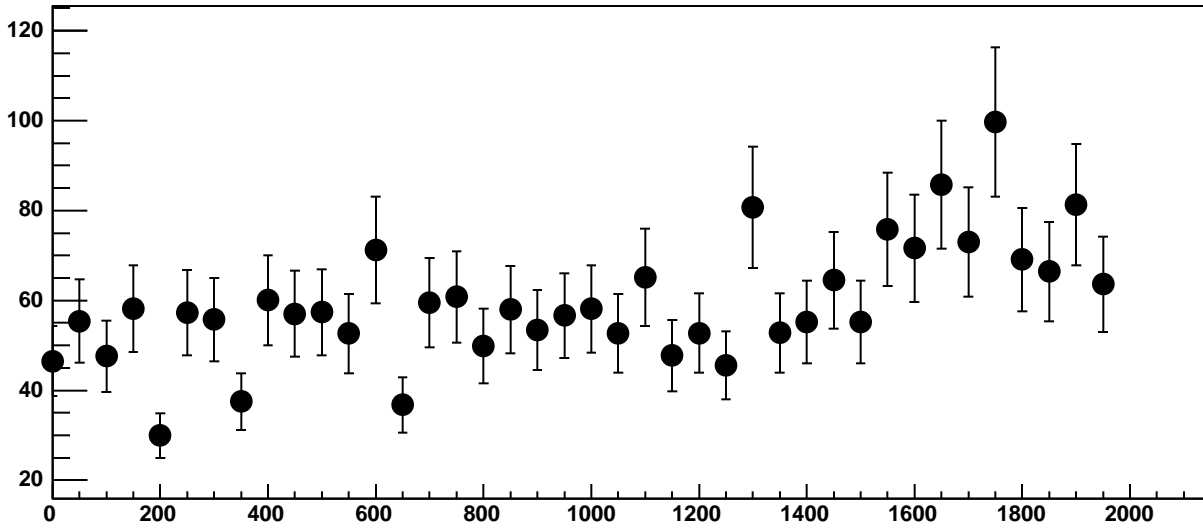
Chip 1, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC



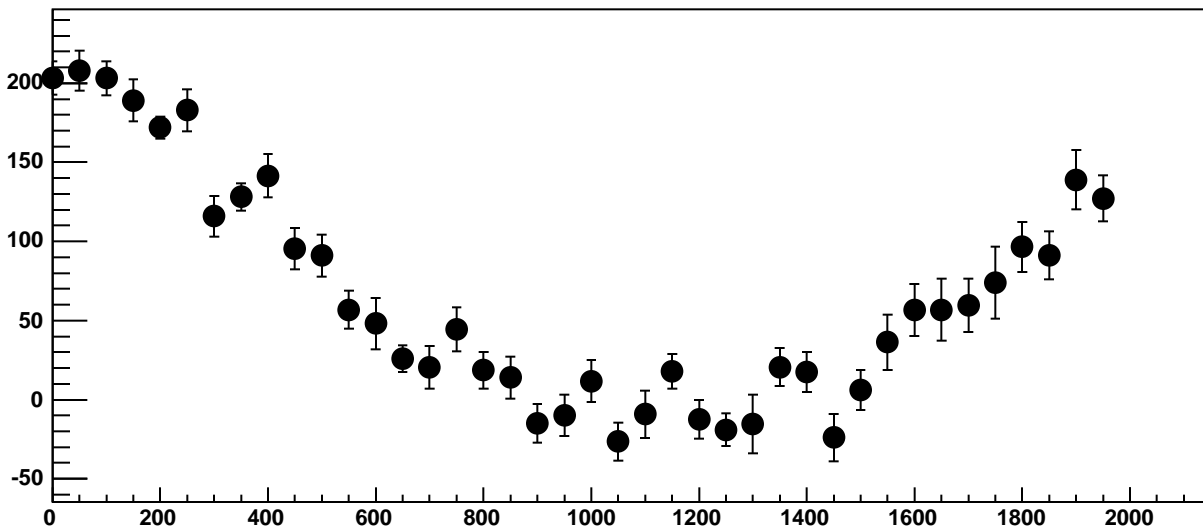
Chip 1, Channel 11, Enable 0, Hold=30, ADC Mean vs DAC



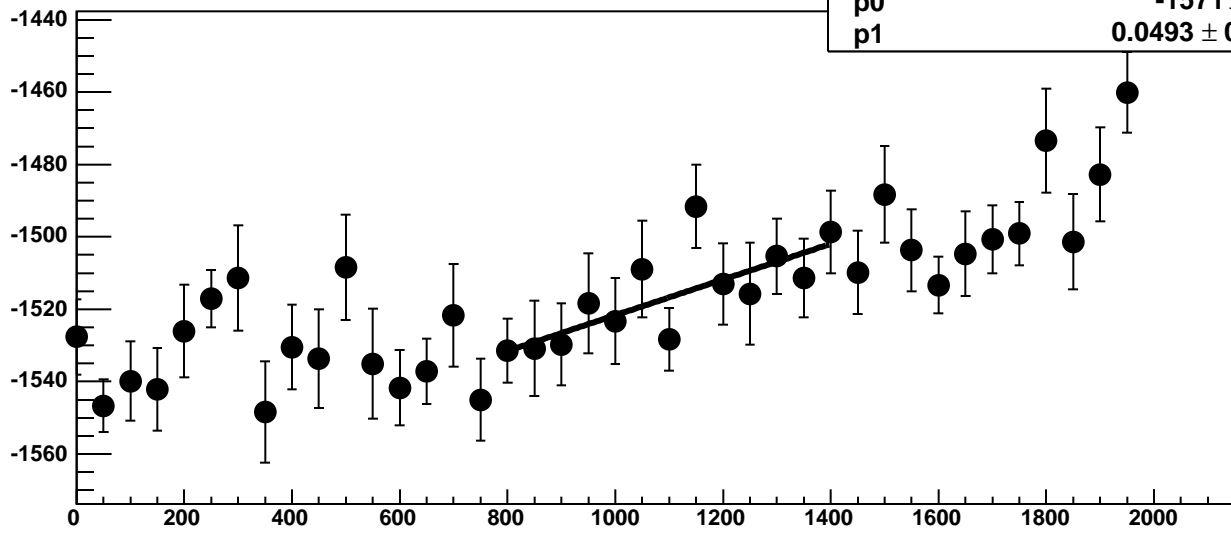
Chip 1, Channel 11, Enable 0, Hold=30, ADC Noise vs DAC



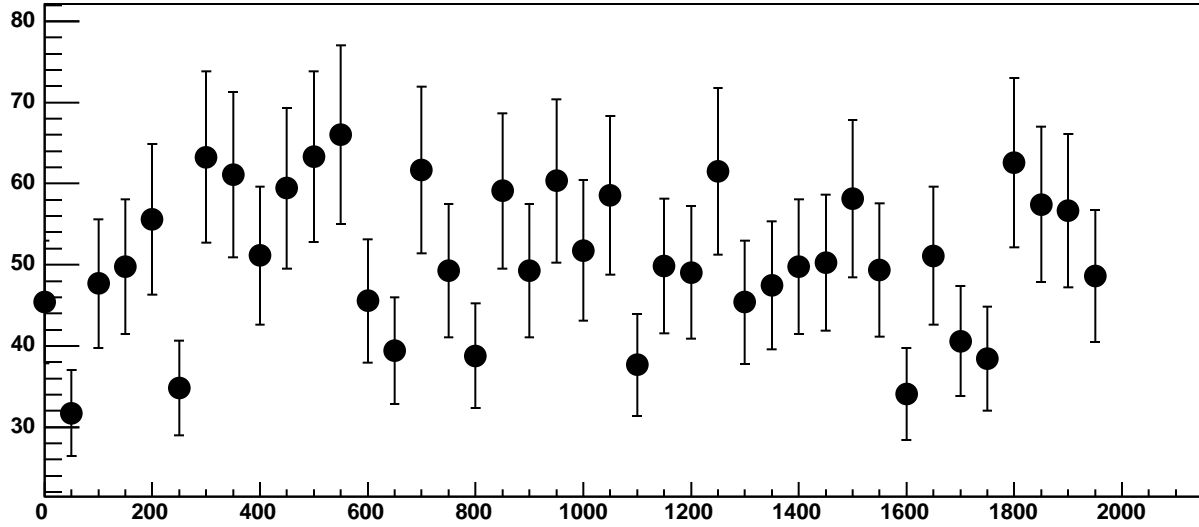
Chip 1, Channel 11, Enable 0, Hold=30, ADC Residuals vs DAC



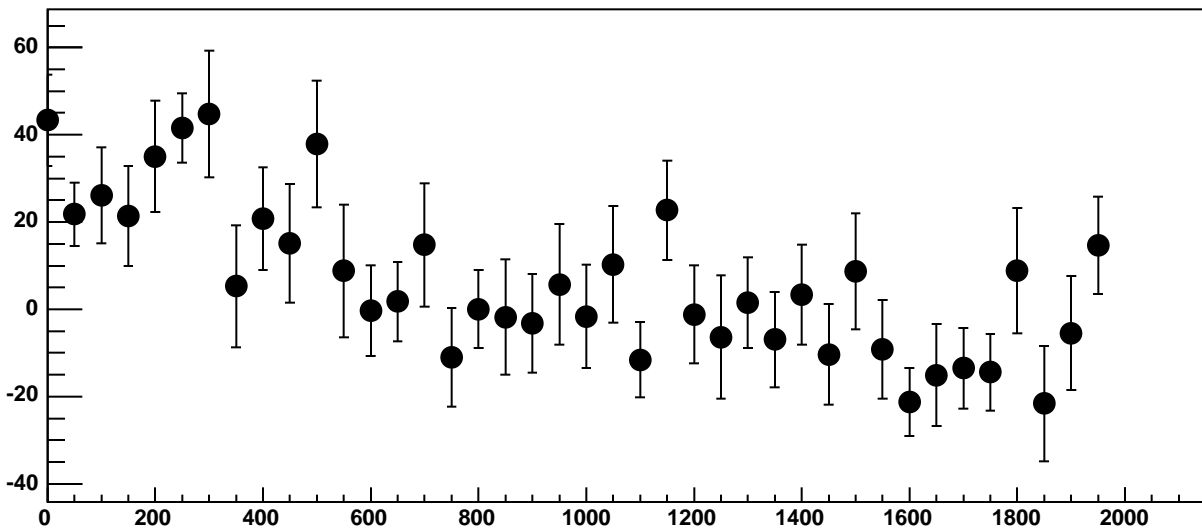
Chip 1, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC



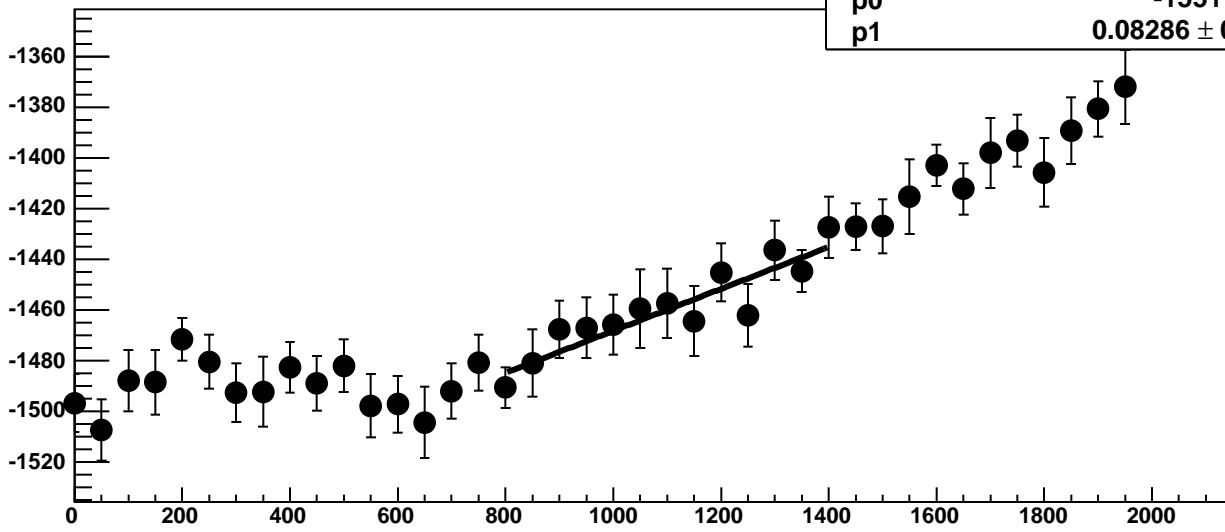
Chip 1, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC



Chip 1, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC

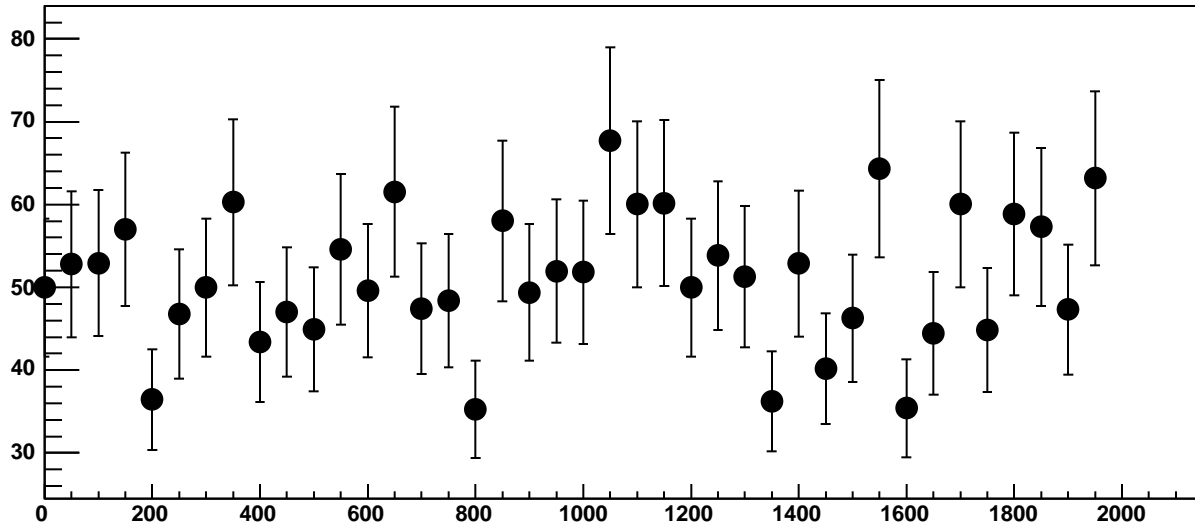


Chip 1, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC

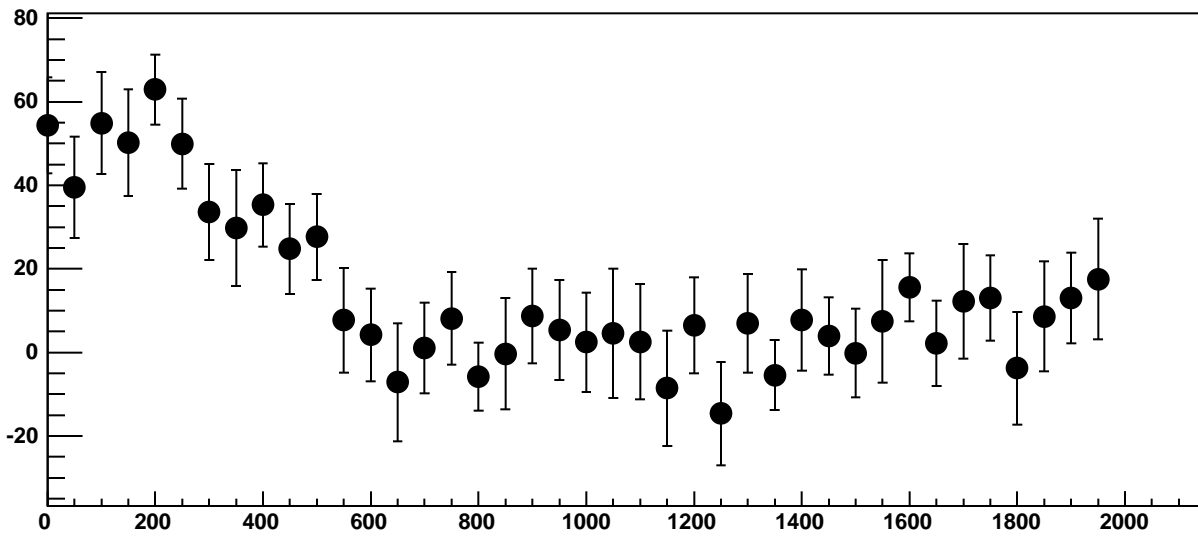


$\chi^2 / \text{ndf}$  4.782 / 11  
p0  $-1551 \pm 16.97$   
p1  $0.08286 \pm 0.01522$

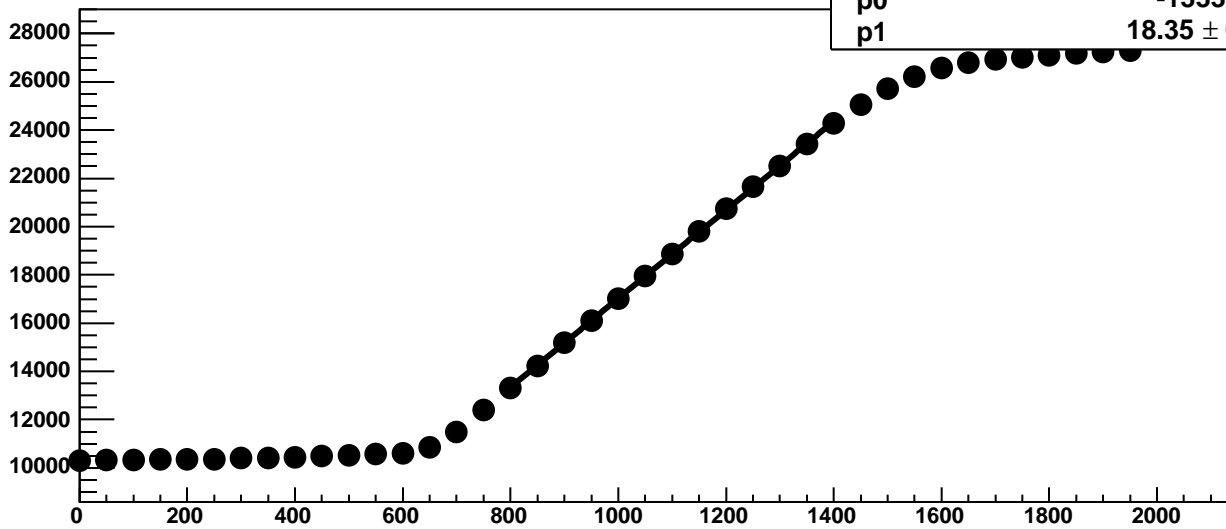
Chip 1, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC



Chip 1, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC

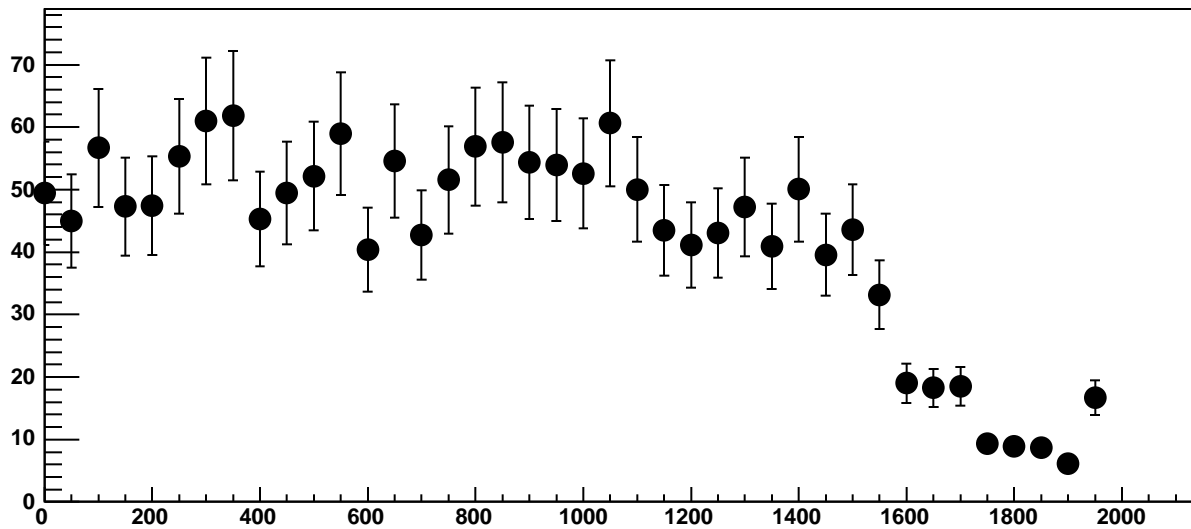


Chip 1, Channel 11, Enable 3!, Hold=30, ADC Mean vs DAC

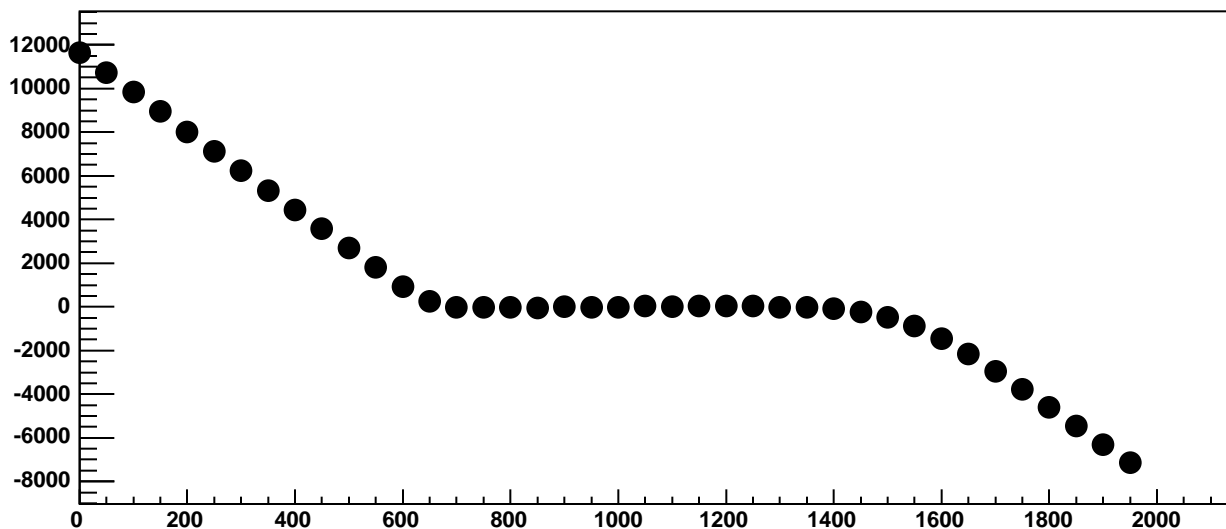


$\chi^2 / \text{ndf}$  138.4 / 11  
p0  $-1333 \pm 19.85$   
p1  $18.35 \pm 0.01728$

Chip 1, Channel 11, Enable 3!, Hold=30, ADC Noise vs DAC

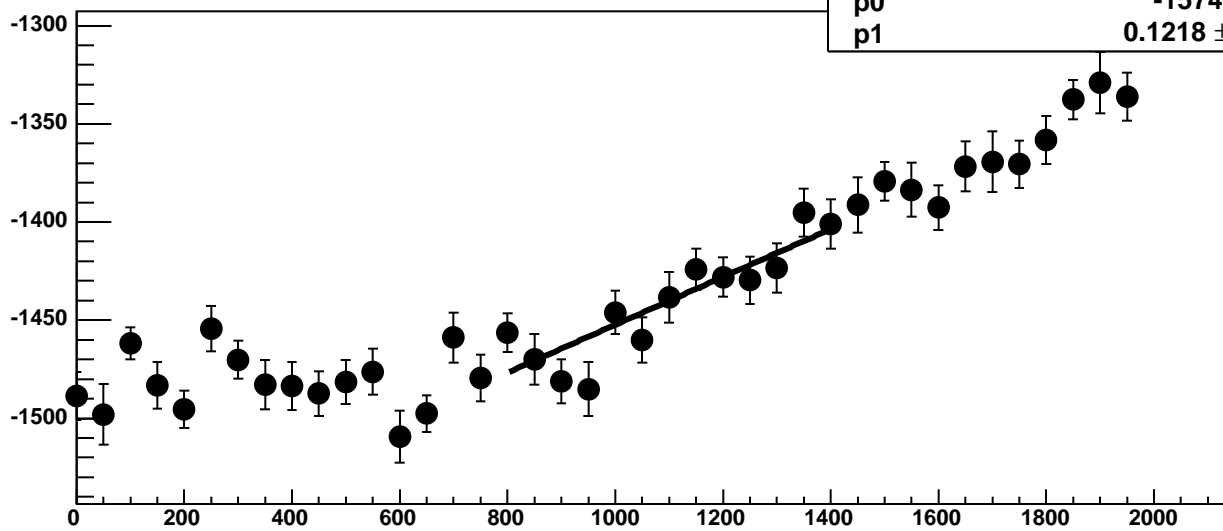


Chip 1, Channel 11, Enable 3!, Hold=30, ADC Residuals vs DAC

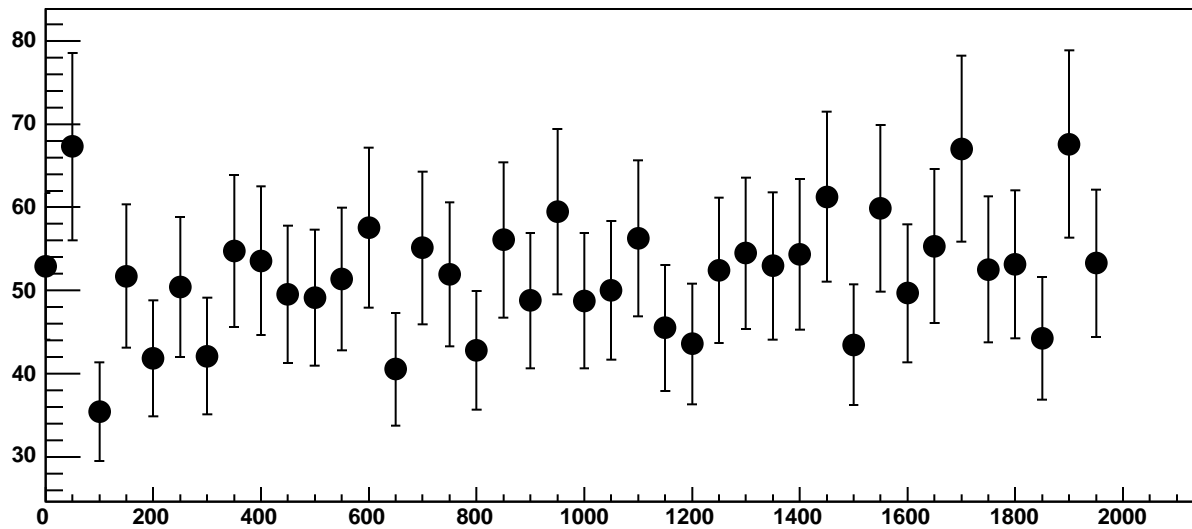




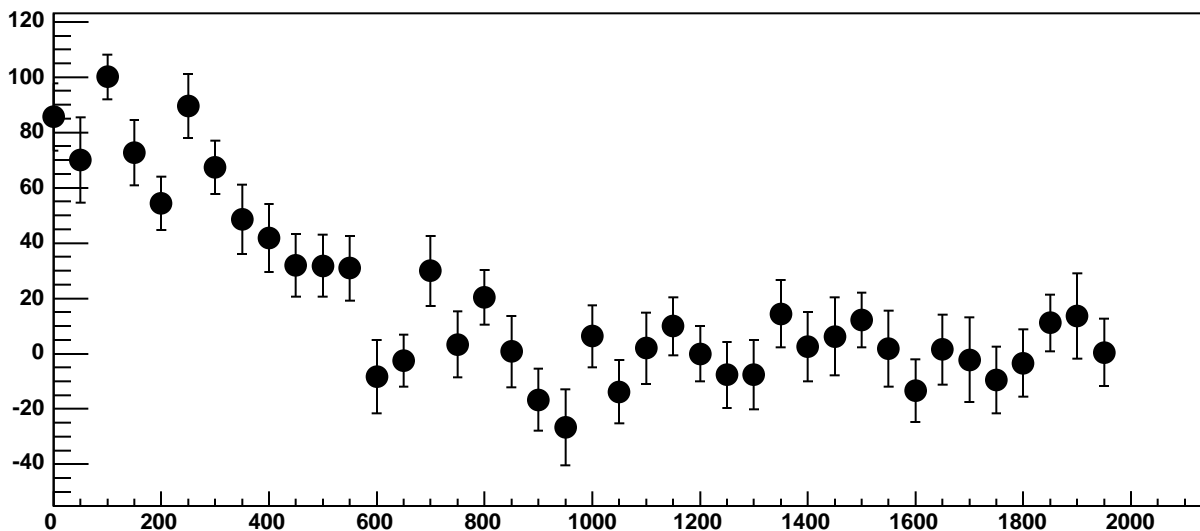
Chip 1, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC



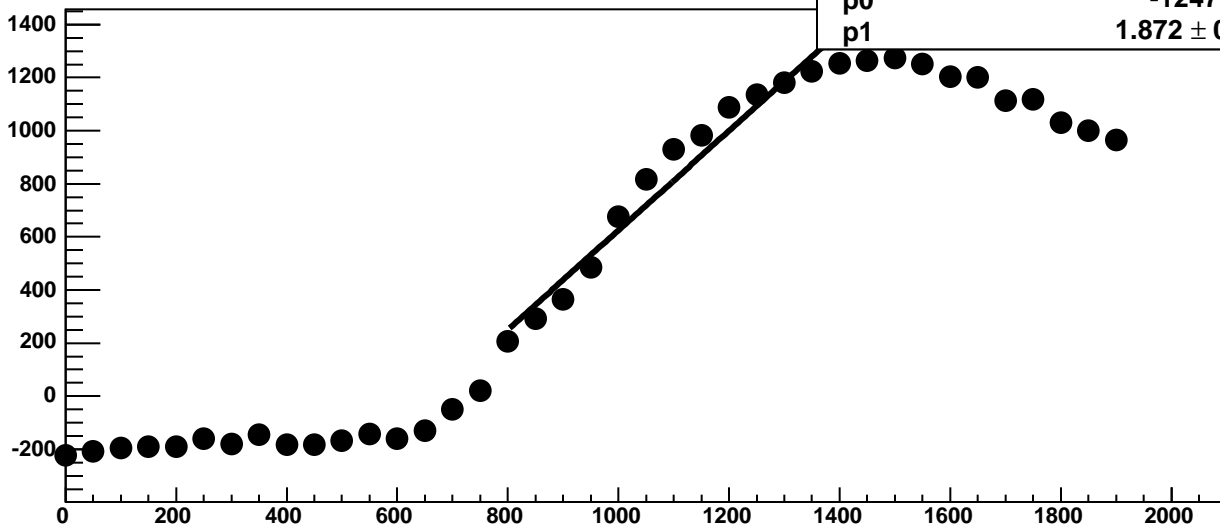
Chip 1, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC



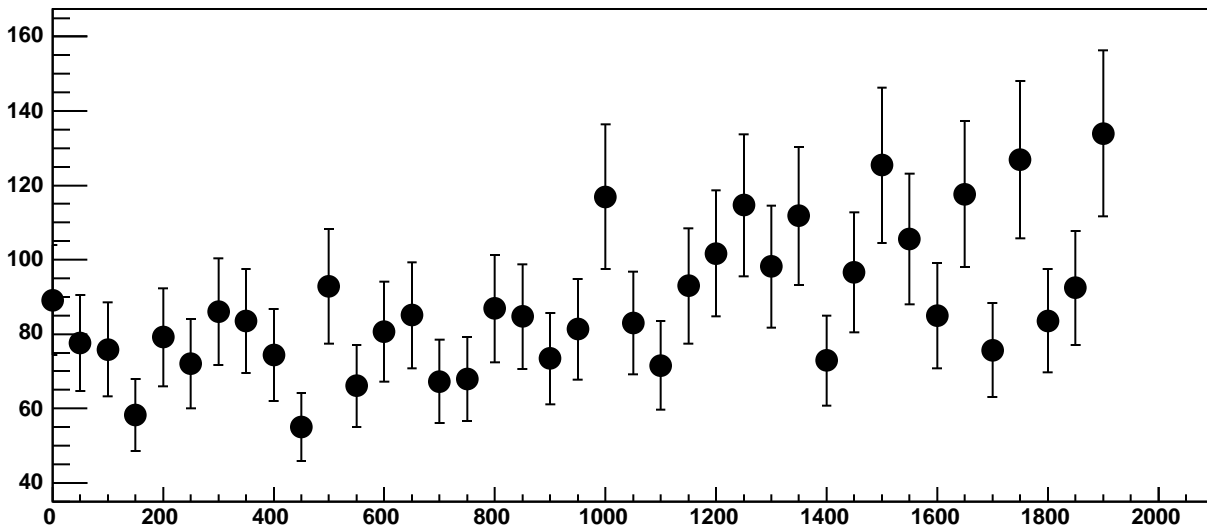
Chip 1, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC



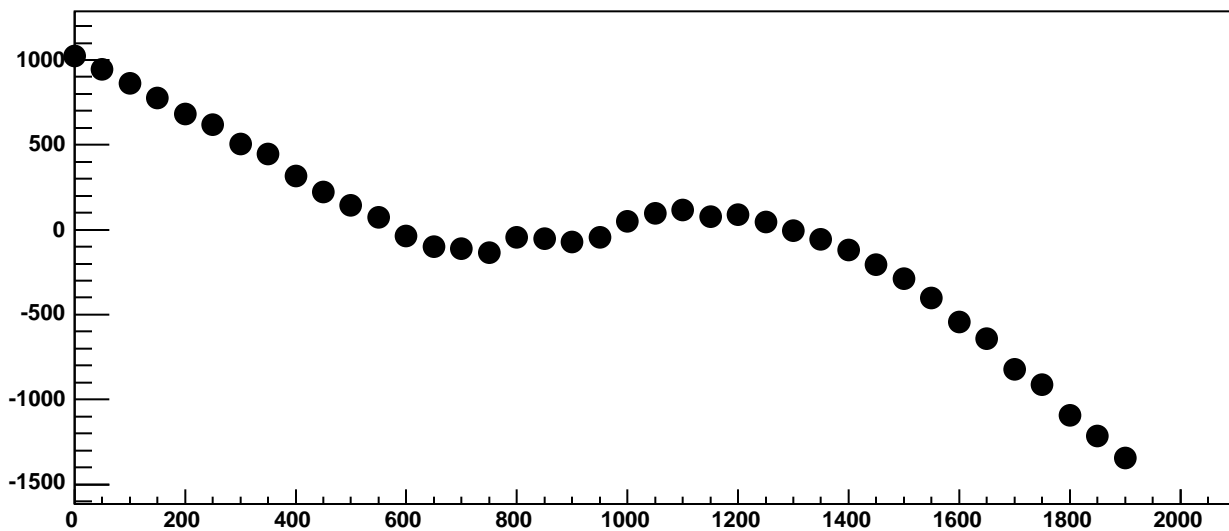
Chip 1, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC



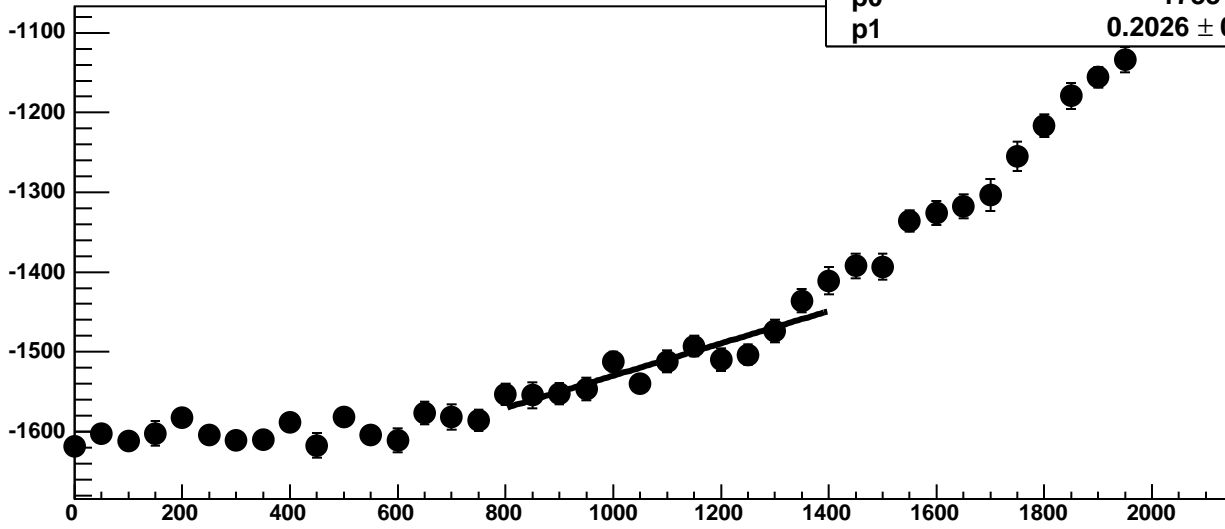
Chip 1, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC



Chip 1, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC

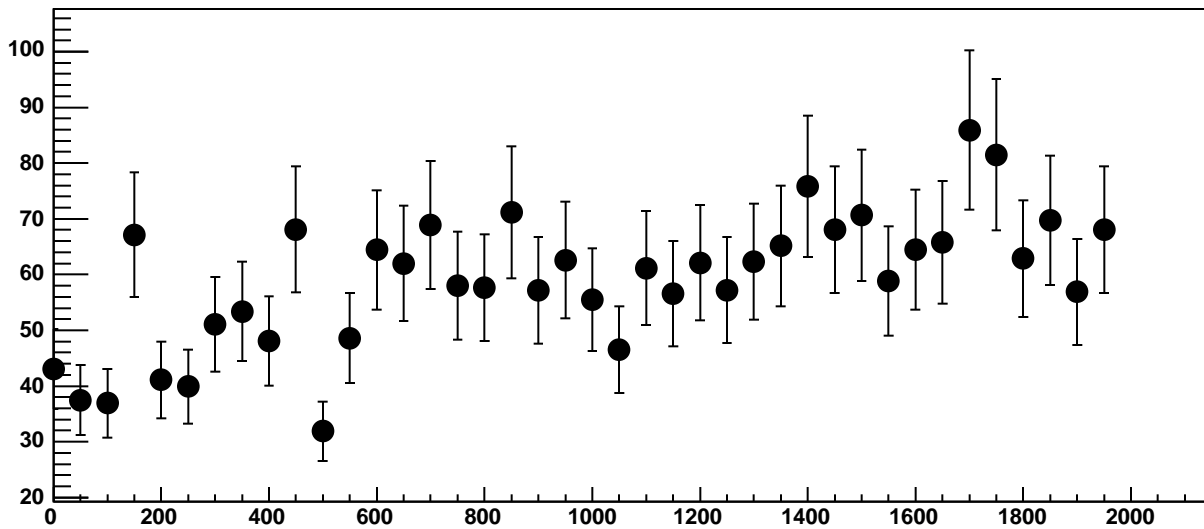


Chip 1, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC

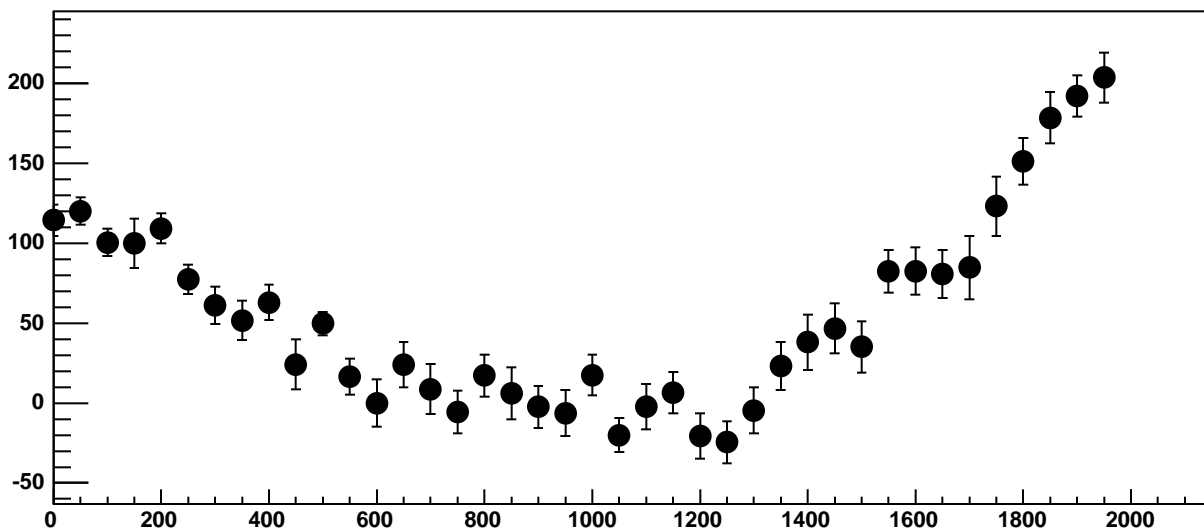


$\chi^2 / \text{ndf}$  20.59 / 11  
p0  $-1733 \pm 23.87$   
p1  $0.2026 \pm 0.02166$

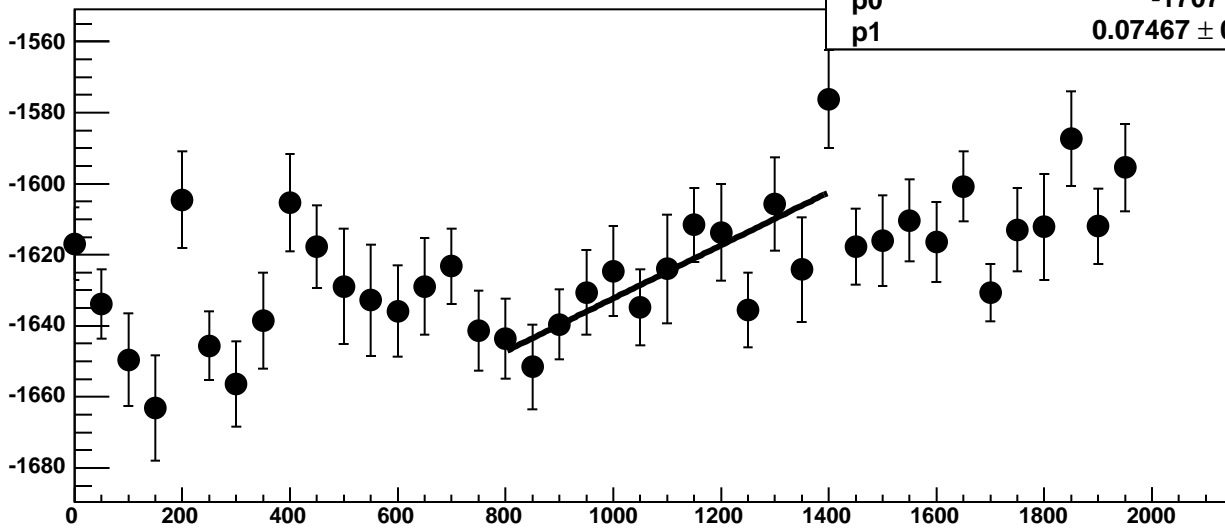
Chip 1, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC



Chip 1, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC

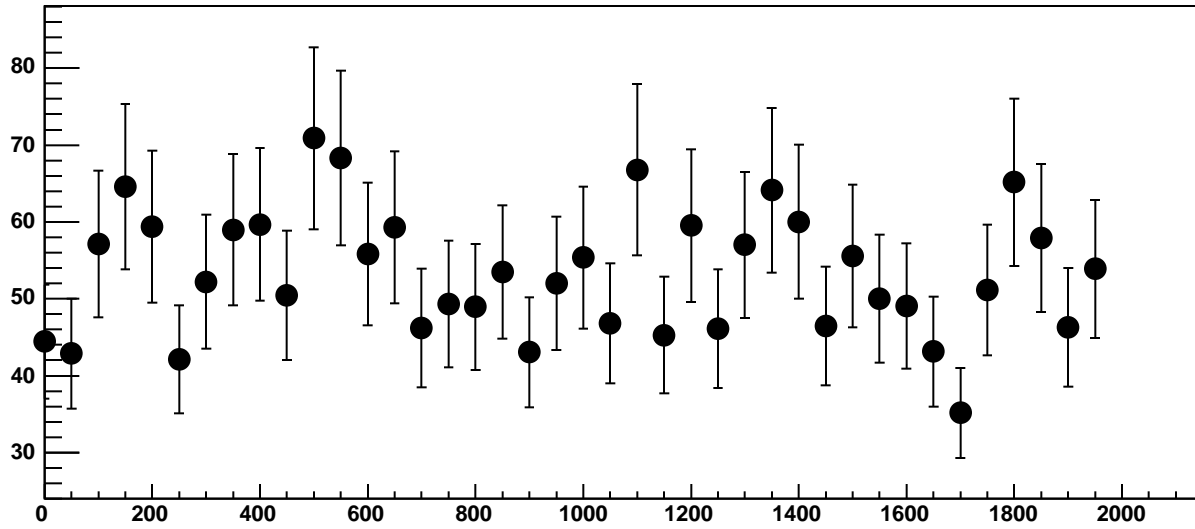


Chip 1, Channel 12, Enable 1, Hold=30, ADC Mean vs DAC

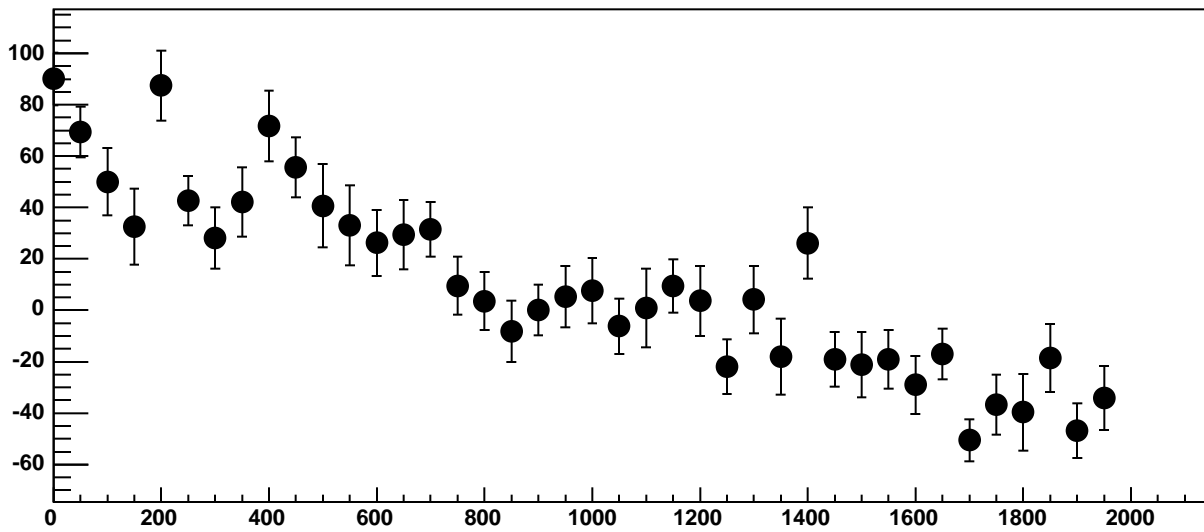


$\chi^2 / \text{ndf}$  11.89 / 11  
p0  $-1707 \pm 19.85$   
p1  $0.07467 \pm 0.01816$

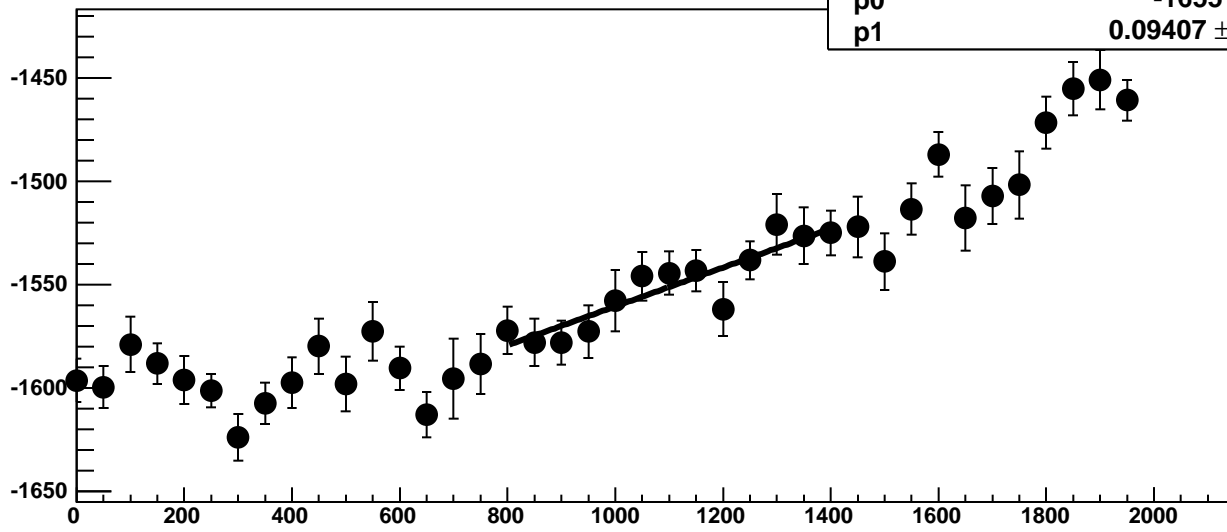
Chip 1, Channel 12, Enable 1, Hold=30, ADC Noise vs DAC



Chip 1, Channel 12, Enable 1, Hold=30, ADC Residuals vs DAC

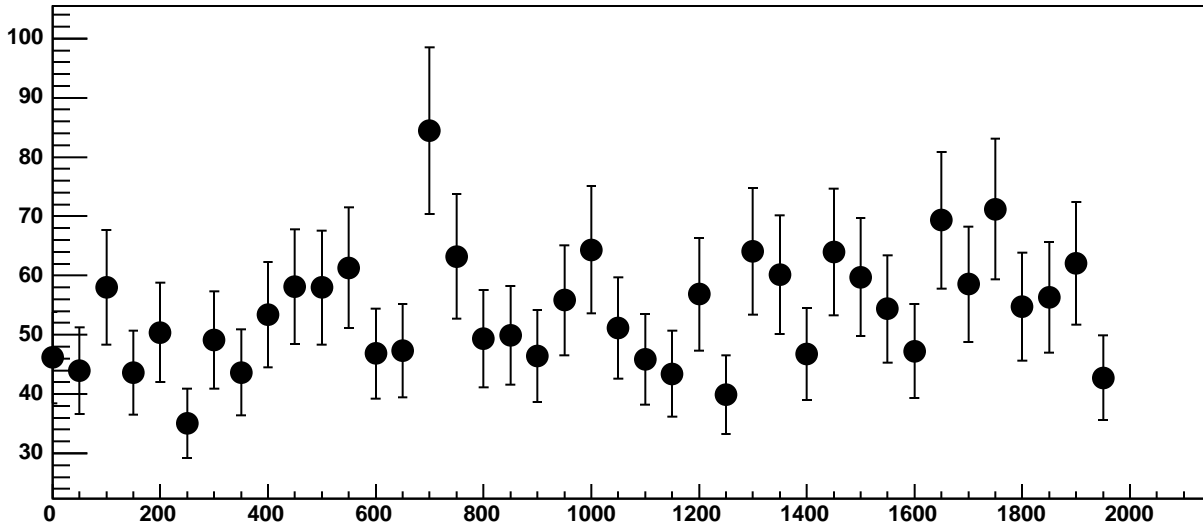


Chip 1, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC

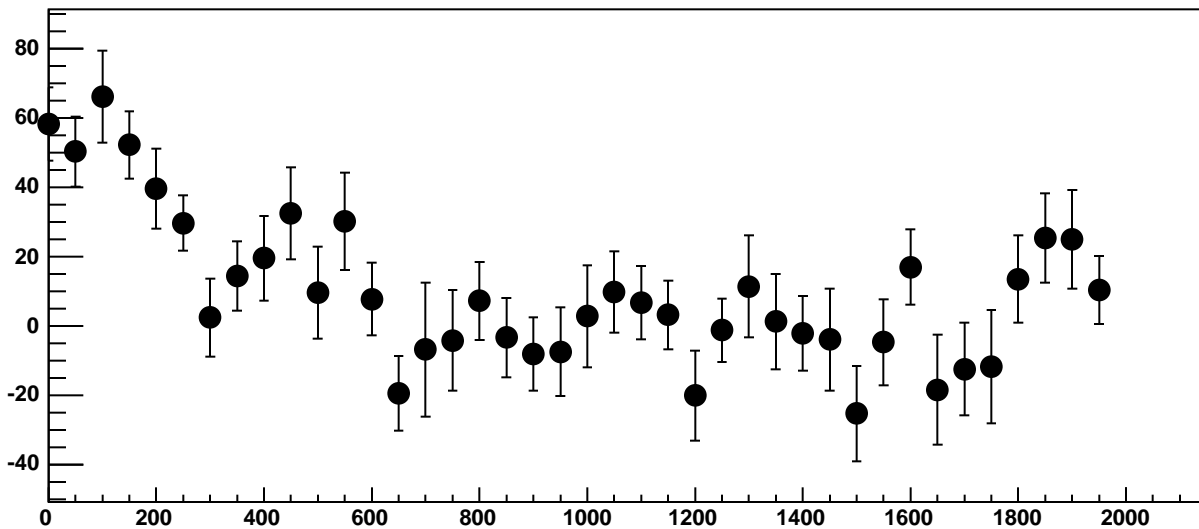


$\chi^2 / \text{ndf}$  5.705 / 11  
p0  $-1655 \pm 19.18$   
p1  $0.09407 \pm 0.0172$

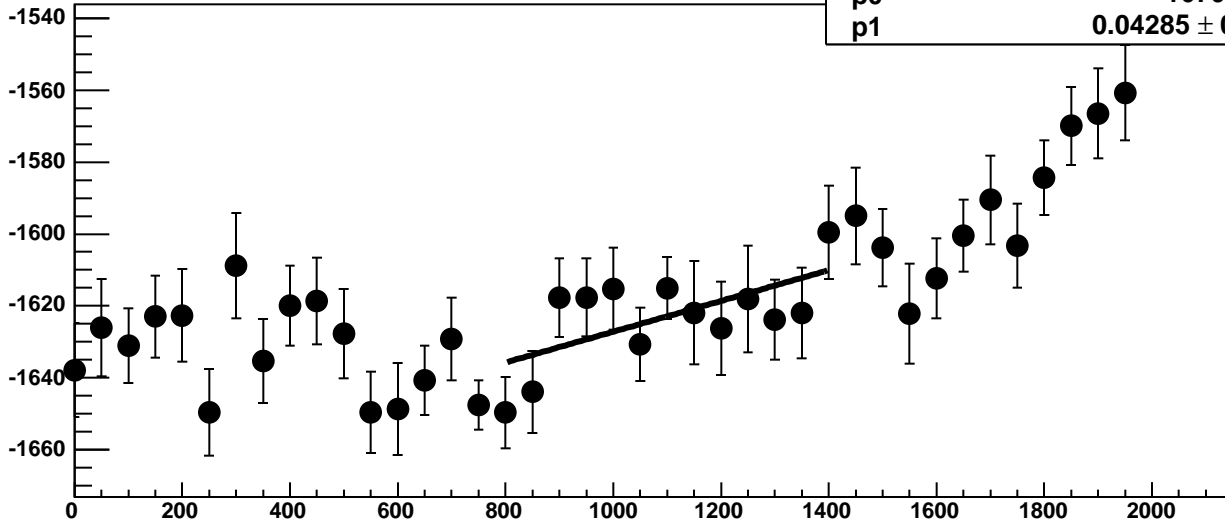
Chip 1, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC



Chip 1, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC

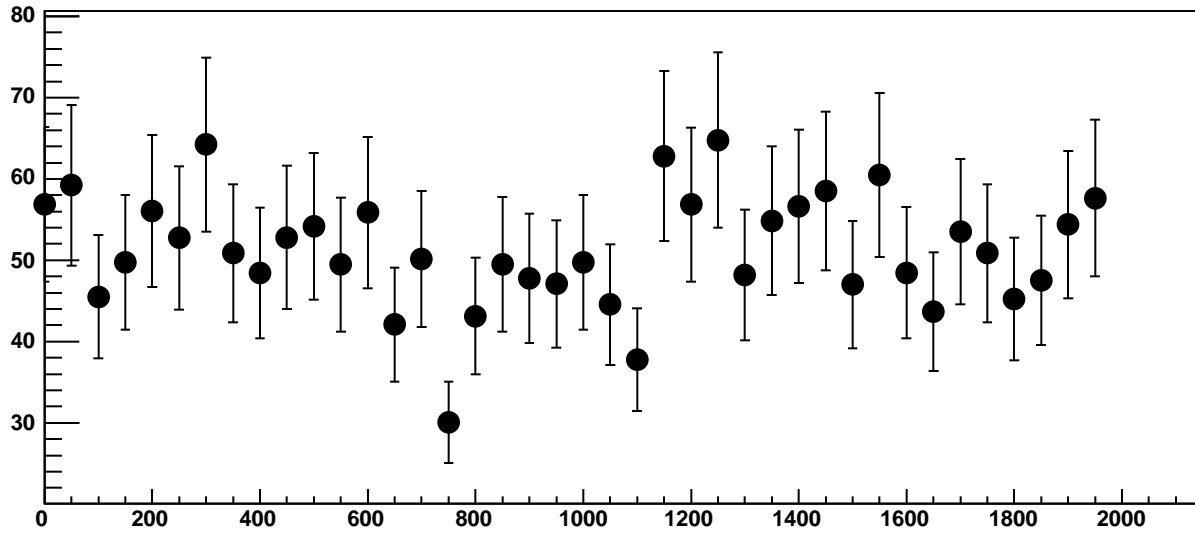


Chip 1, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC

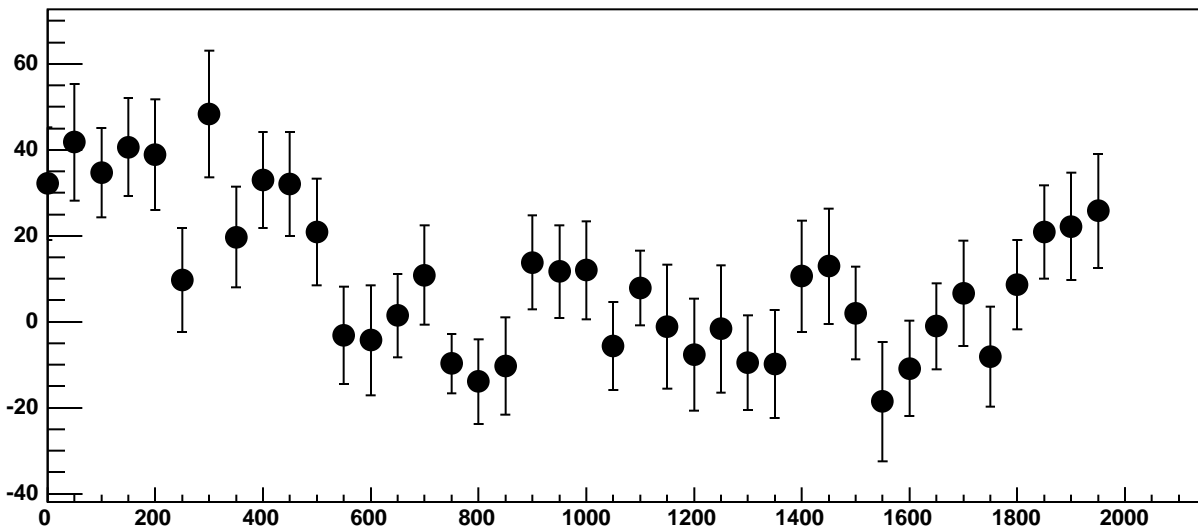


$\chi^2 / \text{ndf}$  10.17 / 11  
p0  $-1670 \pm 18.71$   
p1  $0.04285 \pm 0.01718$

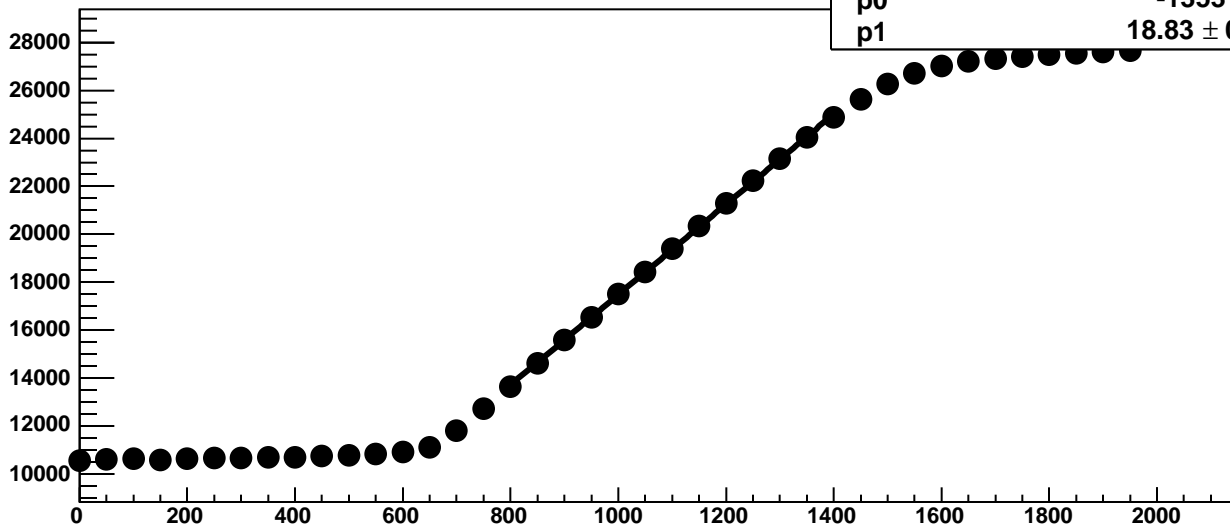
Chip 1, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC



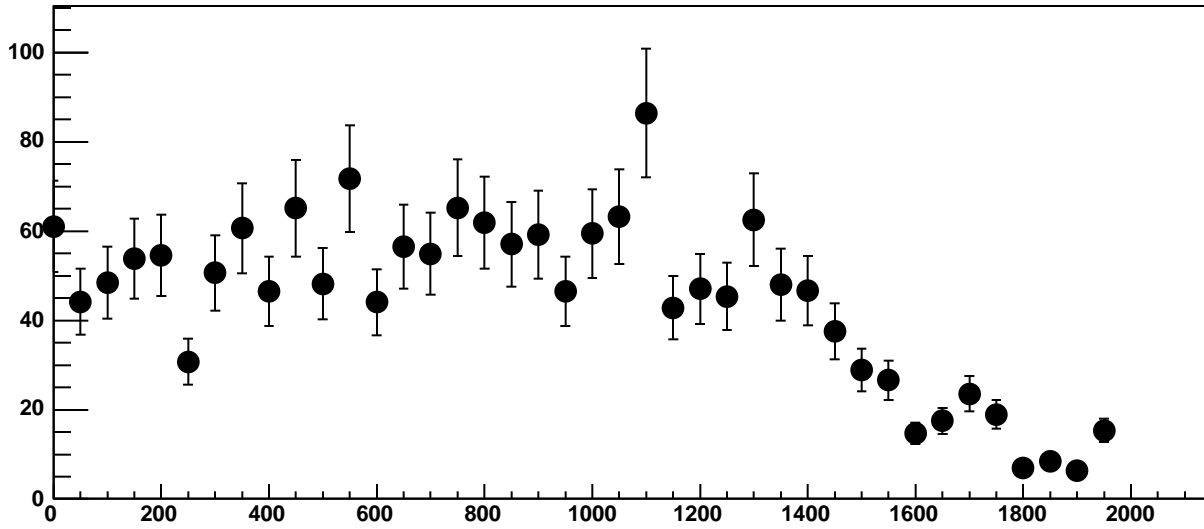
Chip 1, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC



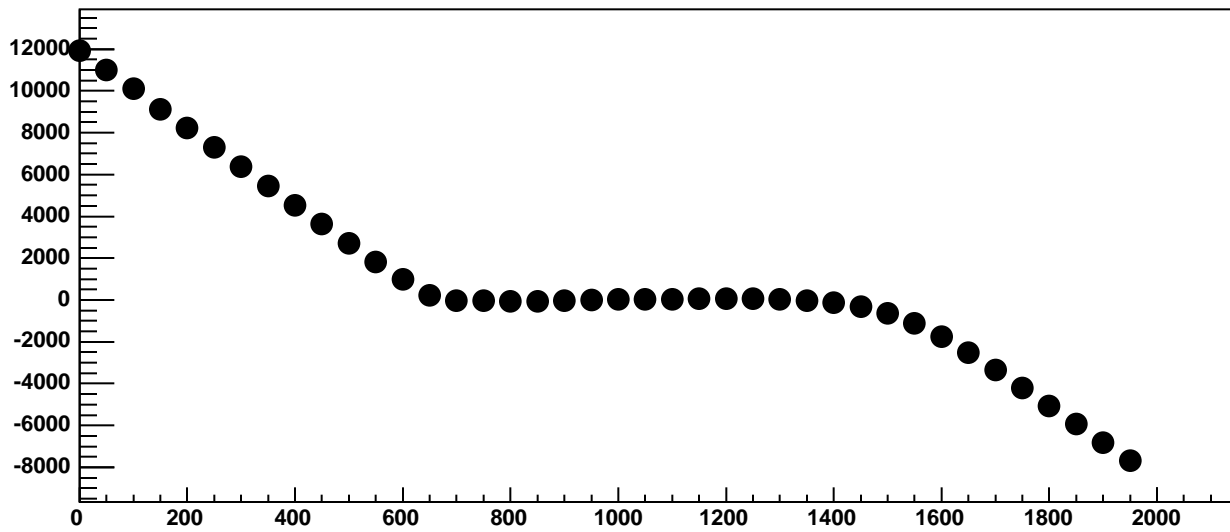
Chip 1, Channel 12, Enable 4!, Hold=30, ADC Mean vs DAC



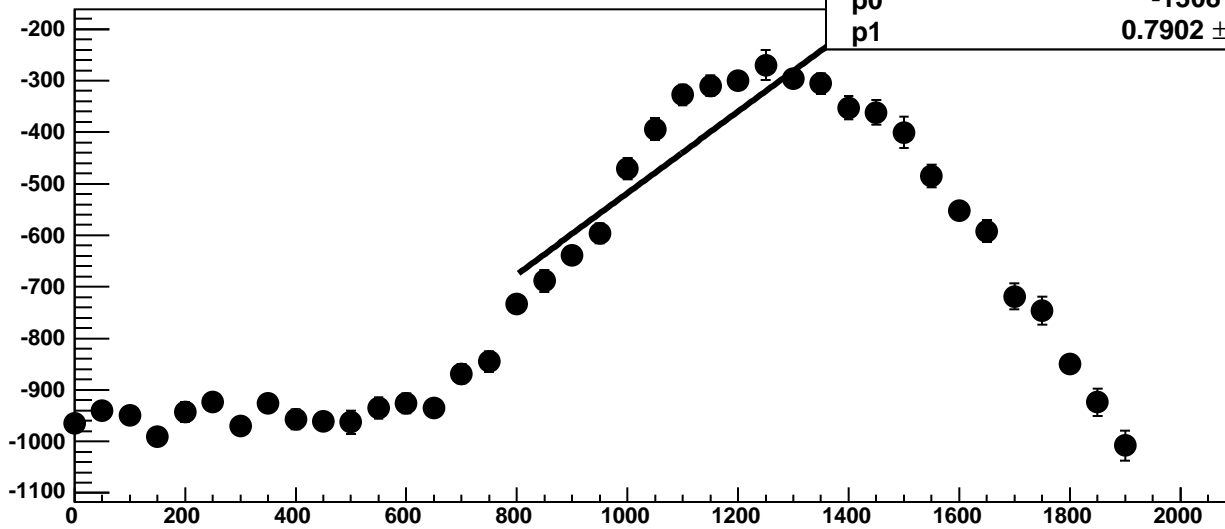
Chip 1, Channel 12, Enable 4!, Hold=30, ADC Noise vs DAC



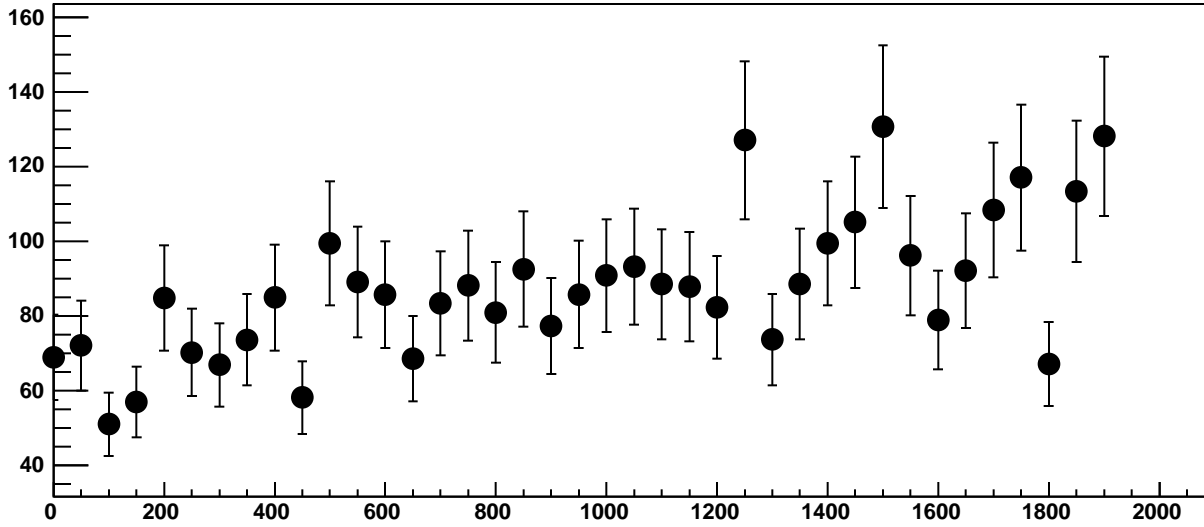
Chip 1, Channel 12, Enable 4!, Hold=30, ADC Residuals vs DAC



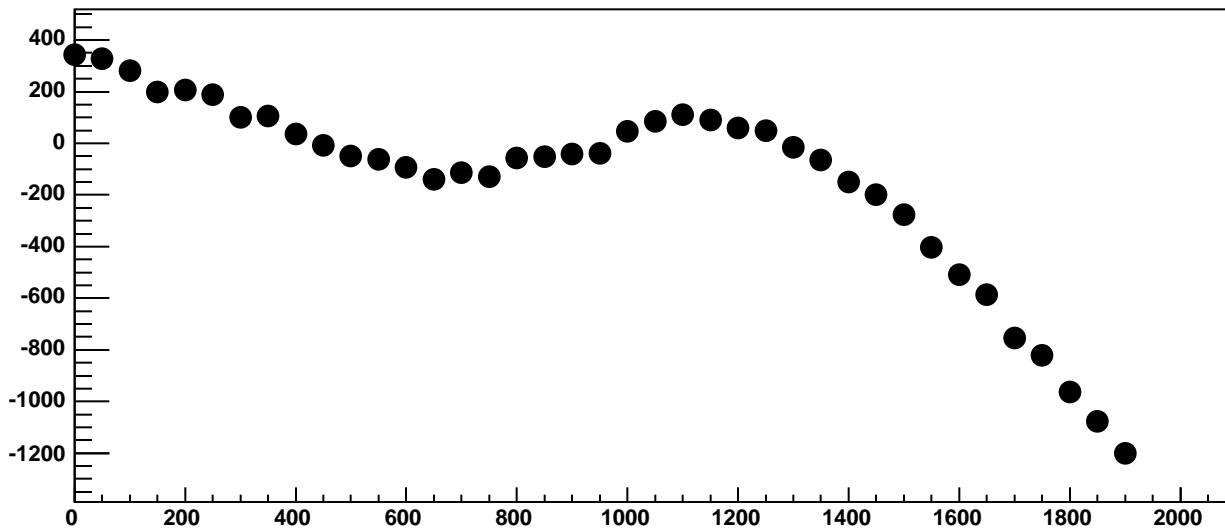
Chip 1, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC



Chip 1, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC

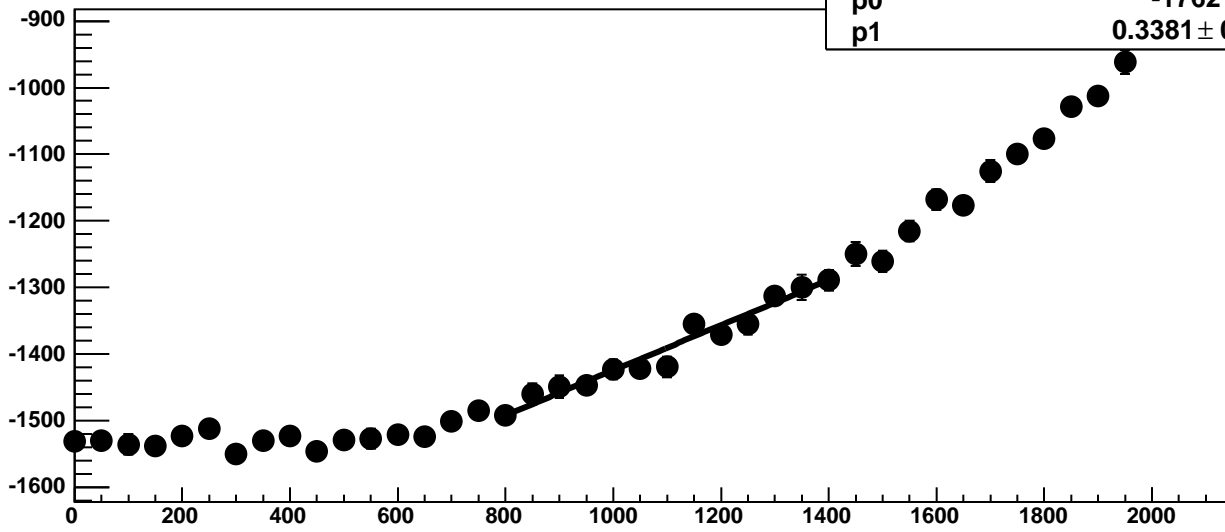


Chip 1, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC



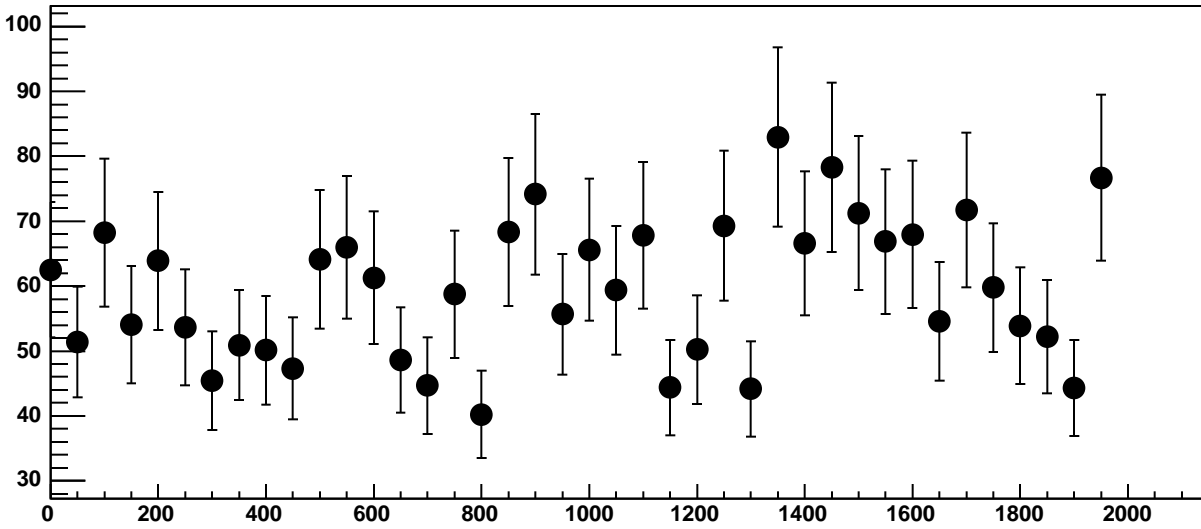


Chip 1, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC

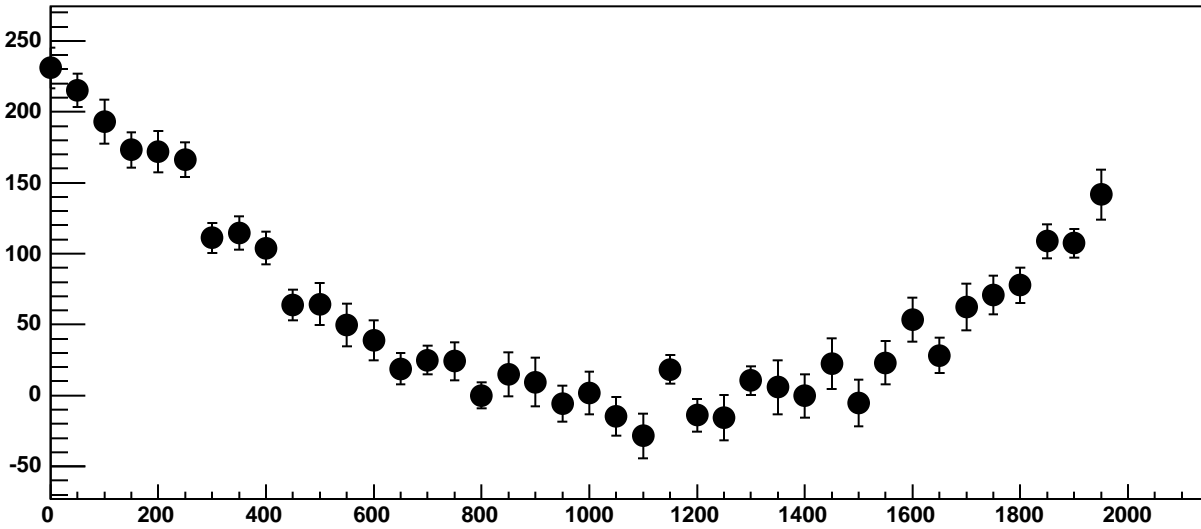


$\chi^2 / \text{ndf}$  12.76 / 11  
p0  $-1762 \pm 20.96$   
p1  $0.3381 \pm 0.01902$

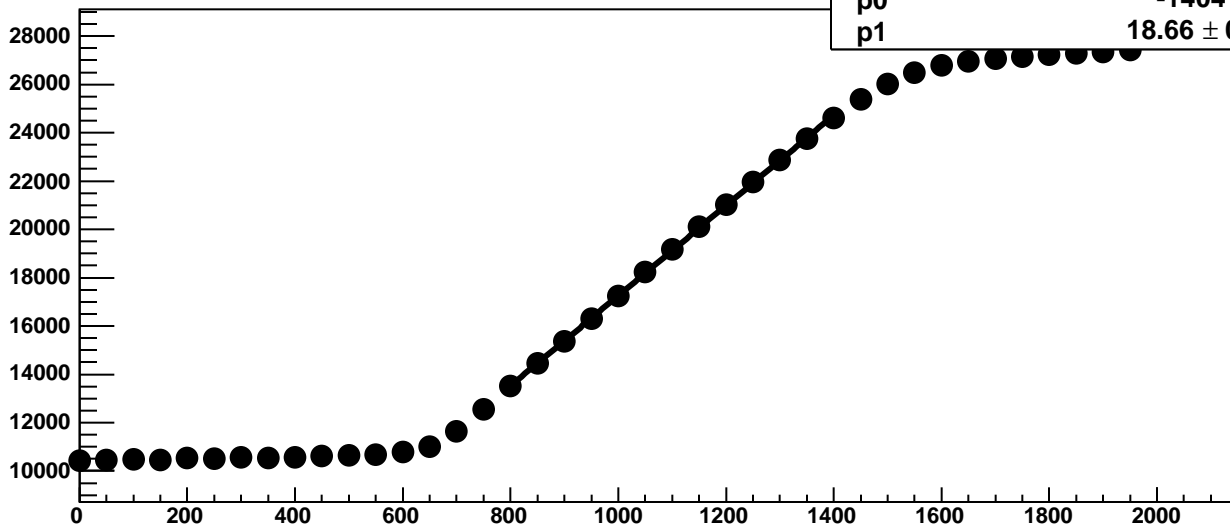
Chip 1, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC



Chip 1, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC

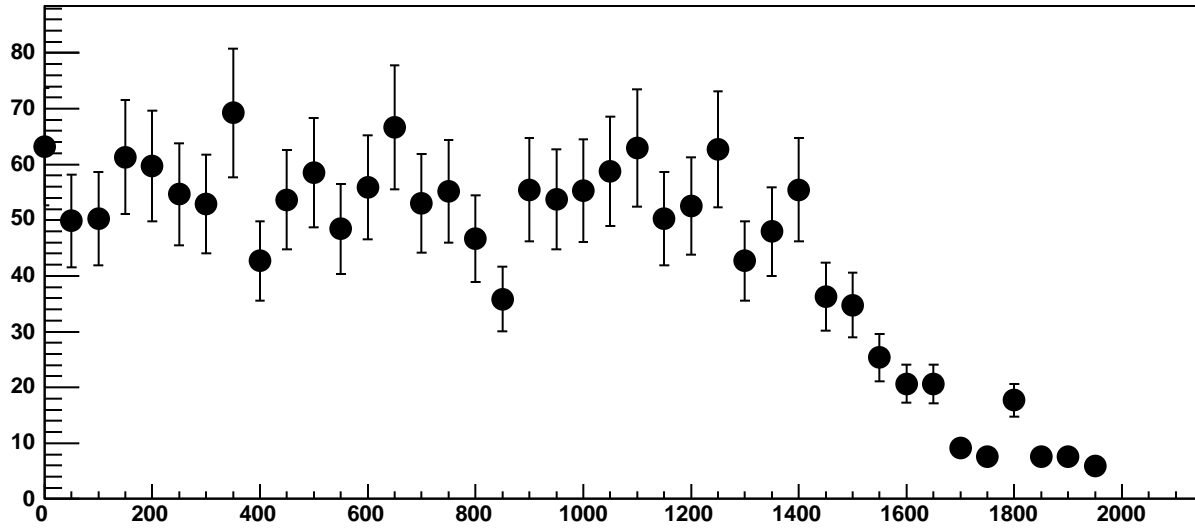


Chip 1, Channel 13, Enable 1!, Hold=30, ADC Mean vs DAC

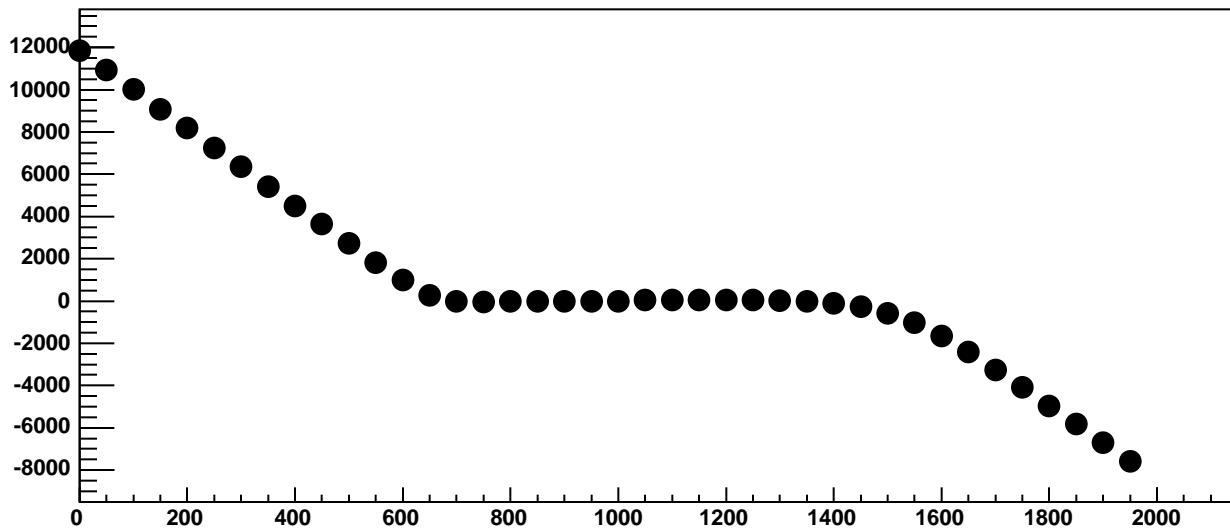


$\chi^2 / \text{ndf}$	159.5 / 11
p0	$-1404 \pm 17.65$
p1	$18.66 \pm 0.01605$

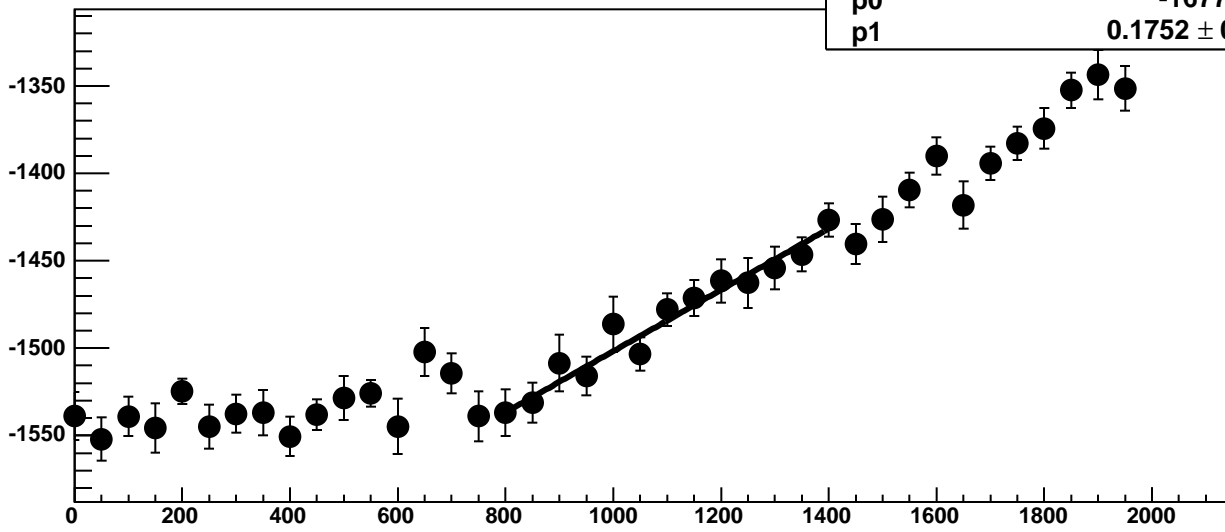
Chip 1, Channel 13, Enable 1!, Hold=30, ADC Noise vs DAC



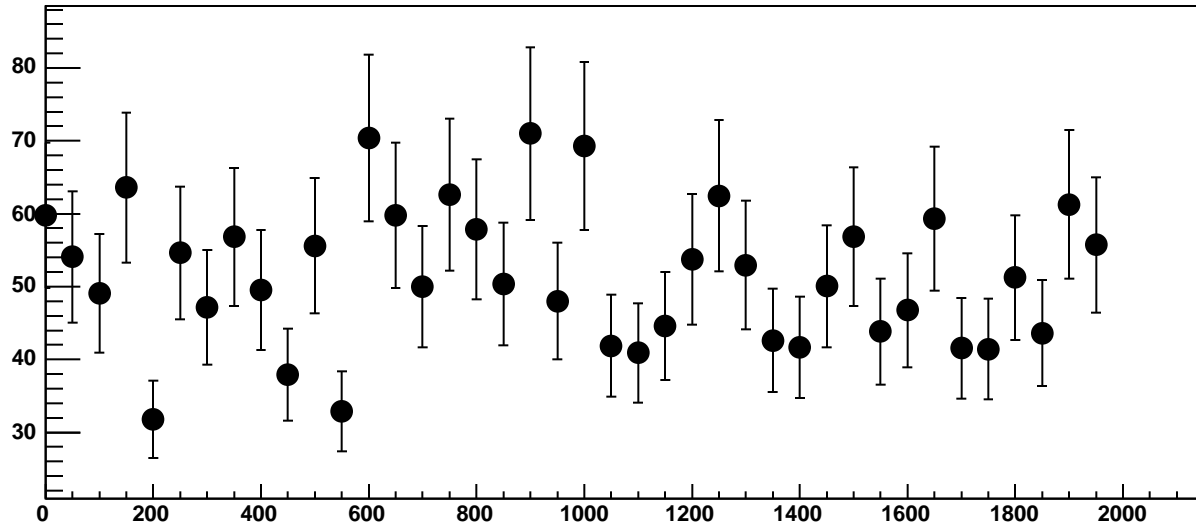
Chip 1, Channel 13, Enable 1!, Hold=30, ADC Residuals vs DAC



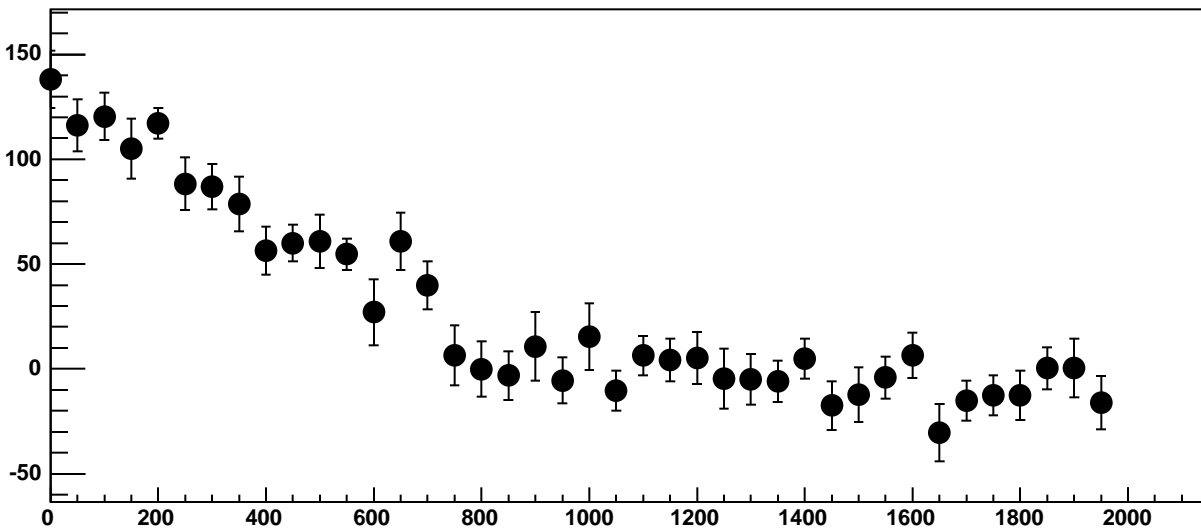
Chip 1, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC



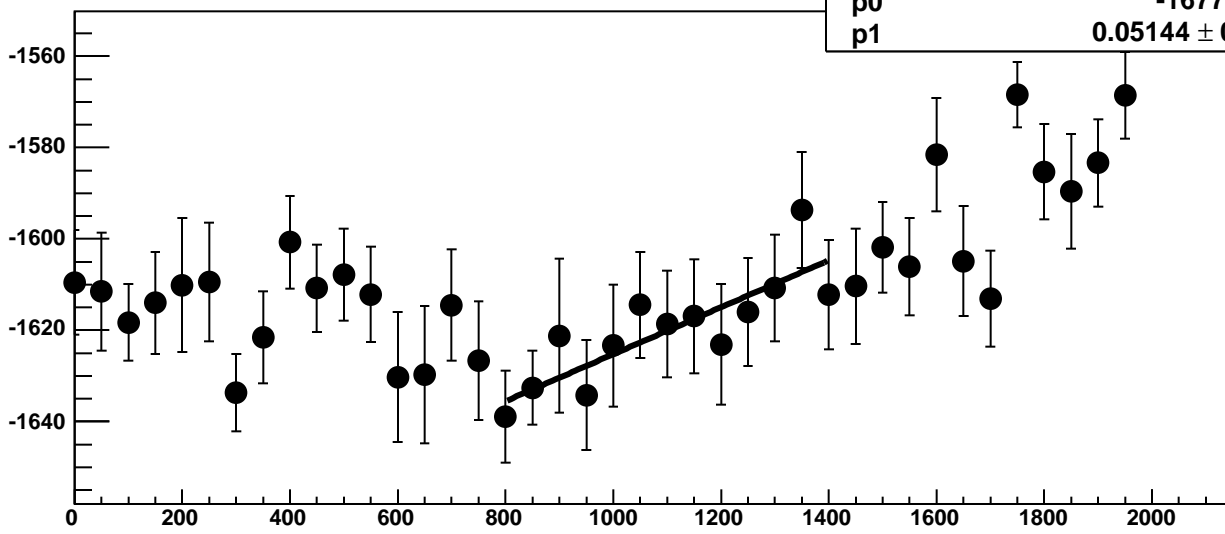
Chip 1, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC



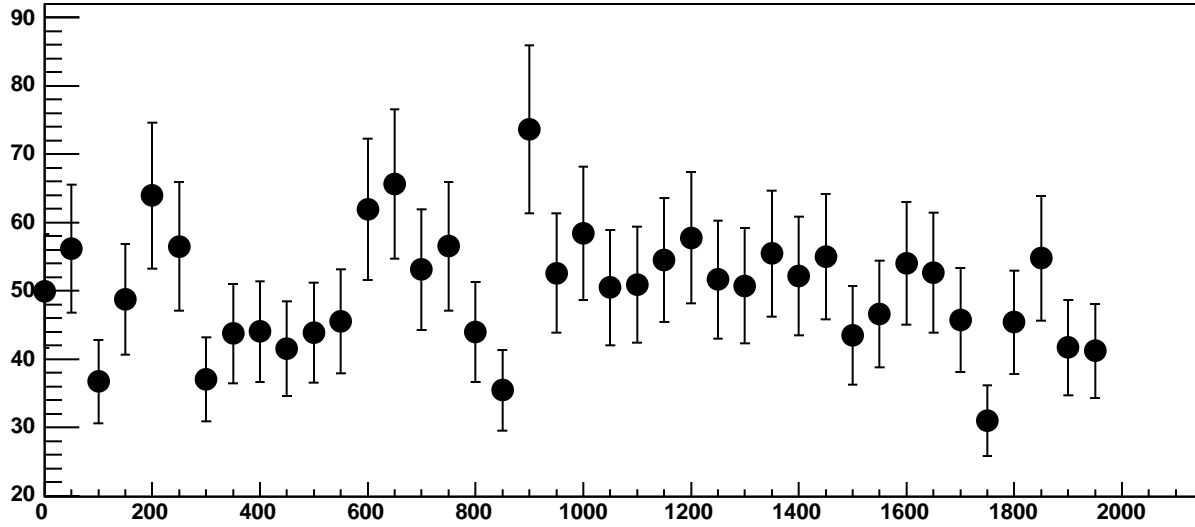
Chip 1, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC



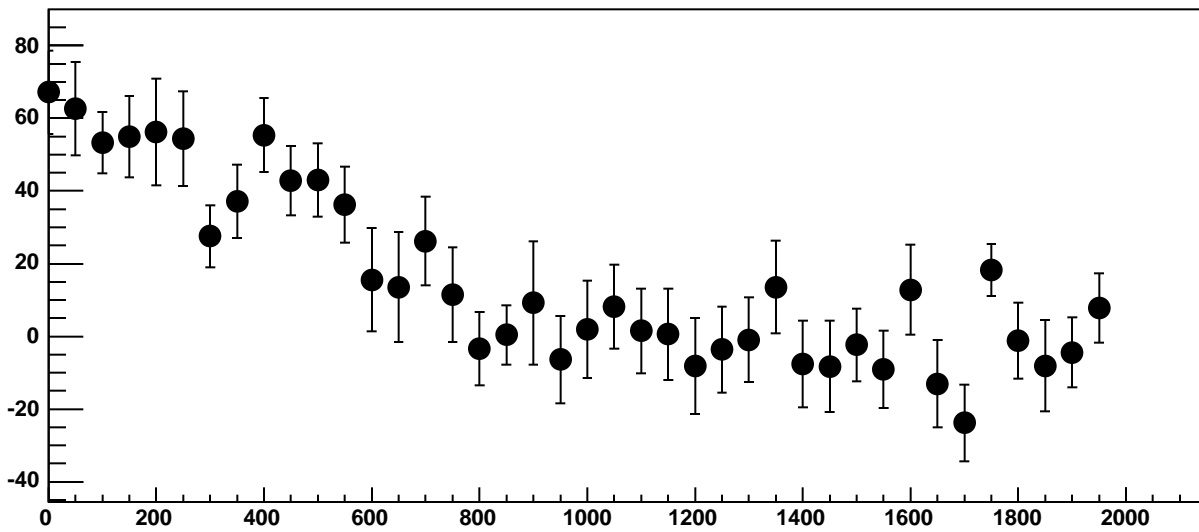
Chip 1, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC



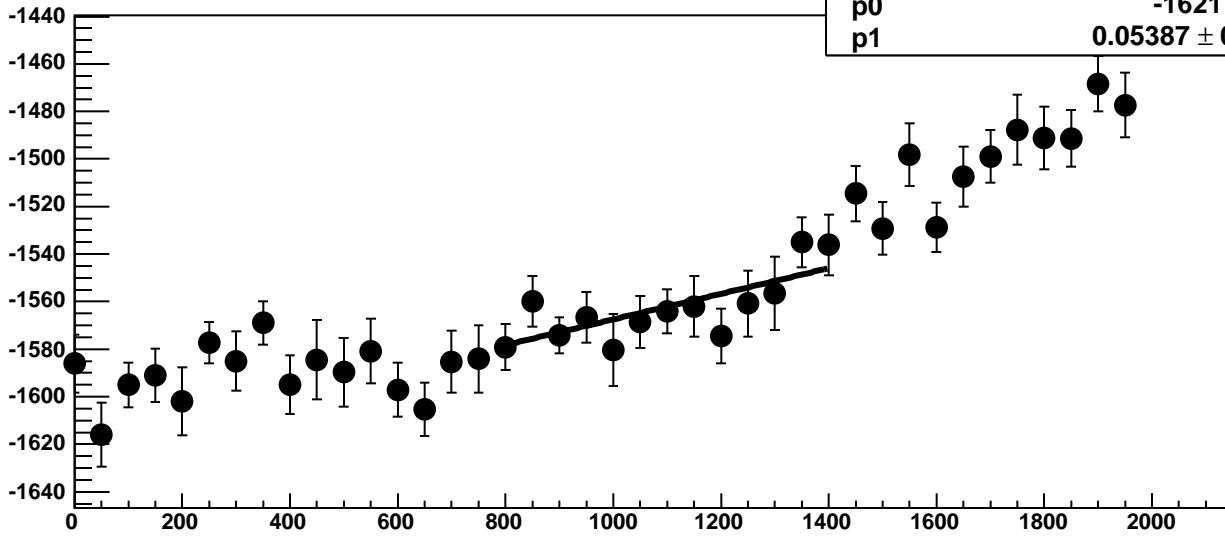
Chip 1, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC



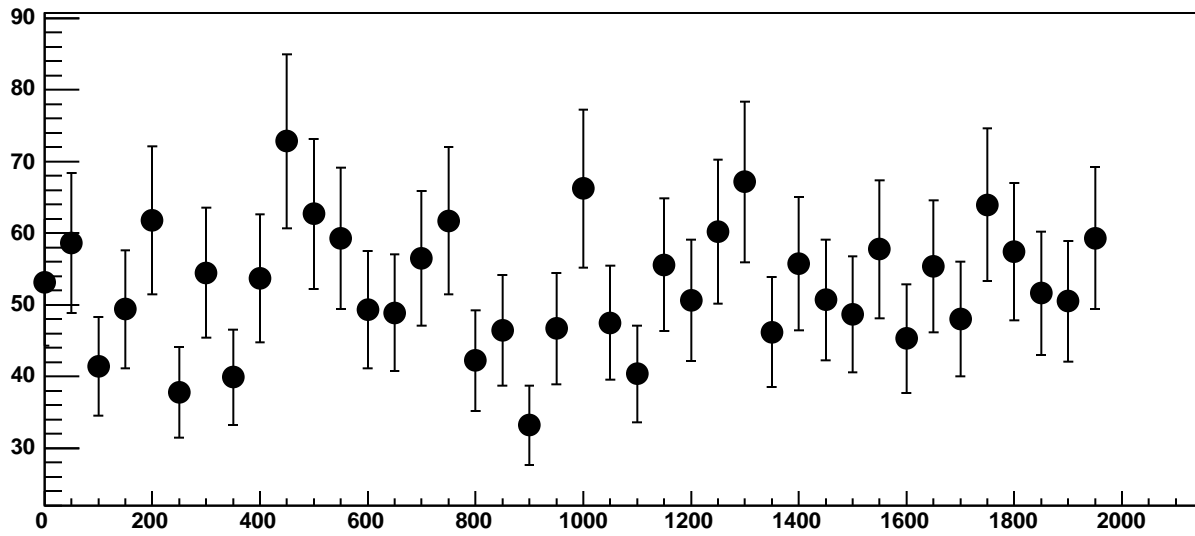
Chip 1, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC



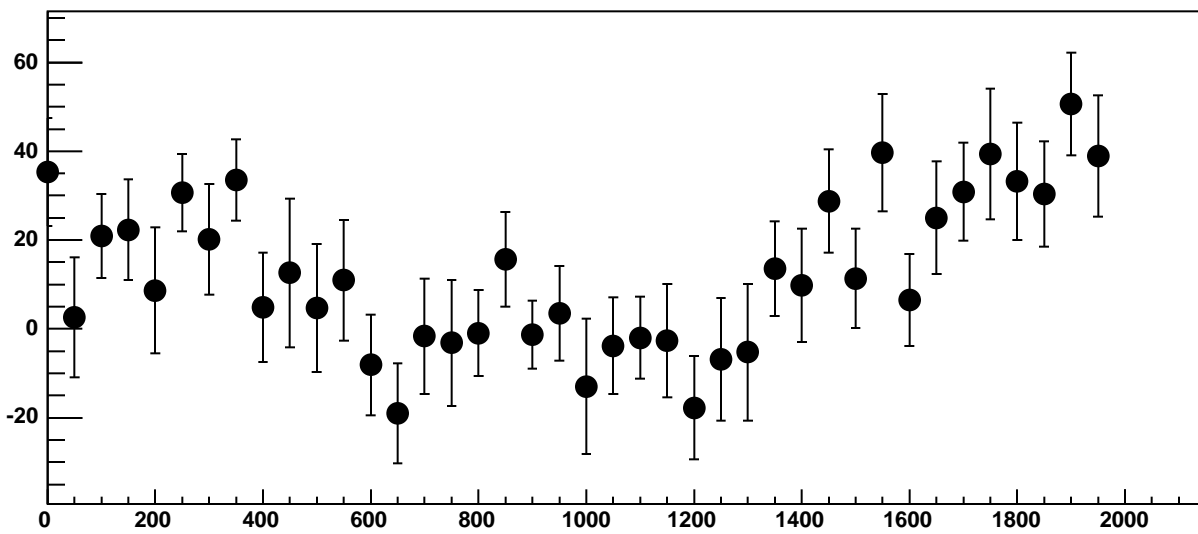
Chip 1, Channel 13, Enable 4, Hold=30, ADC Mean vs DAC



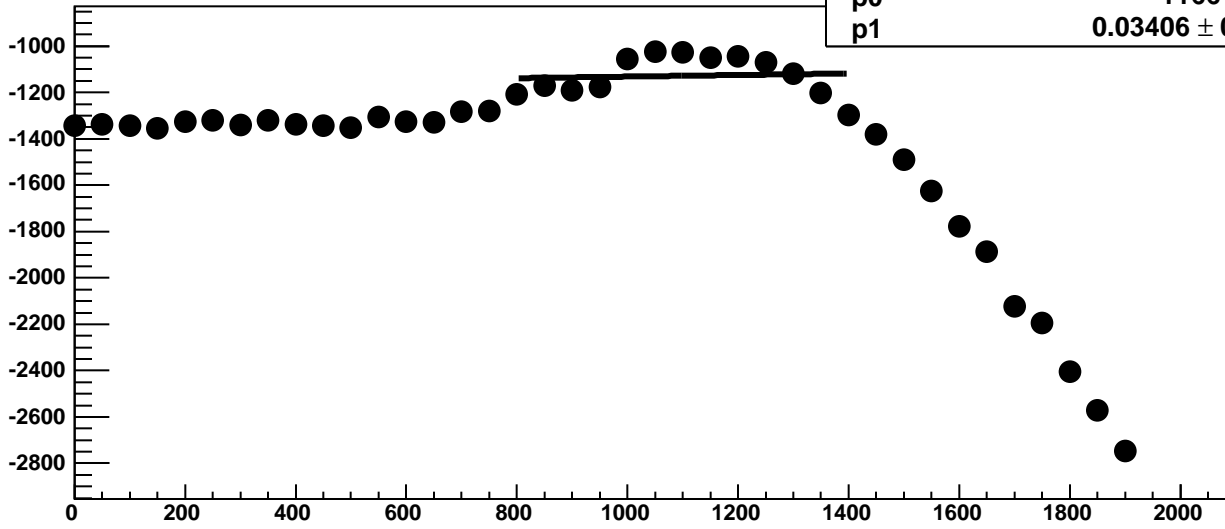
Chip 1, Channel 13, Enable 4, Hold=30, ADC Noise vs DAC



Chip 1, Channel 13, Enable 4, Hold=30, ADC Residuals vs DAC



Chip 1, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

240 / 11

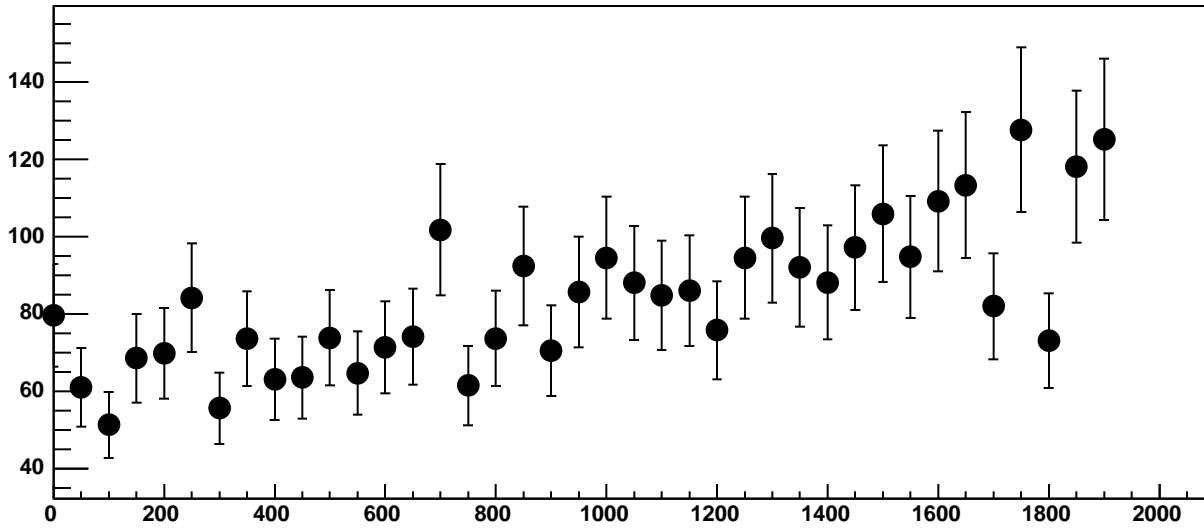
p0

$-1166 \pm 31.63$

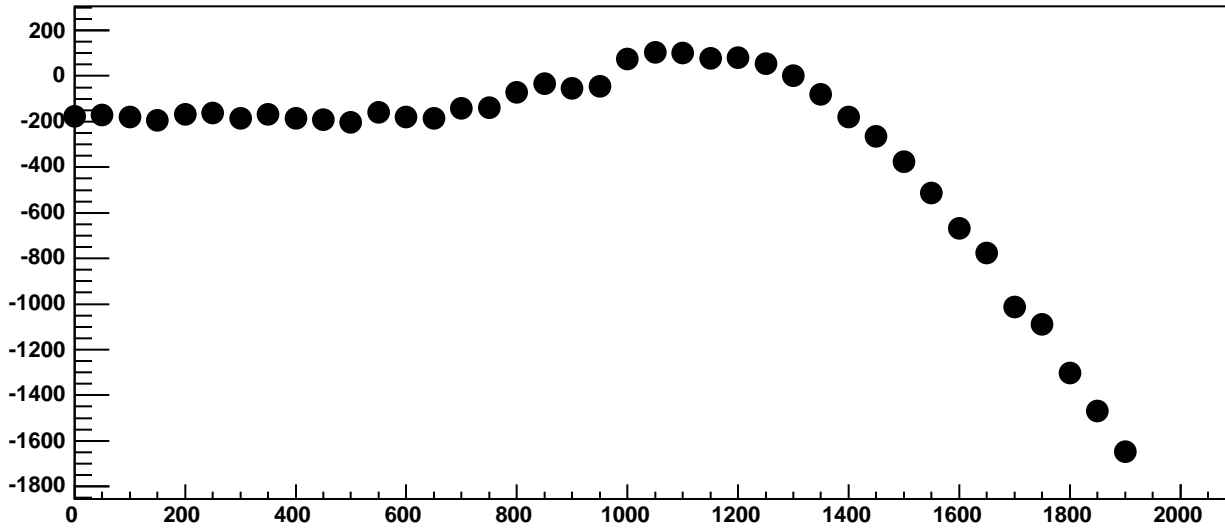
p1

$0.03406 \pm 0.02882$

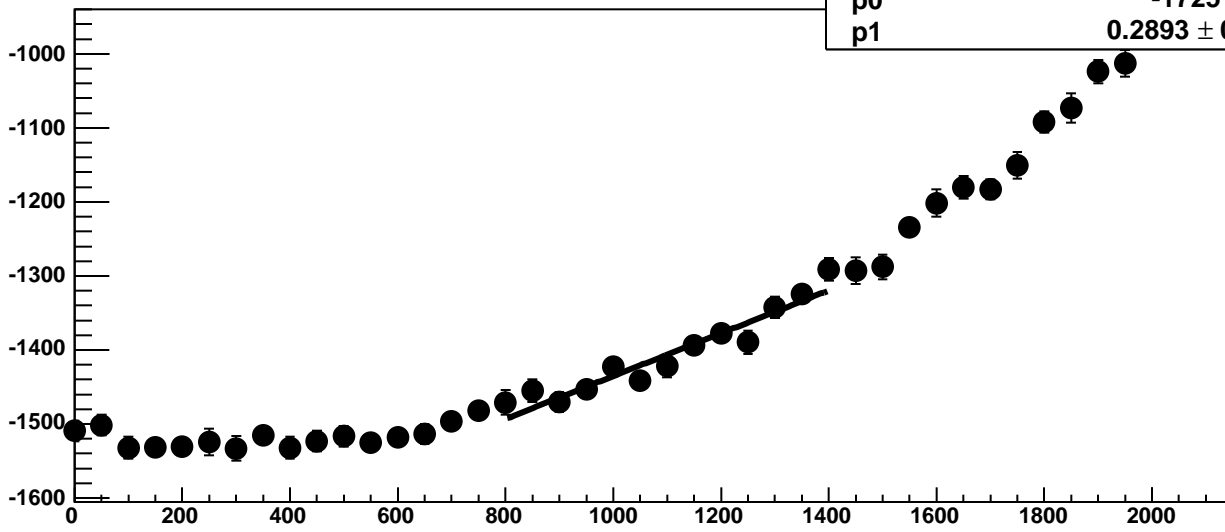
Chip 1, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC



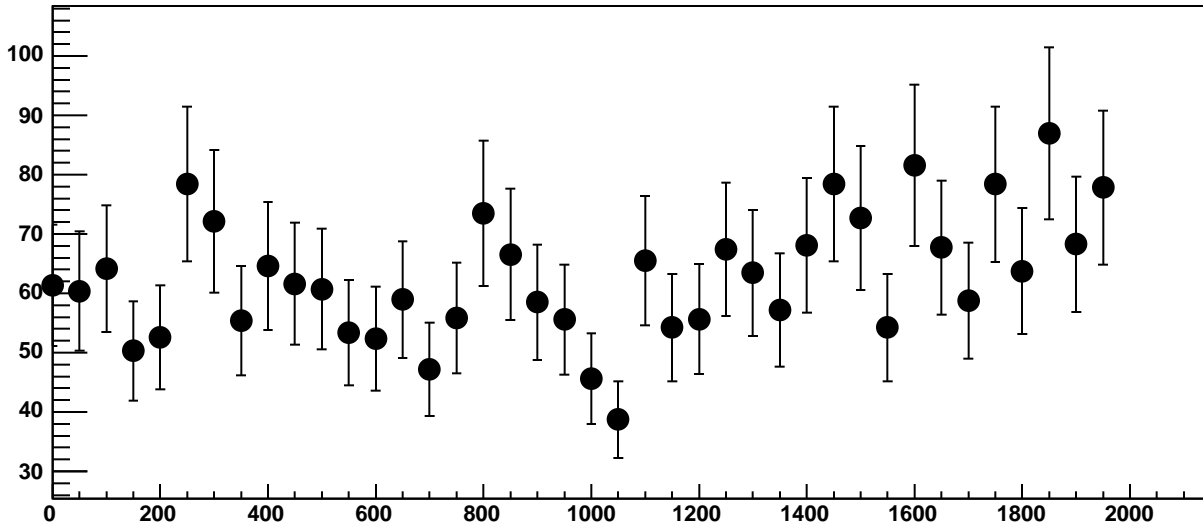
Chip 1, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC



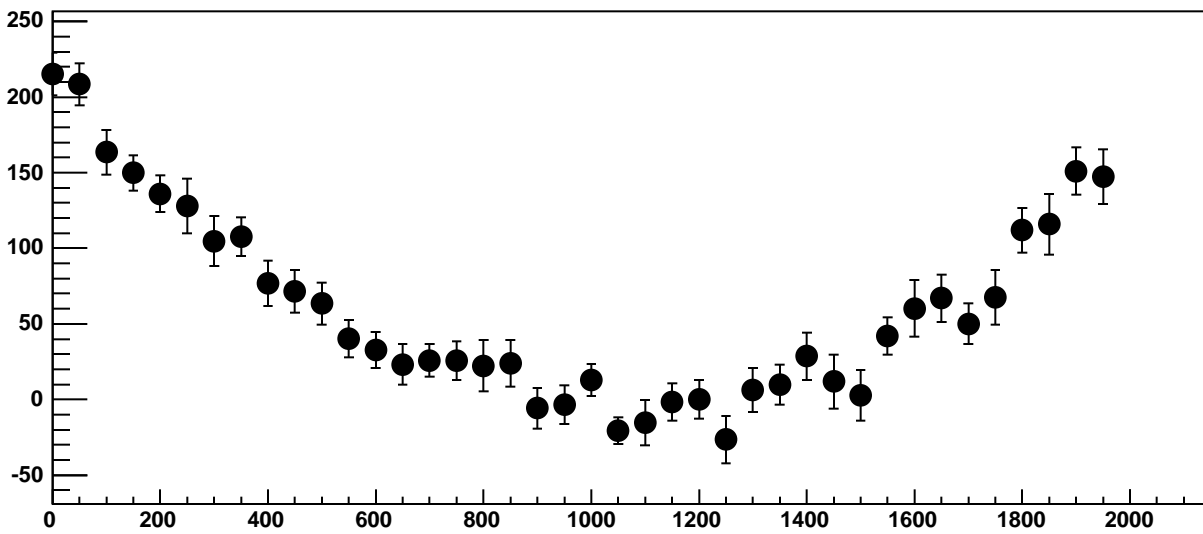
Chip 1, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC



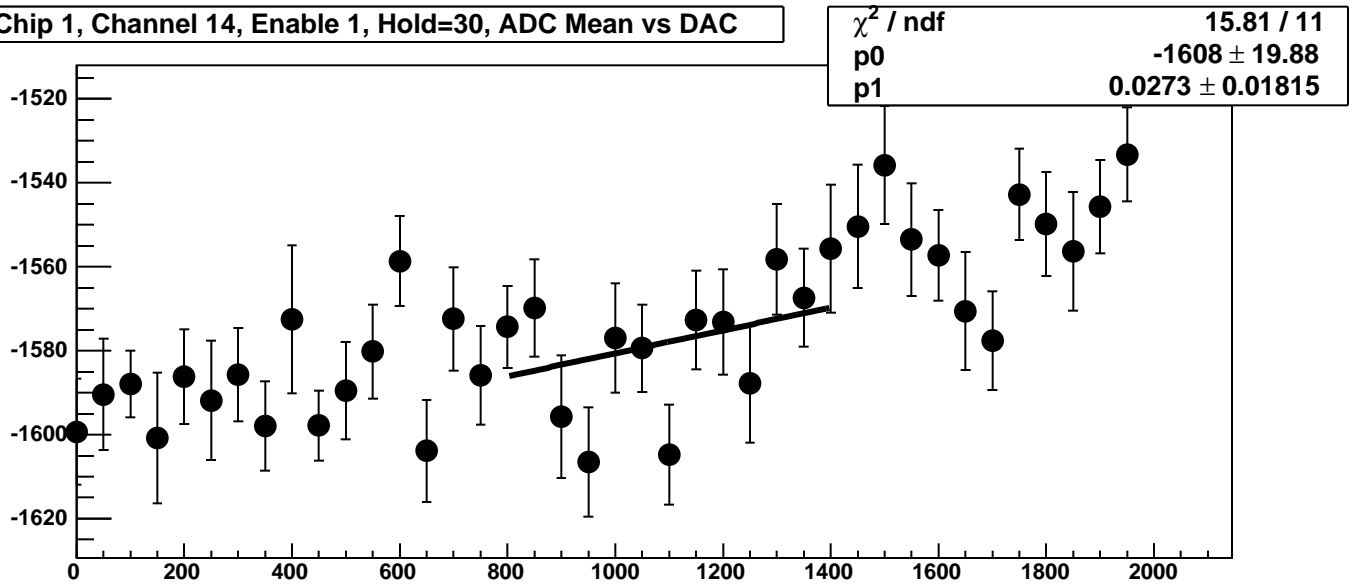
Chip 1, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC



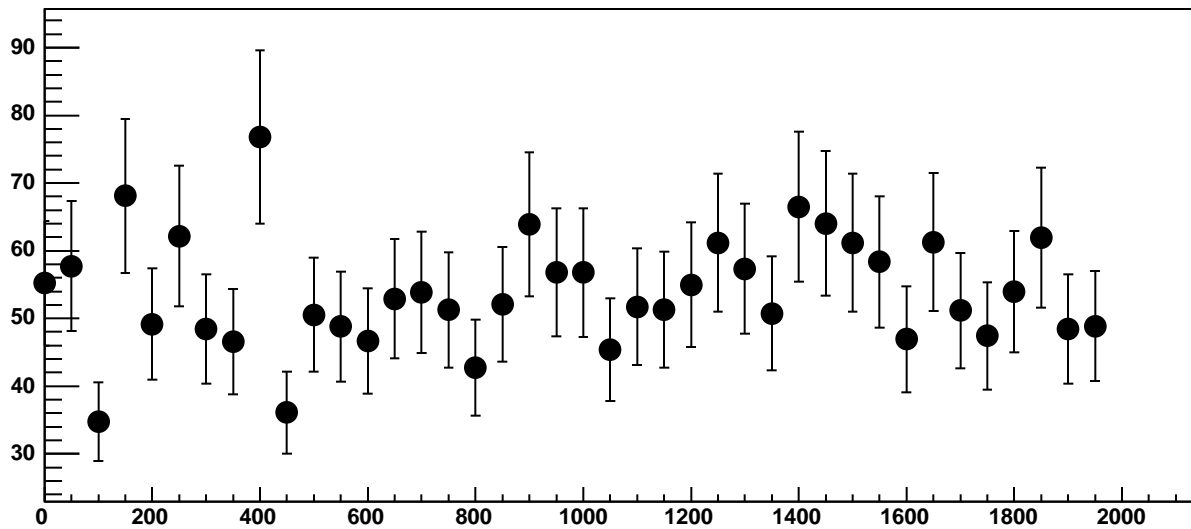
Chip 1, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC



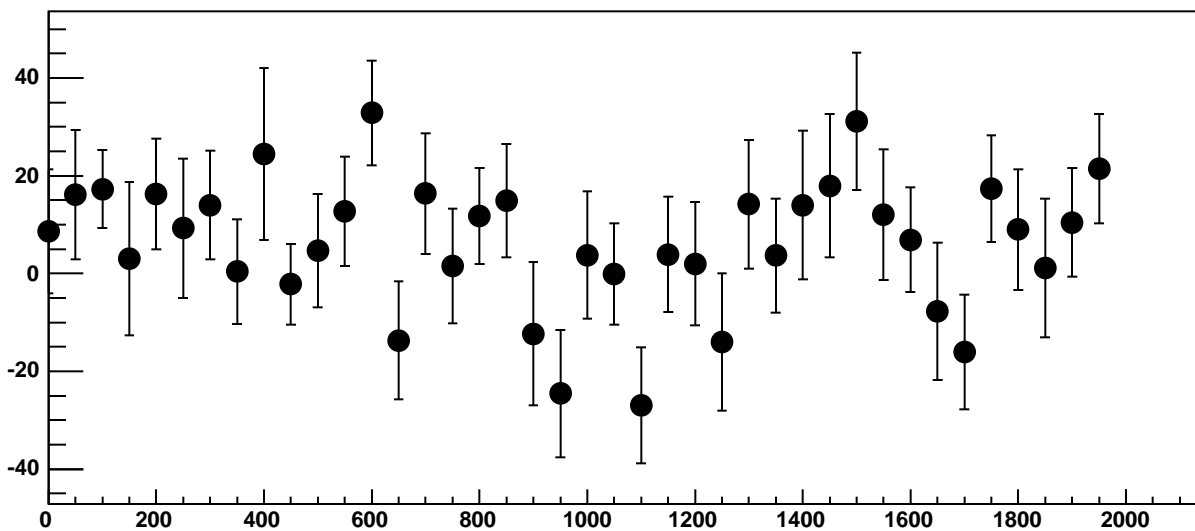
Chip 1, Channel 14, Enable 1, Hold=30, ADC Mean vs DAC



Chip 1, Channel 14, Enable 1, Hold=30, ADC Noise vs DAC

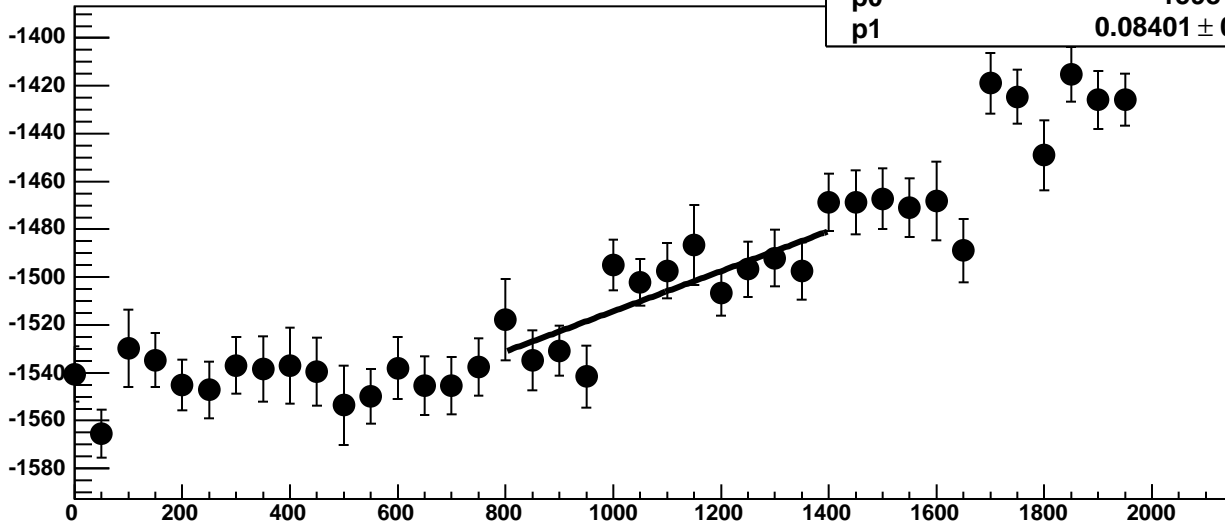


Chip 1, Channel 14, Enable 1, Hold=30, ADC Residuals vs DAC

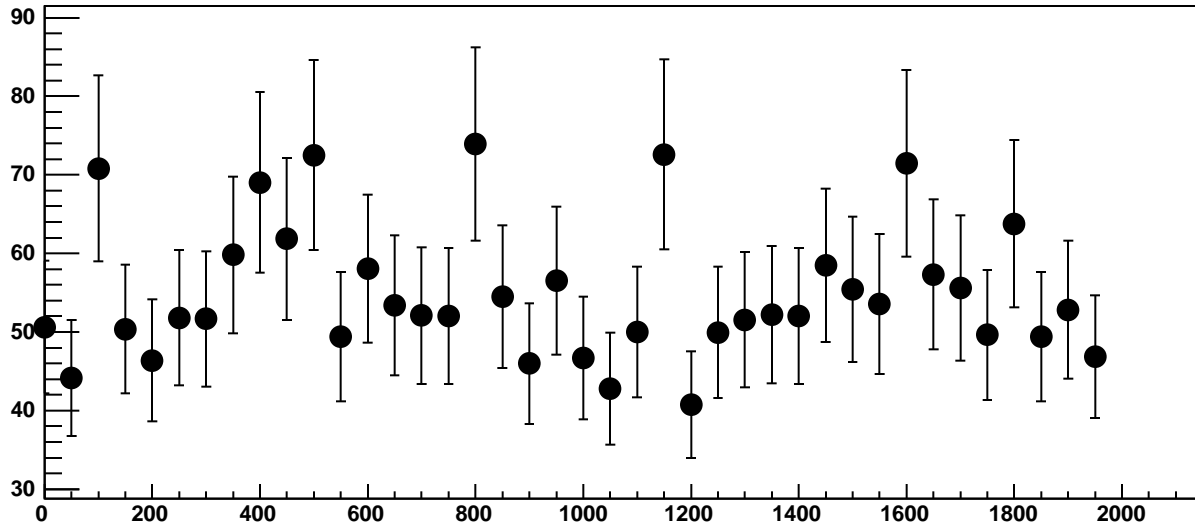




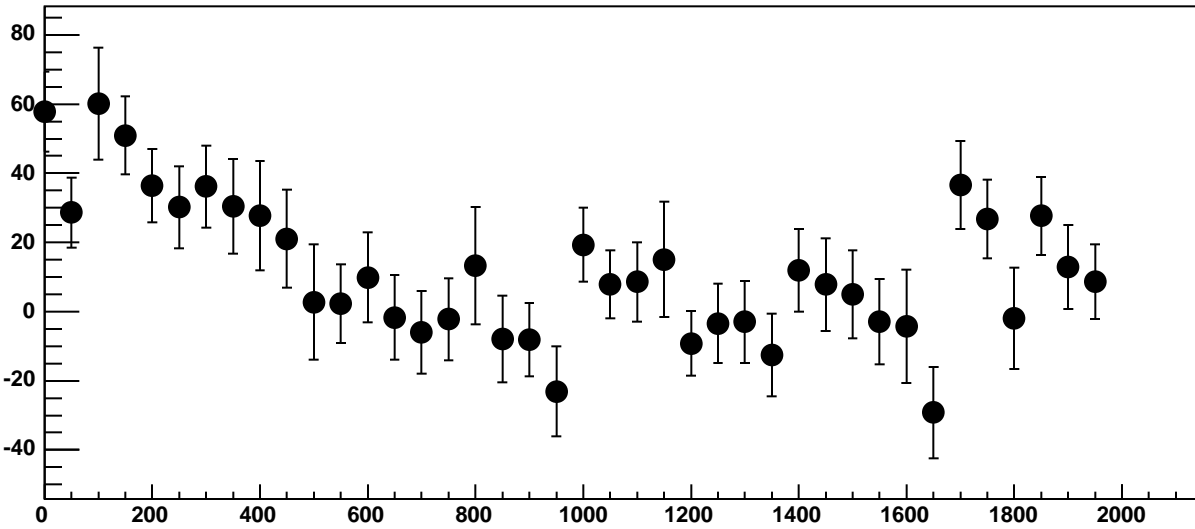
Chip 1, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC



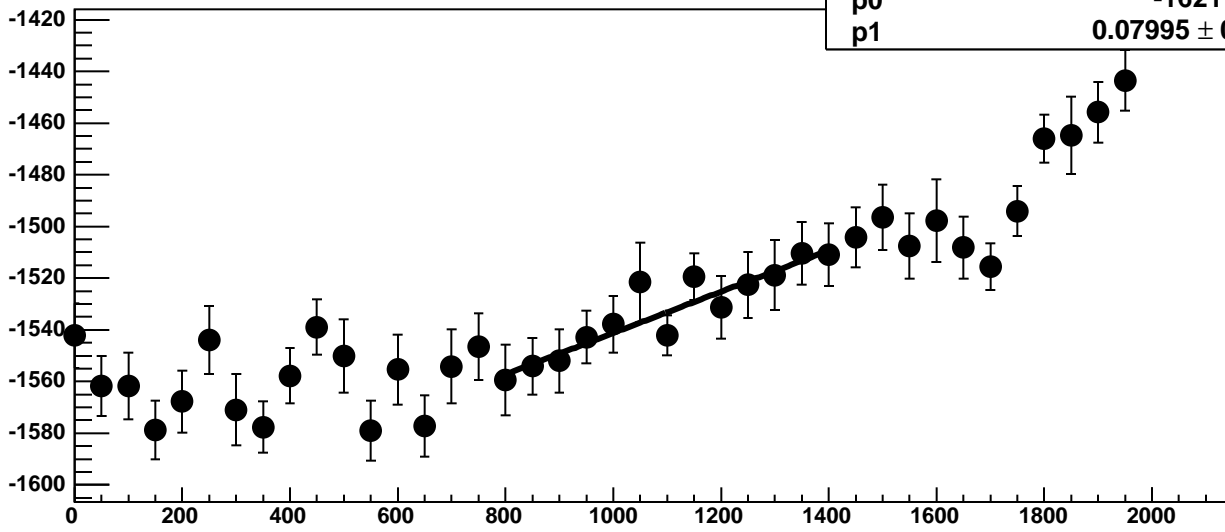
Chip 1, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC



Chip 1, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC

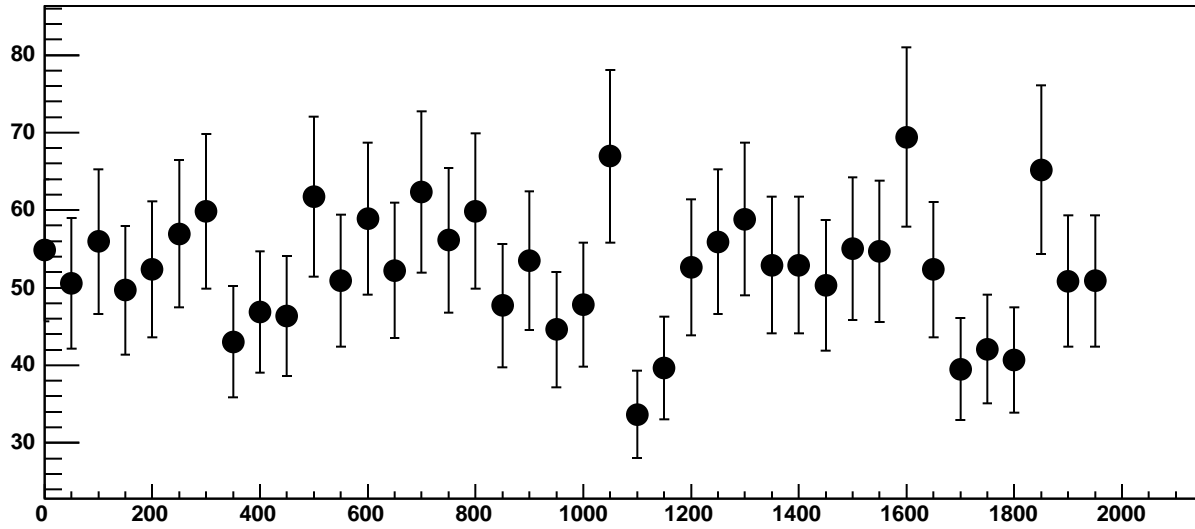


Chip 1, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC

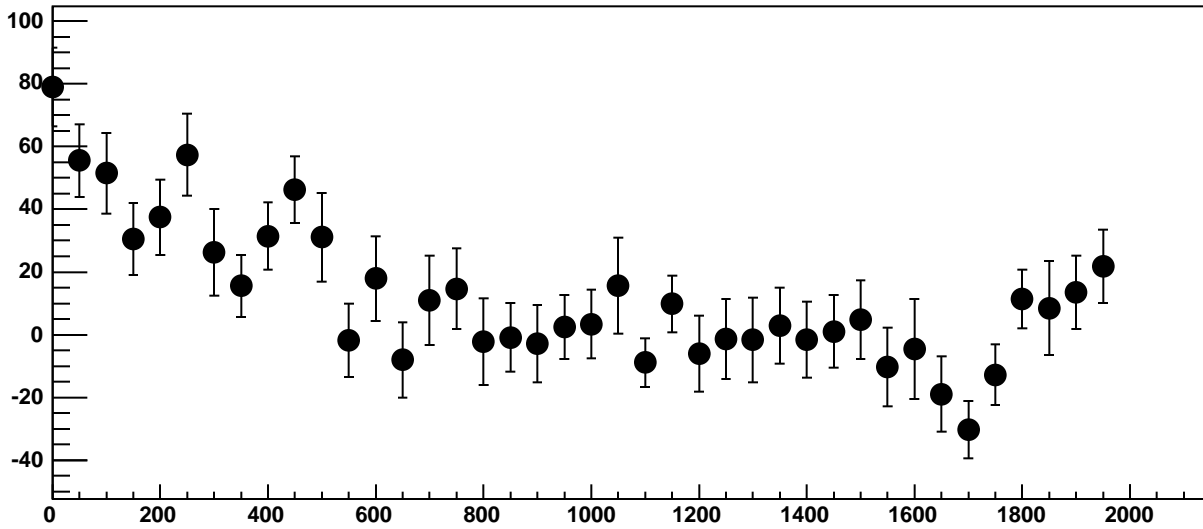


$\chi^2 / \text{ndf}$  4.108 / 11  
p0  $-1621 \pm 20.05$   
p1  $0.07995 \pm 0.01808$

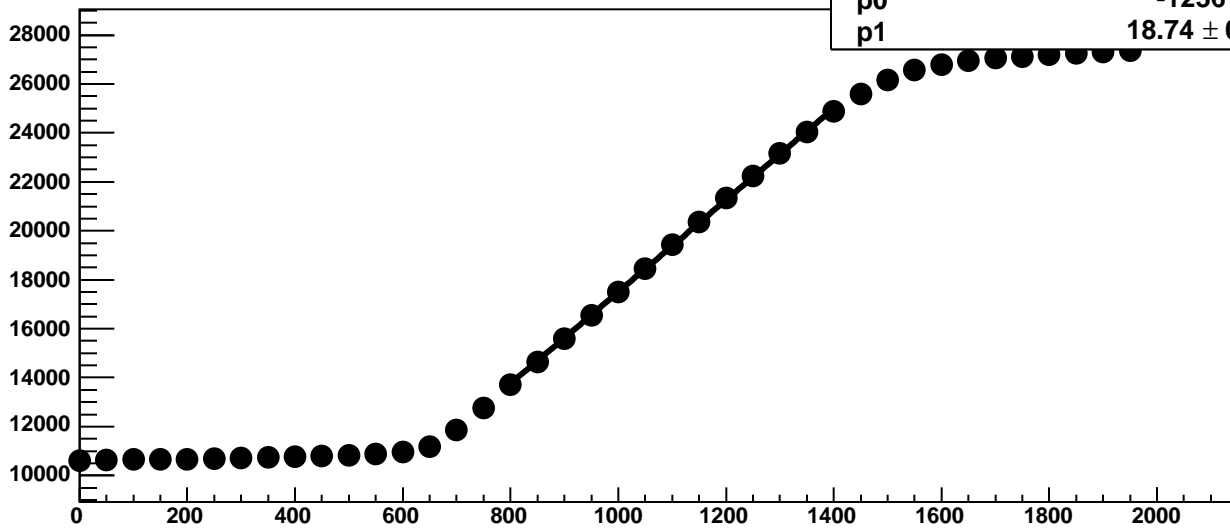
Chip 1, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC



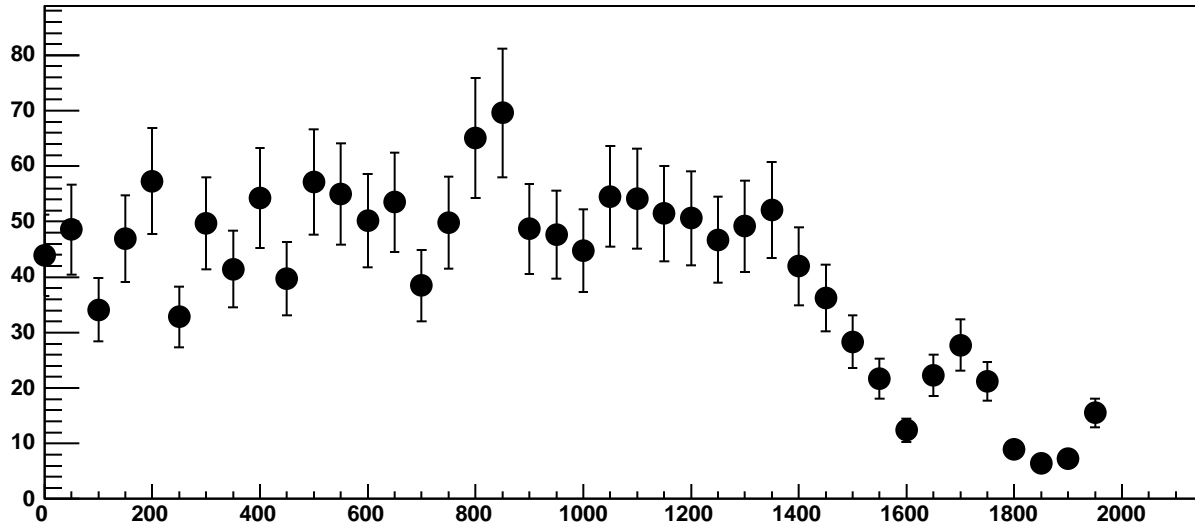
Chip 1, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC



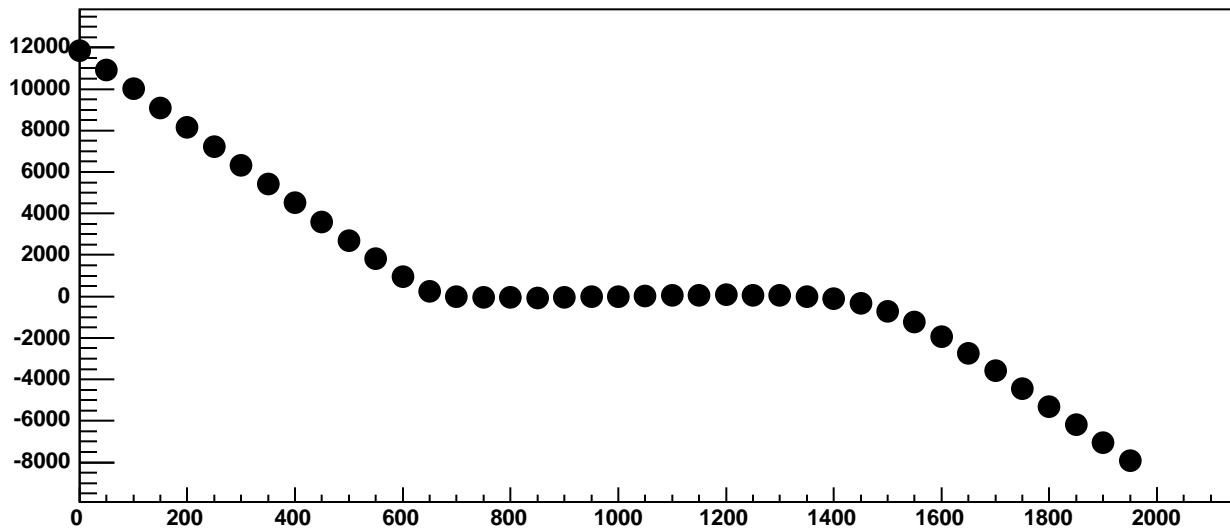
Chip 1, Channel 14, Enable 4!, Hold=30, ADC Mean vs DAC



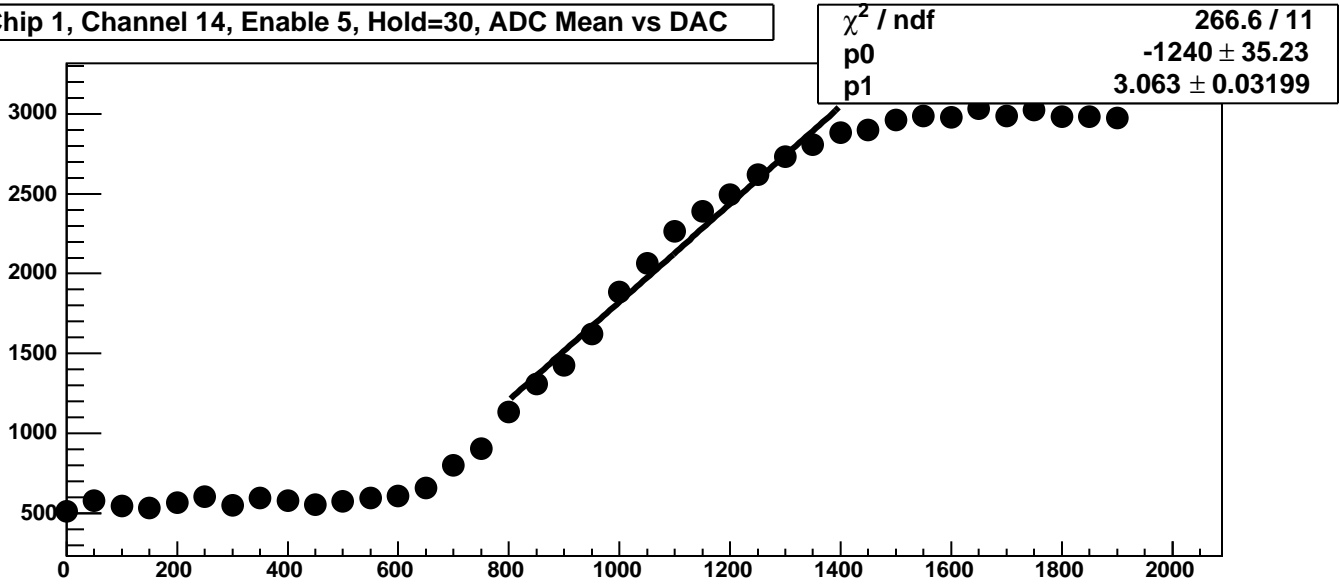
Chip 1, Channel 14, Enable 4!, Hold=30, ADC Noise vs DAC



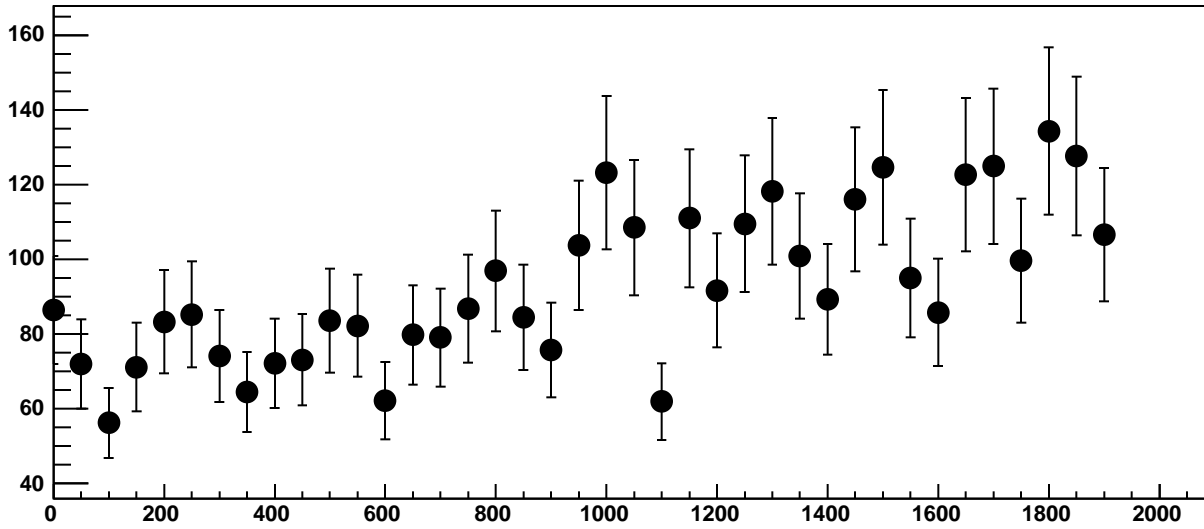
Chip 1, Channel 14, Enable 4!, Hold=30, ADC Residuals vs DAC



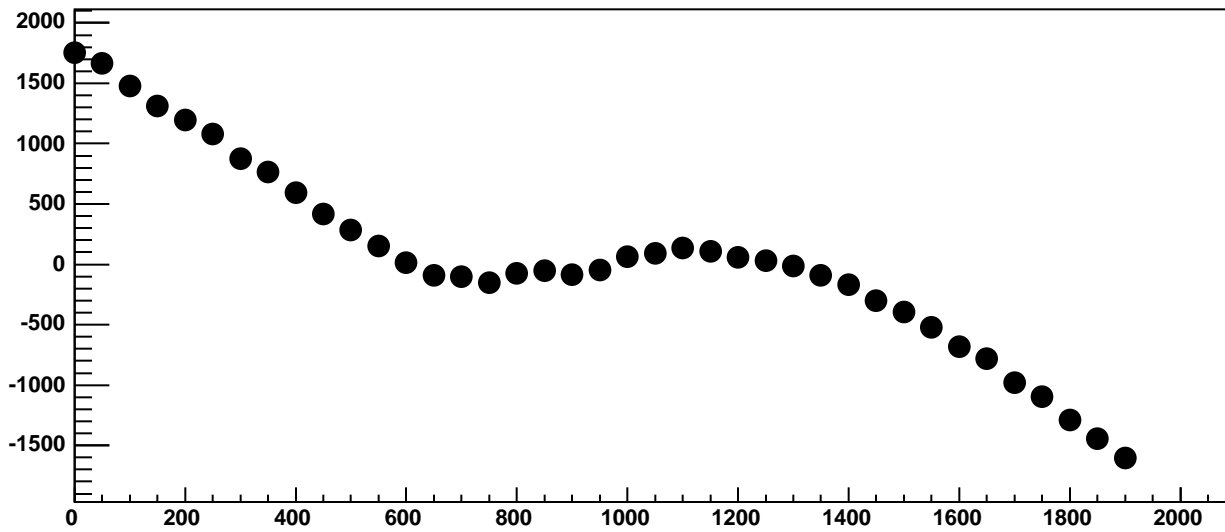
Chip 1, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC



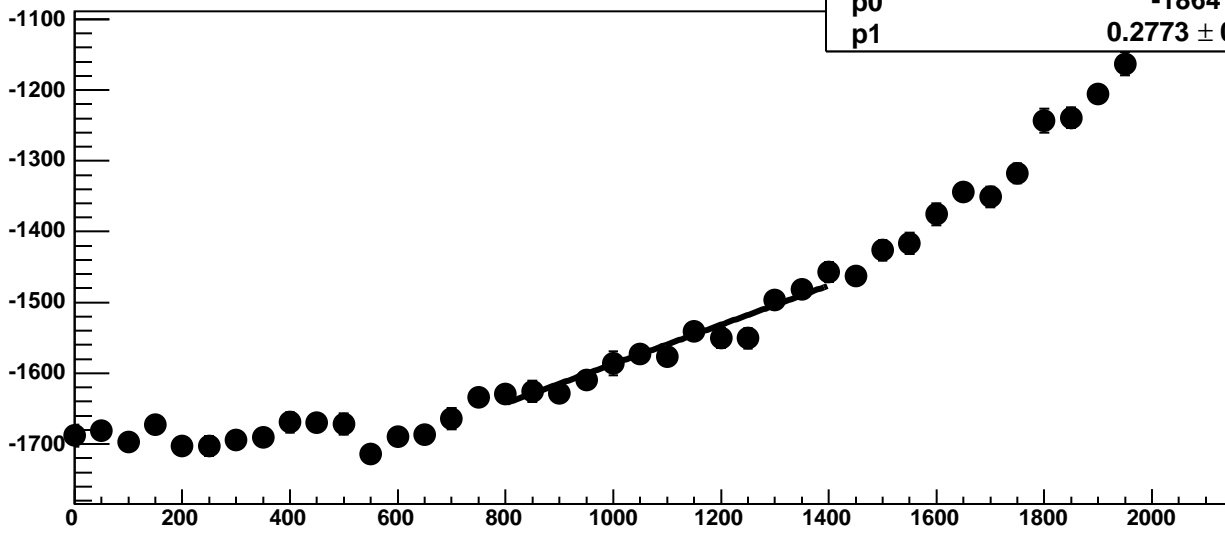
Chip 1, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC



Chip 1, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC

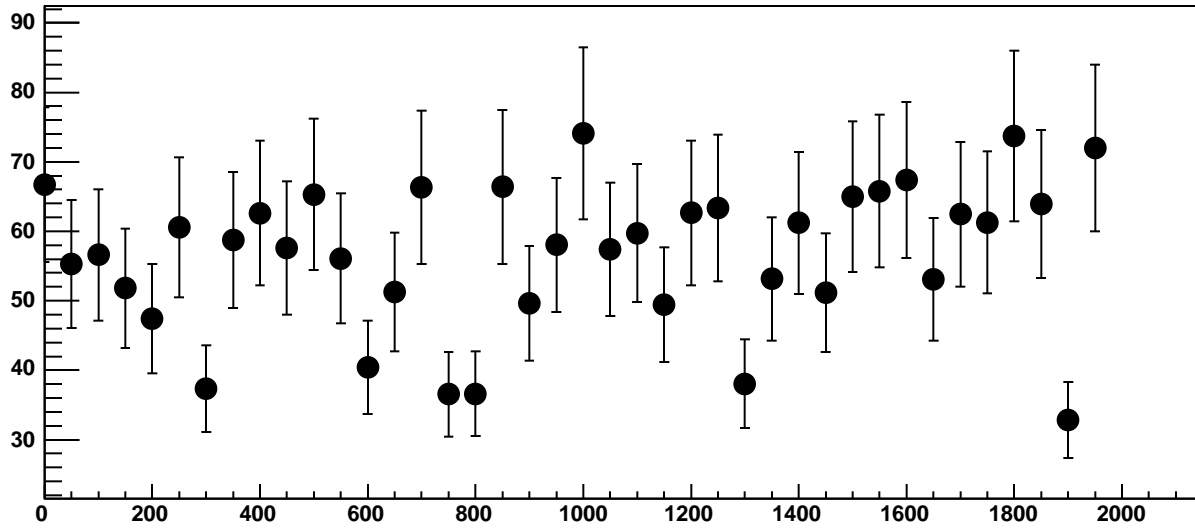


Chip 1, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC

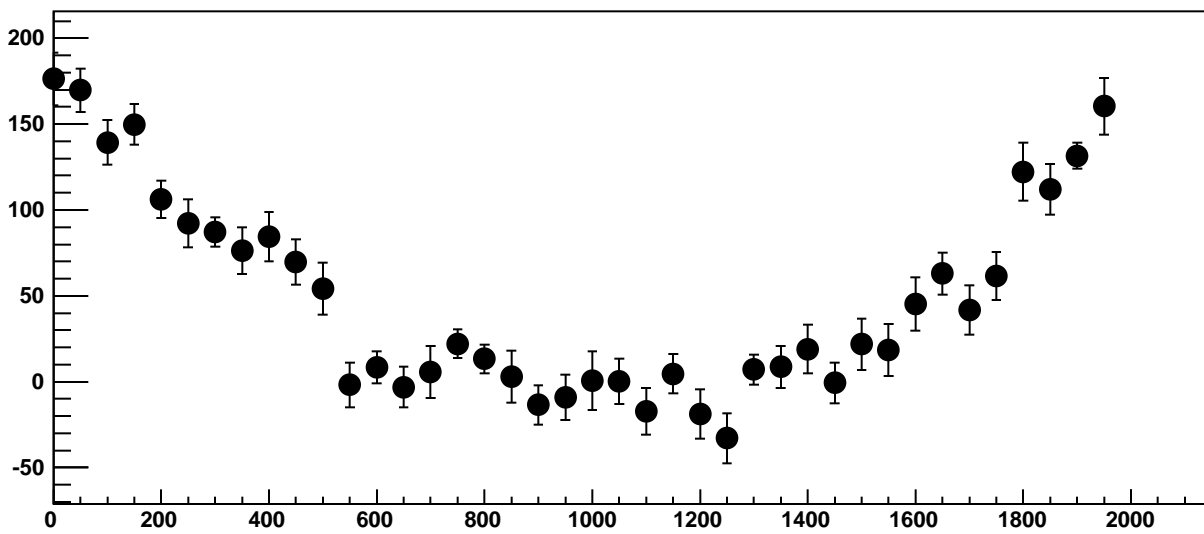


$\chi^2 / \text{ndf}$  15.96 / 11  
p0  $-1864 \pm 18.48$   
p1  $0.2773 \pm 0.01668$

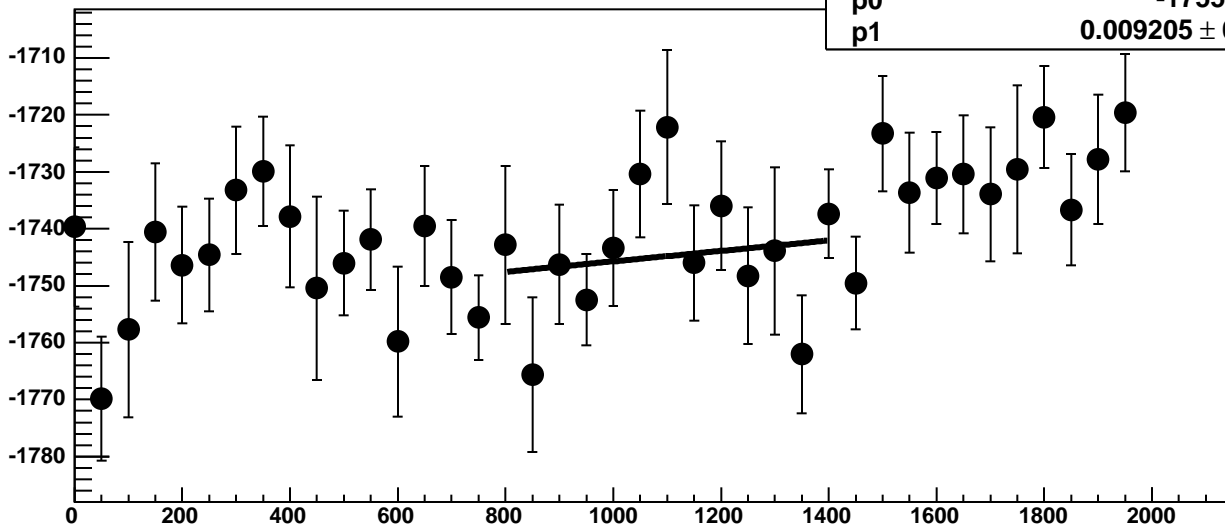
Chip 1, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC



Chip 1, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC

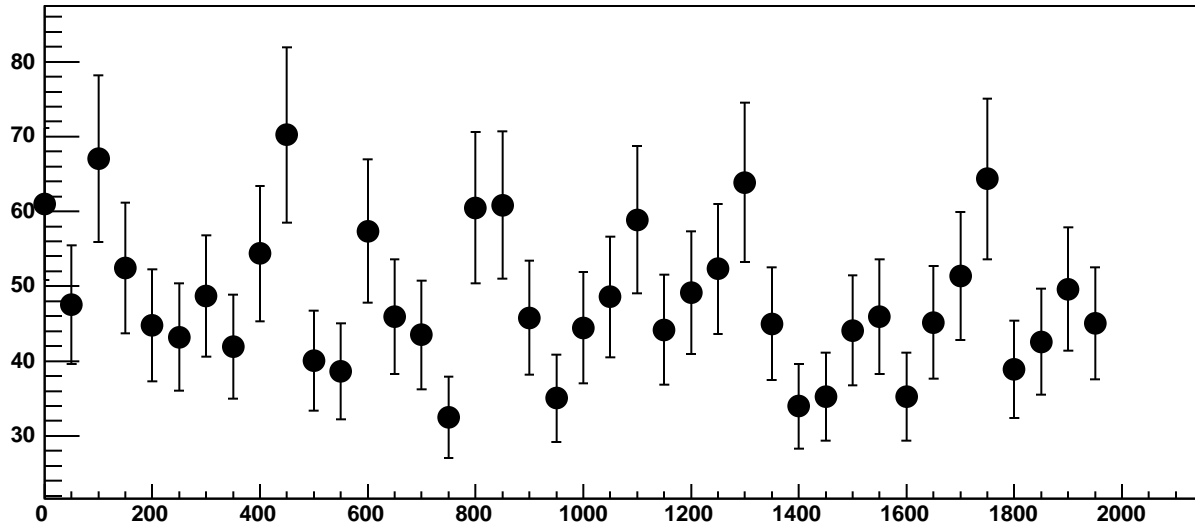


Chip 1, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC

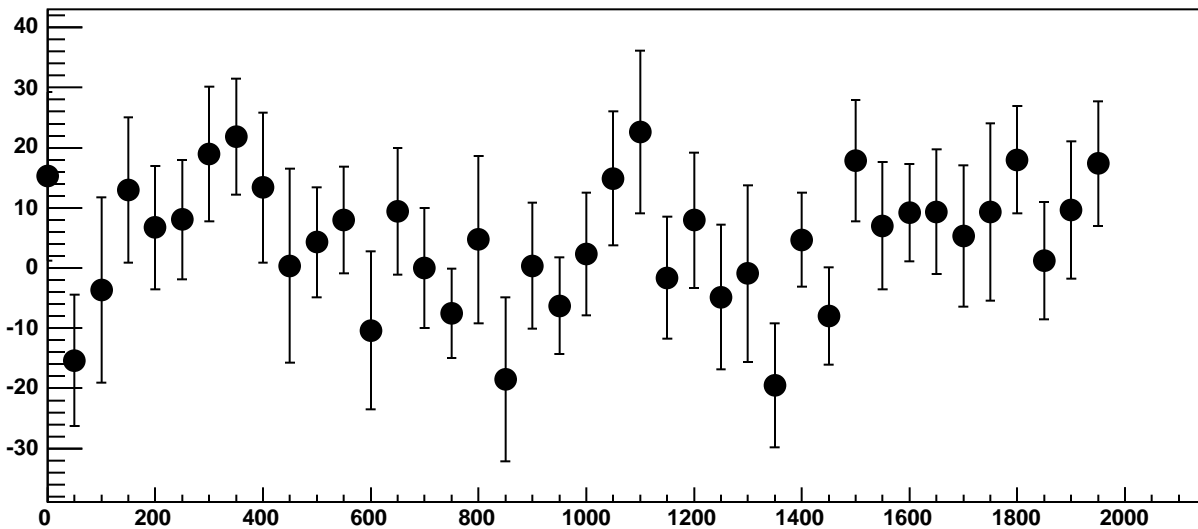


$\chi^2 / \text{ndf}$  11.86 / 11  
p0  $-1755 \pm 17.71$   
p1  $0.009205 \pm 0.01561$

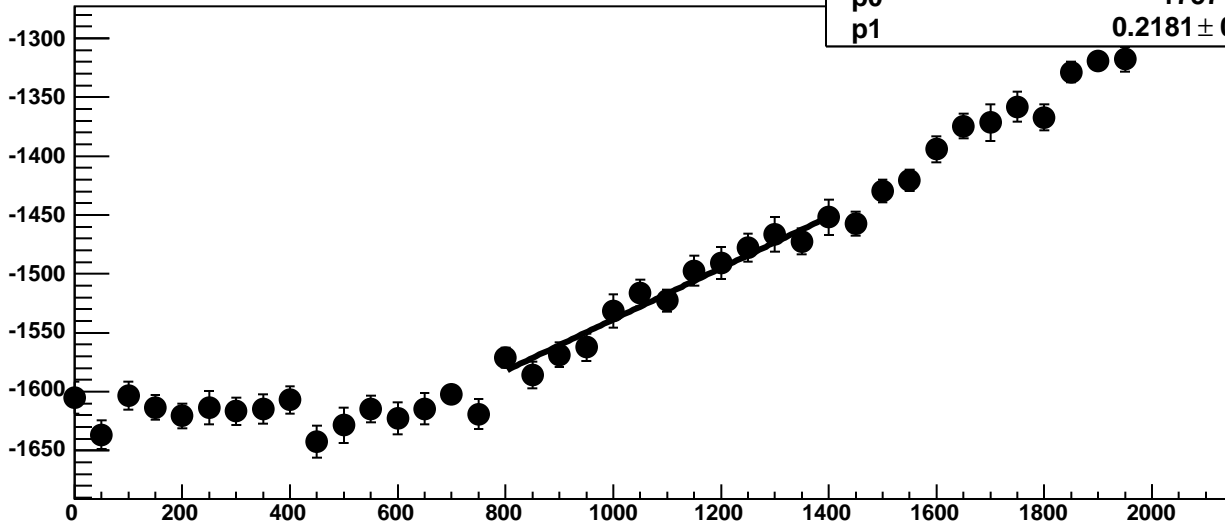
Chip 1, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC



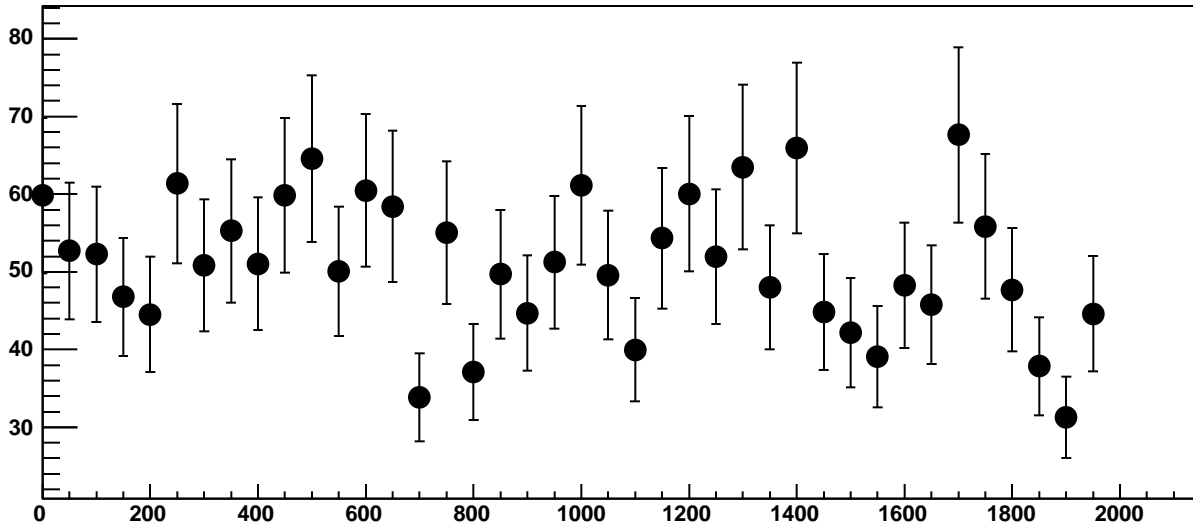
Chip 1, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC



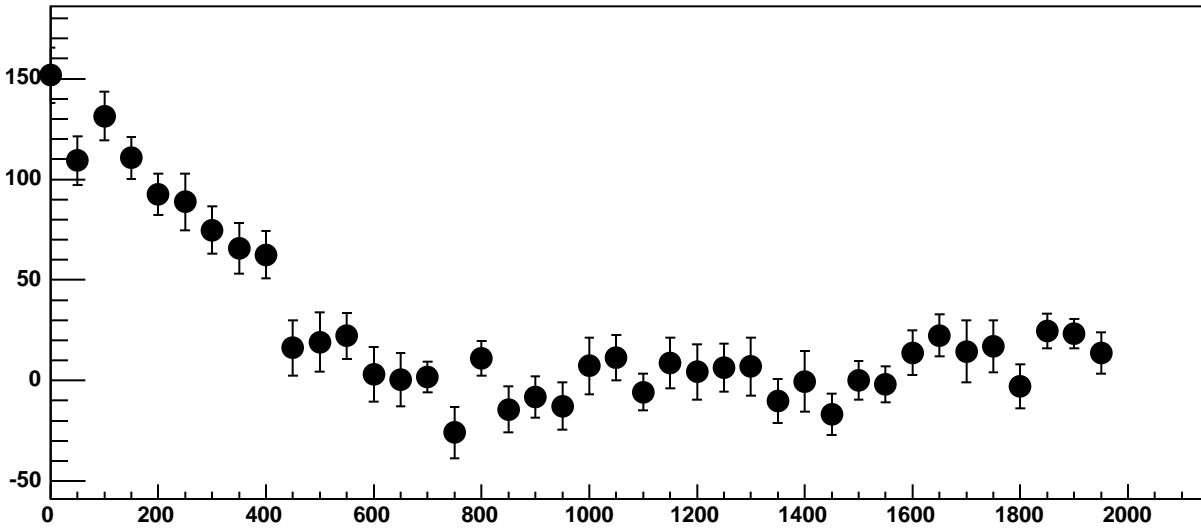
Chip 1, Channel 15, Enable 2, Hold=30, ADC Mean vs DAC



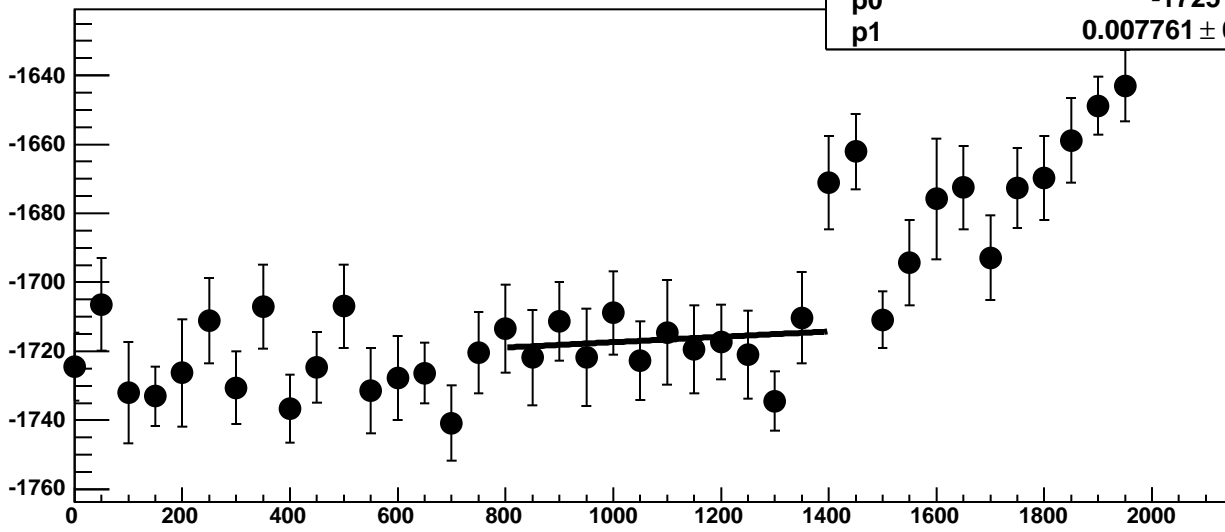
Chip 1, Channel 15, Enable 2, Hold=30, ADC Noise vs DAC



Chip 1, Channel 15, Enable 2, Hold=30, ADC Residuals vs DAC

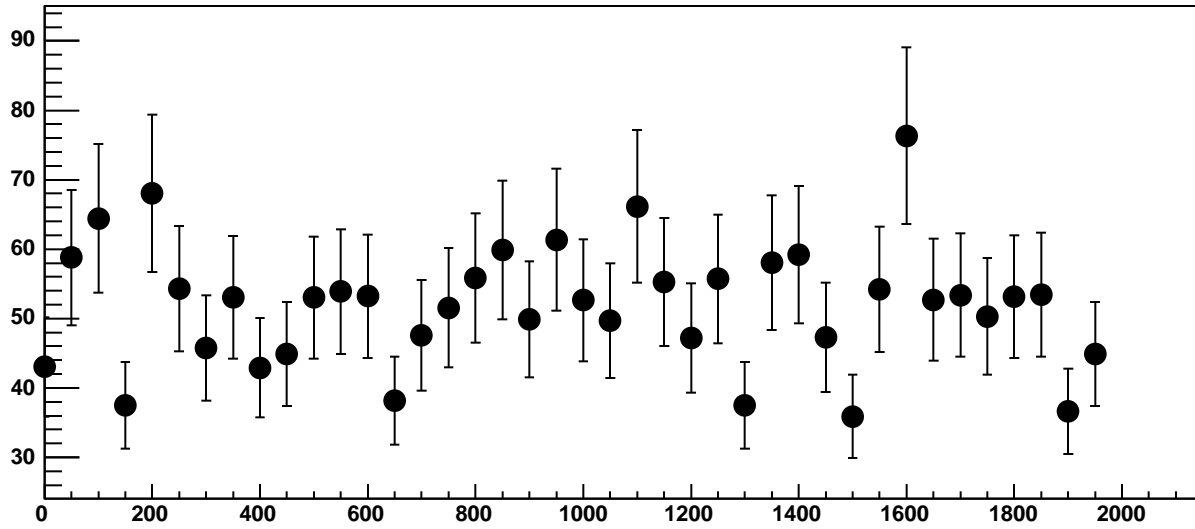


Chip 1, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC

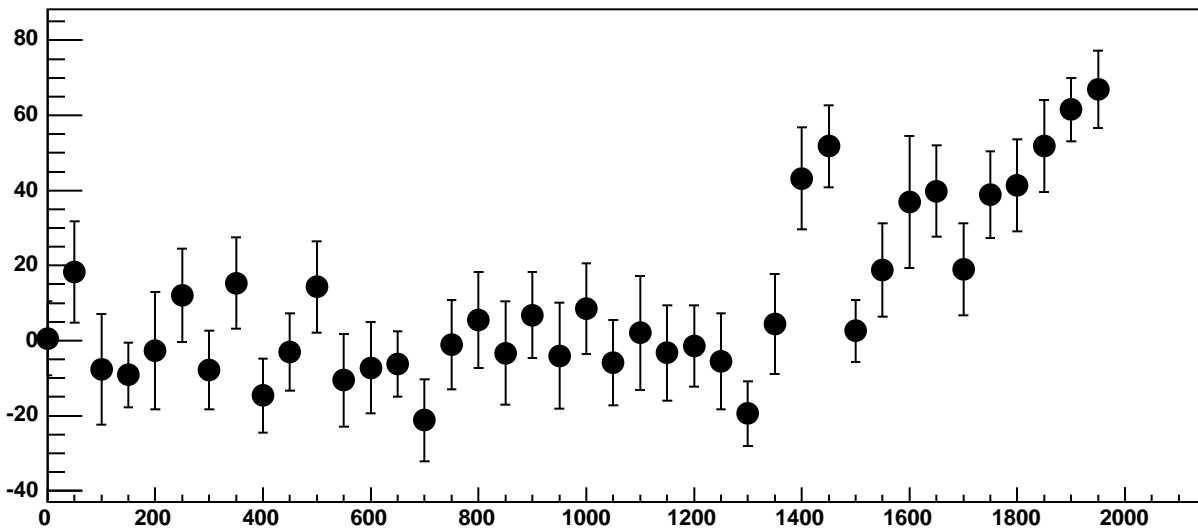


$\chi^2 / \text{ndf}$  17.03 / 11  
p0  $-1725 \pm 20.68$   
p1  $0.007761 \pm 0.01831$

Chip 1, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC

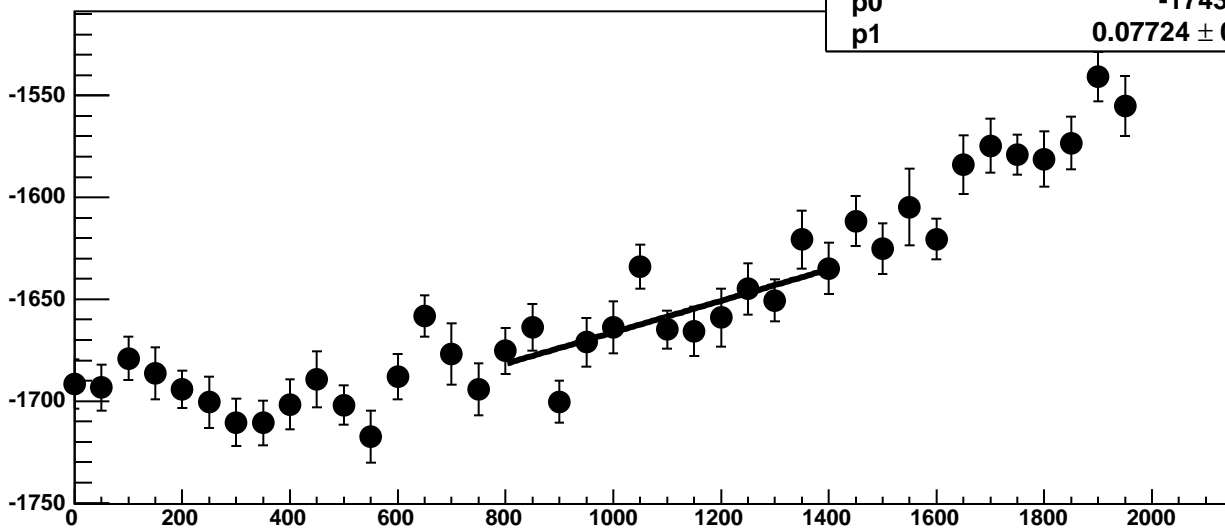


Chip 1, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC



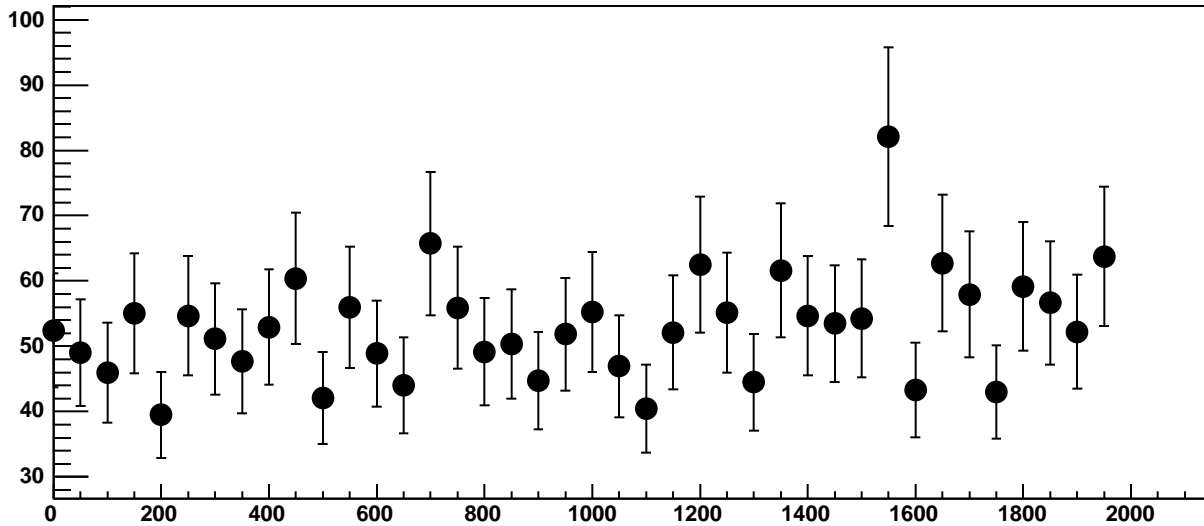


Chip 1, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC

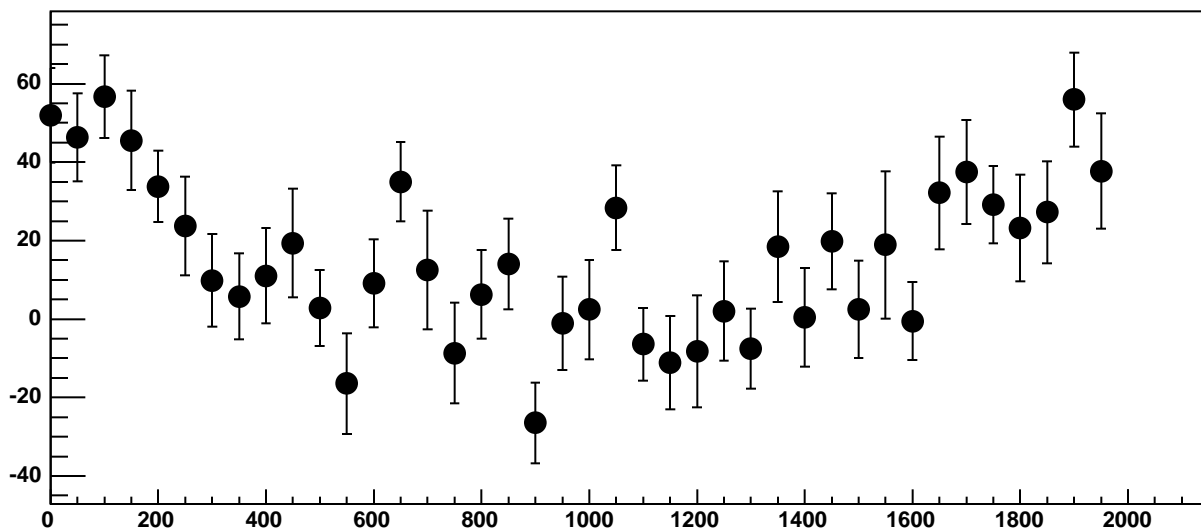


$\chi^2 / \text{ndf}$  19.34 / 11  
p0  $-1743 \pm 19.3$   
p1  $0.07724 \pm 0.01753$

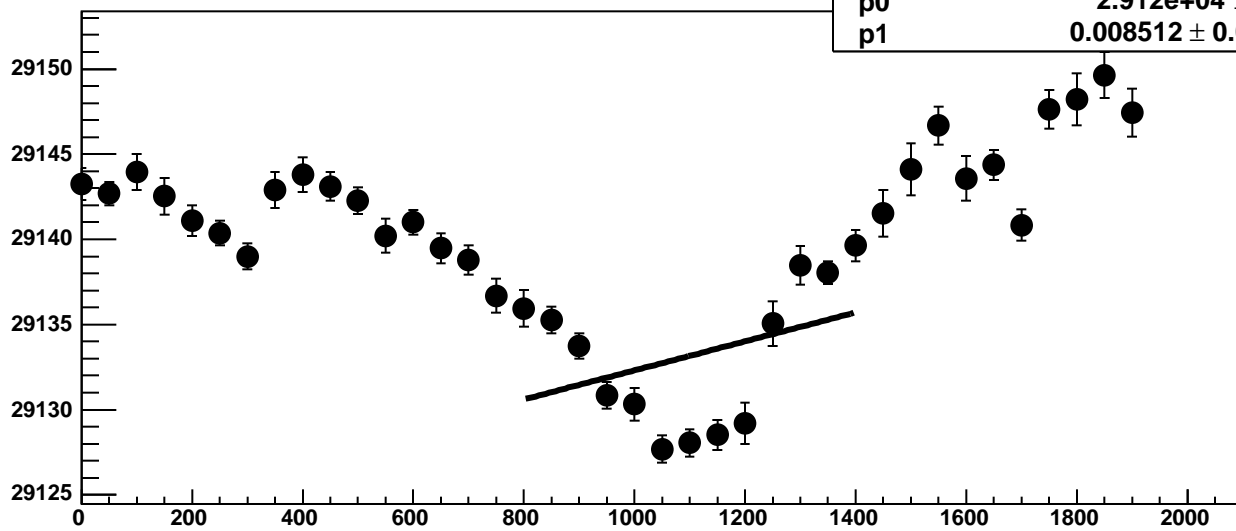
Chip 1, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC



Chip 1, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC

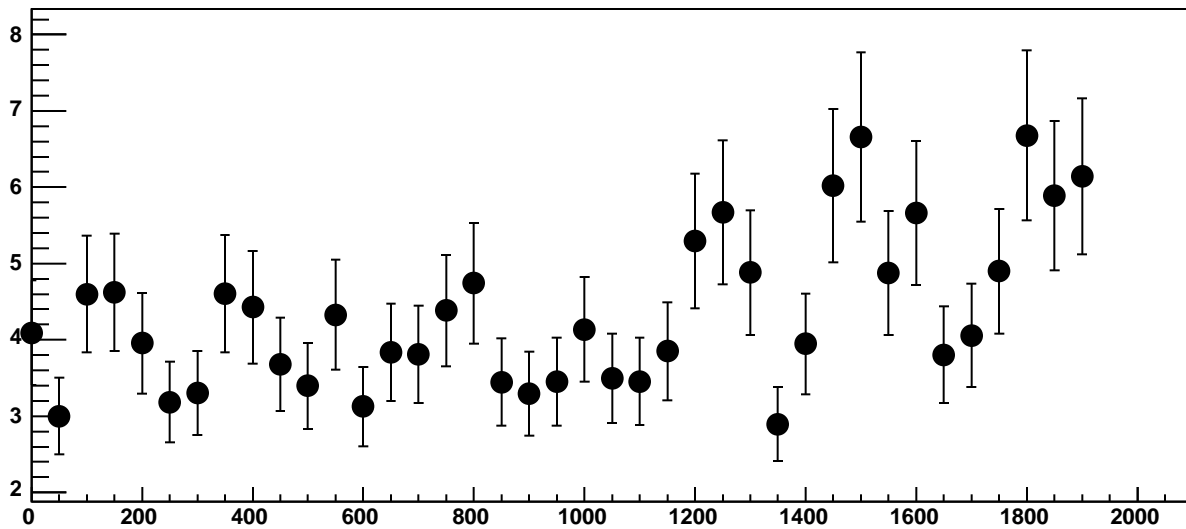


Chip 1, Channel 15, Enable 5!, Hold=30, ADC Mean vs DAC

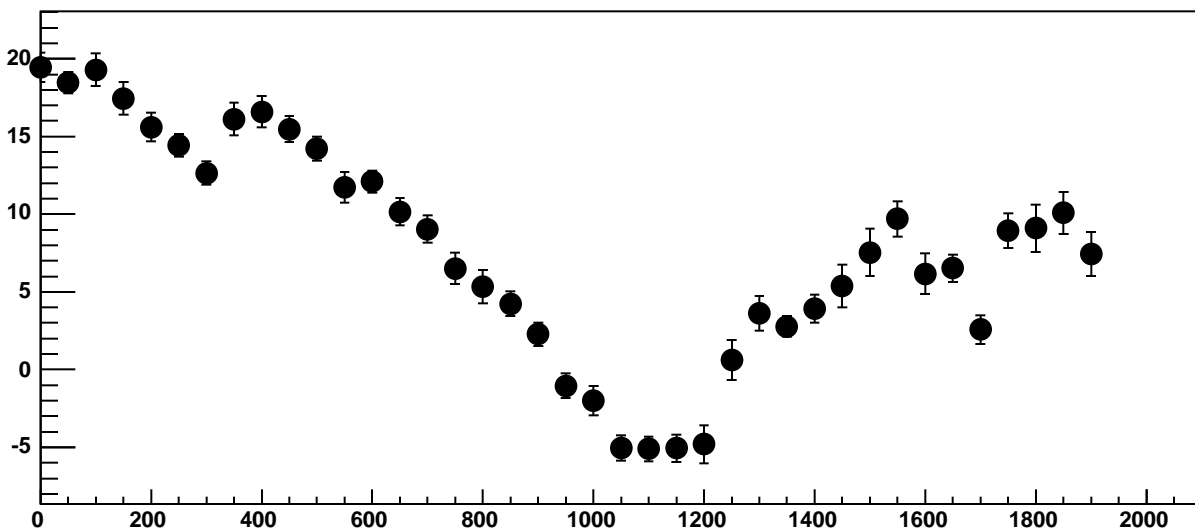


$\chi^2 / \text{ndf}$  244.2 / 11  
p0  $2.912\text{e}+04 \pm 1.429$   
p1  $0.008512 \pm 0.001288$

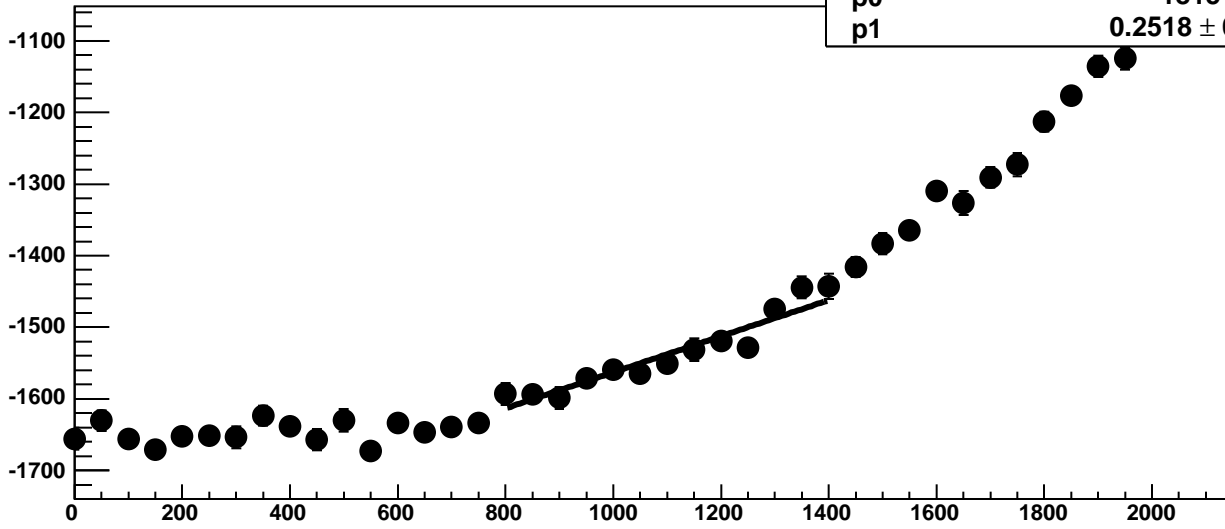
Chip 1, Channel 15, Enable 5!, Hold=30, ADC Noise vs DAC



Chip 1, Channel 15, Enable 5!, Hold=30, ADC Residuals vs DAC

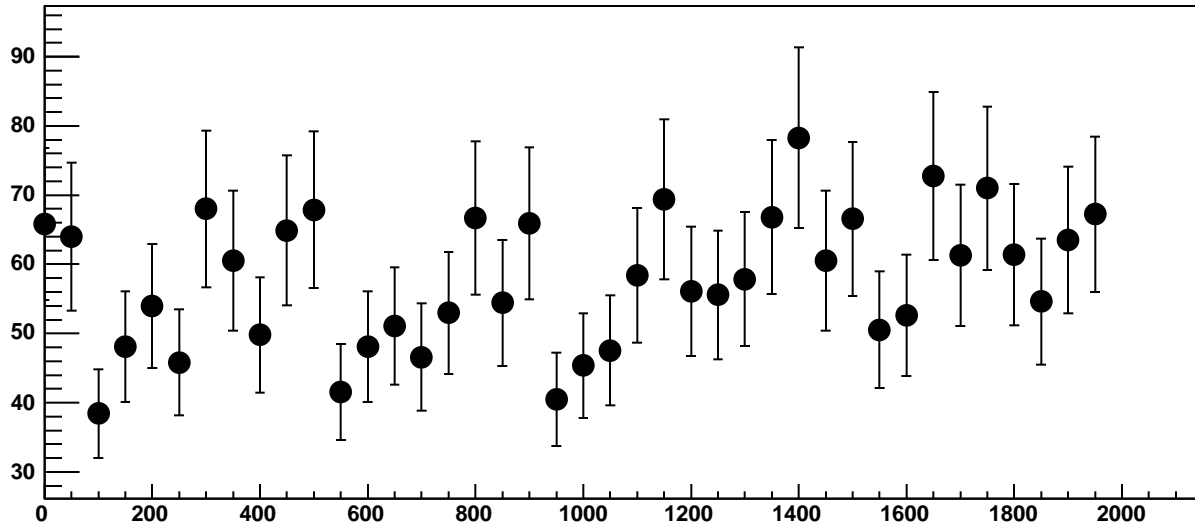


Chip 1, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC

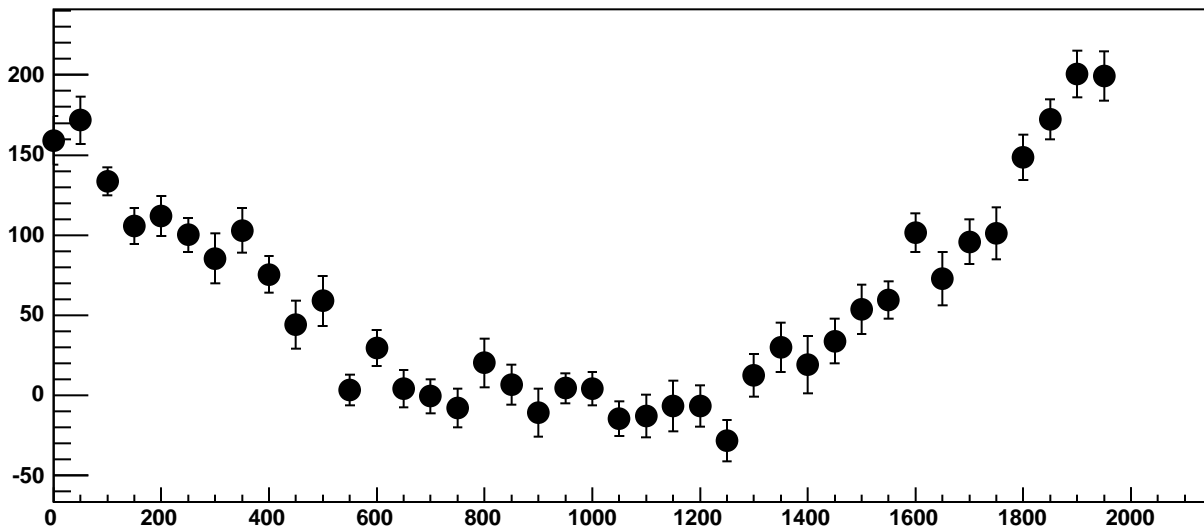


$\chi^2 / \text{ndf}$  16.8 / 11  
p0  $-1815 \pm 22.86$   
p1  $0.2518 \pm 0.02101$

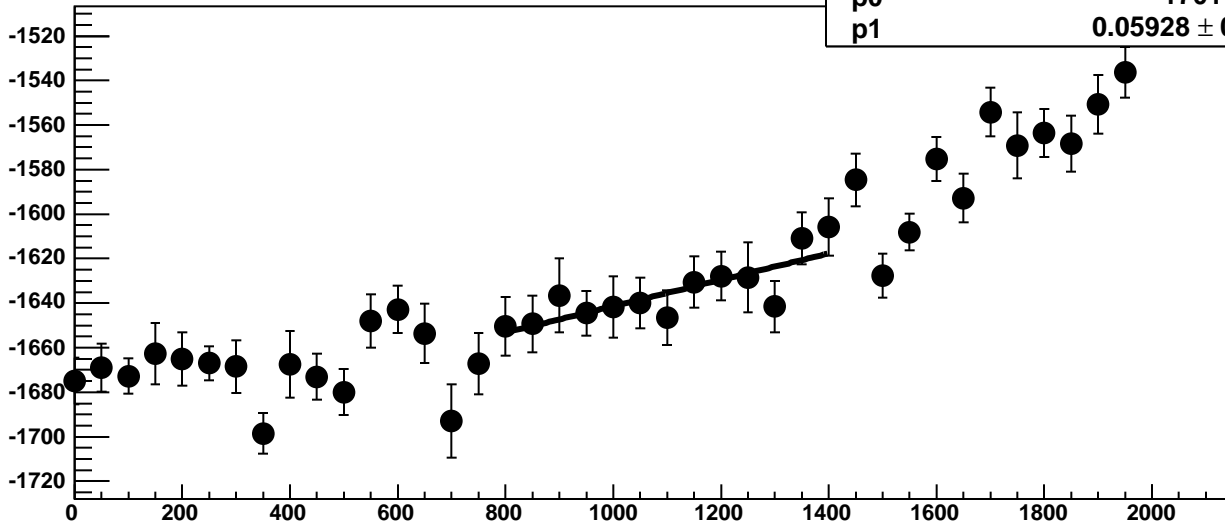
Chip 1, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC



Chip 1, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC

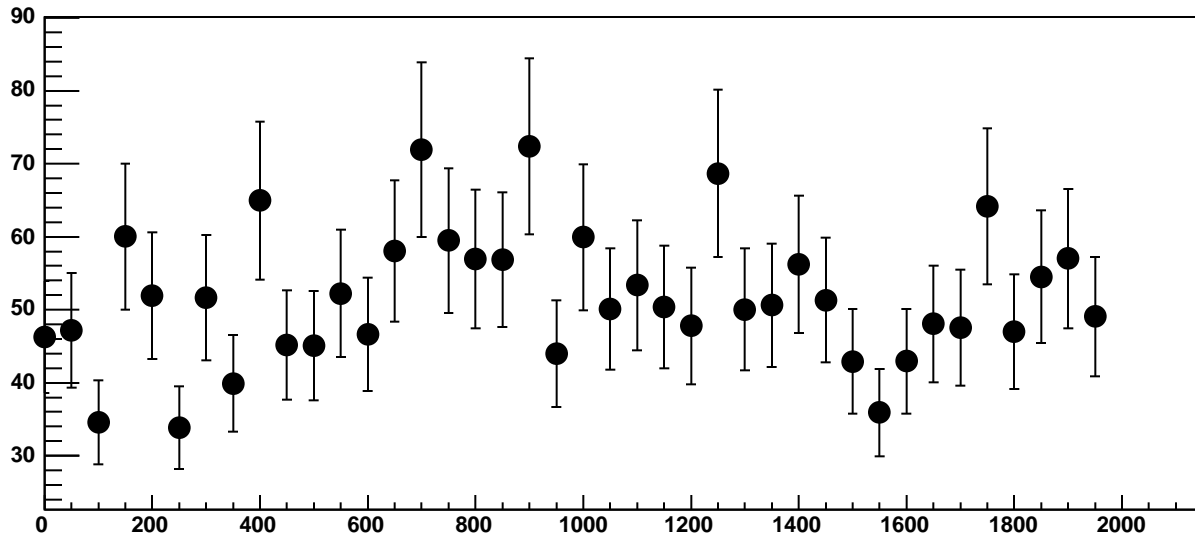


Chip 1, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC

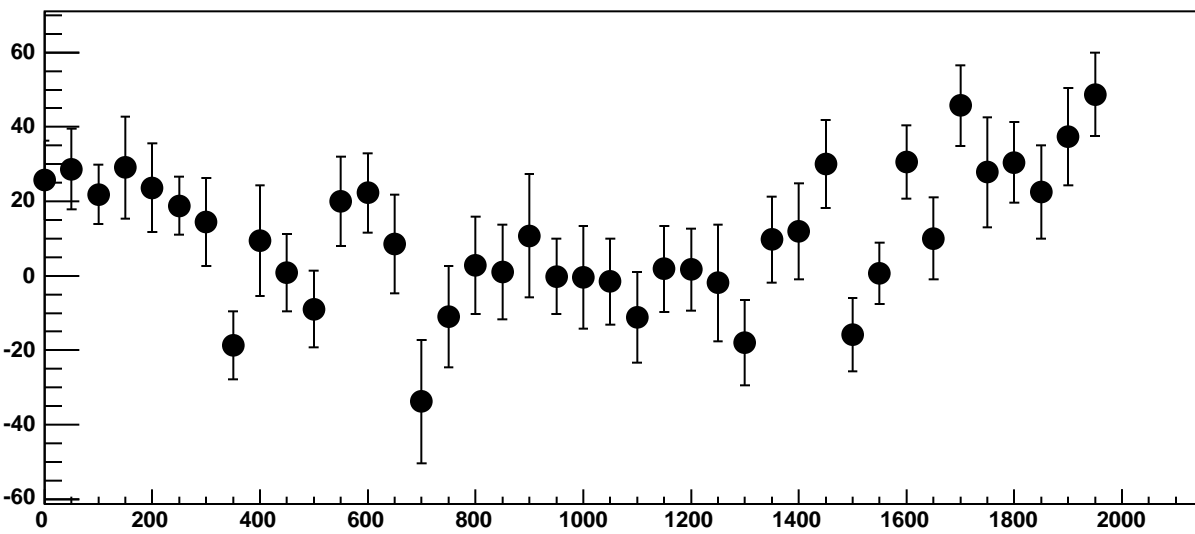


$\chi^2 / \text{ndf}$  5.371 / 11  
p0  $-1701 \pm 21.02$   
p1  $0.05928 \pm 0.01874$

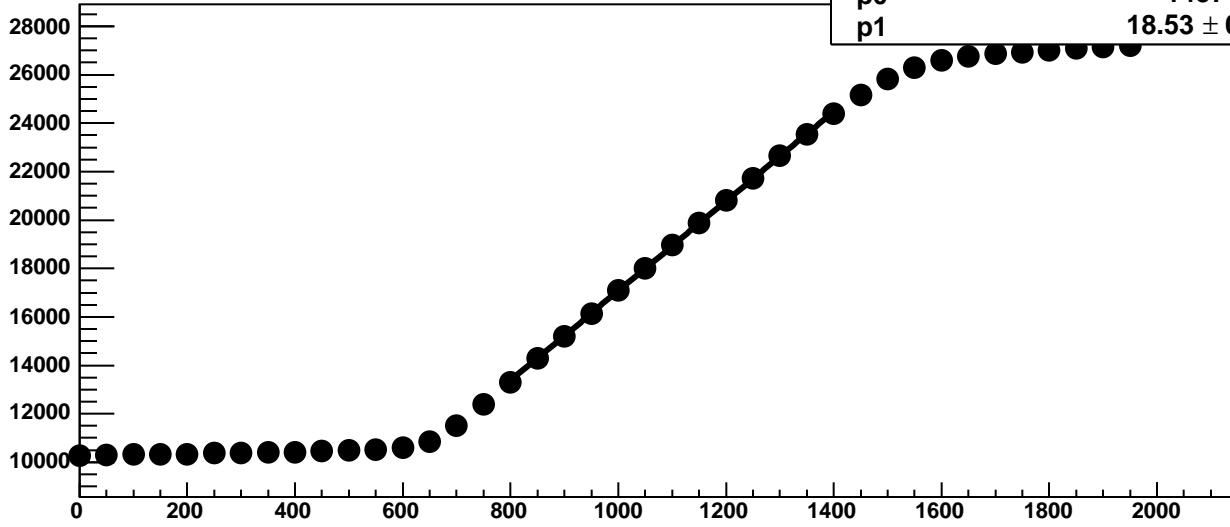
Chip 1, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC



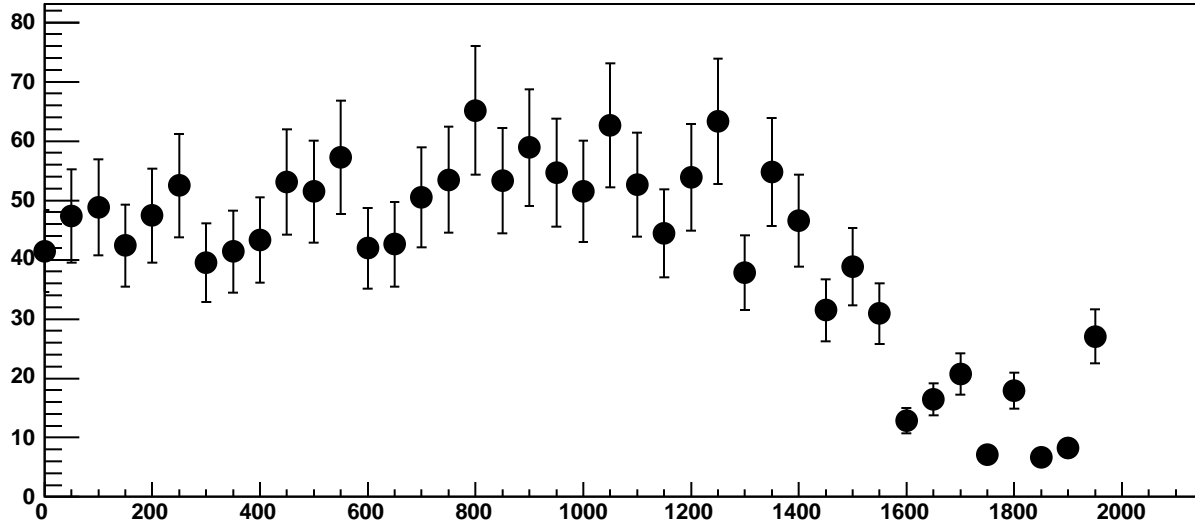
Chip 1, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC



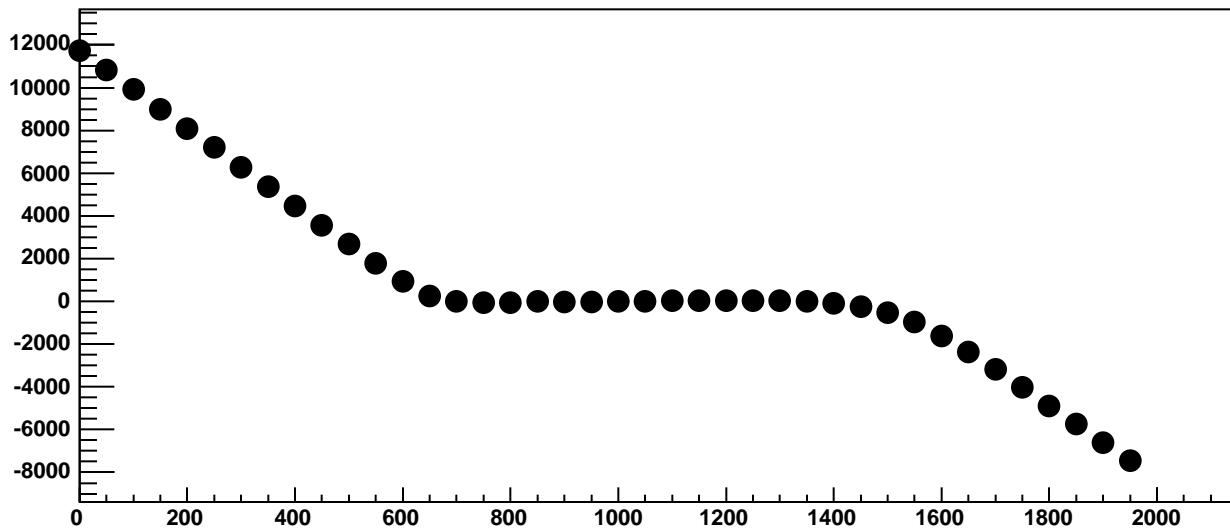
Chip 1, Channel 16, Enable 2!, Hold=30, ADC Mean vs DAC



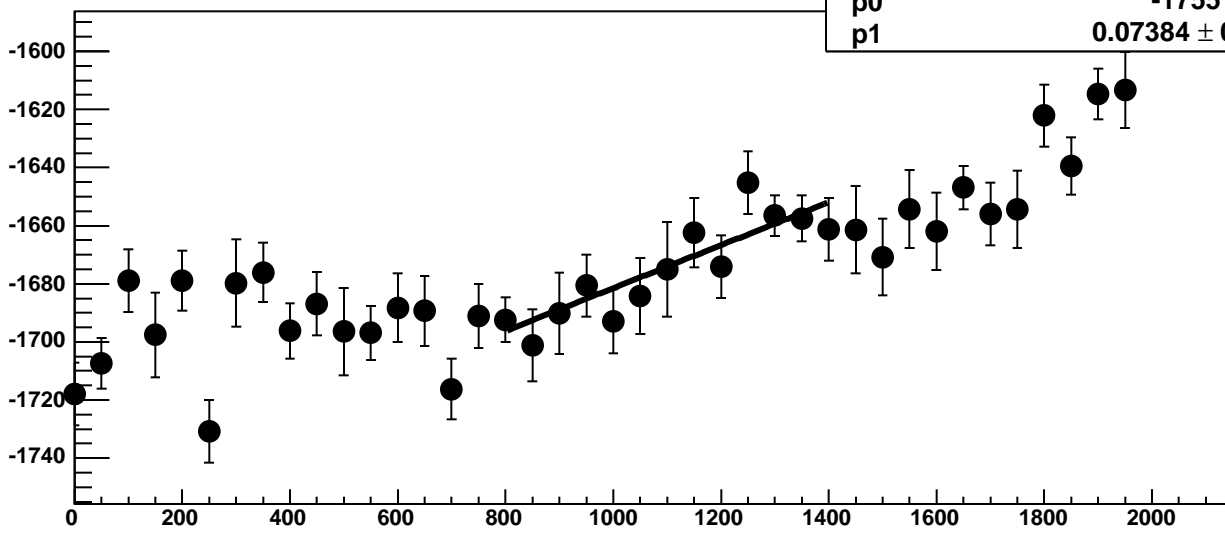
Chip 1, Channel 16, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 1, Channel 16, Enable 2!, Hold=30, ADC Residuals vs DAC

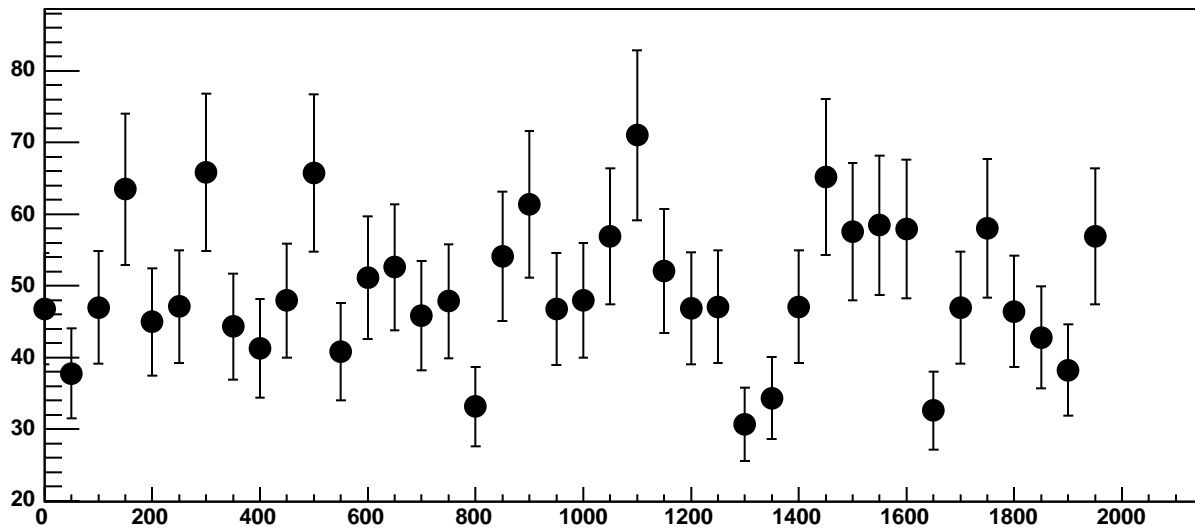


Chip 1, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC

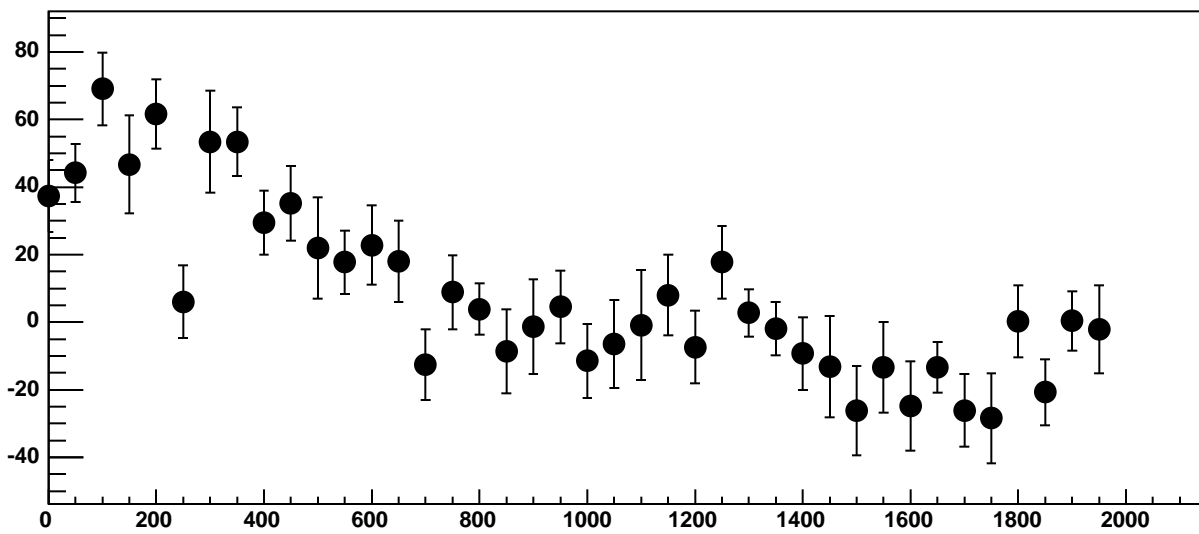


$\chi^2 / \text{ndf}$  6.828 / 11  
p0  $-1755 \pm 15.73$   
p1  $0.07384 \pm 0.01377$

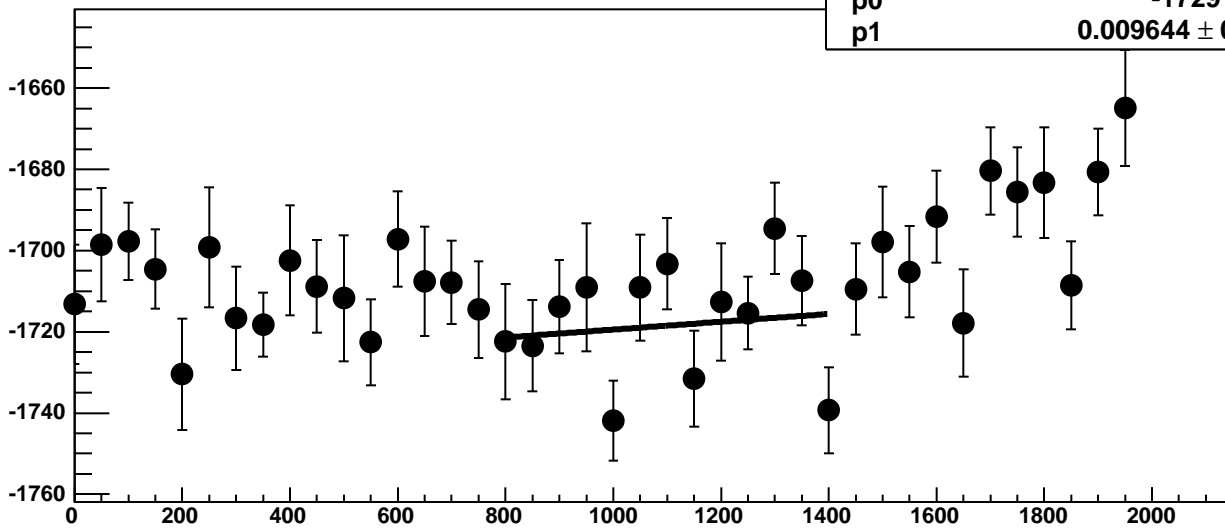
Chip 1, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC



Chip 1, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC

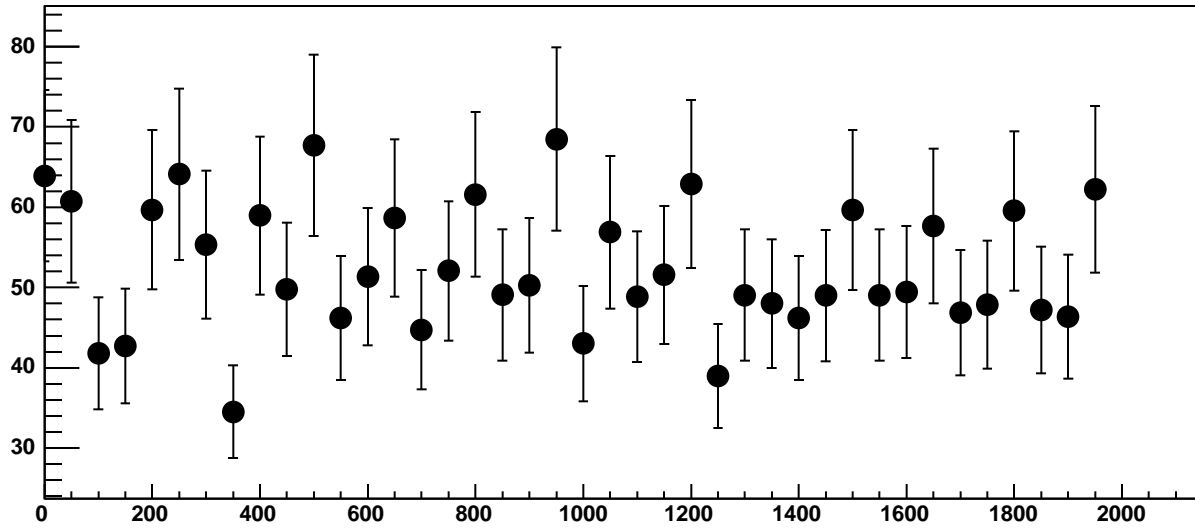


Chip 1, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC

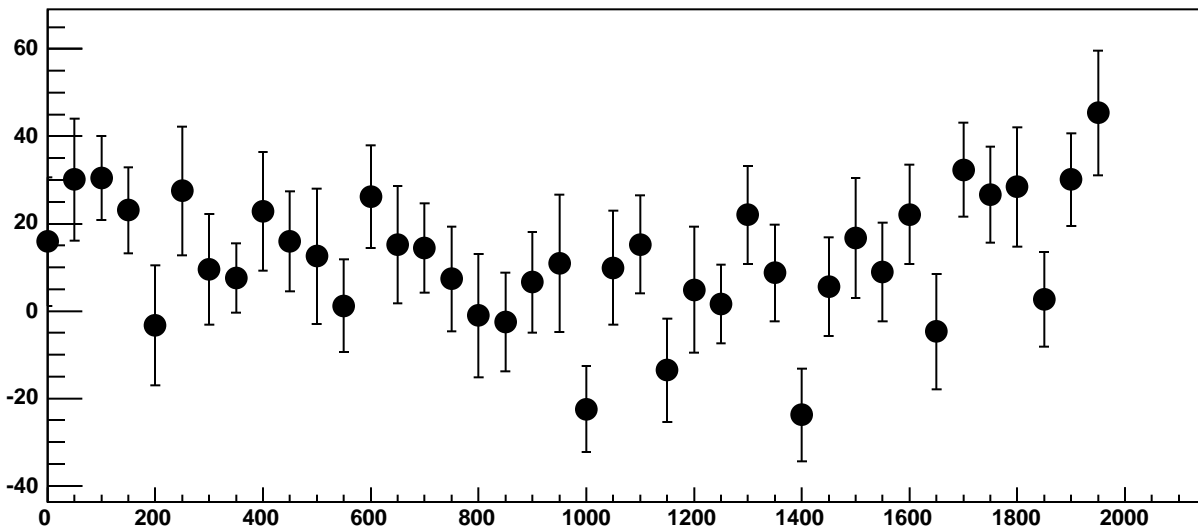


$\chi^2 / \text{ndf}$  19.34 / 11  
p0  $-1729 \pm 19.54$   
p1  $0.009644 \pm 0.01719$

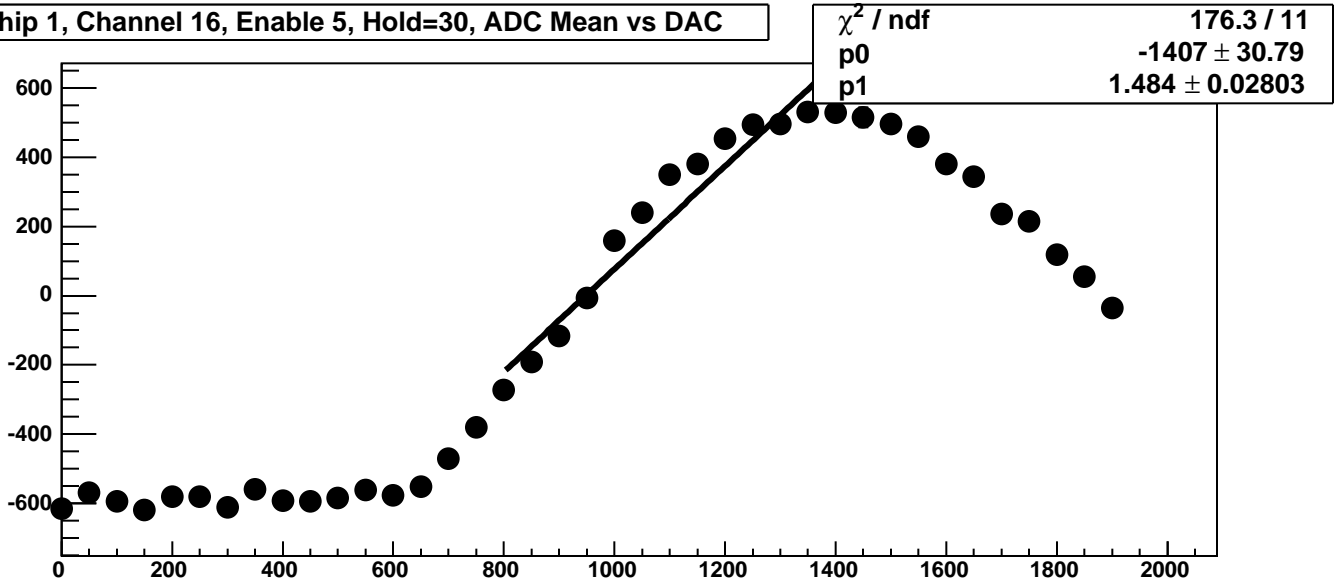
Chip 1, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC



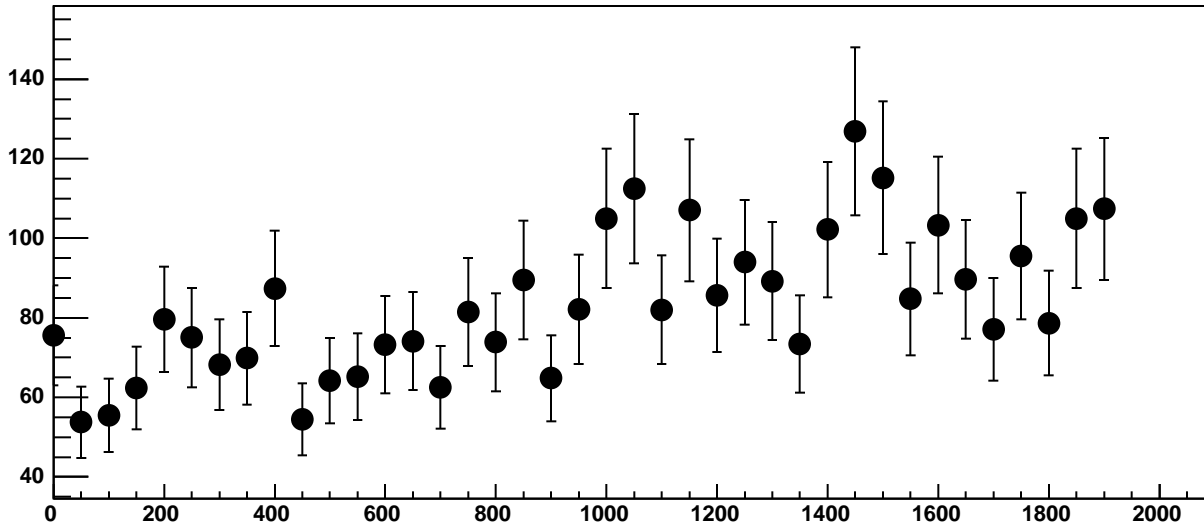
Chip 1, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC



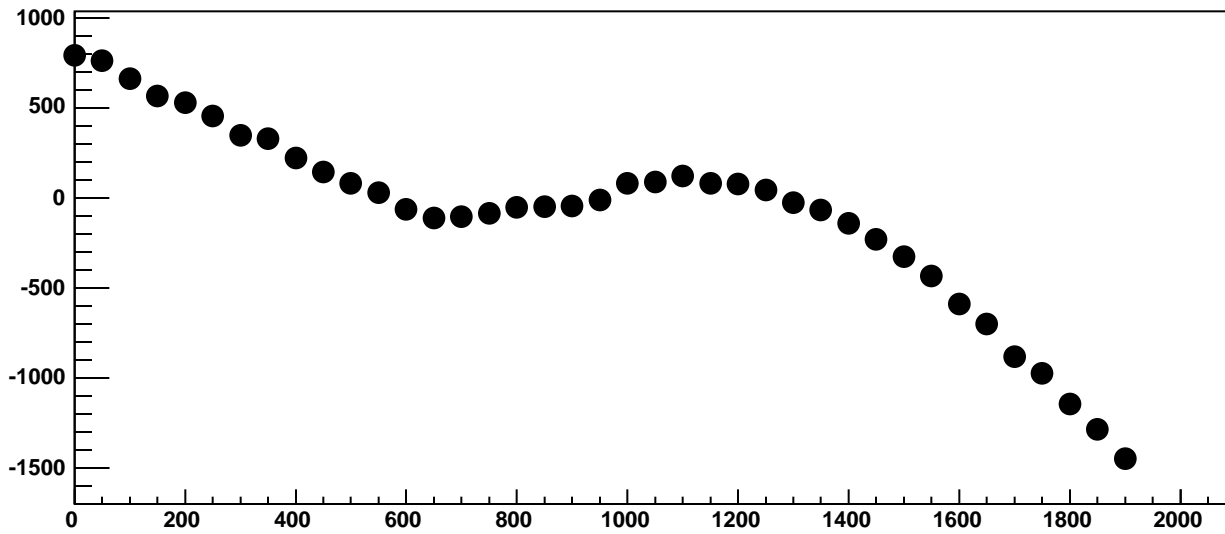
Chip 1, Channel 16, Enable 5, Hold=30, ADC Mean vs DAC



Chip 1, Channel 16, Enable 5, Hold=30, ADC Noise vs DAC

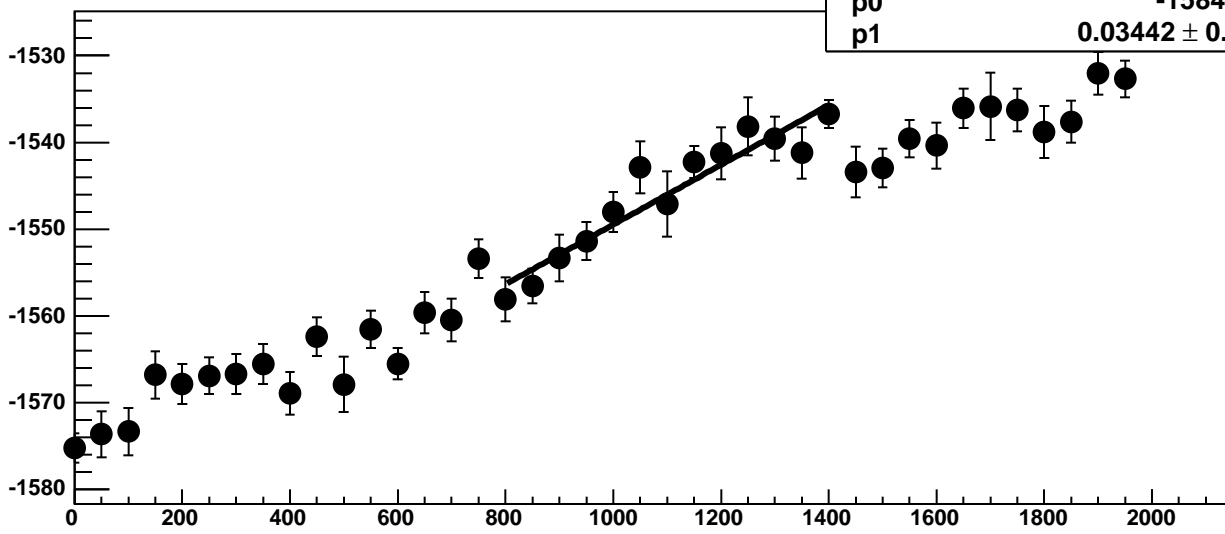


Chip 1, Channel 16, Enable 5, Hold=30, ADC Residuals vs DAC

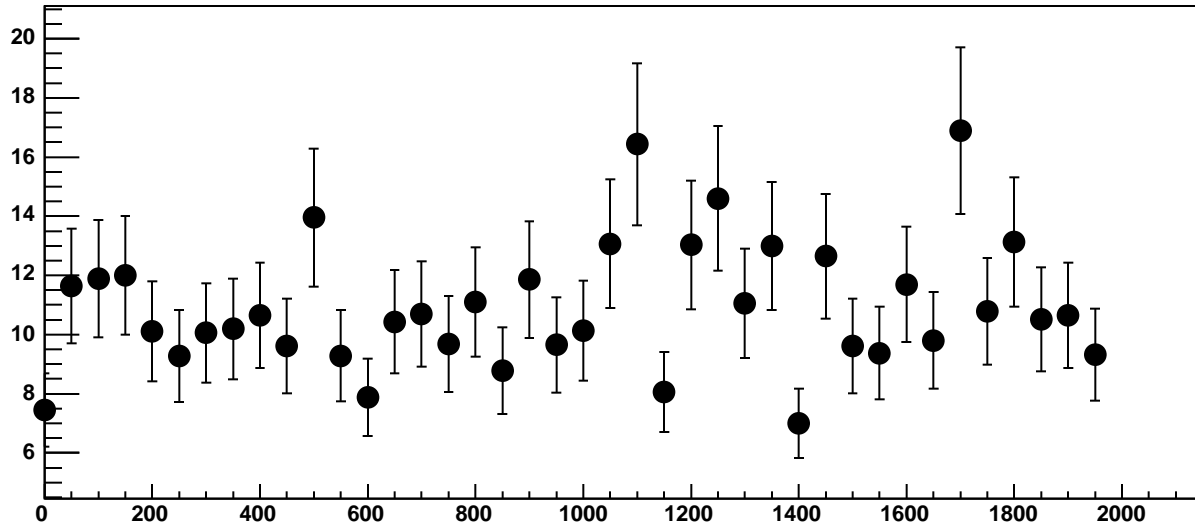




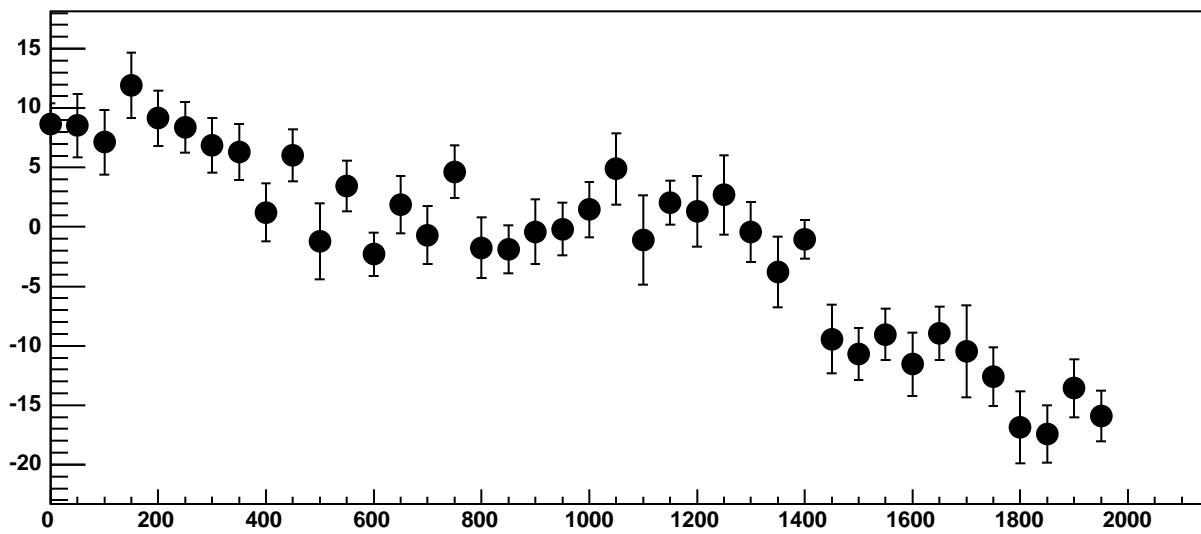
Chip 1, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC



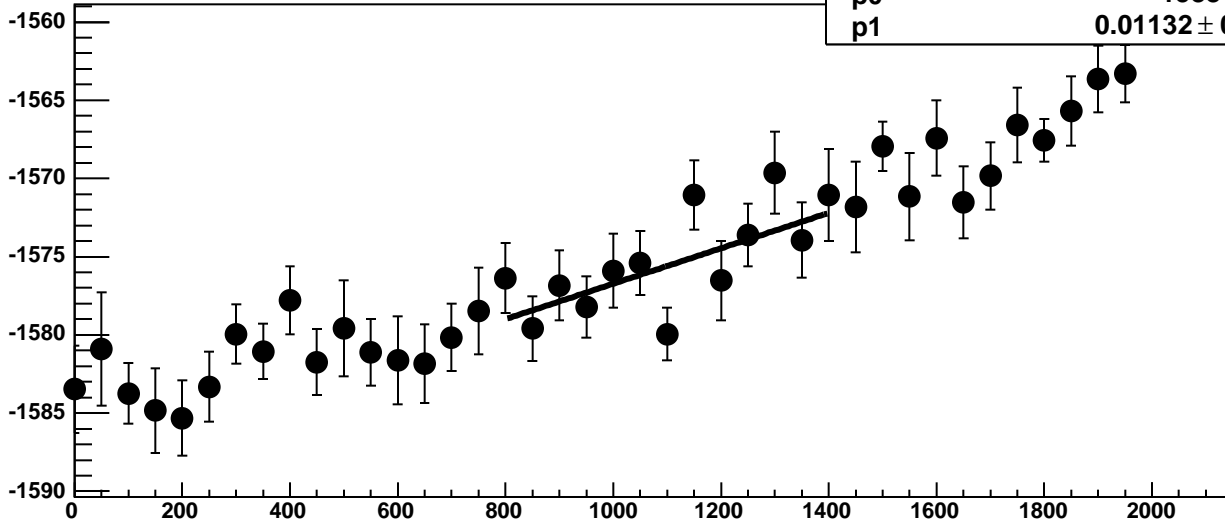
Chip 1, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC



Chip 1, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC

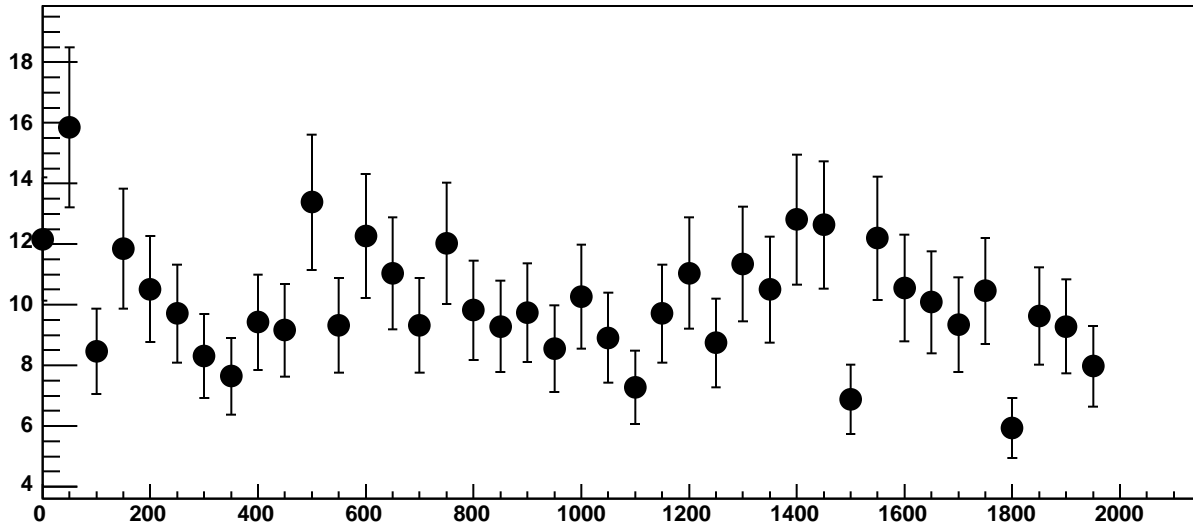


Chip 1, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC

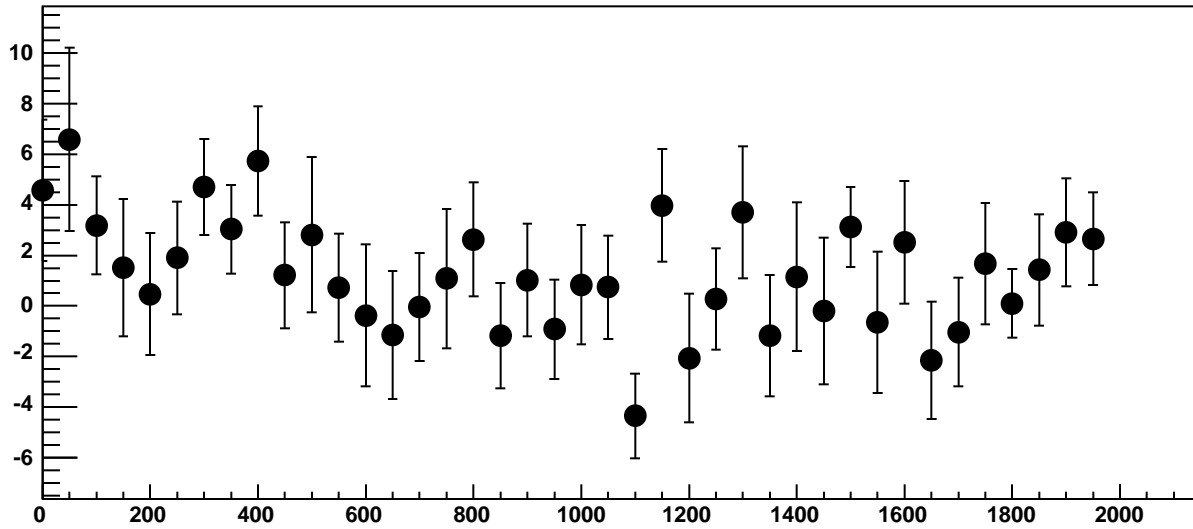


$\chi^2 / \text{ndf}$  15.45 / 11  
p0  $-1588 \pm 3.816$   
p1  $0.01132 \pm 0.00349$

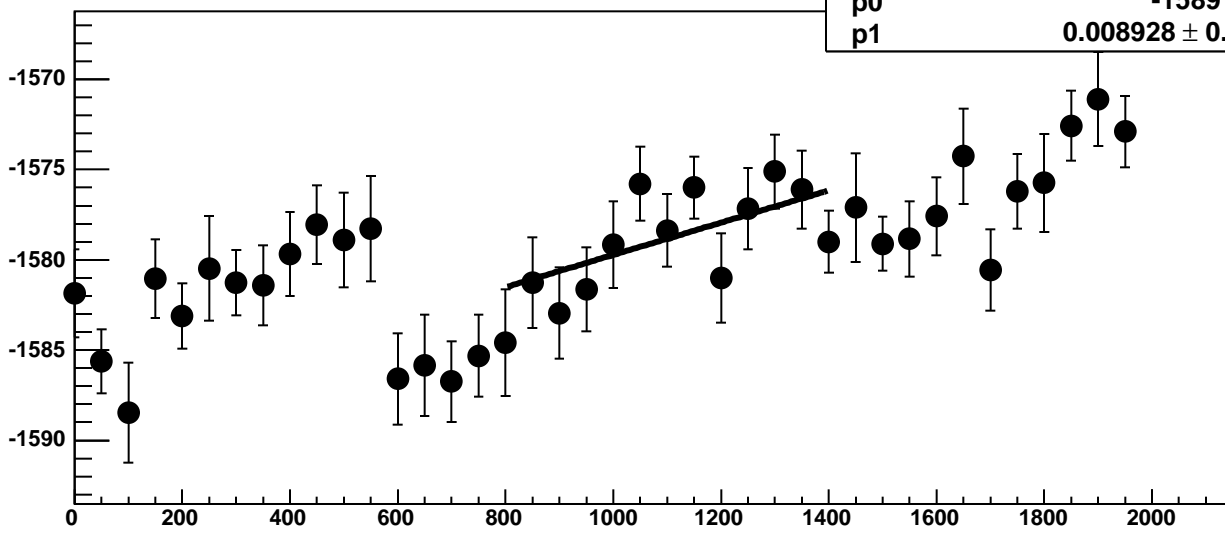
Chip 1, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC



Chip 1, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC

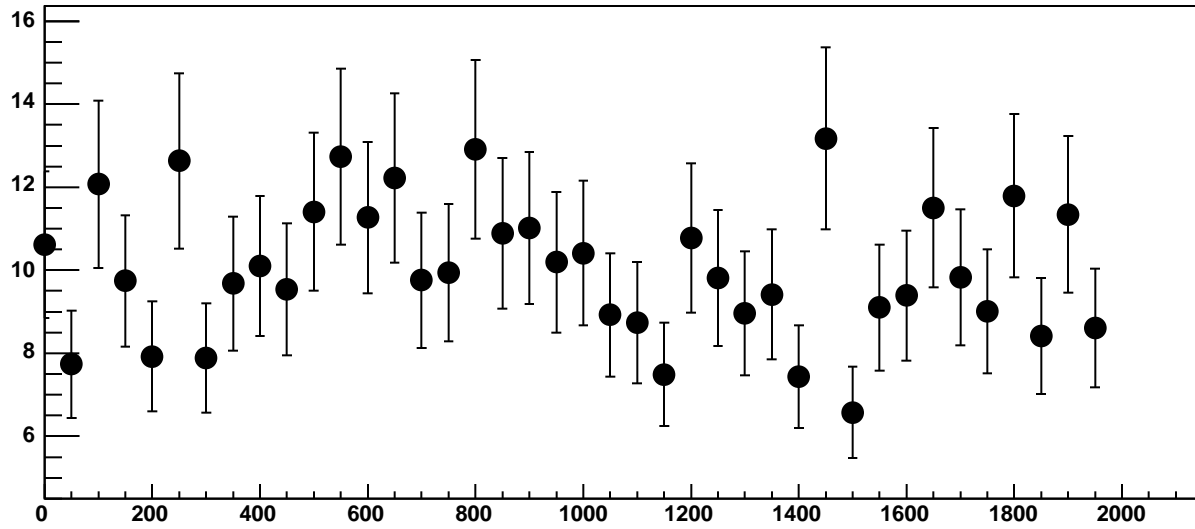


Chip 1, Channel 17, Enable 2, Hold=30, ADC Mean vs DAC

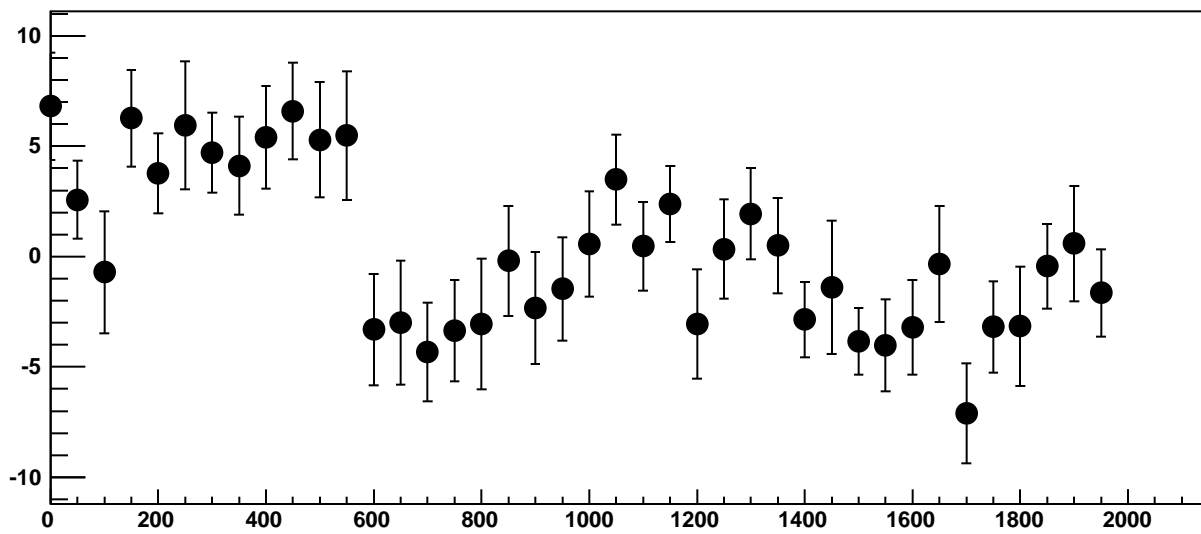


$\chi^2 / \text{ndf}$  12.54 / 11  
p0  $-1589 \pm 3.844$   
p1  $0.008928 \pm 0.003338$

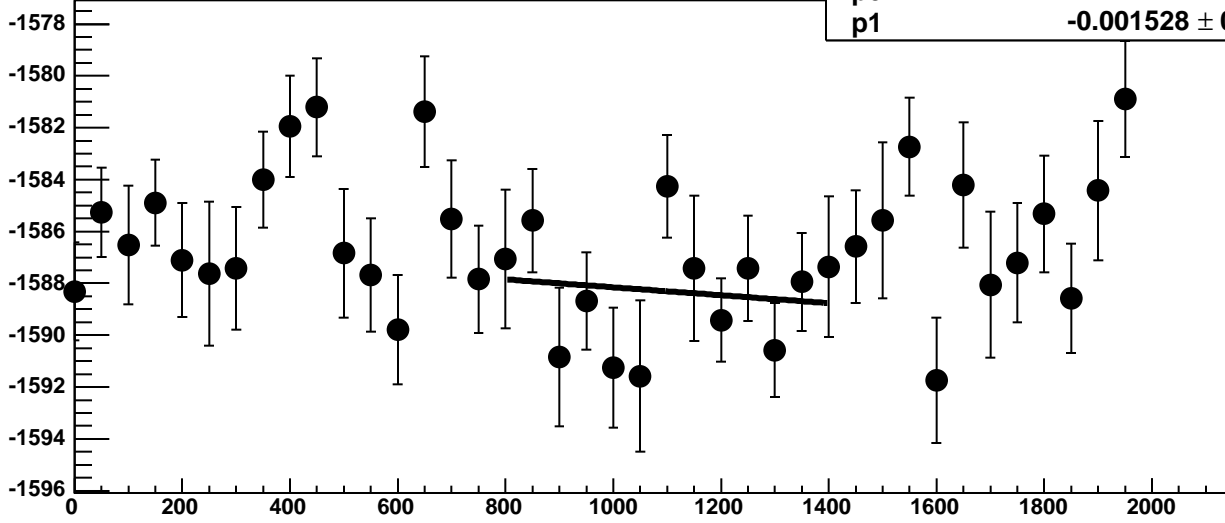
Chip 1, Channel 17, Enable 2, Hold=30, ADC Noise vs DAC



Chip 1, Channel 17, Enable 2, Hold=30, ADC Residuals vs DAC

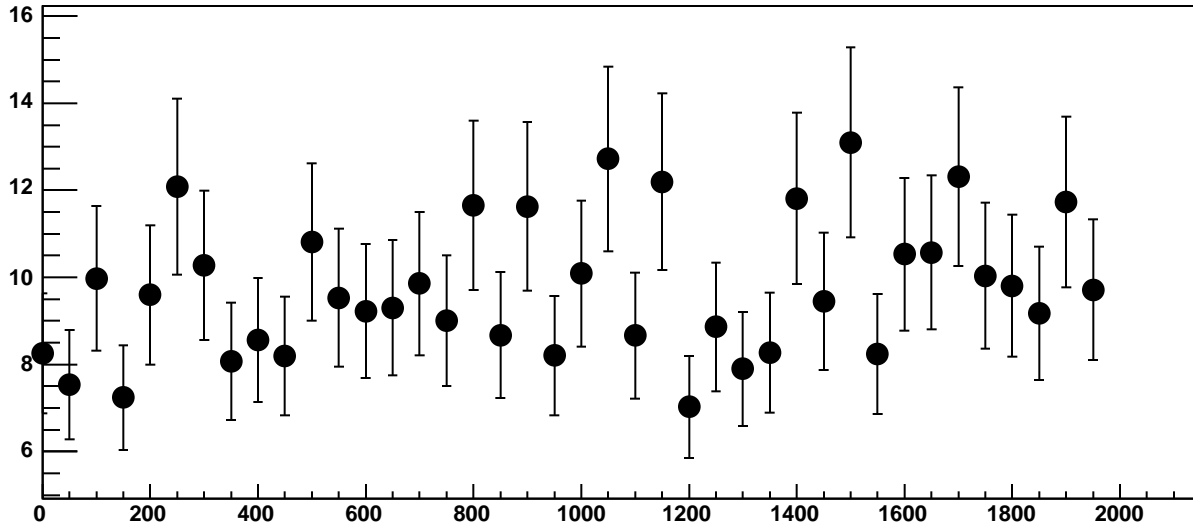


Chip 1, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC

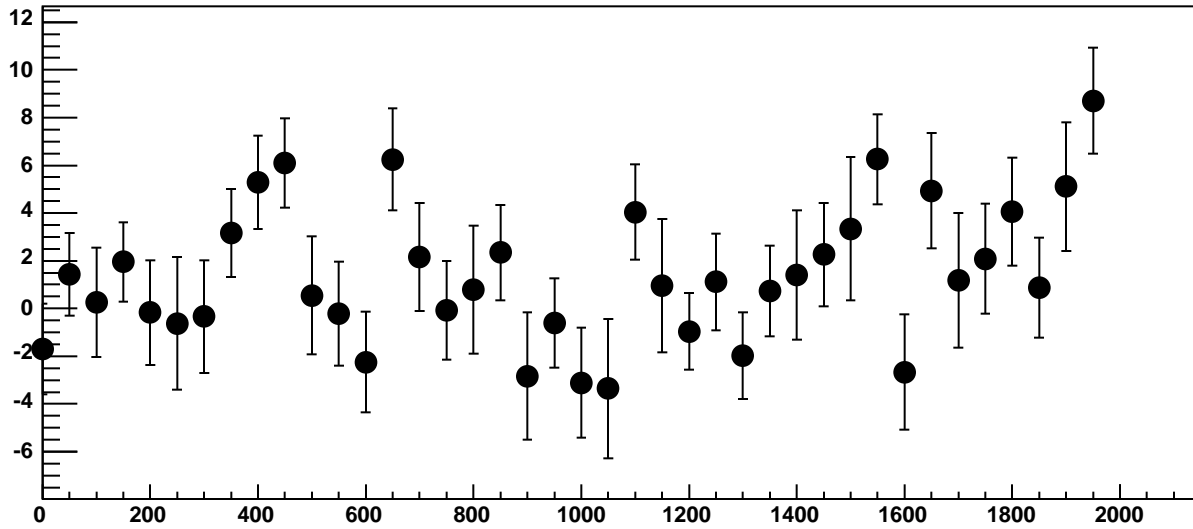


$\chi^2 / \text{ndf}$  12.35 / 11  
p0  $-1587 \pm 3.716$   
p1  $-0.001528 \pm 0.00328$

Chip 1, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC



Chip 1, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC



Chip 1, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

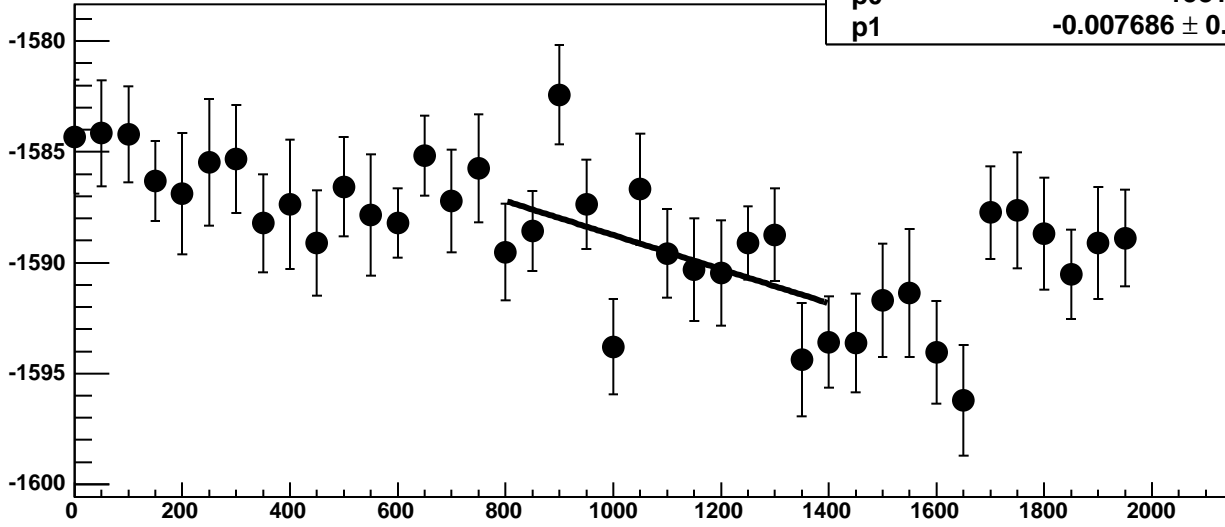
18.49 / 11

p0

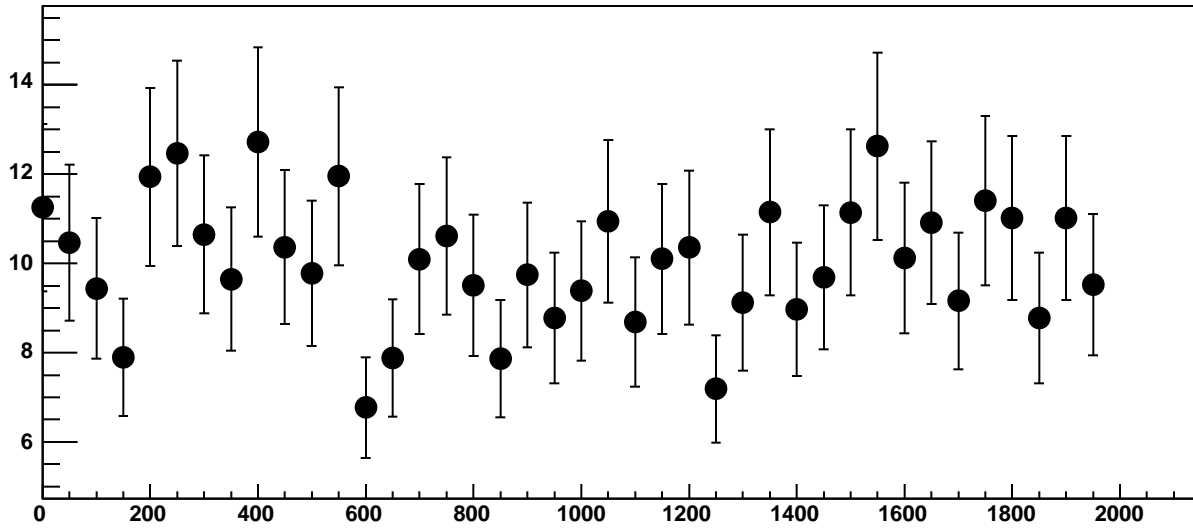
$-1581 \pm 3.441$

p1

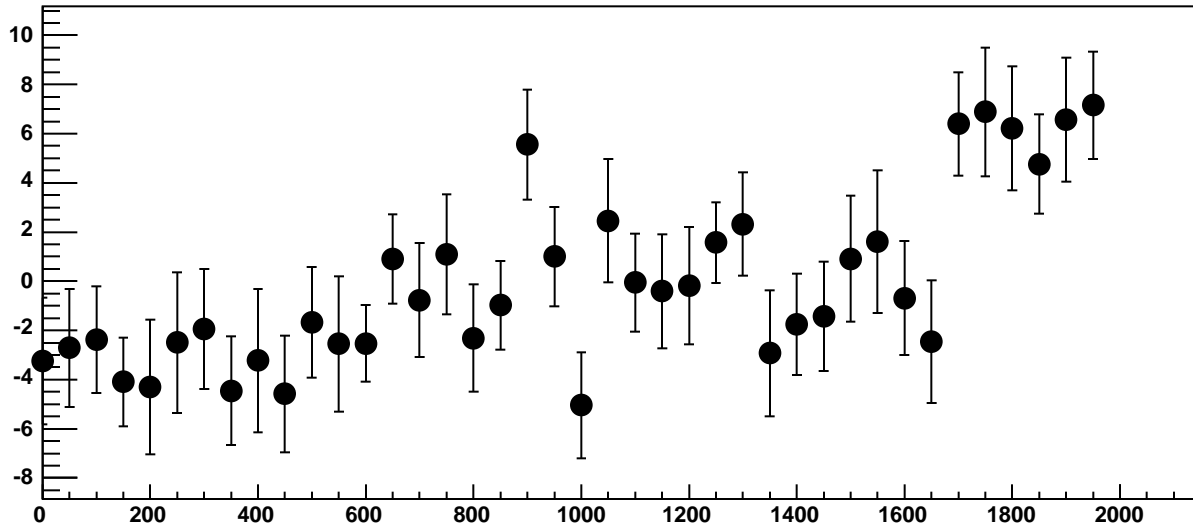
$-0.007686 \pm 0.003092$



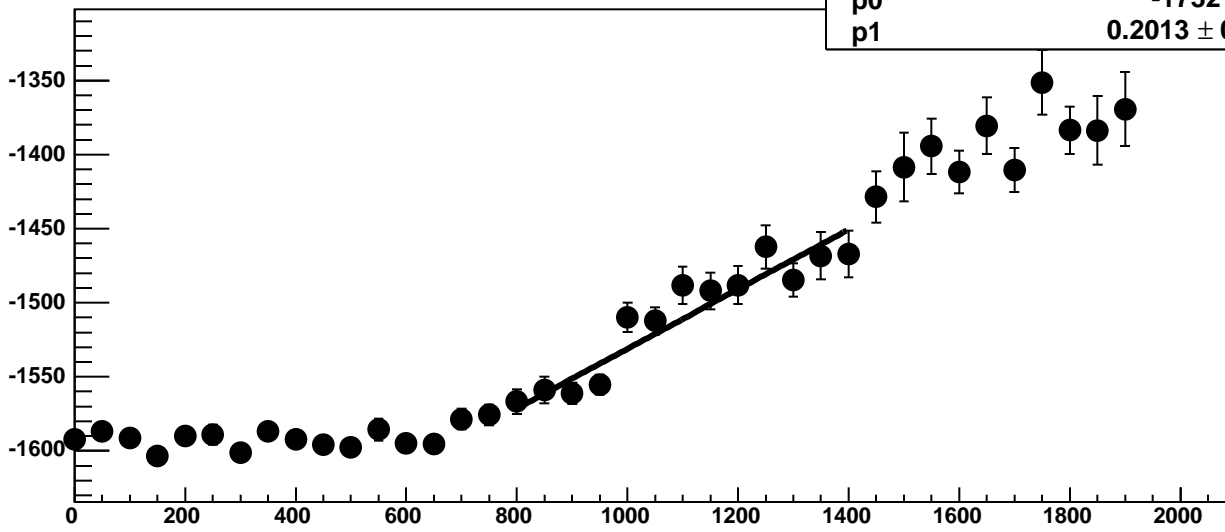
Chip 1, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC



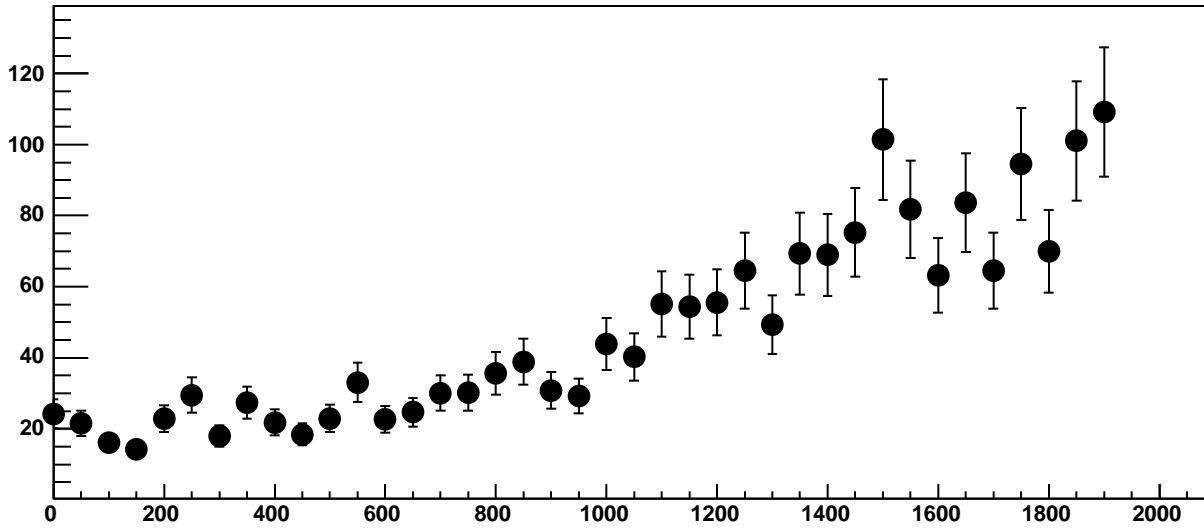
Chip 1, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC



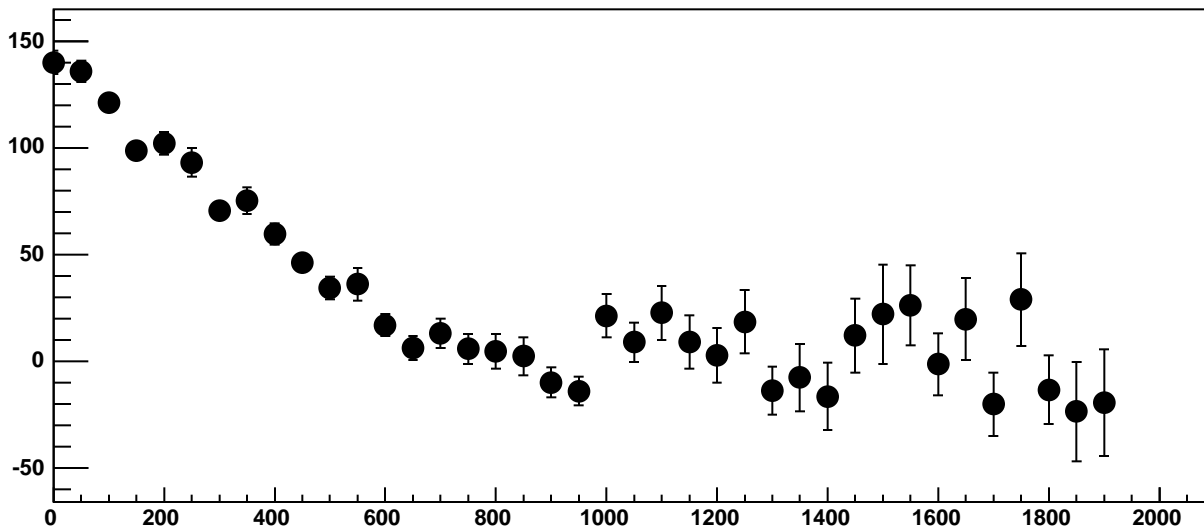
Chip 1, Channel 17, Enable 5!, Hold=30, ADC Mean vs DAC



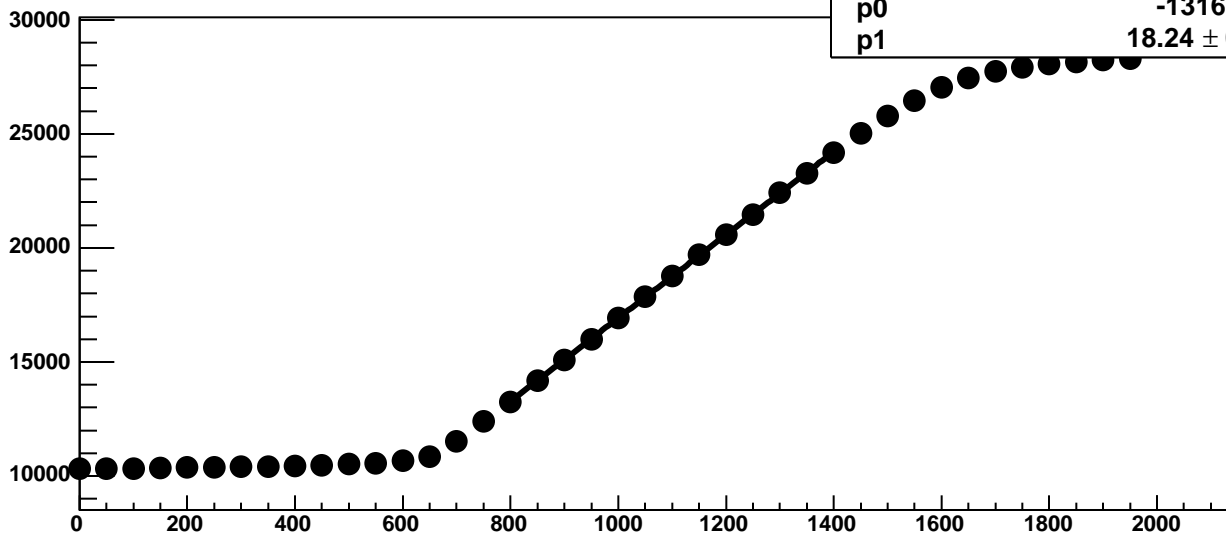
Chip 1, Channel 17, Enable 5!, Hold=30, ADC Noise vs DAC



Chip 1, Channel 17, Enable 5!, Hold=30, ADC Residuals vs DAC

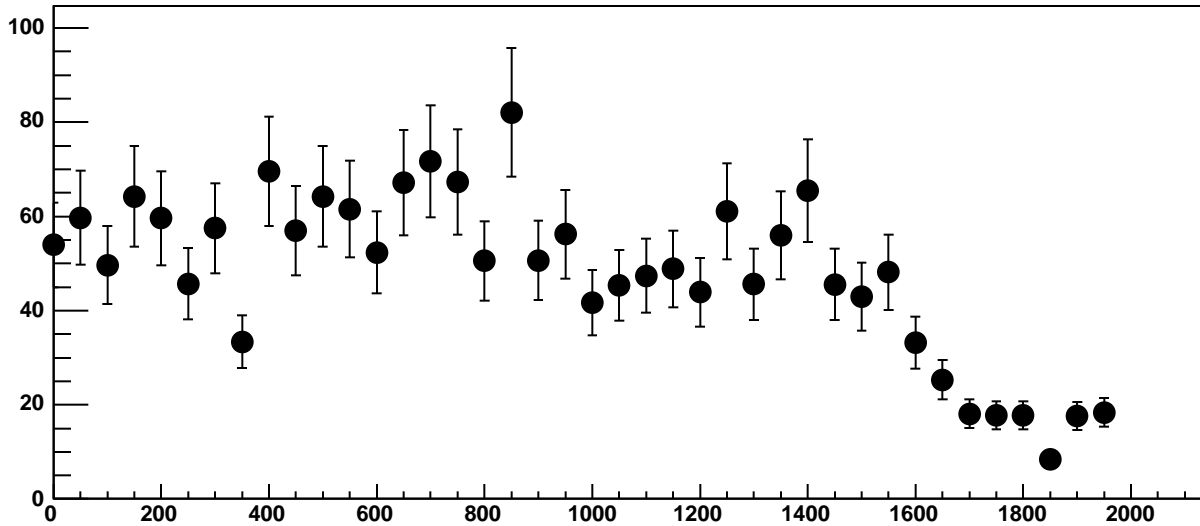


Chip 2, Channel 0, Enable 0!, Hold=30, ADC Mean vs DAC

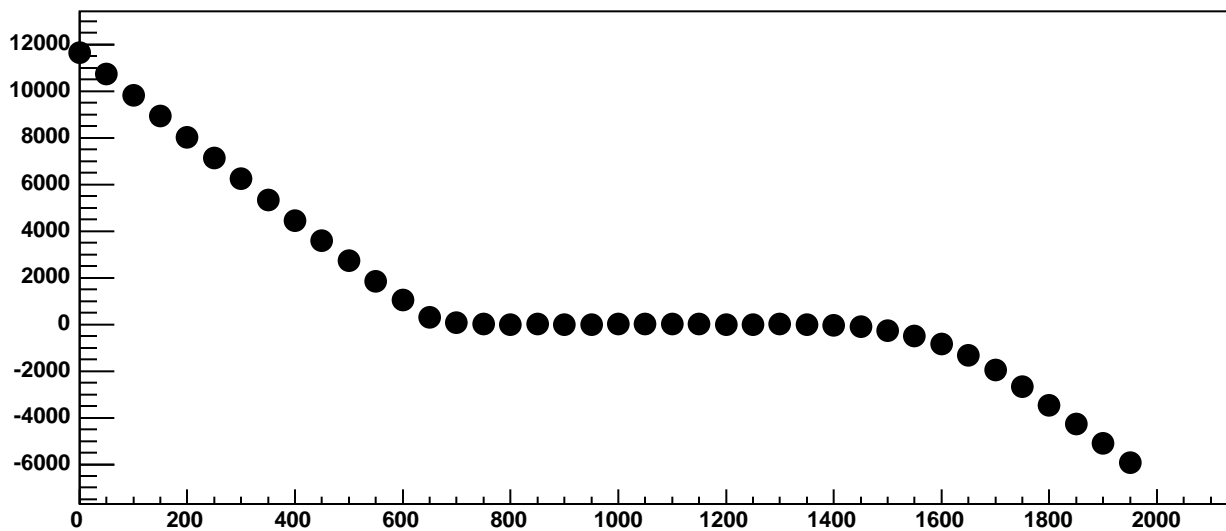


$\chi^2 / \text{ndf}$	30.95 / 11
p0	$-1316 \pm 21.09$
p1	$18.24 \pm 0.01895$

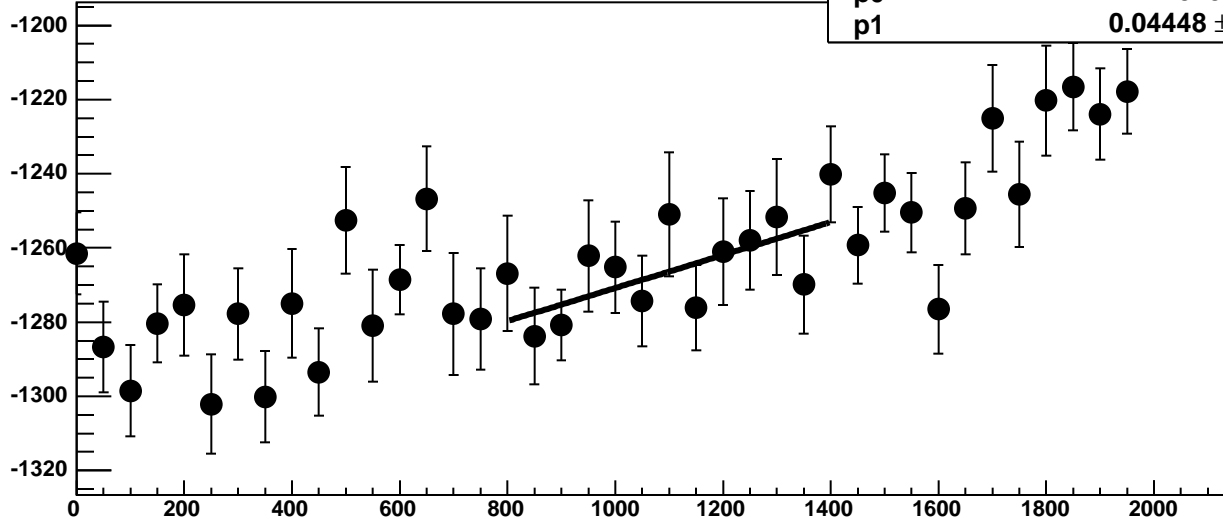
Chip 2, Channel 0, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 2, Channel 0, Enable 0!, Hold=30, ADC Residuals vs DAC

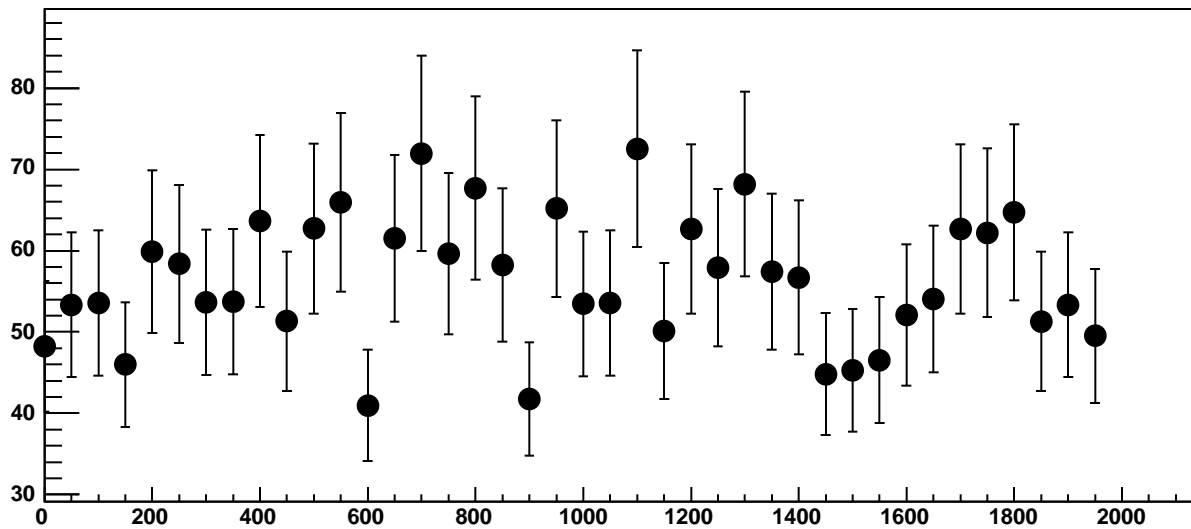


Chip 2, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC

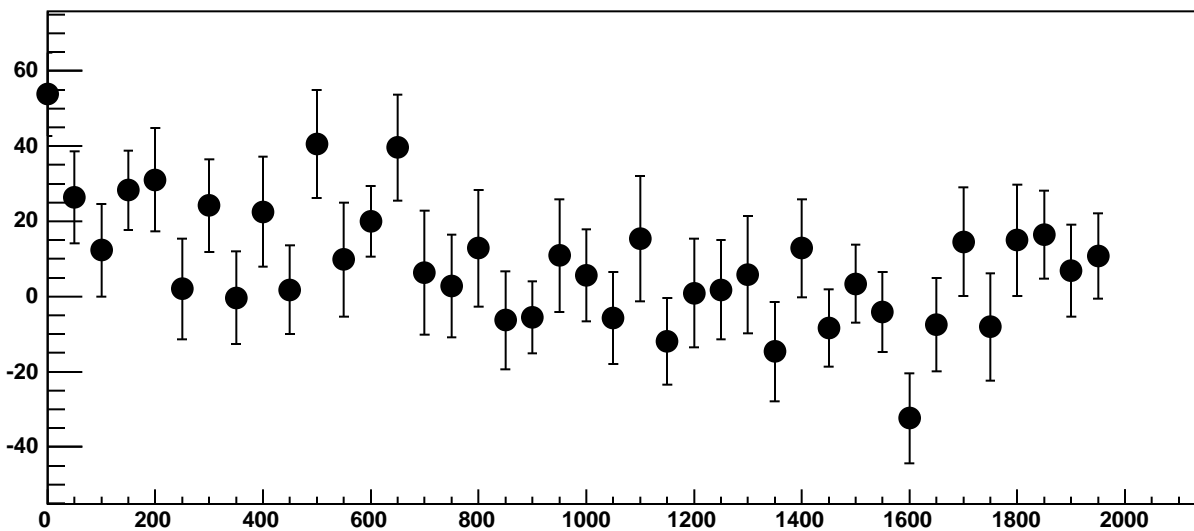


$\chi^2 / \text{ndf}$  6.507 / 11  
p0  $-1315 \pm 21.66$   
p1  $0.04448 \pm 0.0196$

Chip 2, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC

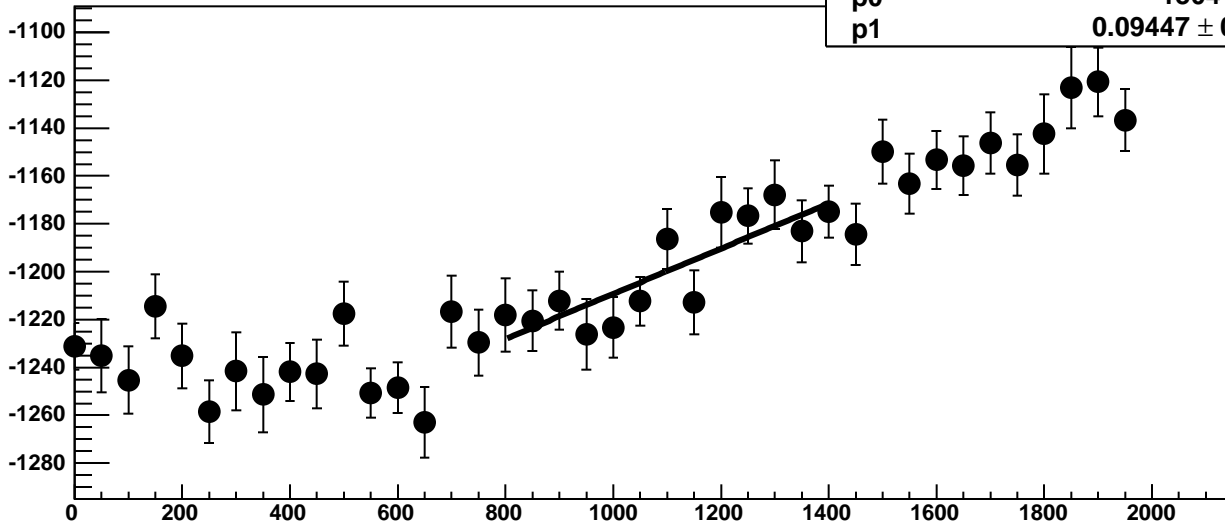


Chip 2, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC

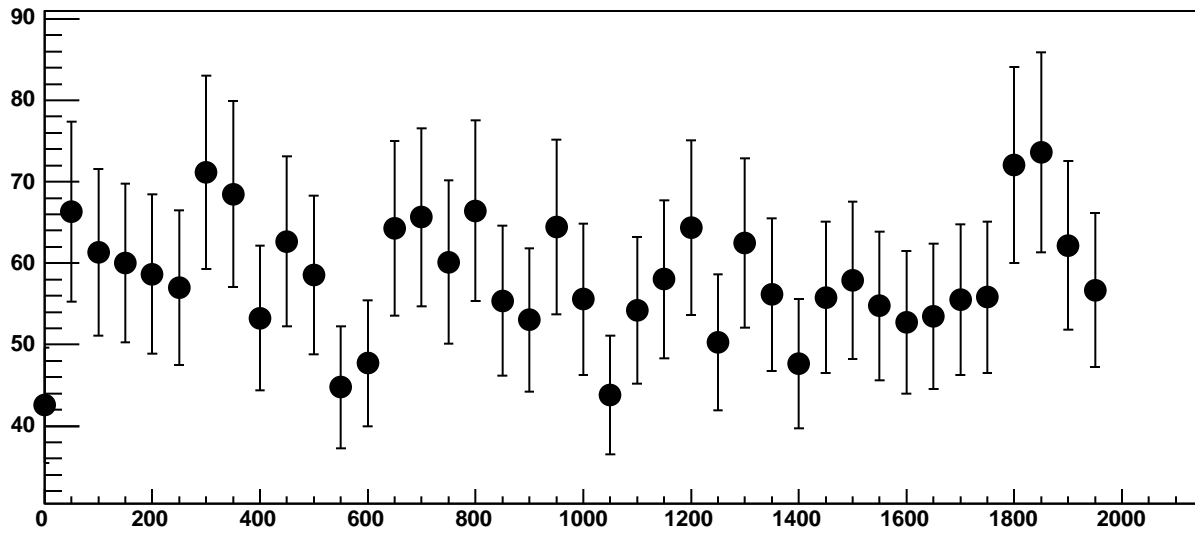




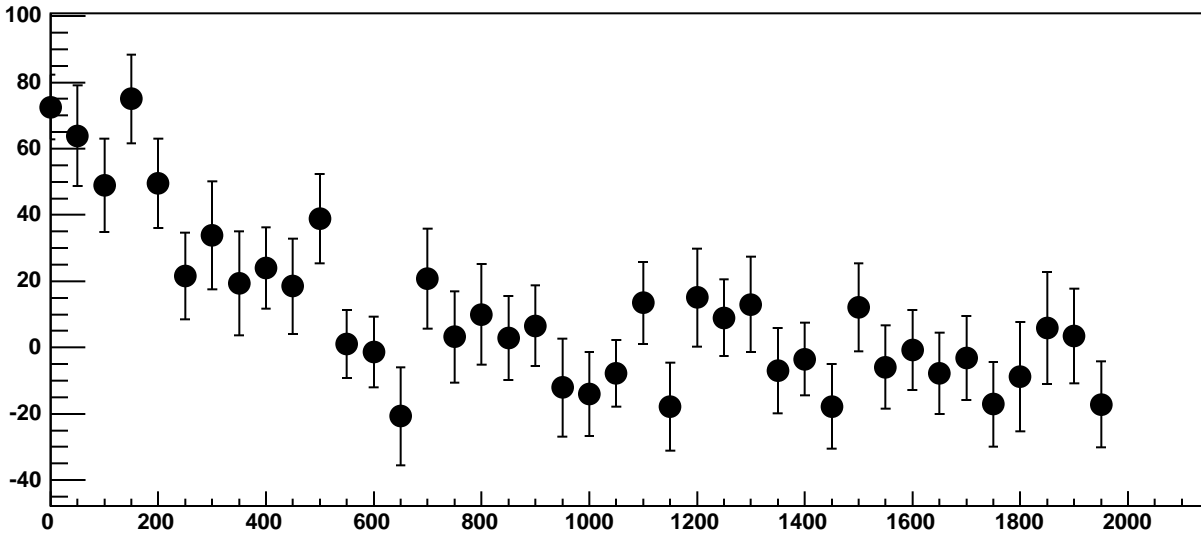
Chip 2, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC



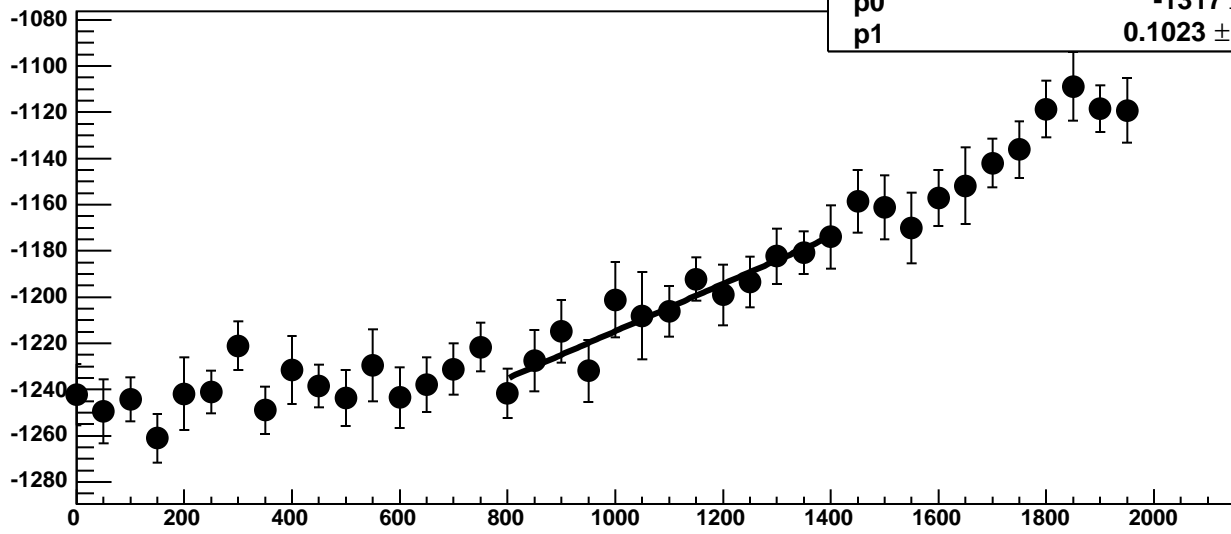
Chip 2, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC



Chip 2, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC

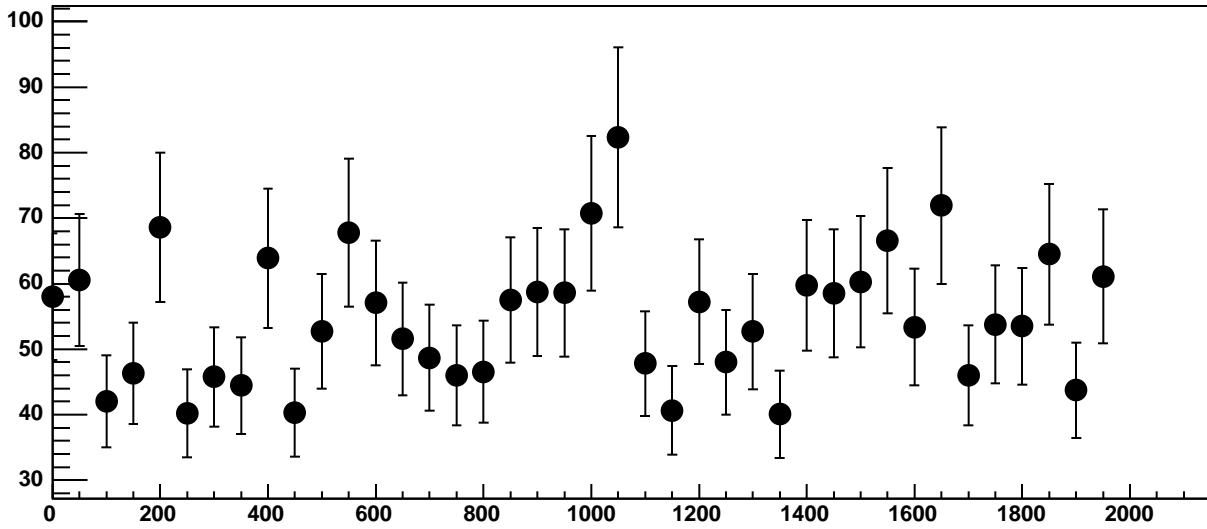


Chip 2, Channel 0, Enable 3, Hold=30, ADC Mean vs DAC

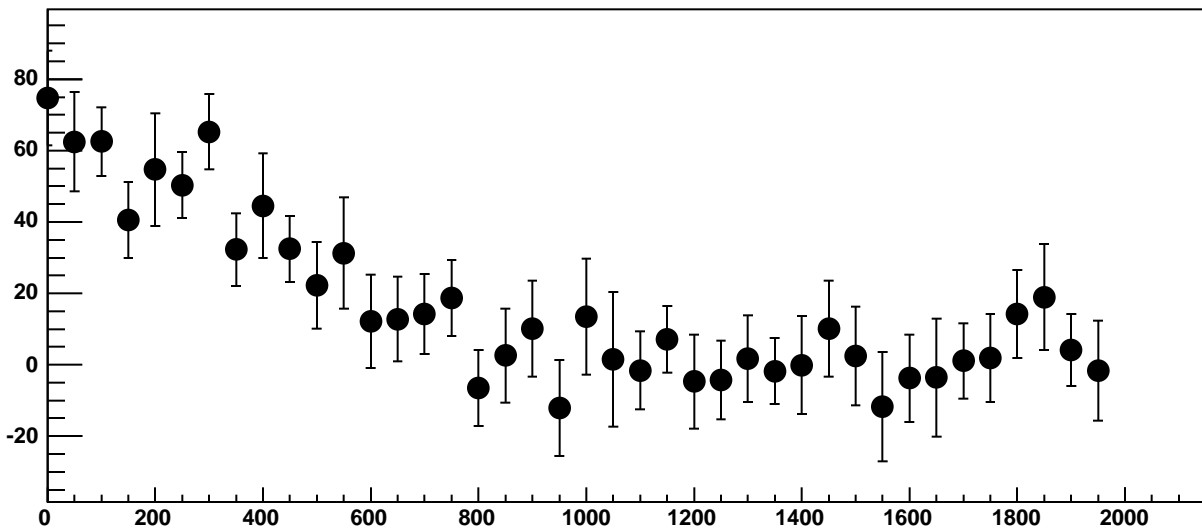


$\chi^2 / \text{ndf}$  3.429 / 11  
p0 -1317 ± 19.88  
p1 0.1023 ± 0.0175

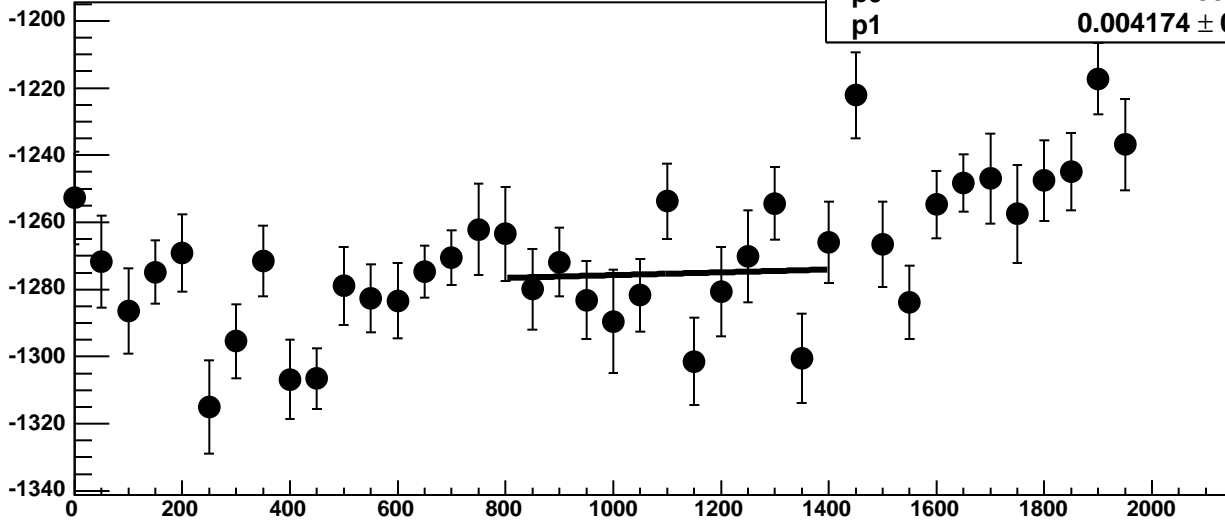
Chip 2, Channel 0, Enable 3, Hold=30, ADC Noise vs DAC



Chip 2, Channel 0, Enable 3, Hold=30, ADC Residuals vs DAC



Chip 2, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

18.51 / 11

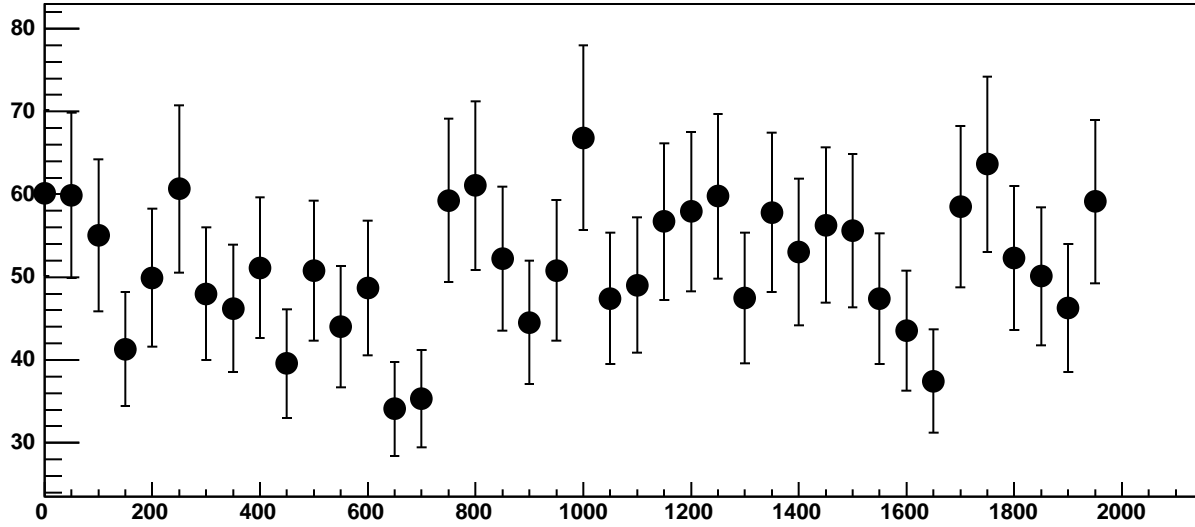
p0

$-1280 \pm 20.31$

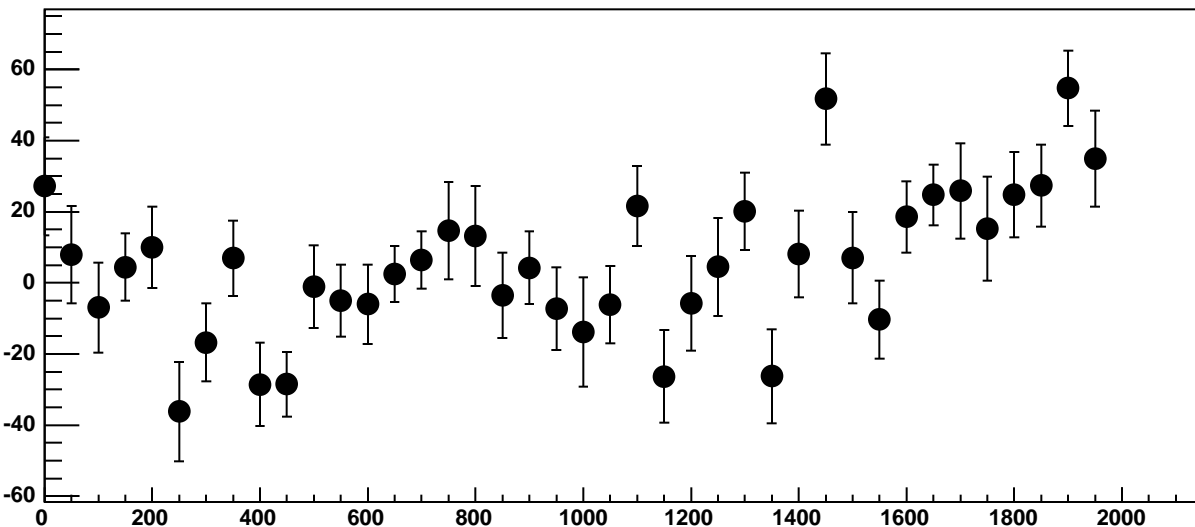
p1

$0.004174 \pm 0.01827$

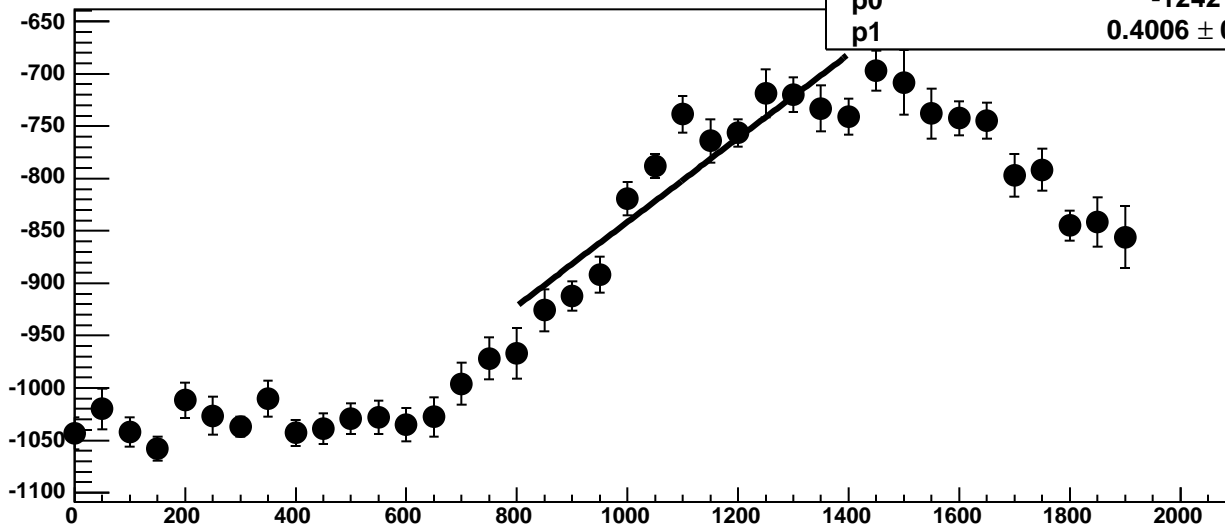
Chip 2, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC



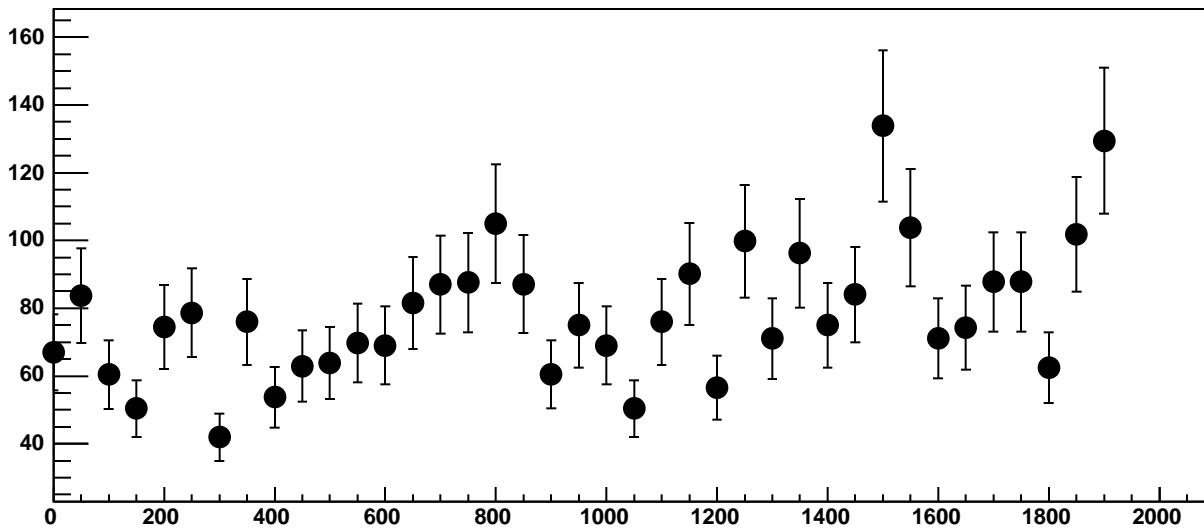
Chip 2, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC



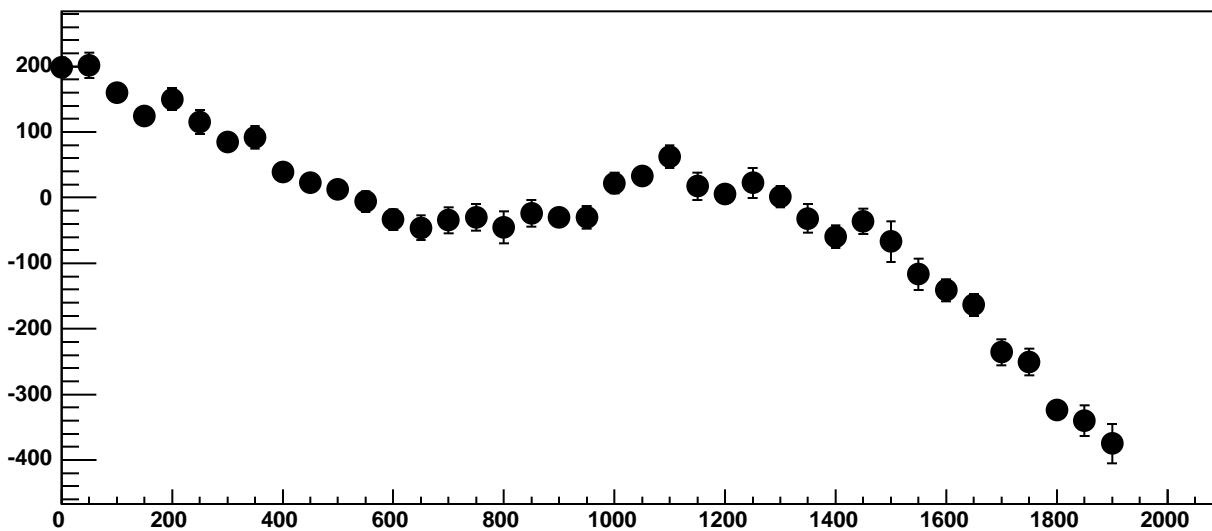
Chip 2, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC



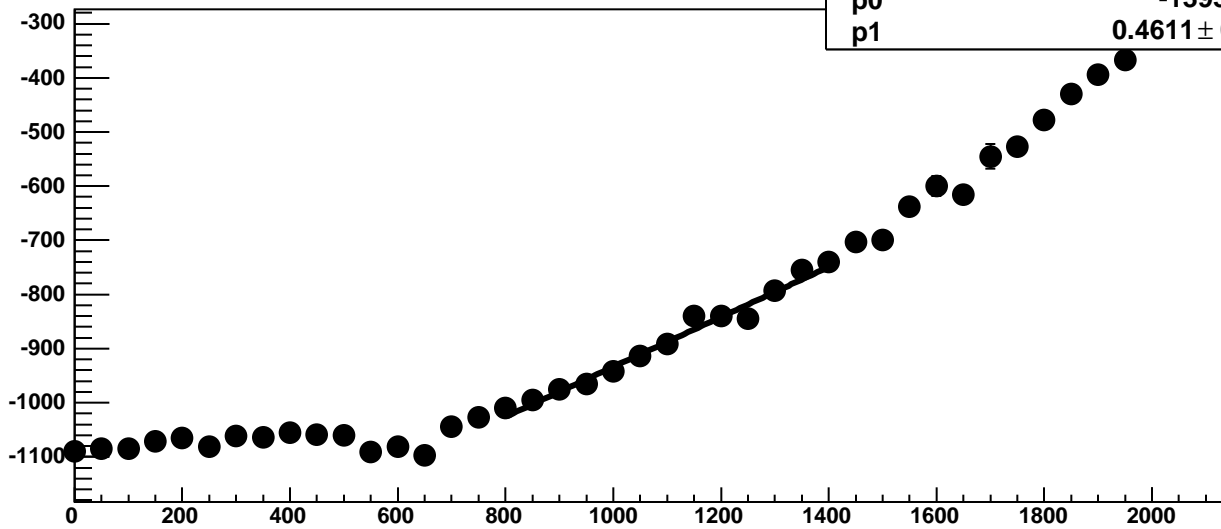
Chip 2, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC



Chip 2, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC

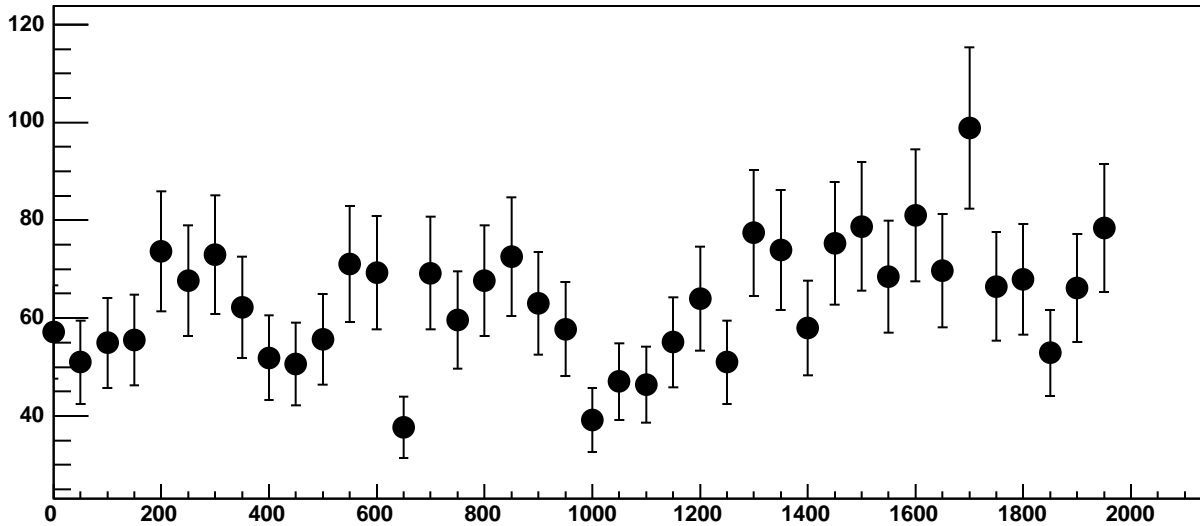


Chip 2, Channel 1, Enable 0, Hold=30, ADC Mean vs DAC

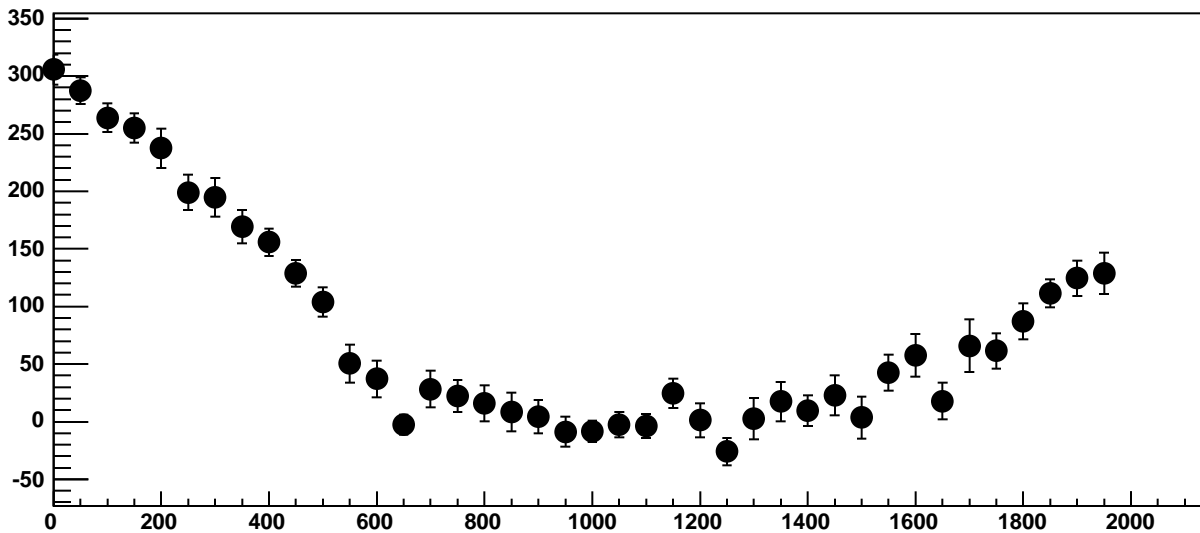


$\chi^2 / \text{ndf}$  13.15 / 11  
p0  $-1395 \pm 23.9$   
p1  $0.4611 \pm 0.02162$

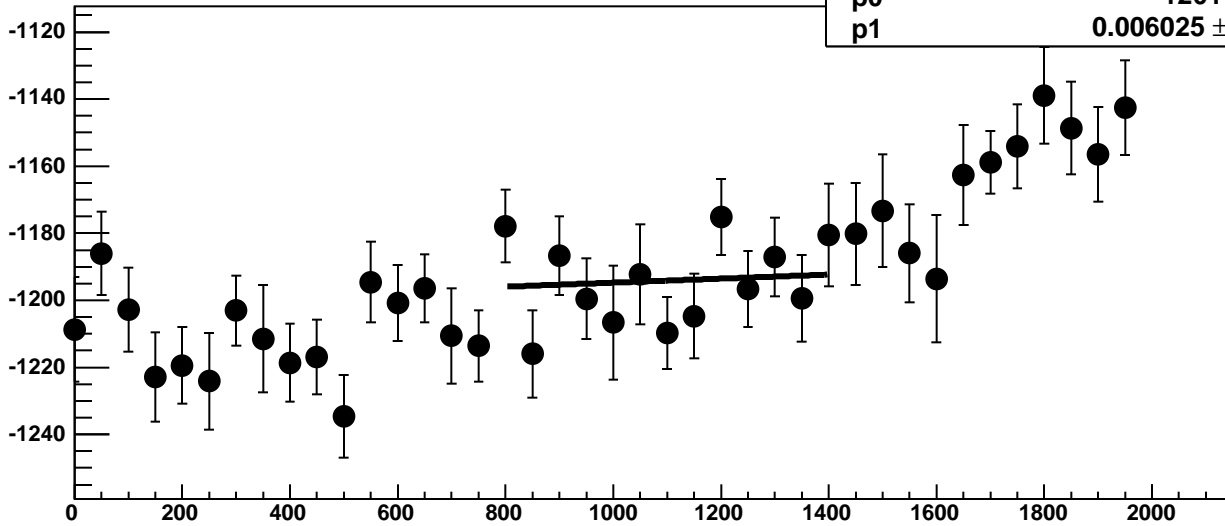
Chip 2, Channel 1, Enable 0, Hold=30, ADC Noise vs DAC



Chip 2, Channel 1, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 2, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

13.19 / 11

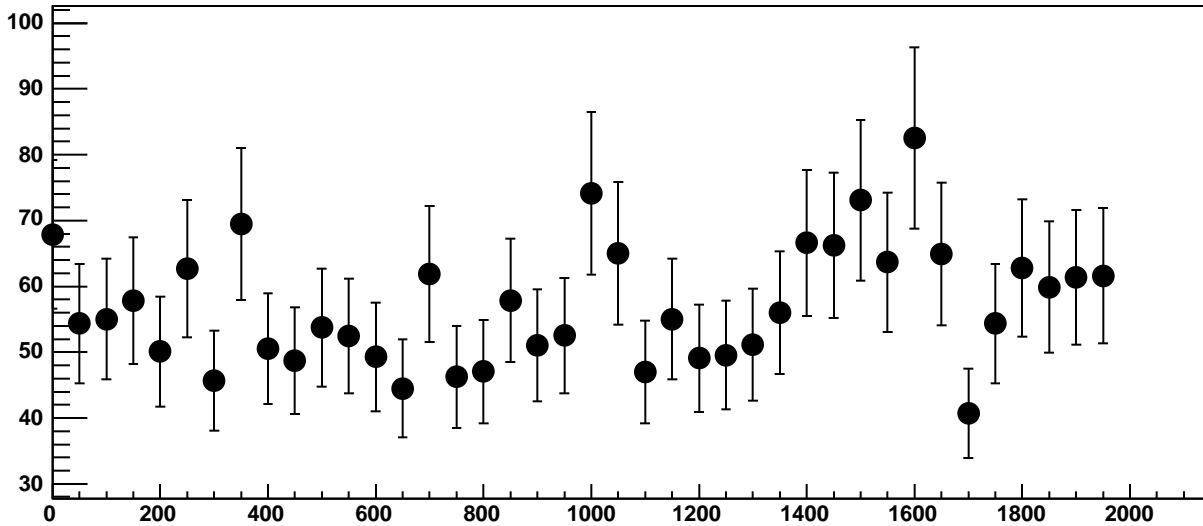
p0

$-1201 \pm 20.39$

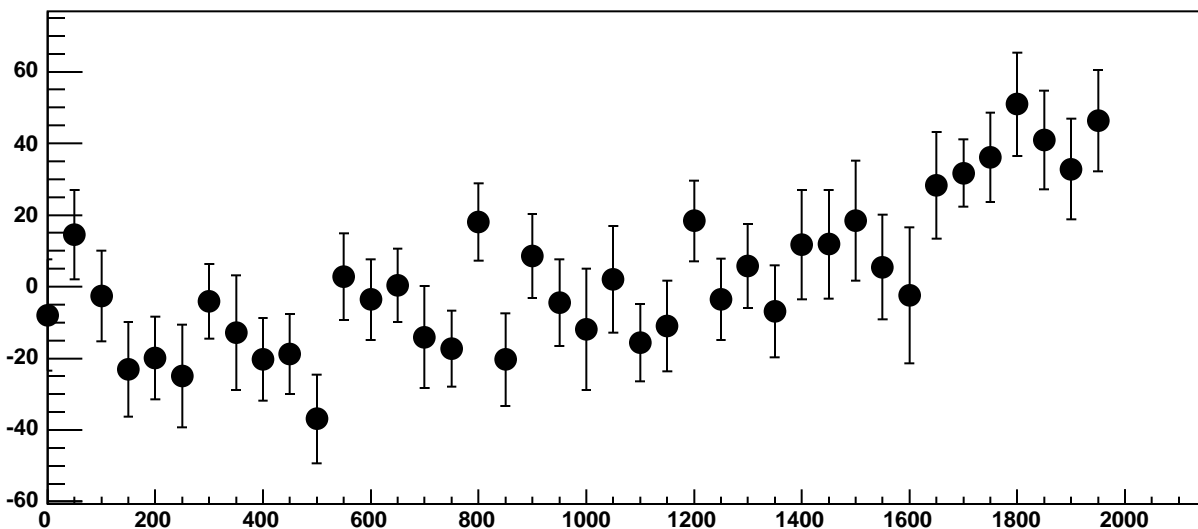
p1

$0.006025 \pm 0.0184$

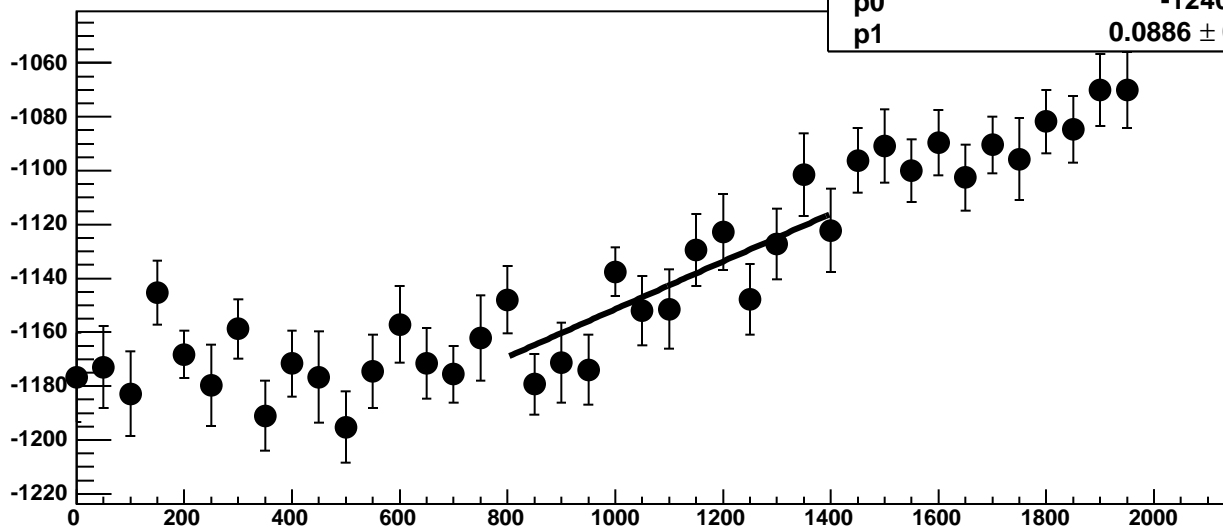
Chip 2, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC



Chip 2, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC

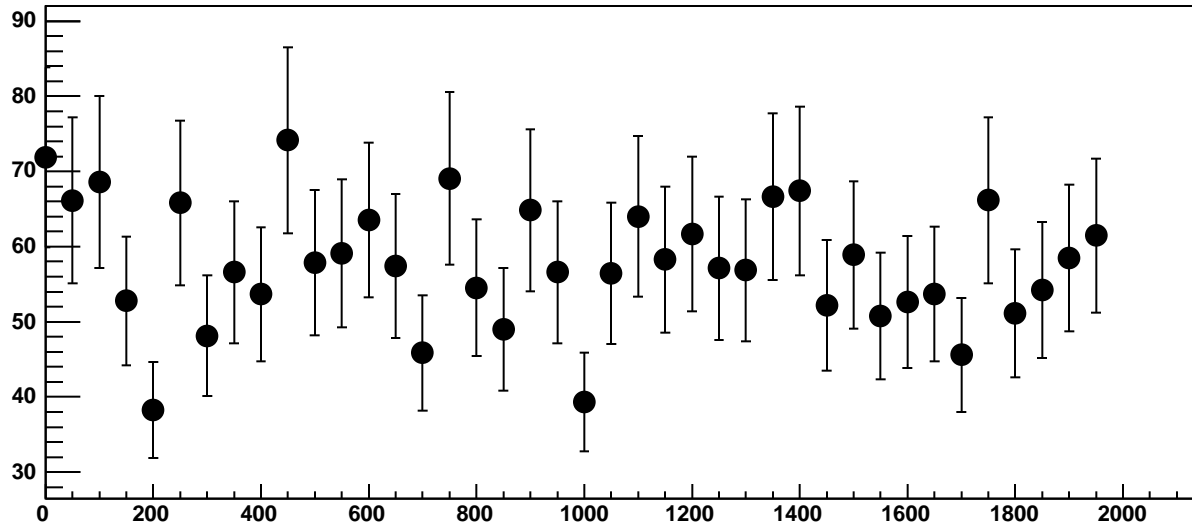


Chip 2, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC

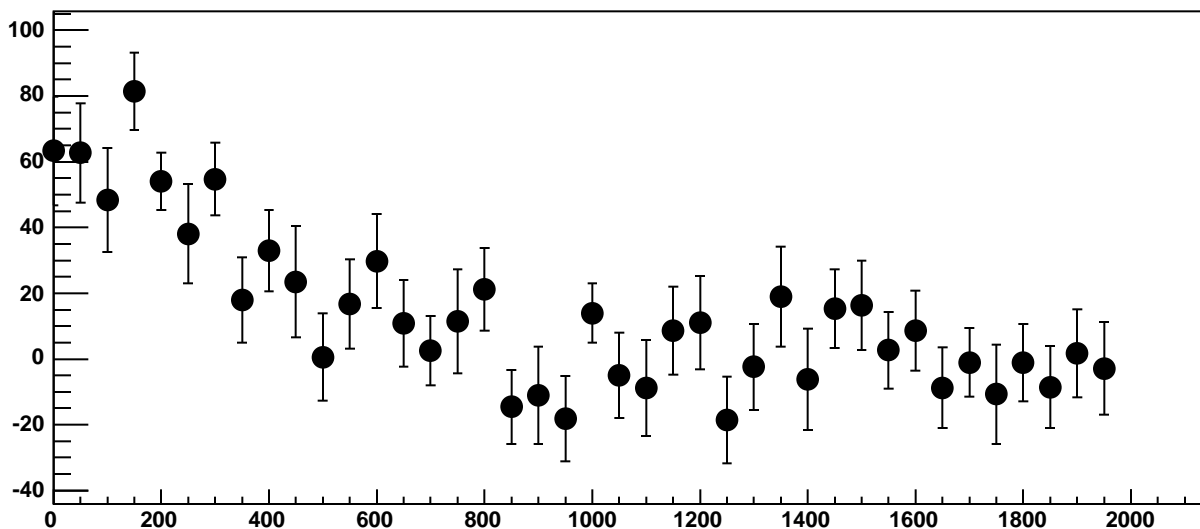


$\chi^2 / \text{ndf}$  14.66 / 11  
p0  $-1240 \pm 21.6$   
p1  $0.0886 \pm 0.01984$

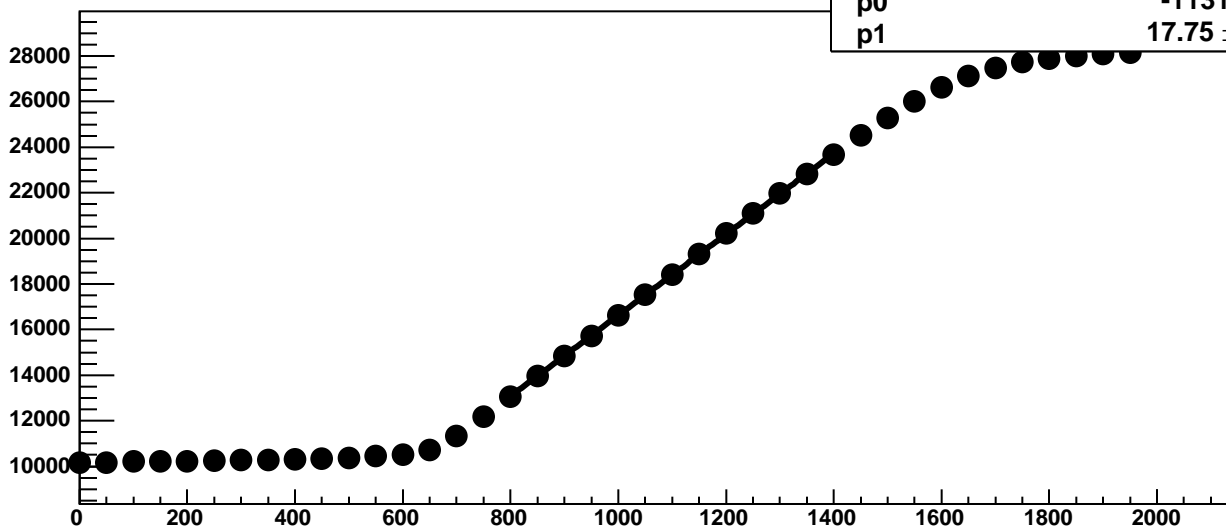
Chip 2, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC



Chip 2, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC

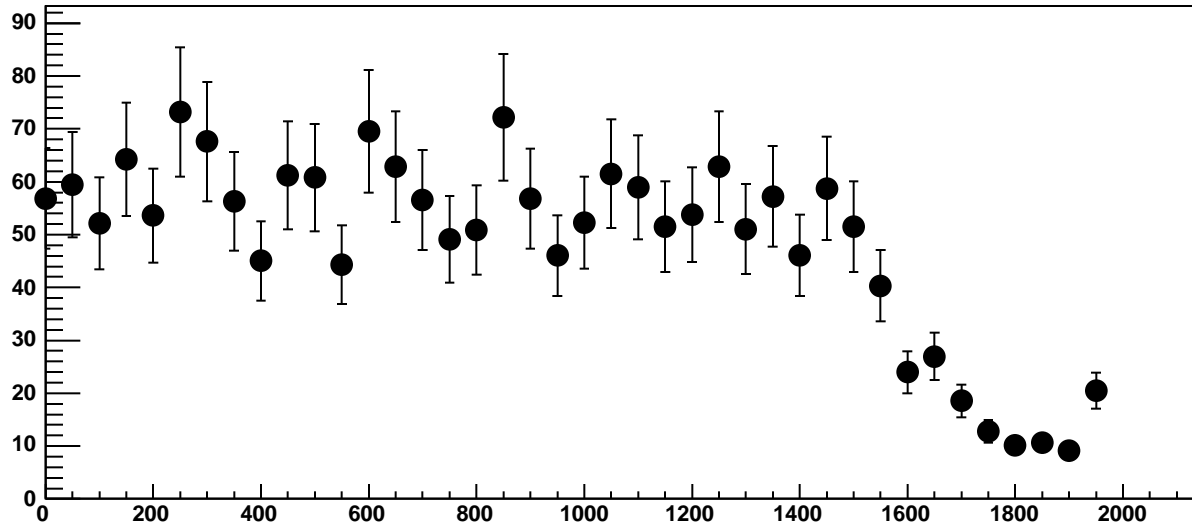


Chip 2, Channel 1, Enable 3!, Hold=30, ADC Mean vs DAC

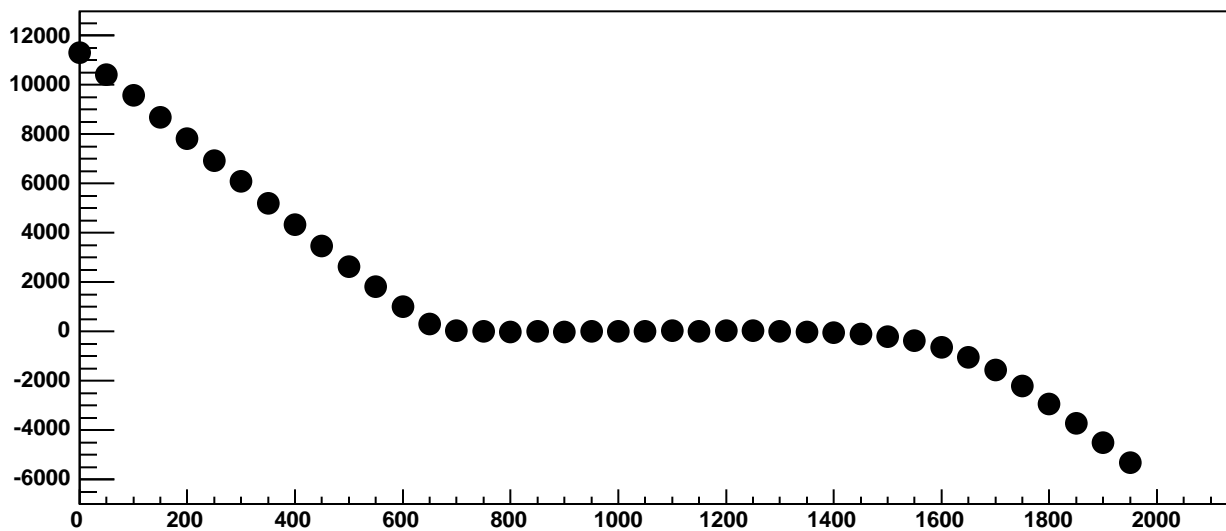


$\chi^2 / \text{ndf}$	52.03 / 11
p0	-1131 ± 20.36
p1	17.75 ± 0.0181

Chip 2, Channel 1, Enable 3!, Hold=30, ADC Noise vs DAC

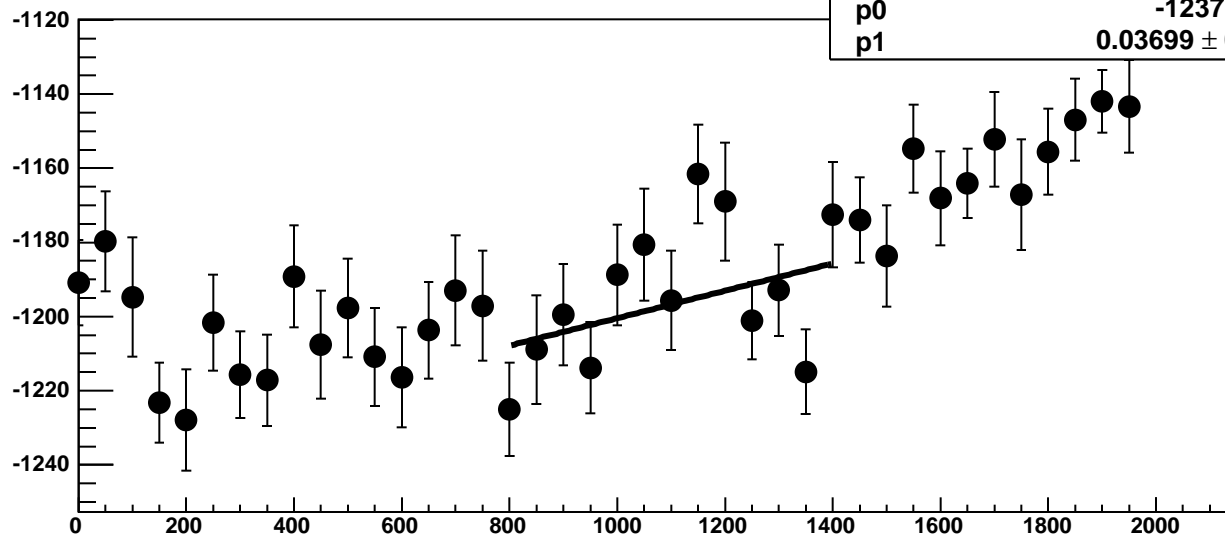


Chip 2, Channel 1, Enable 3!, Hold=30, ADC Residuals vs DAC

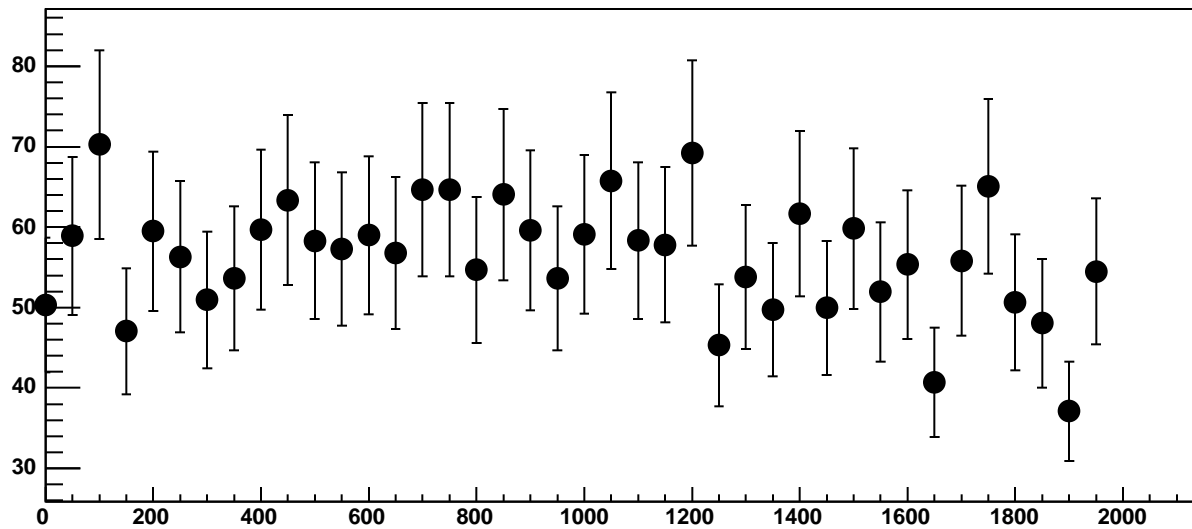




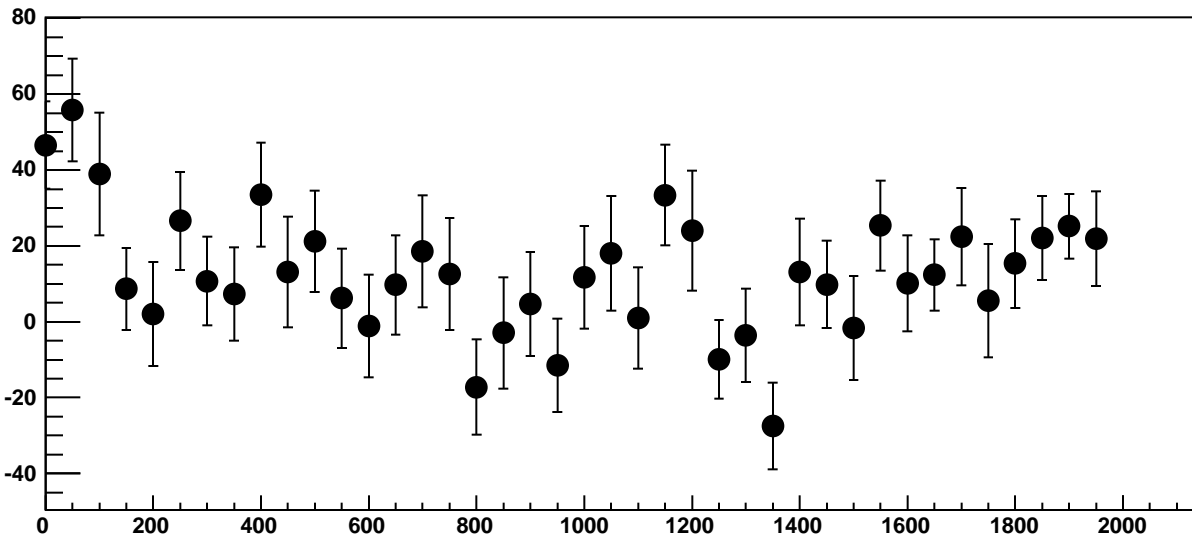
Chip 2, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC



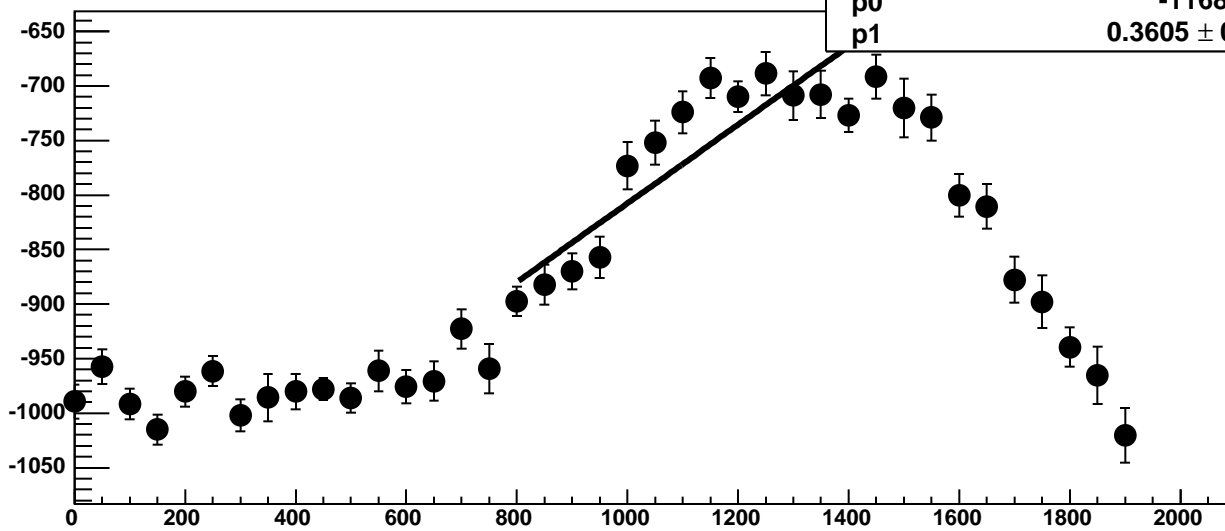
Chip 2, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC



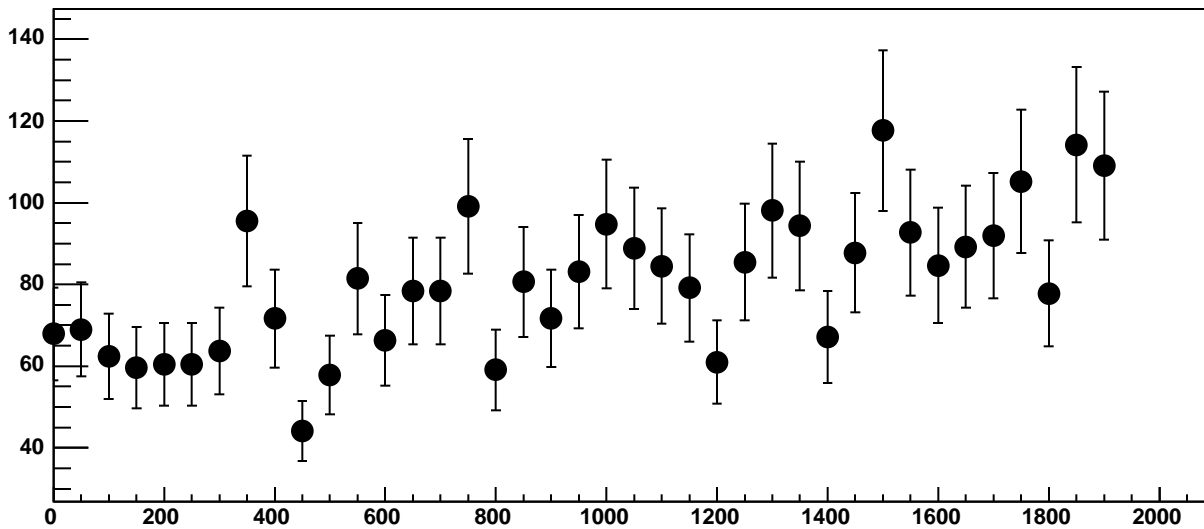
Chip 2, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC



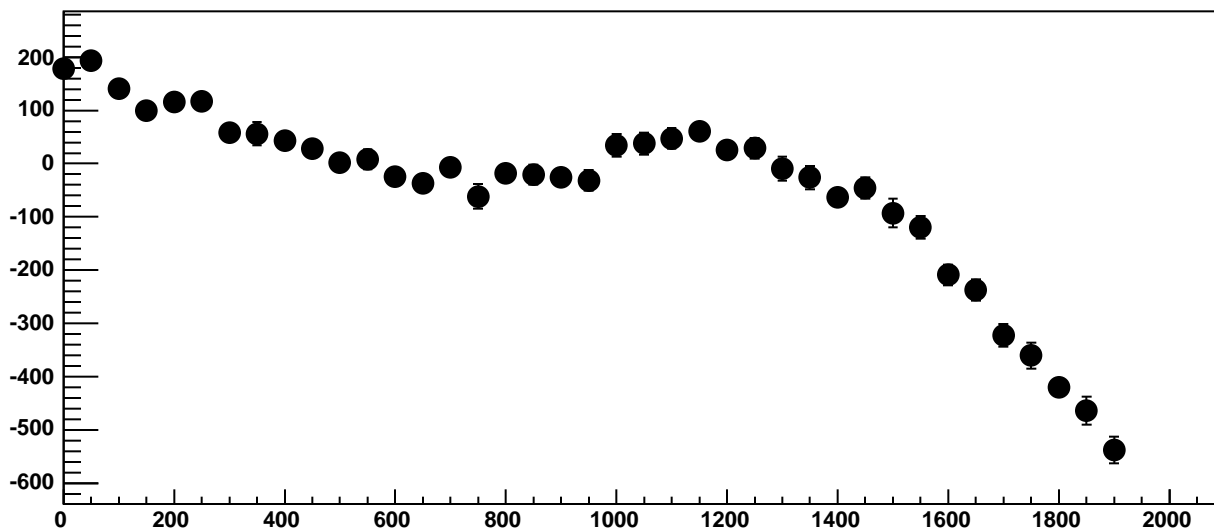
Chip 2, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC



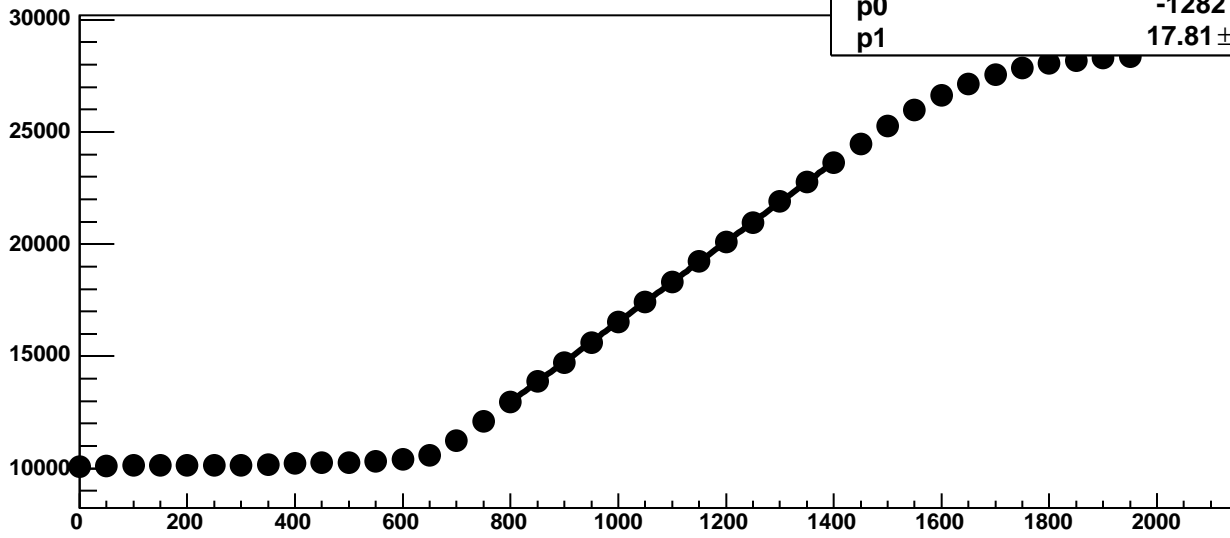
Chip 2, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC



Chip 2, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC

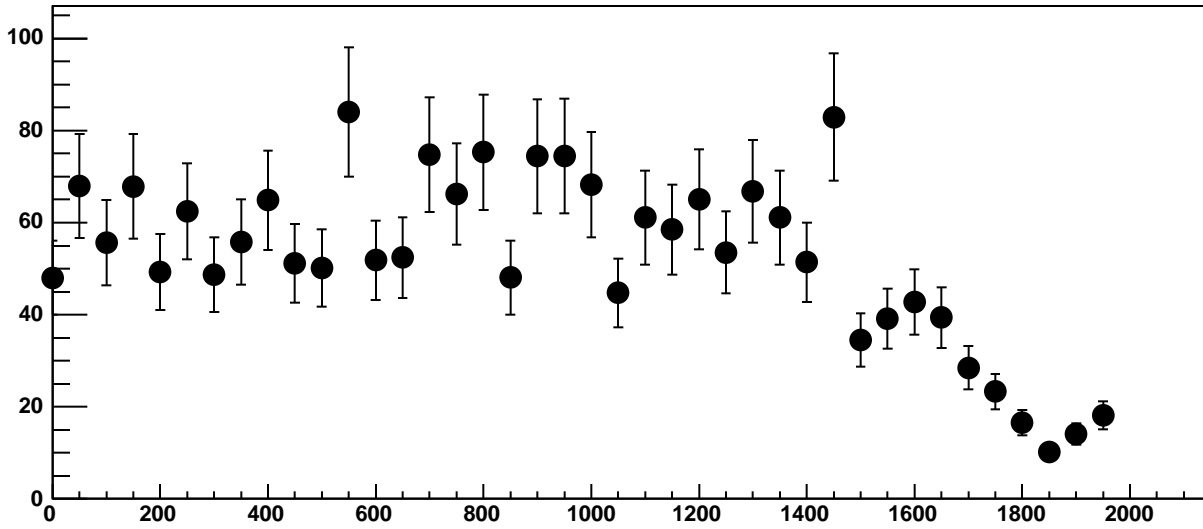


Chip 2, Channel 2, Enable 0!, Hold=30, ADC Mean vs DAC

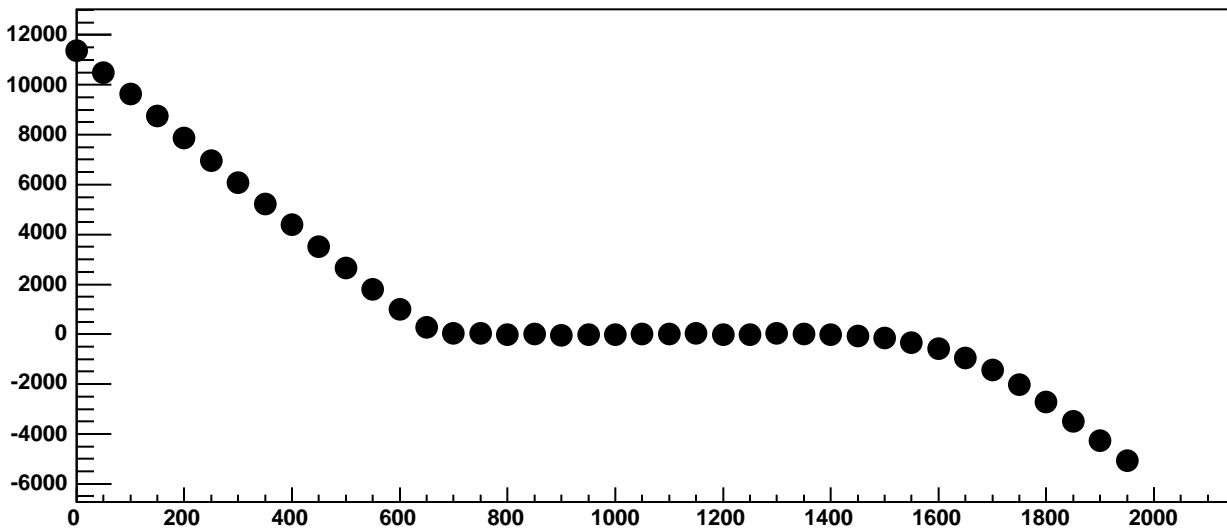


$\chi^2 / \text{ndf}$  32.5 / 11  
p0  $-1282 \pm 22.89$   
p1  $17.81 \pm 0.0203$

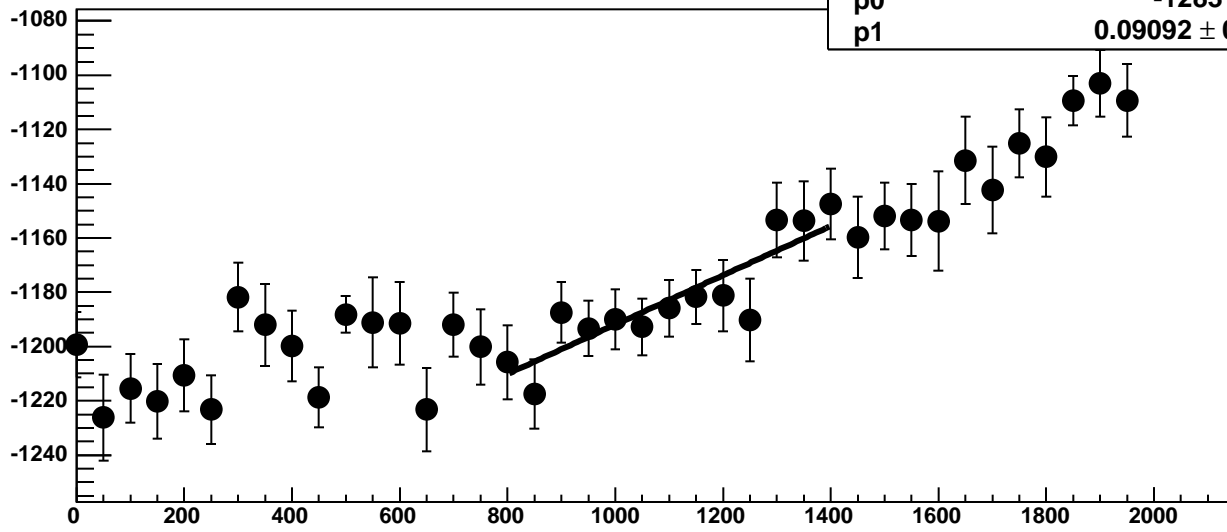
Chip 2, Channel 2, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 2, Channel 2, Enable 0!, Hold=30, ADC Residuals vs DAC

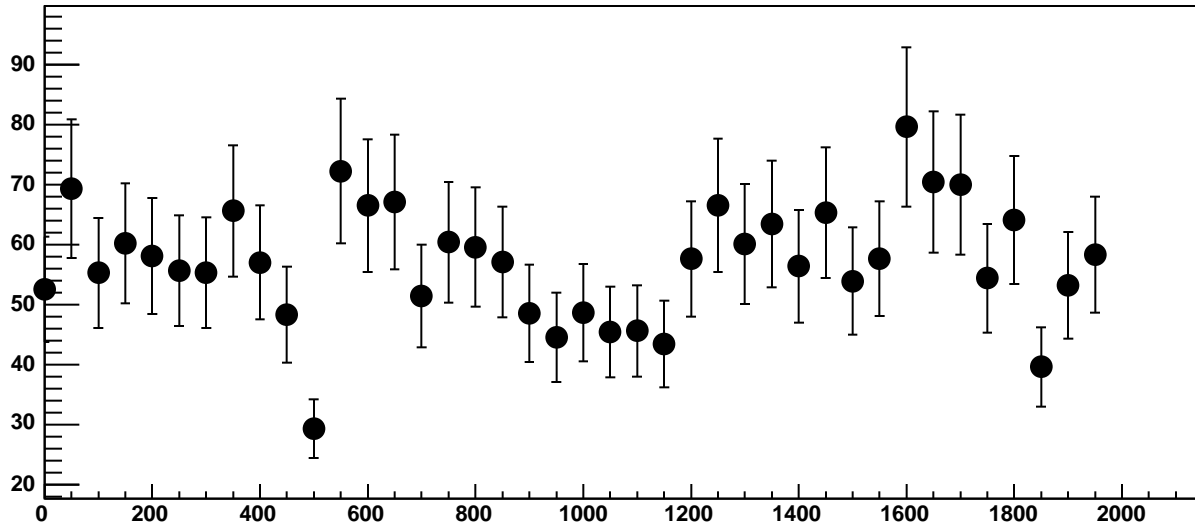


Chip 2, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC

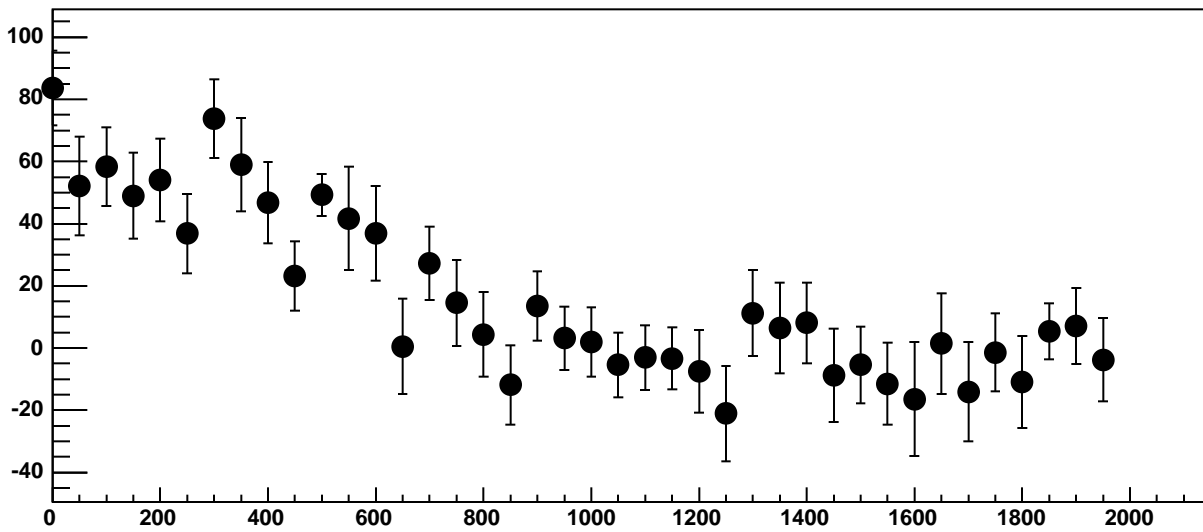


$\chi^2 / \text{ndf}$  6.525 / 11  
p0  $-1283 \pm 21.09$   
p1  $0.09092 \pm 0.01926$

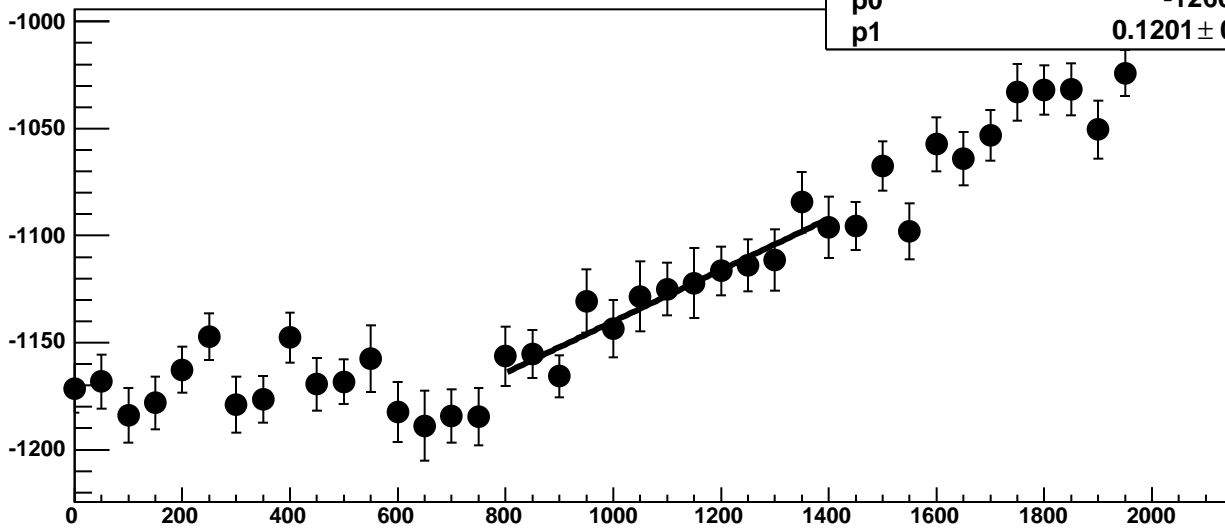
Chip 2, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC



Chip 2, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC

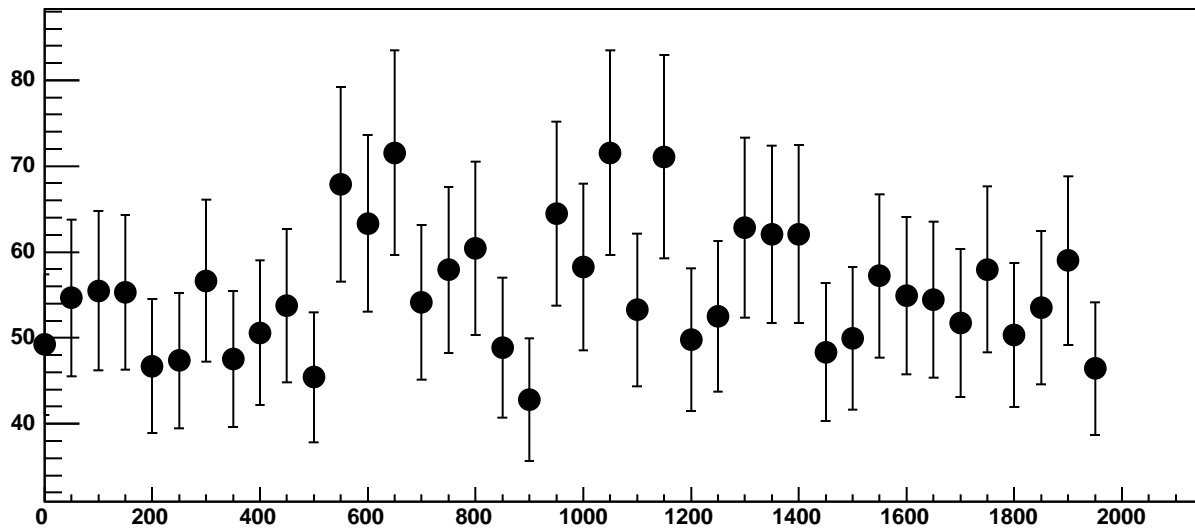


Chip 2, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC

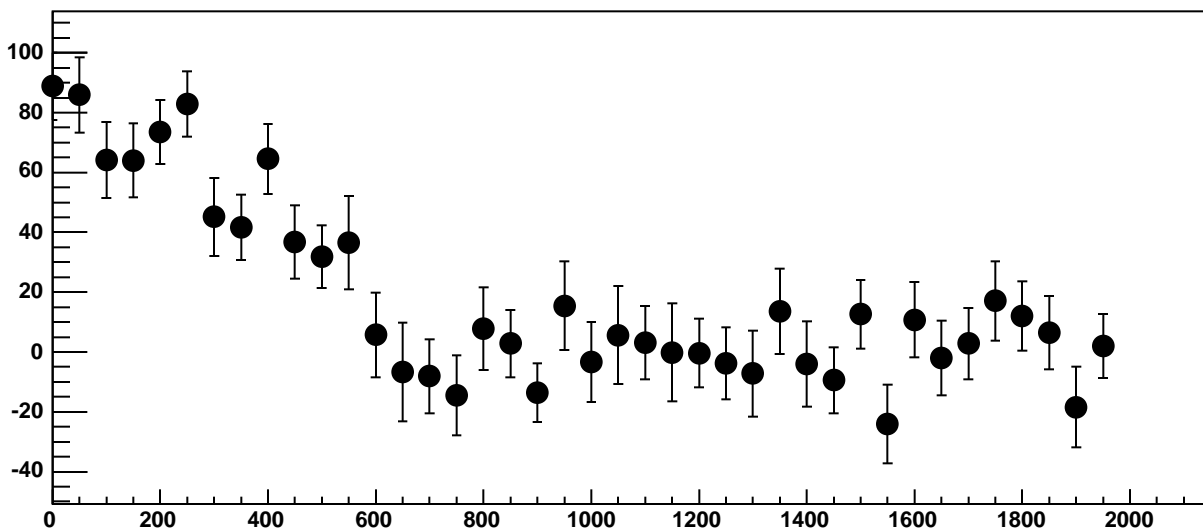


$\chi^2 / \text{ndf}$  4.984 / 11  
p0  $-1260 \pm 21.1$   
p1  $0.1201 \pm 0.01922$

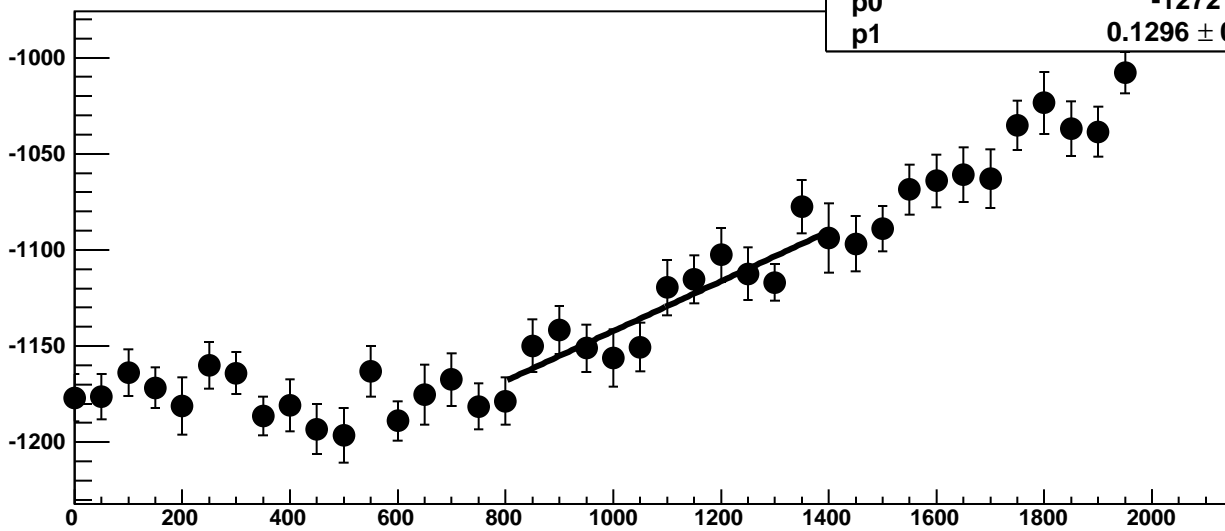
Chip 2, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC



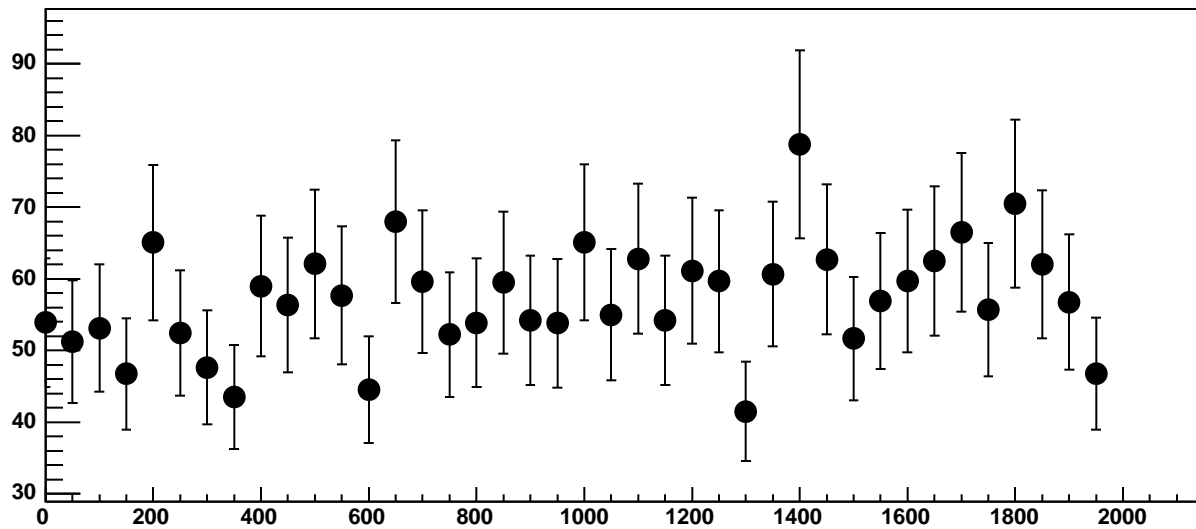
Chip 2, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC



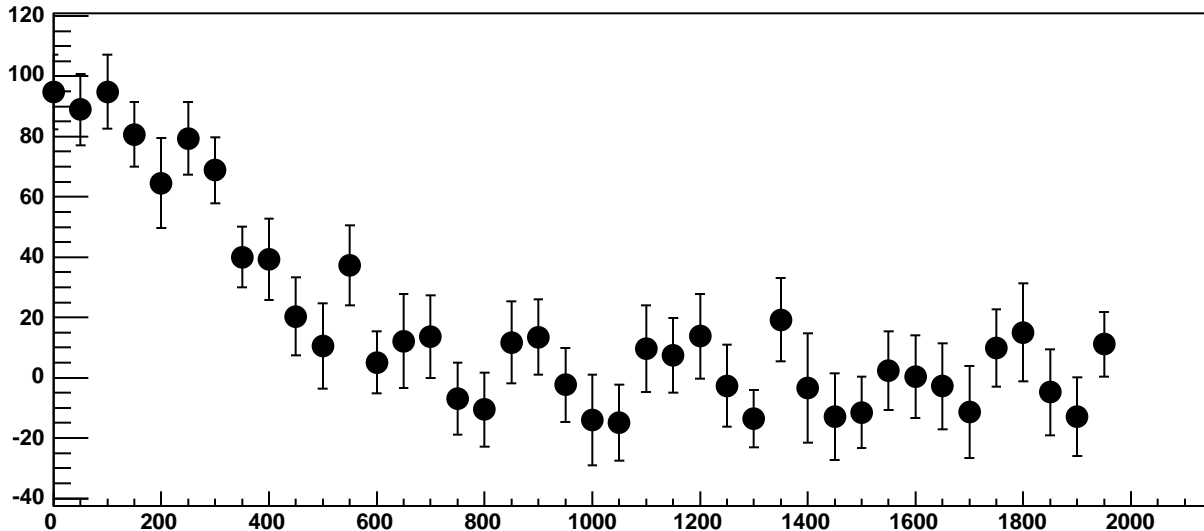
Chip 2, Channel 2, Enable 3, Hold=30, ADC Mean vs DAC



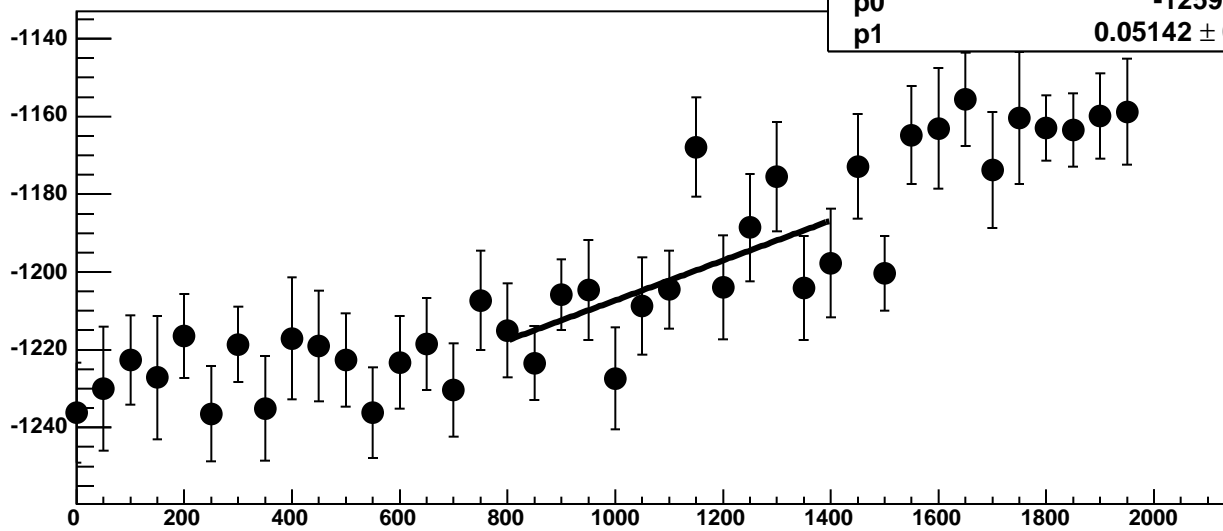
Chip 2, Channel 2, Enable 3, Hold=30, ADC Noise vs DAC



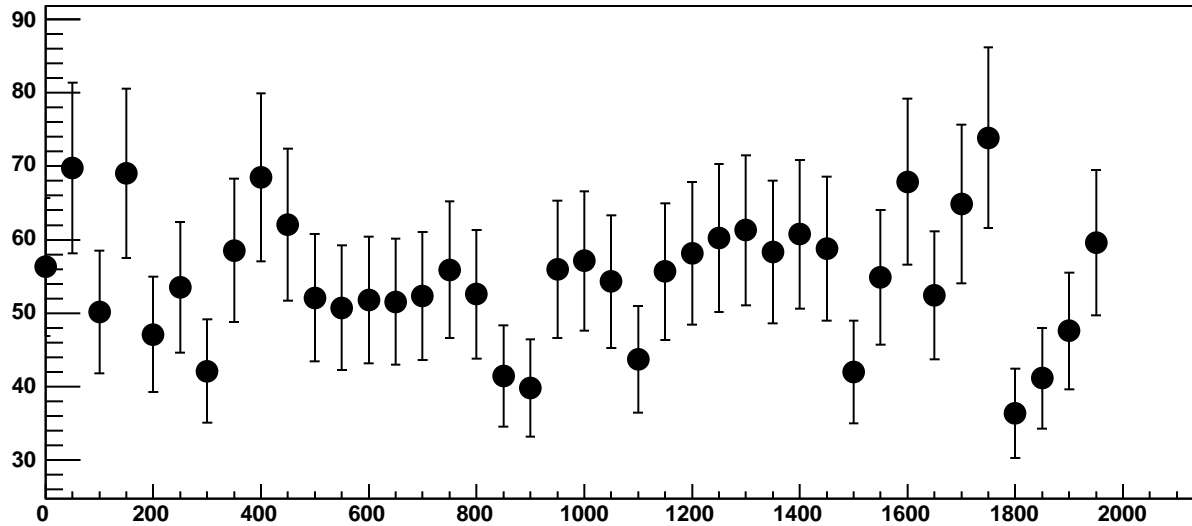
Chip 2, Channel 2, Enable 3, Hold=30, ADC Residuals vs DAC



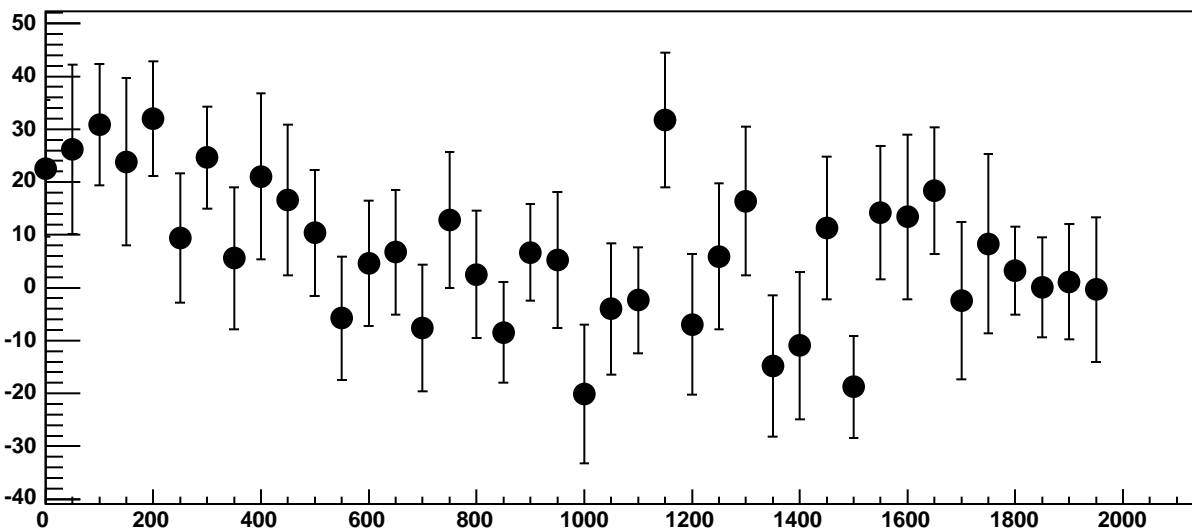
Chip 2, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC



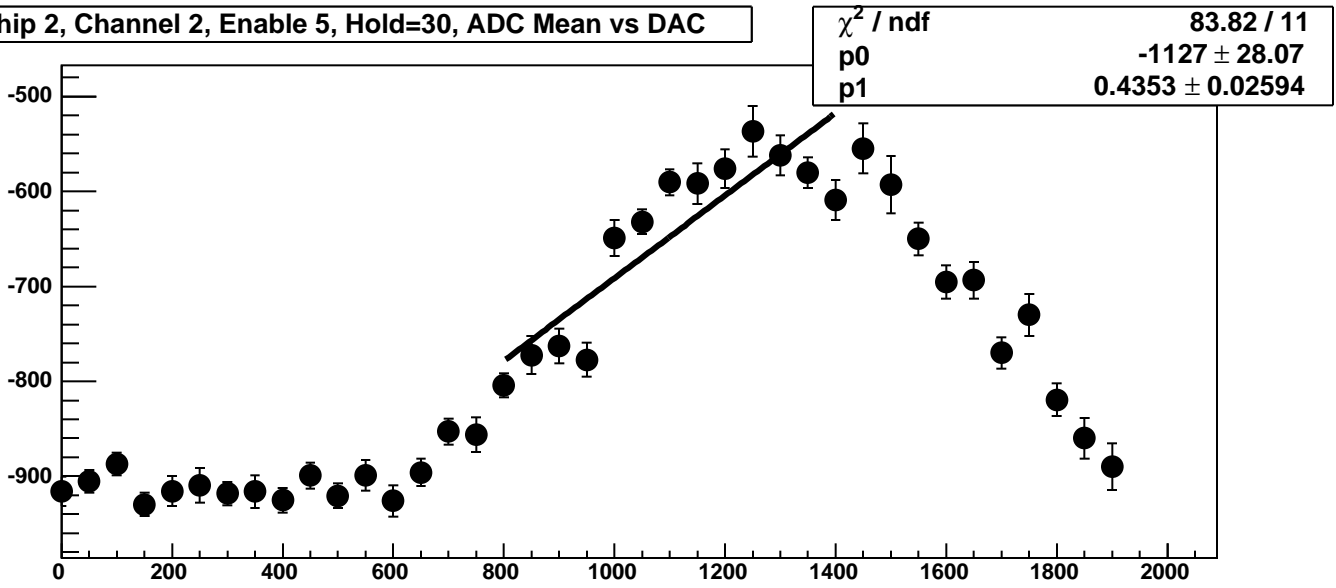
Chip 2, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC



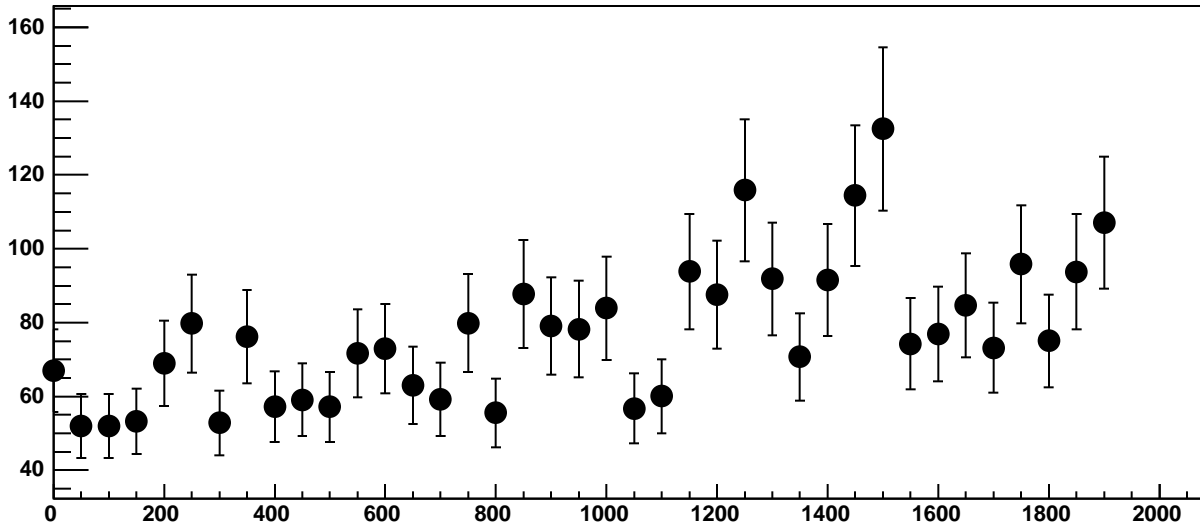
Chip 2, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC



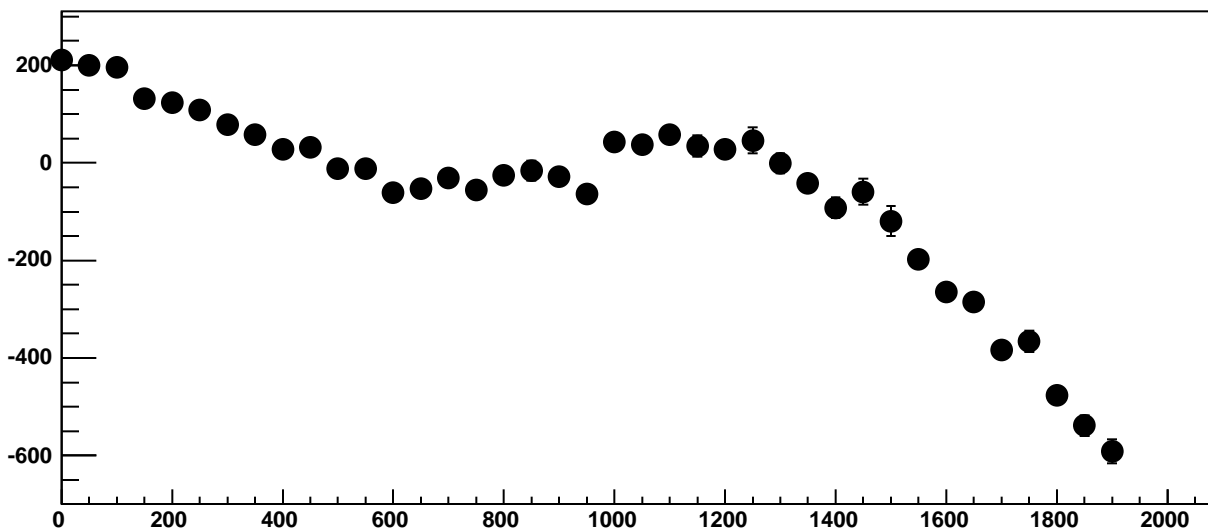
Chip 2, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC



Chip 2, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC

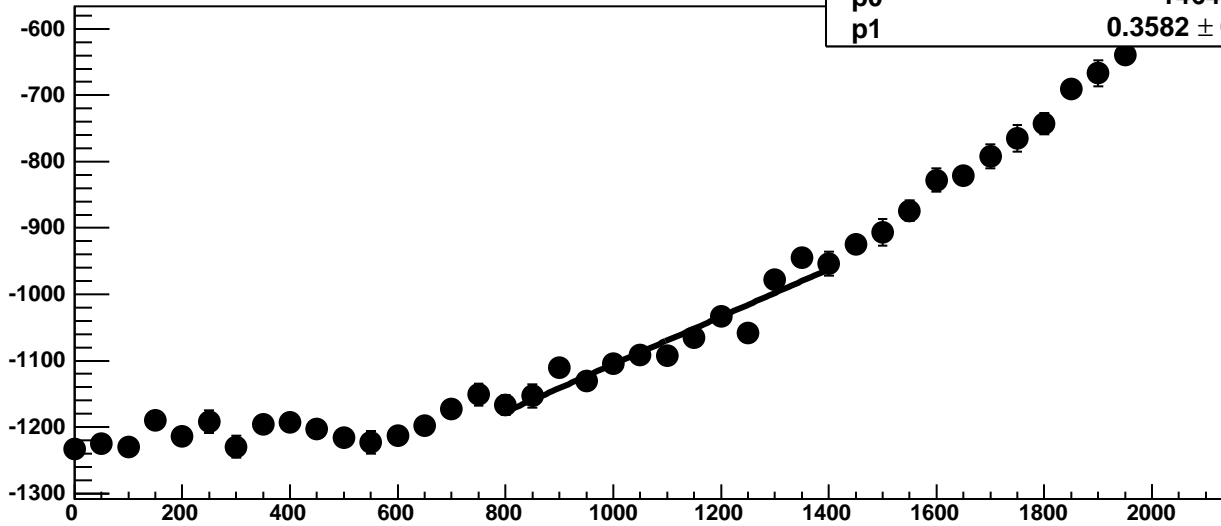


Chip 2, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC



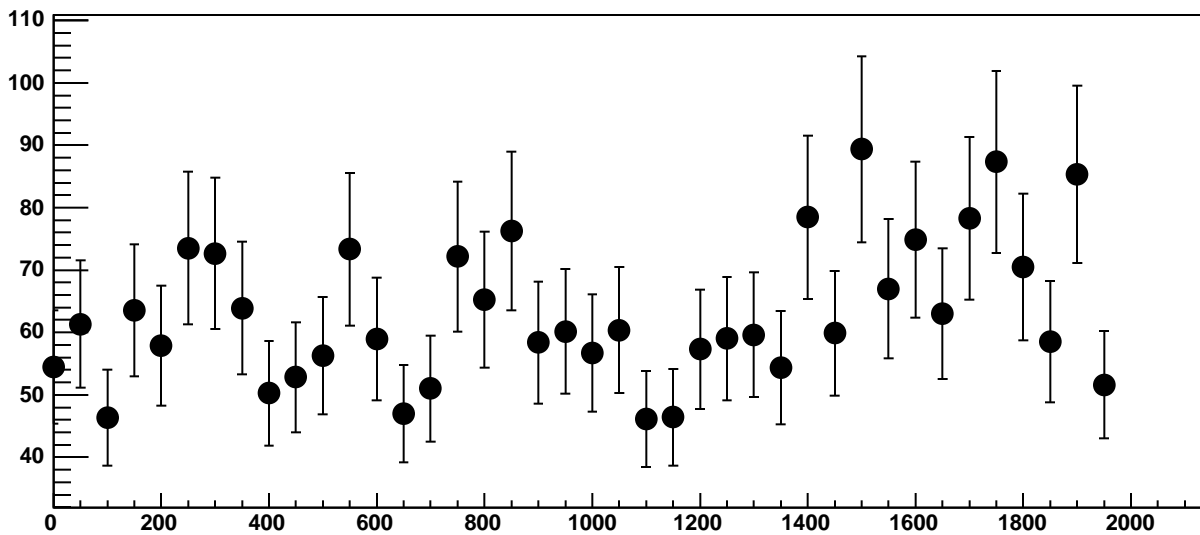


Chip 2, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC

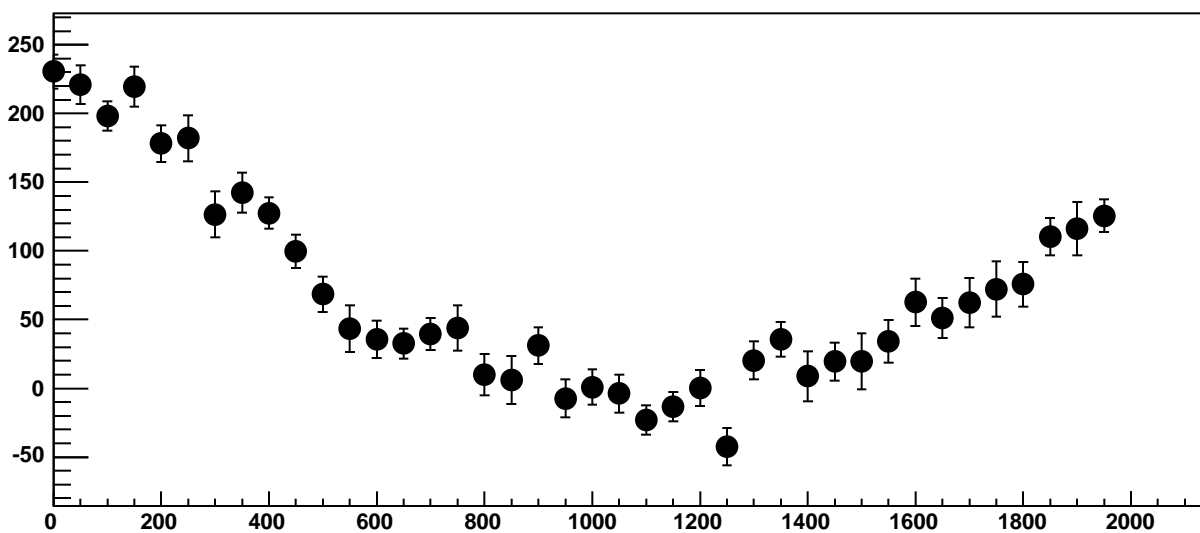


$\chi^2 / \text{ndf}$  32.99 / 11  
p0  $-1464 \pm 24.38$   
p1  $0.3582 \pm 0.02177$

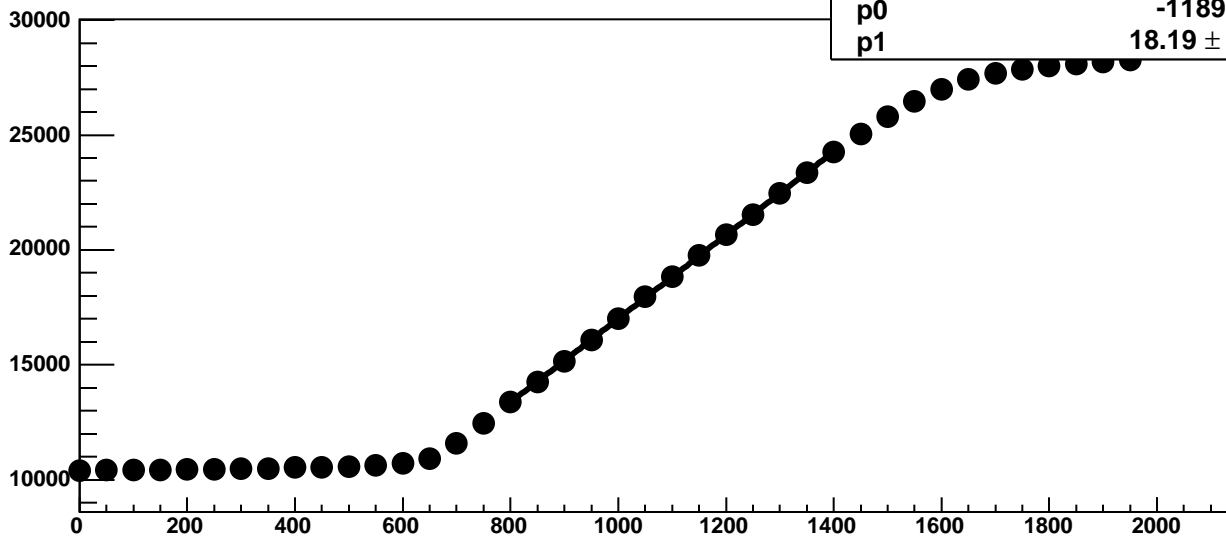
Chip 2, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC



Chip 2, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC

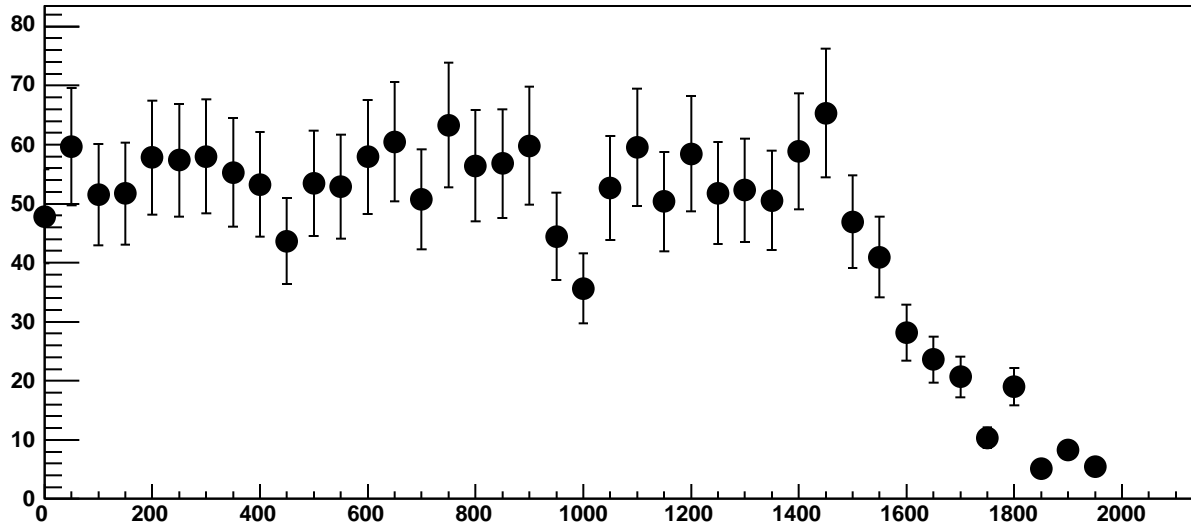


Chip 2, Channel 3, Enable 1!, Hold=30, ADC Mean vs DAC

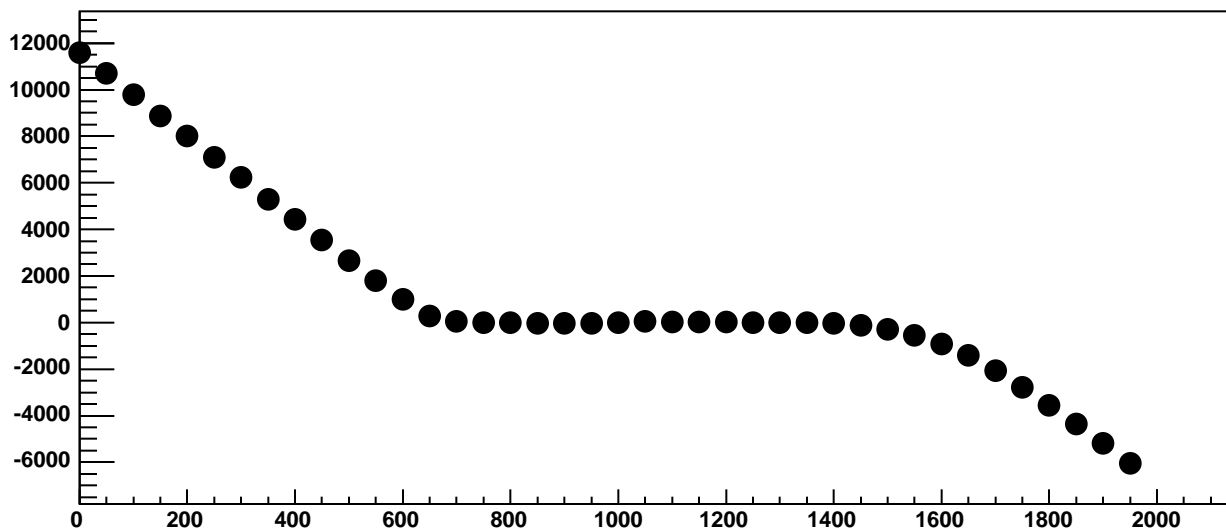


$\chi^2 / \text{ndf}$  39.14 / 11  
p0  $-1189 \pm 20.25$   
p1  $18.19 \pm 0.01831$

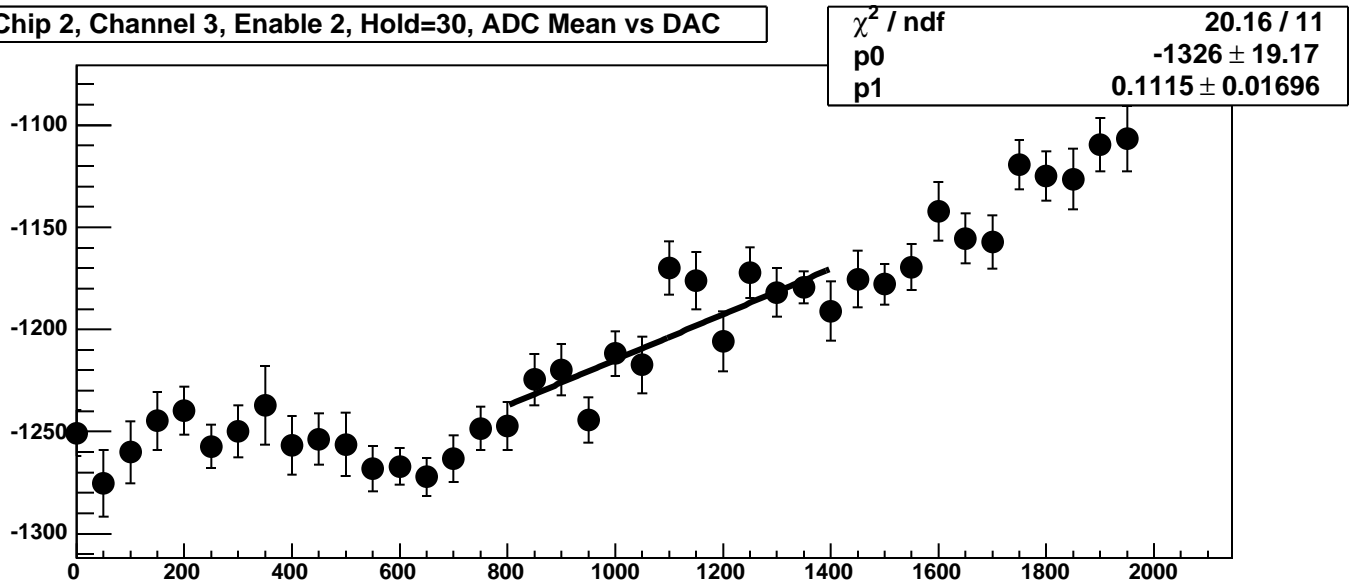
Chip 2, Channel 3, Enable 1!, Hold=30, ADC Noise vs DAC



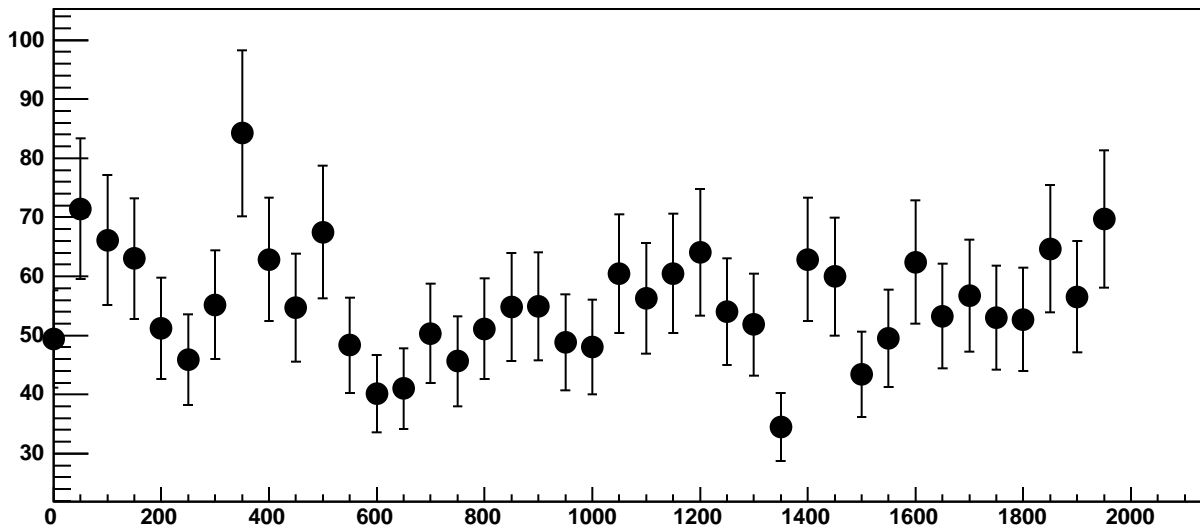
Chip 2, Channel 3, Enable 1!, Hold=30, ADC Residuals vs DAC



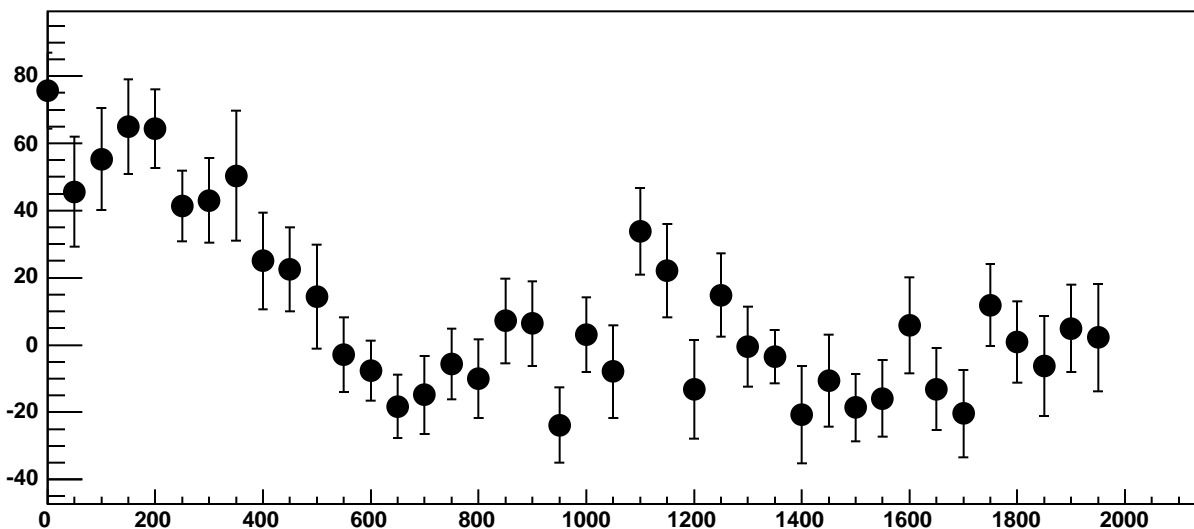
Chip 2, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC



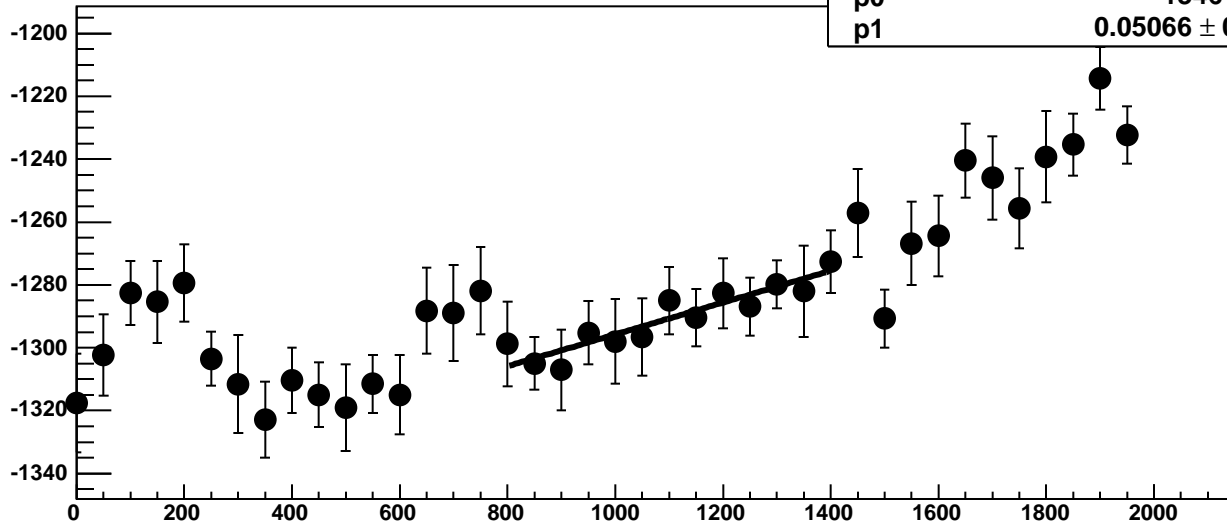
Chip 2, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC



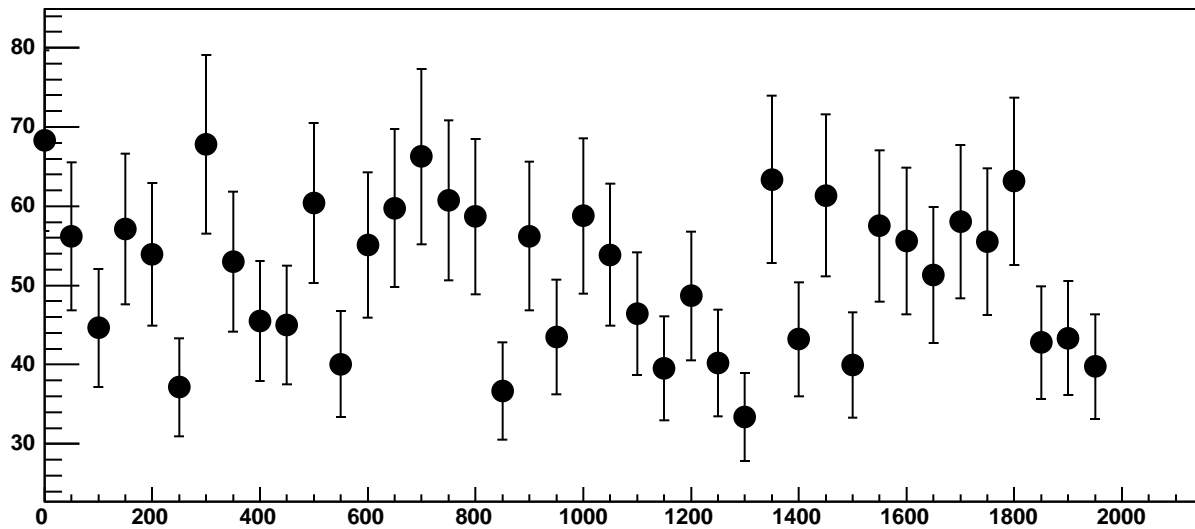
Chip 2, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC



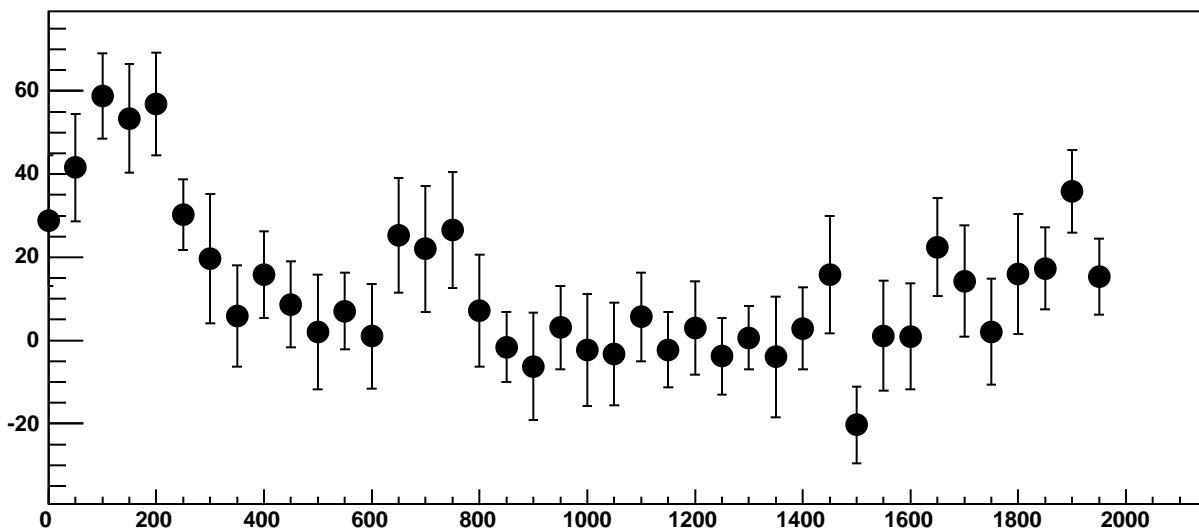
Chip 2, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC



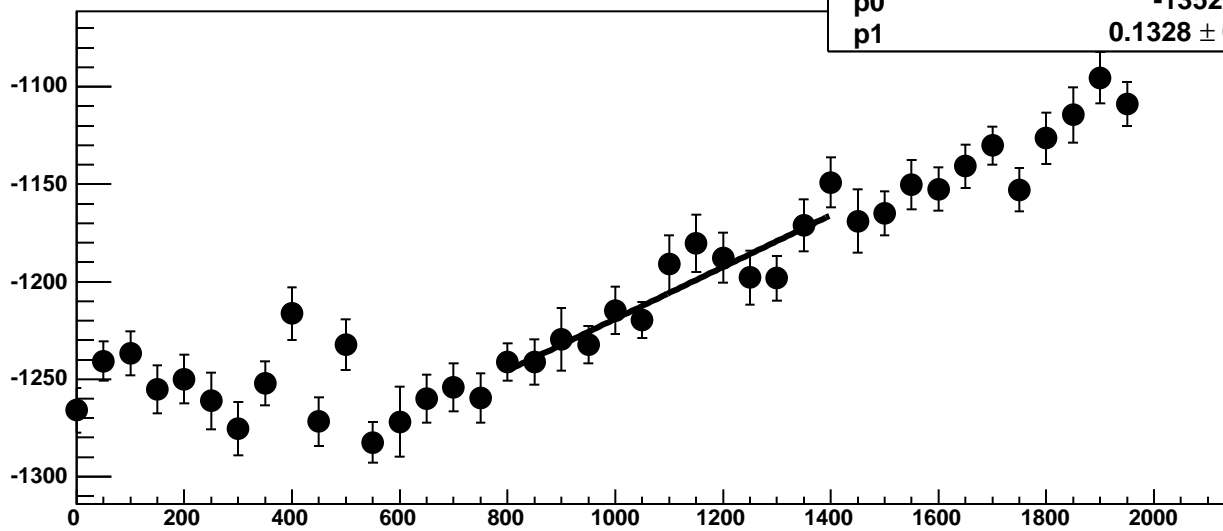
Chip 2, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC



Chip 2, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC

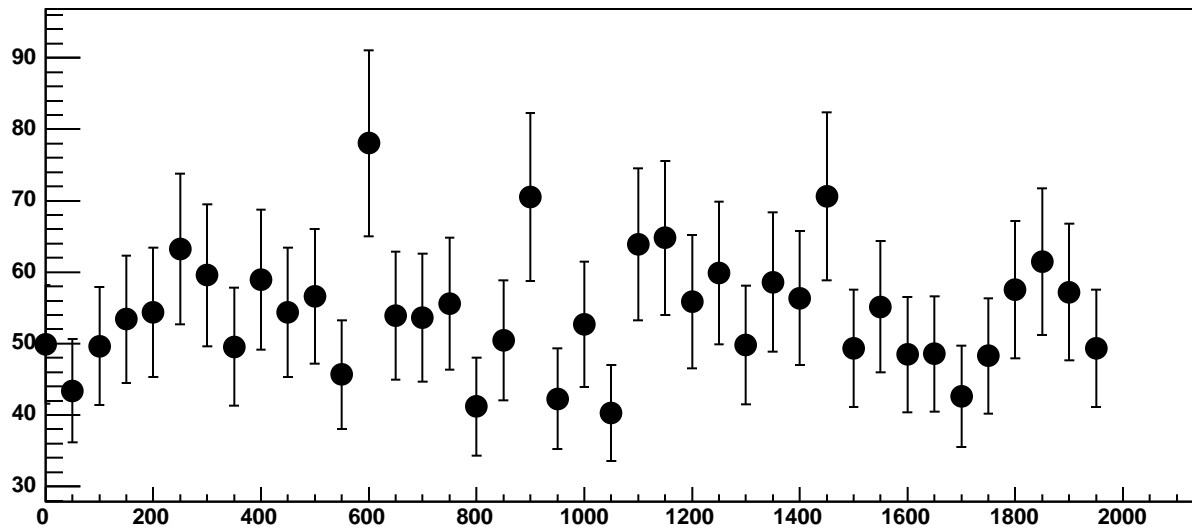


Chip 2, Channel 3, Enable 4, Hold=30, ADC Mean vs DAC

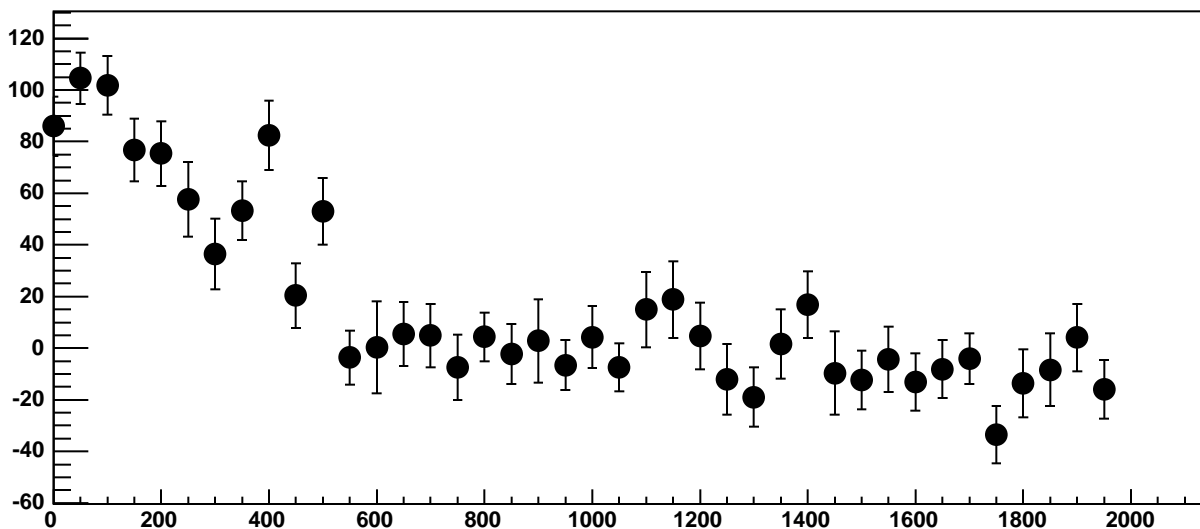


$\chi^2 / \text{ndf}$  9.529 / 11  
p0  $-1352 \pm 19.03$   
p1  $0.1328 \pm 0.01747$

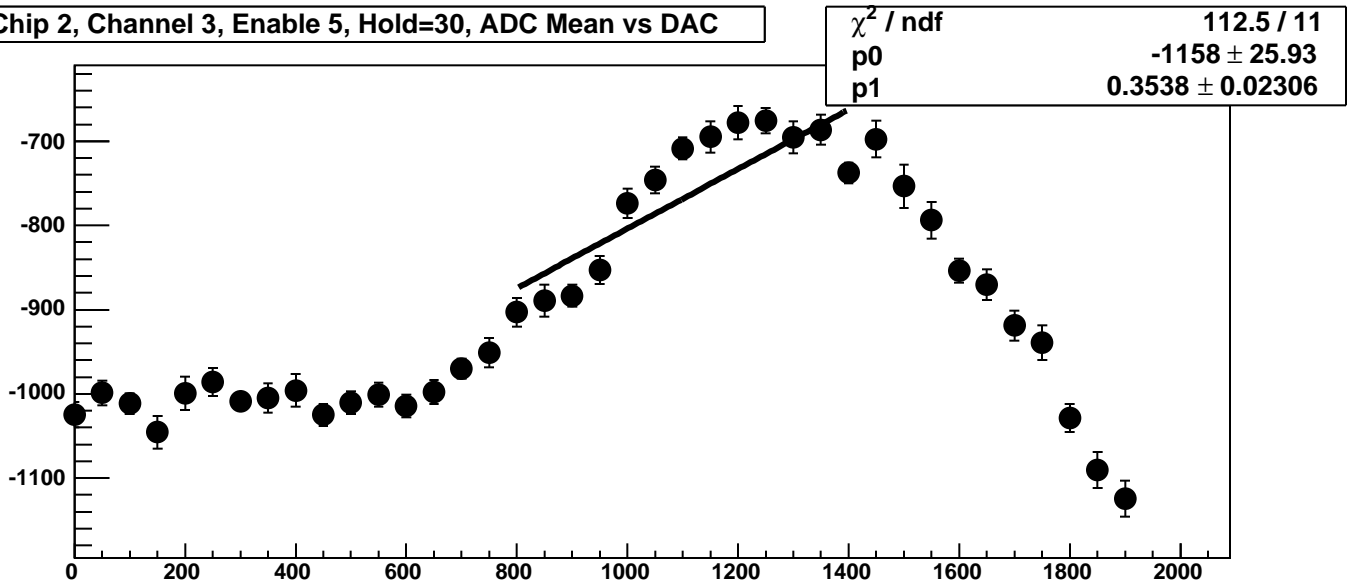
Chip 2, Channel 3, Enable 4, Hold=30, ADC Noise vs DAC



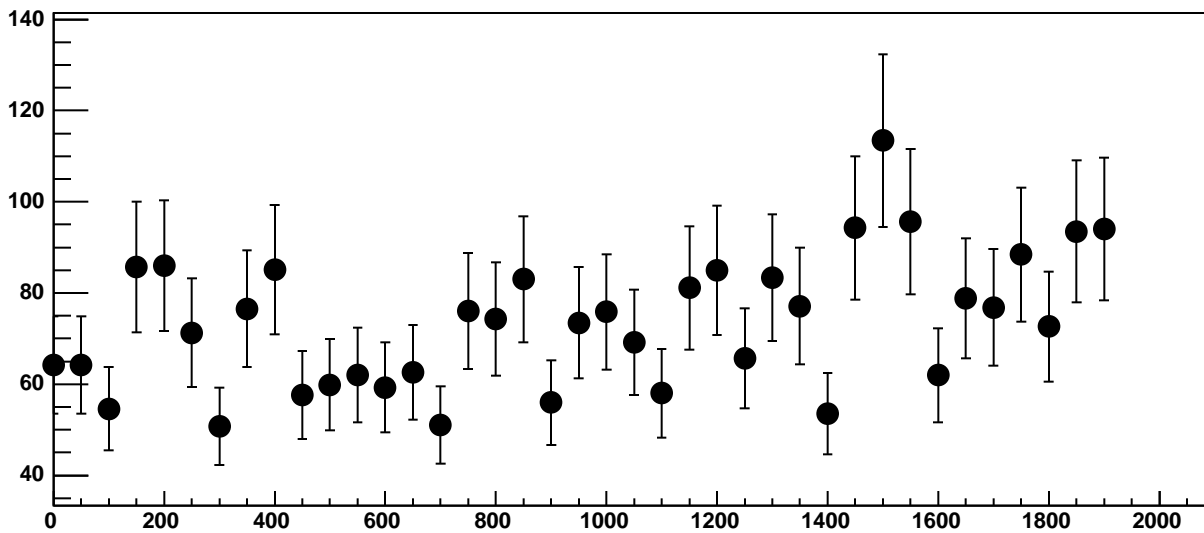
Chip 2, Channel 3, Enable 4, Hold=30, ADC Residuals vs DAC



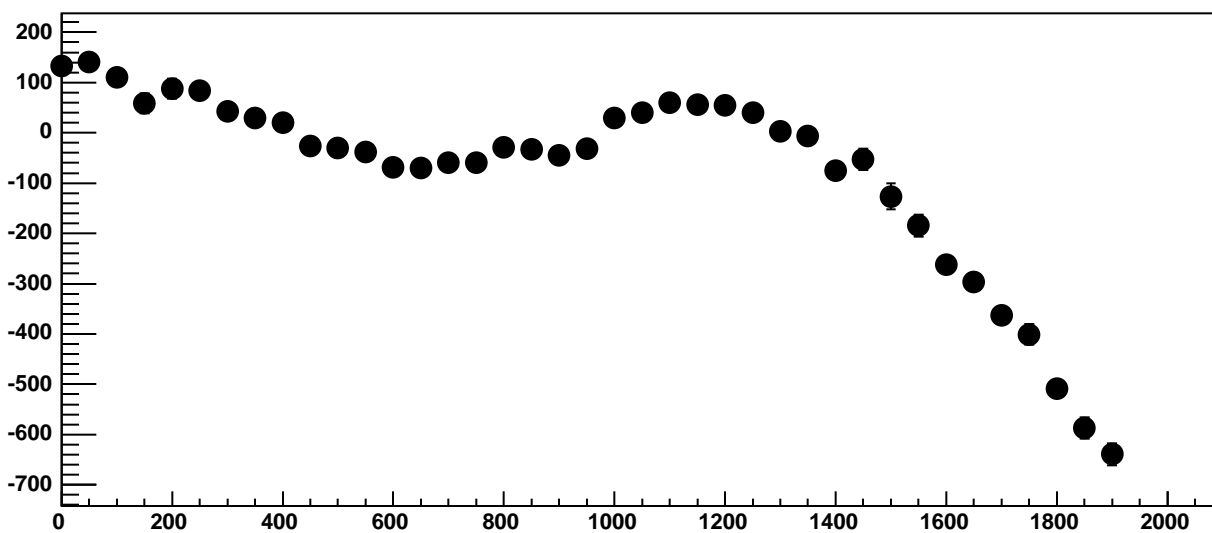
Chip 2, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC



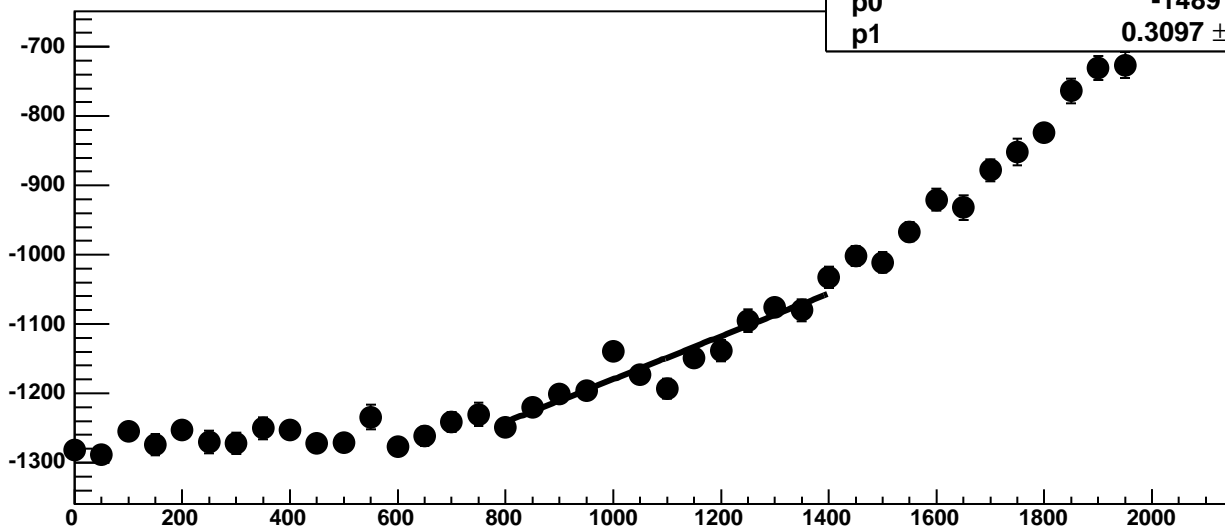
Chip 2, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC



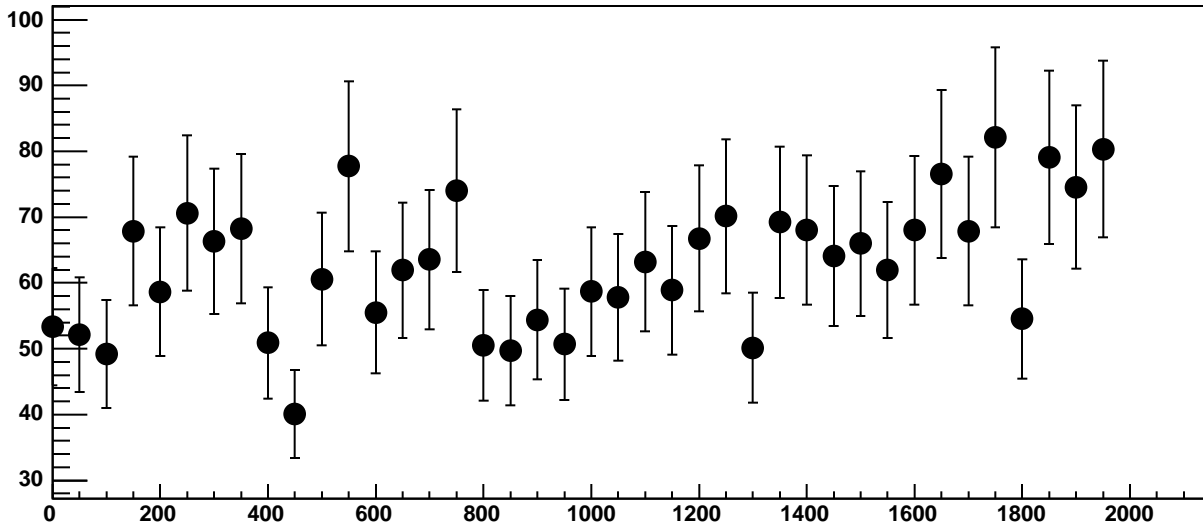
Chip 2, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC



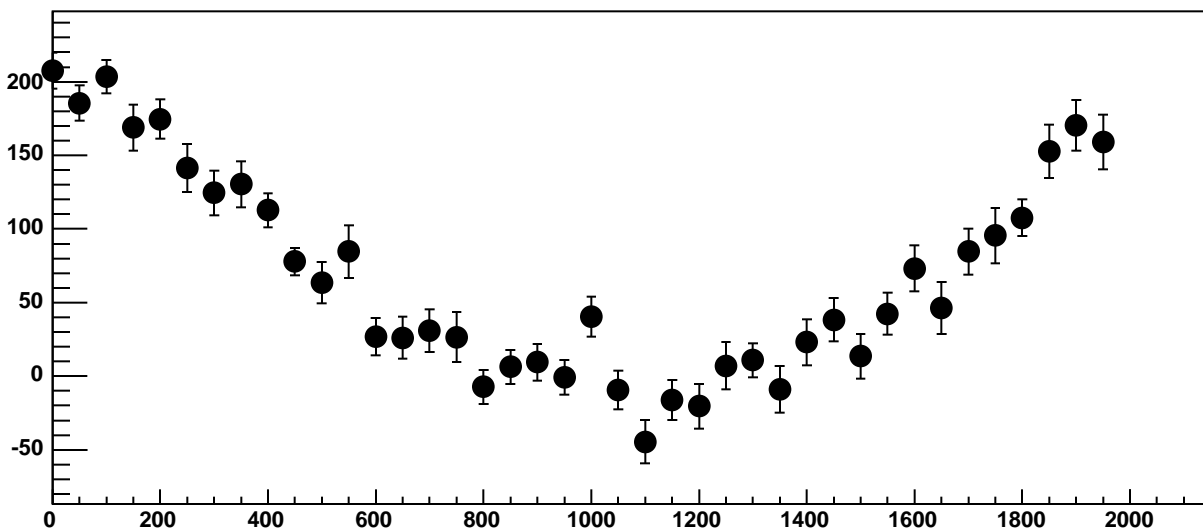
Chip 2, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC



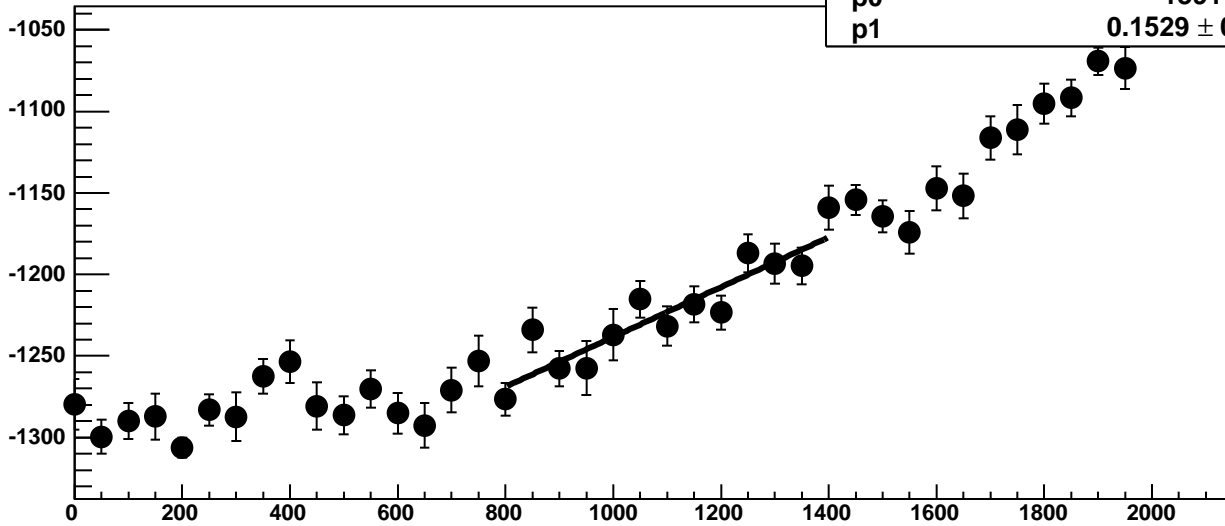
Chip 2, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC



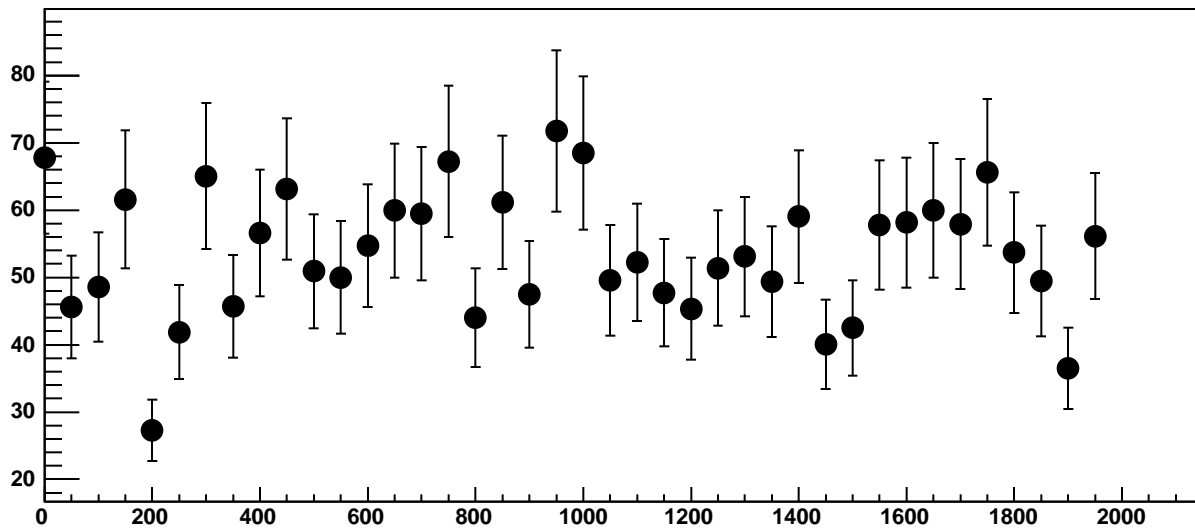
Chip 2, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC



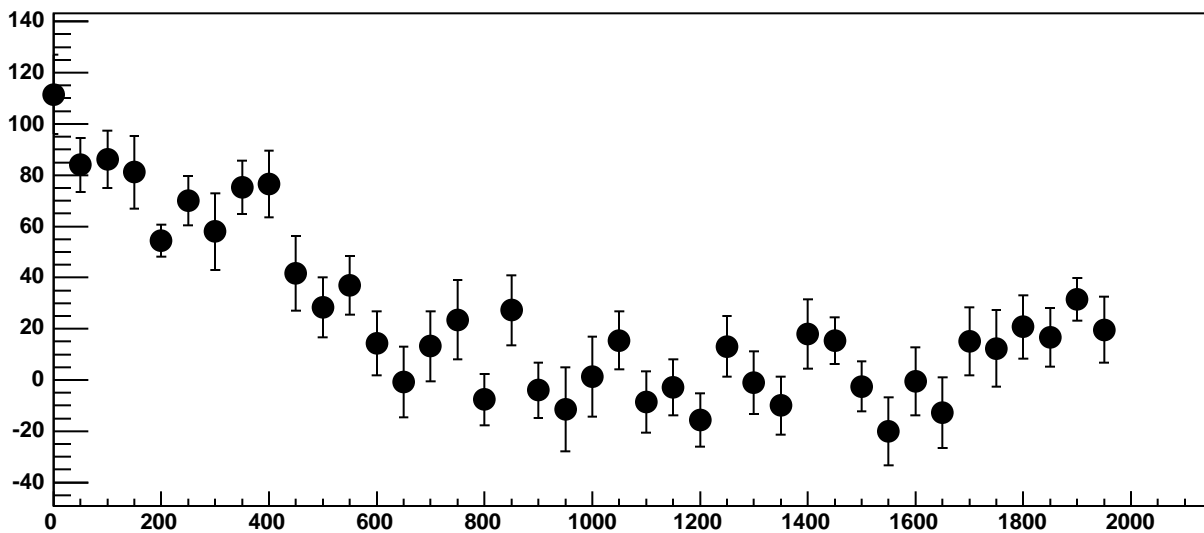
Chip 2, Channel 4, Enable 1, Hold=30, ADC Mean vs DAC



Chip 2, Channel 4, Enable 1, Hold=30, ADC Noise vs DAC

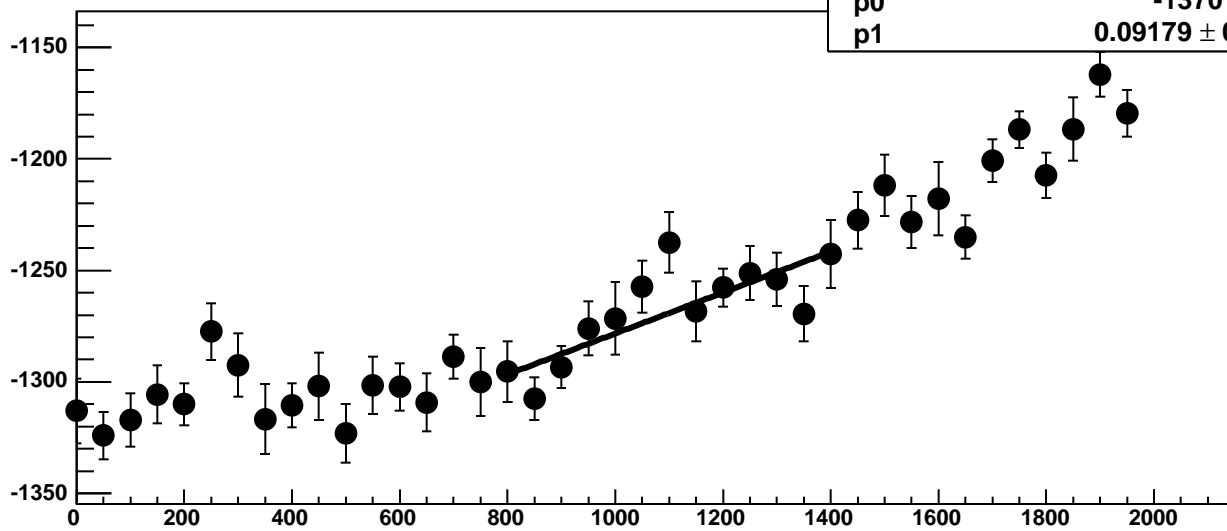


Chip 2, Channel 4, Enable 1, Hold=30, ADC Residuals vs DAC

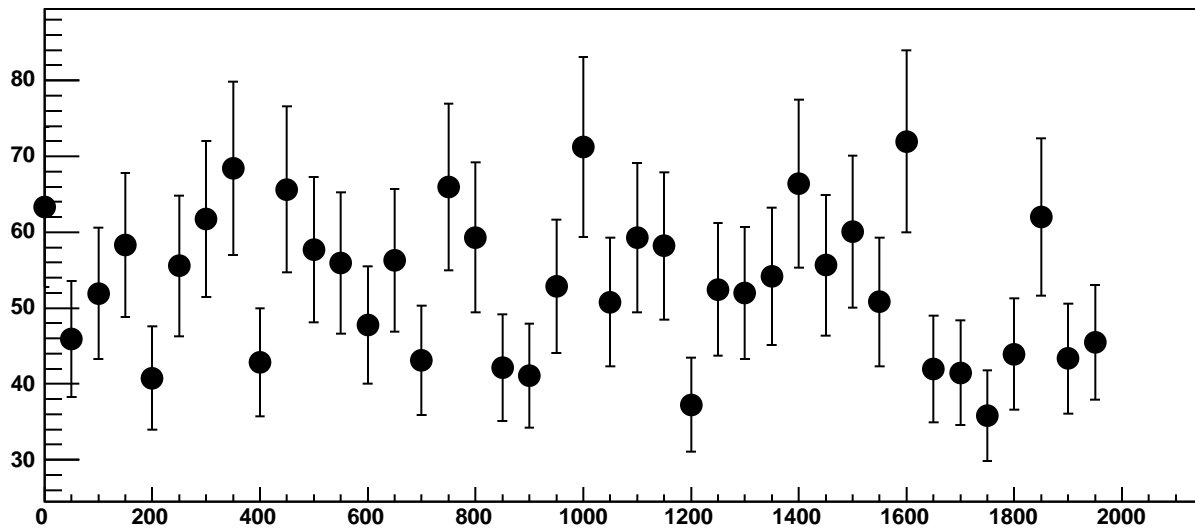




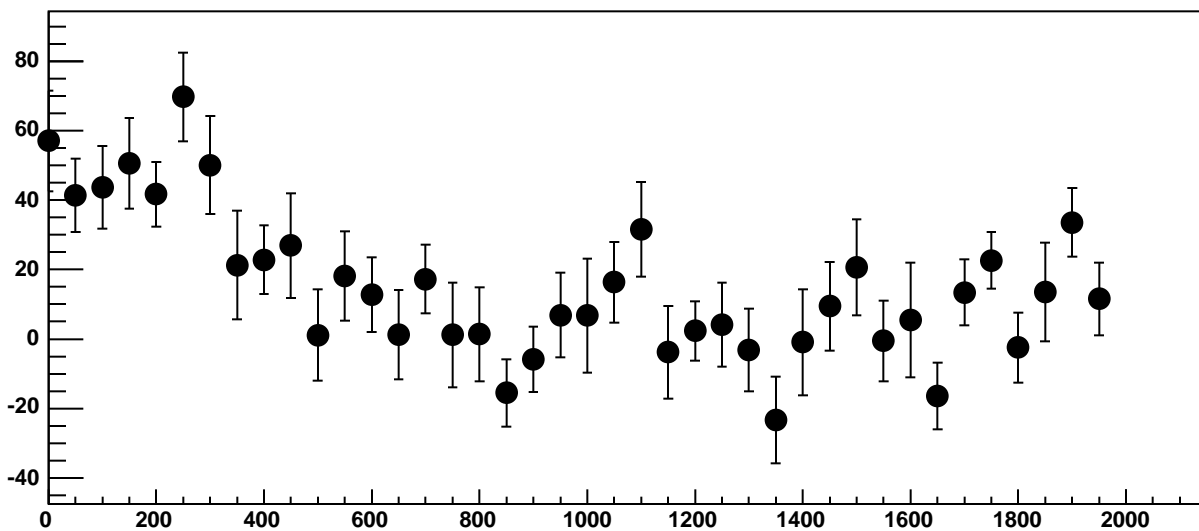
Chip 2, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC



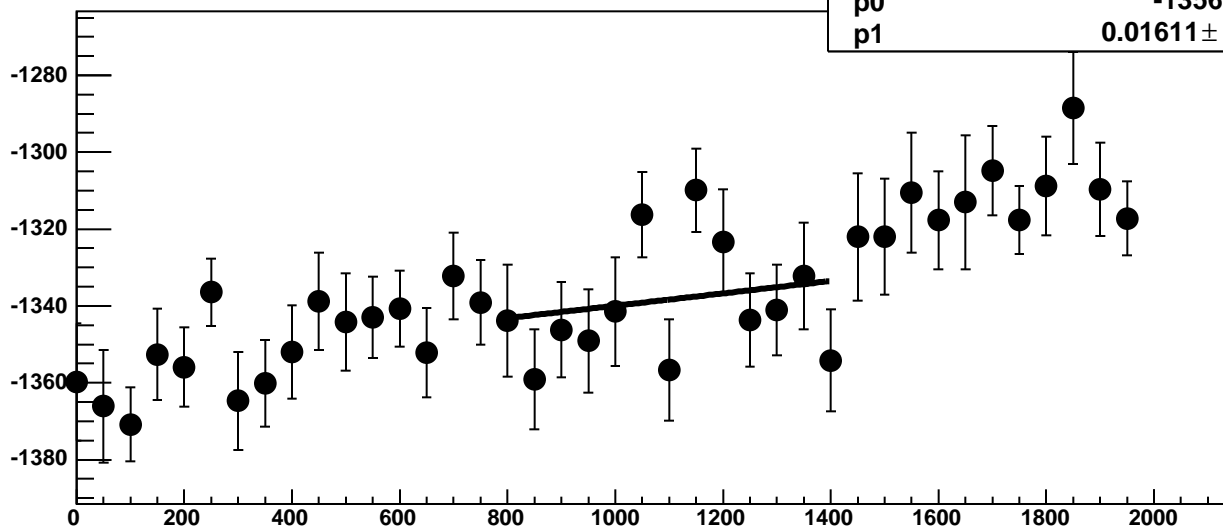
Chip 2, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC



Chip 2, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC

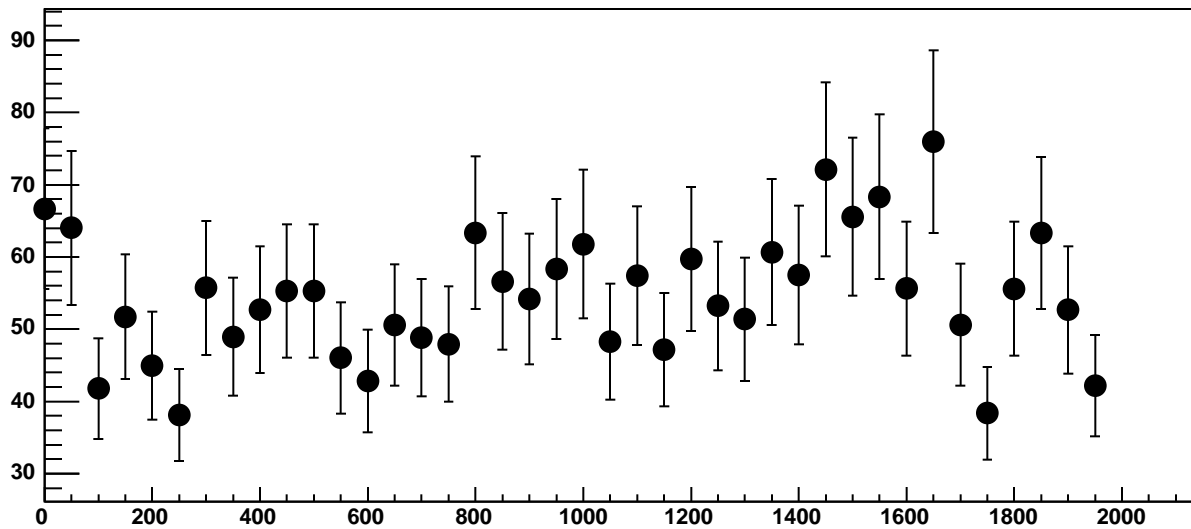


Chip 2, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC

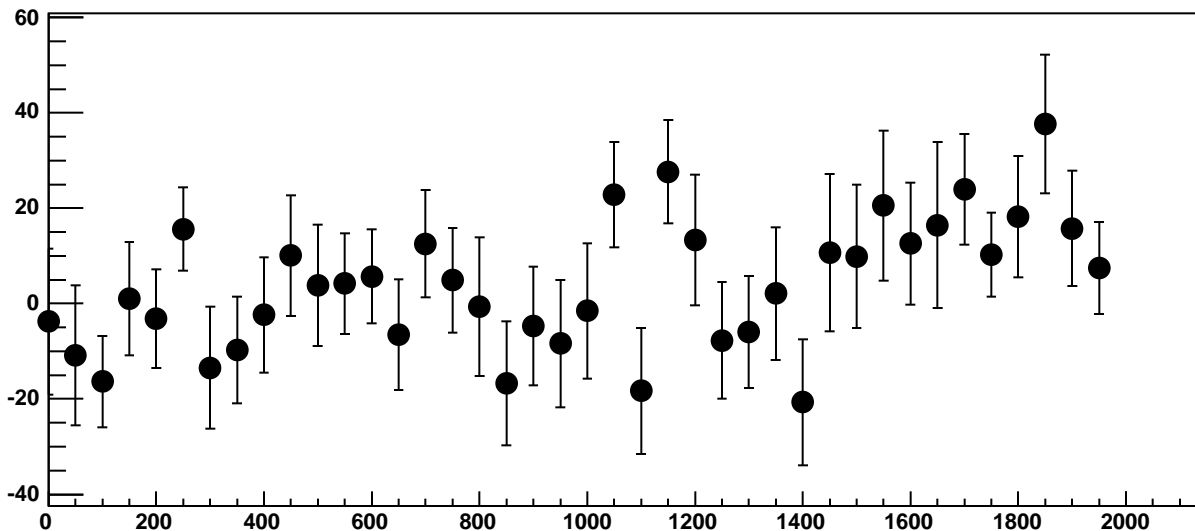


$\chi^2 / \text{ndf}$  19.01 / 11  
p0  $-1356 \pm 21.97$   
p1  $0.01611 \pm 0.01961$

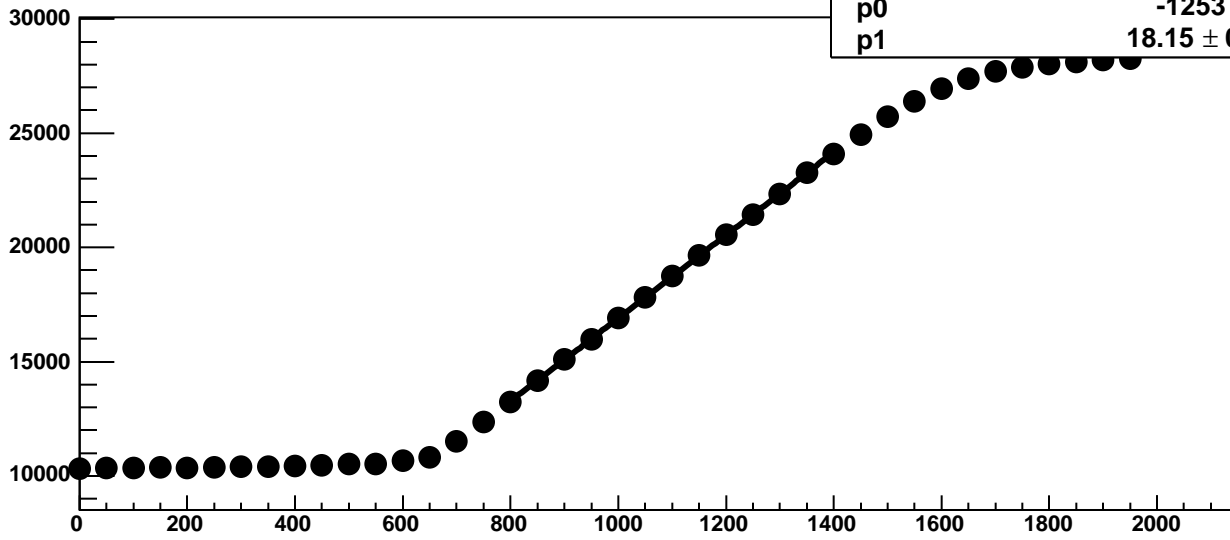
Chip 2, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC



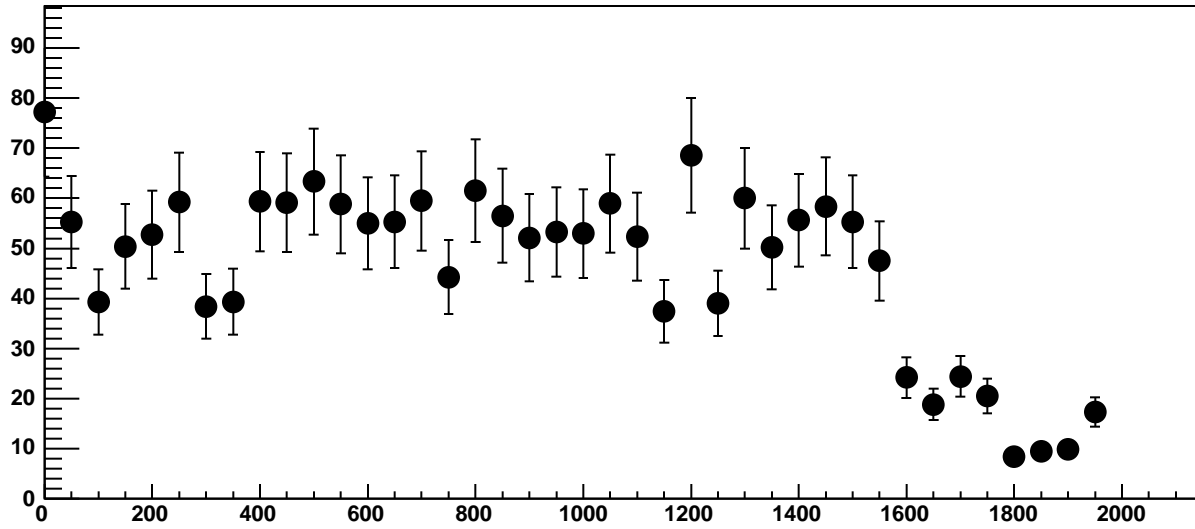
Chip 2, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC



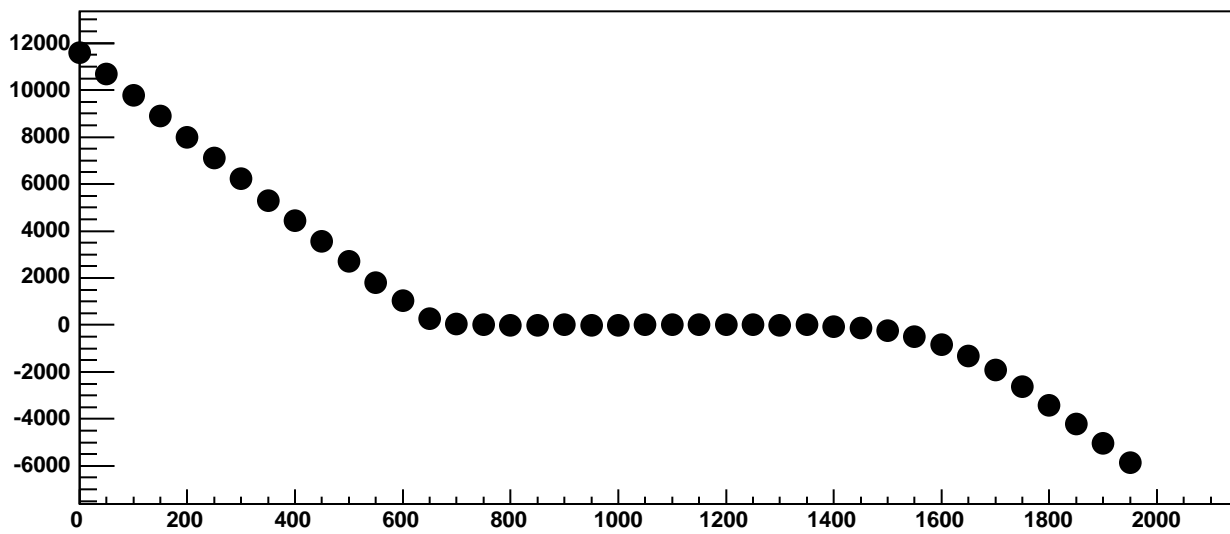
Chip 2, Channel 4, Enable 4!, Hold=30, ADC Mean vs DAC



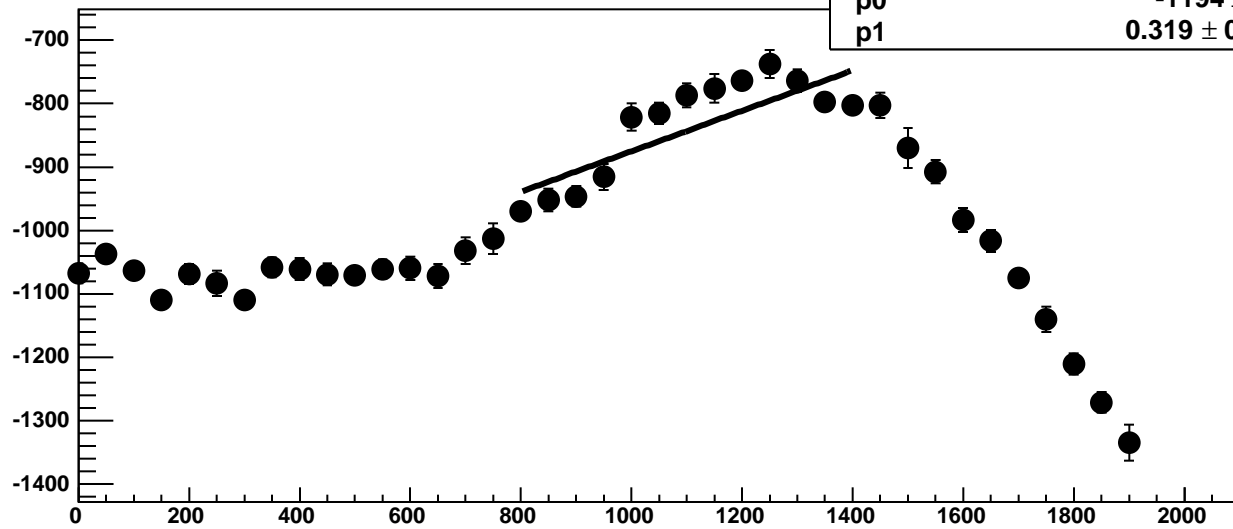
Chip 2, Channel 4, Enable 4!, Hold=30, ADC Noise vs DAC



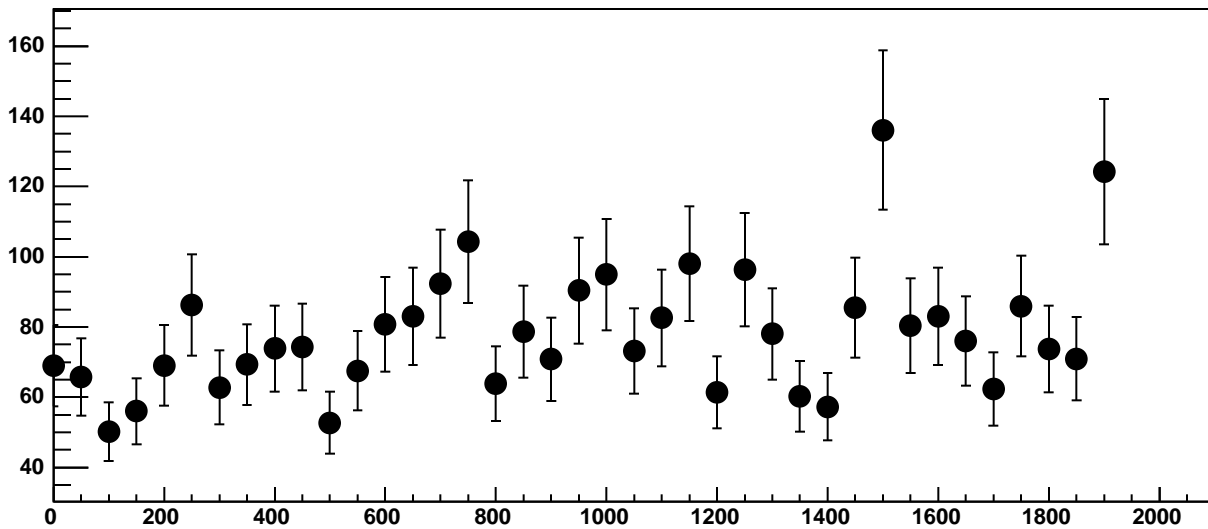
Chip 2, Channel 4, Enable 4!, Hold=30, ADC Residuals vs DAC



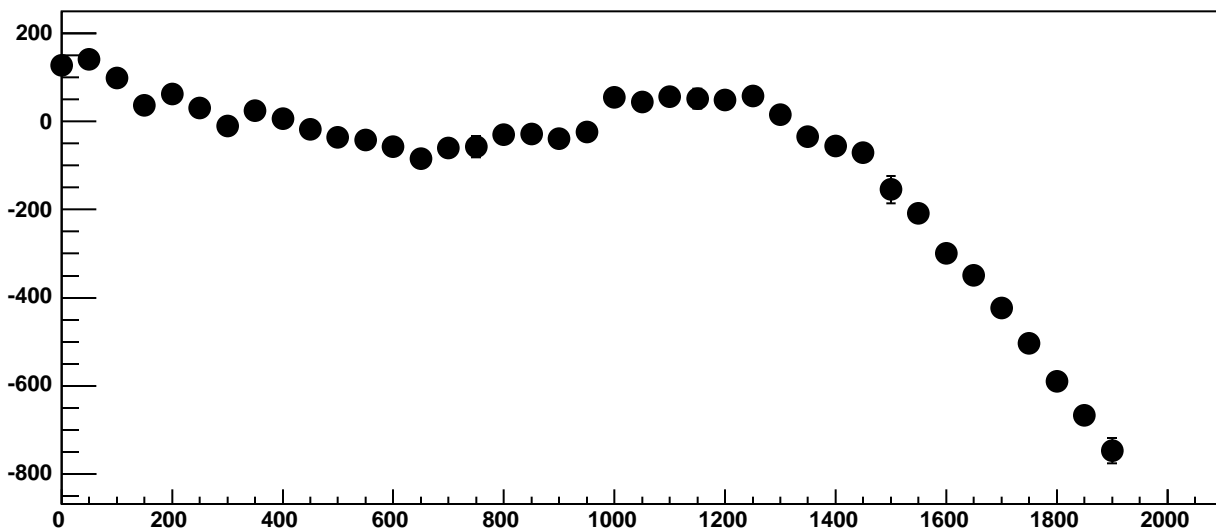
Chip 2, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC



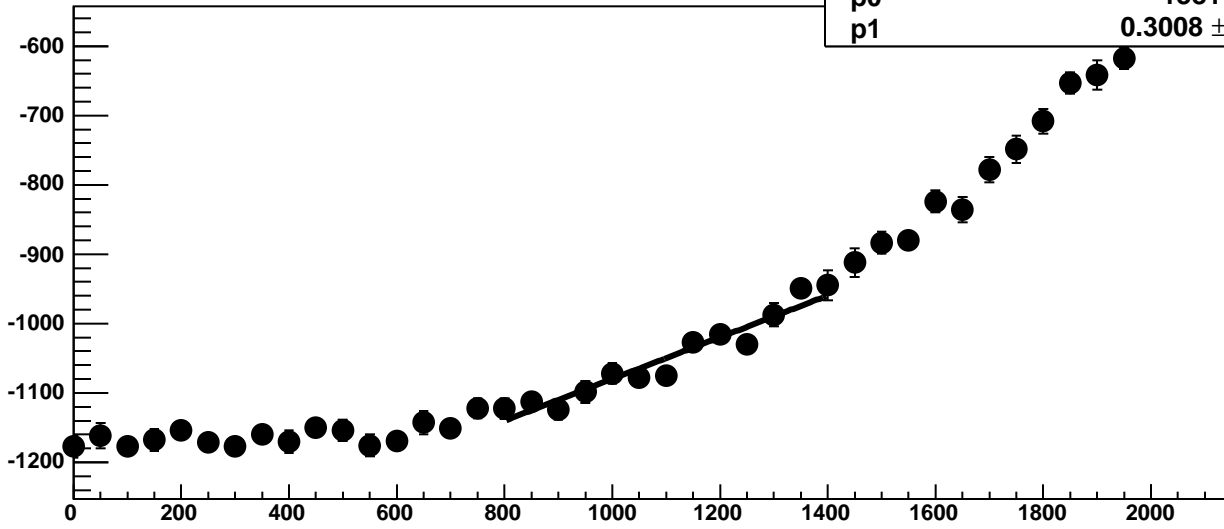
Chip 2, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC



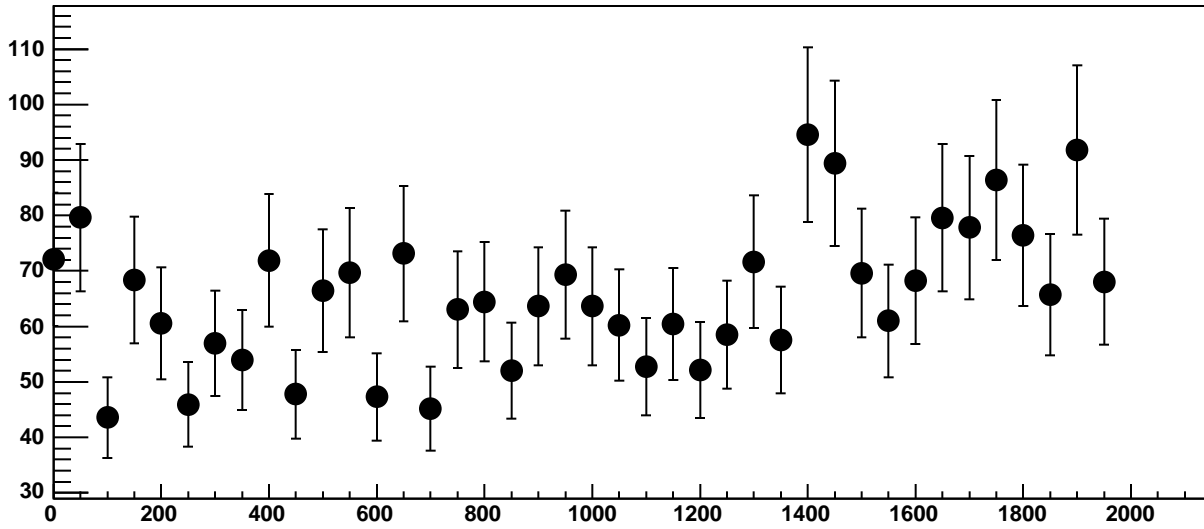
Chip 2, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC



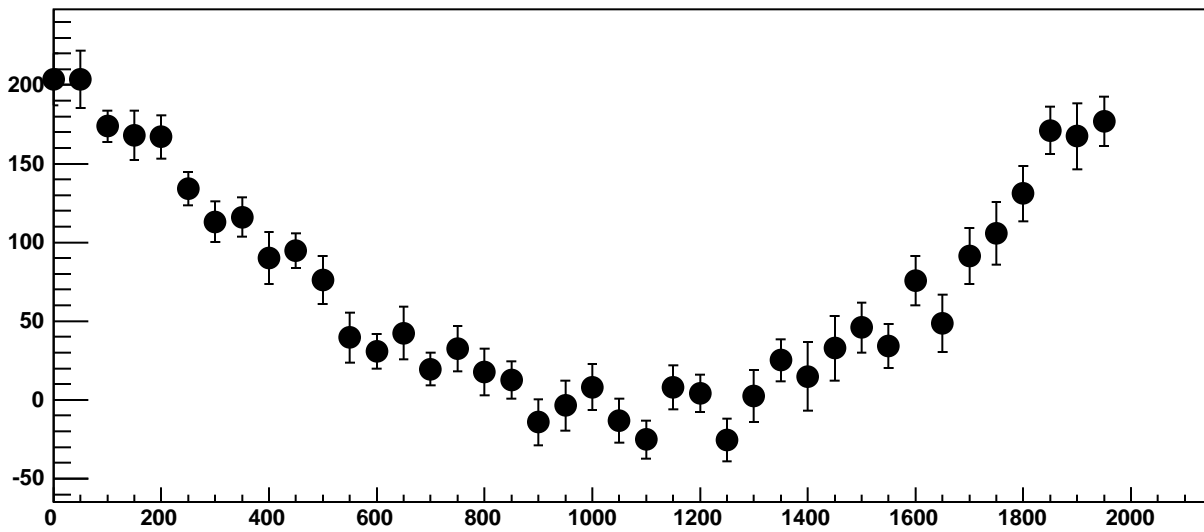
Chip 2, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC



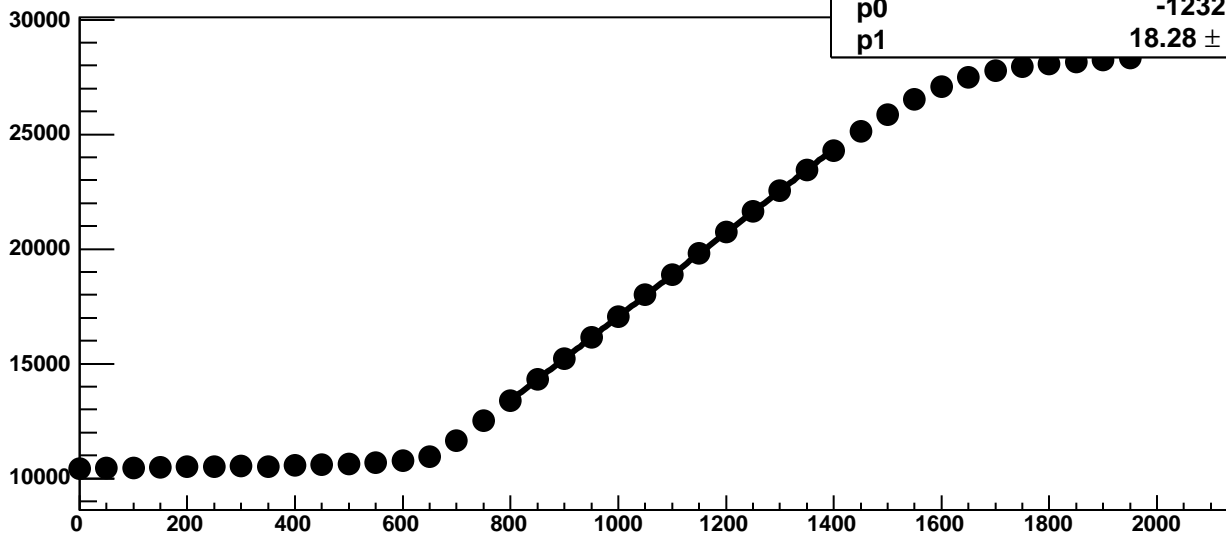
Chip 2, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC



Chip 2, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC

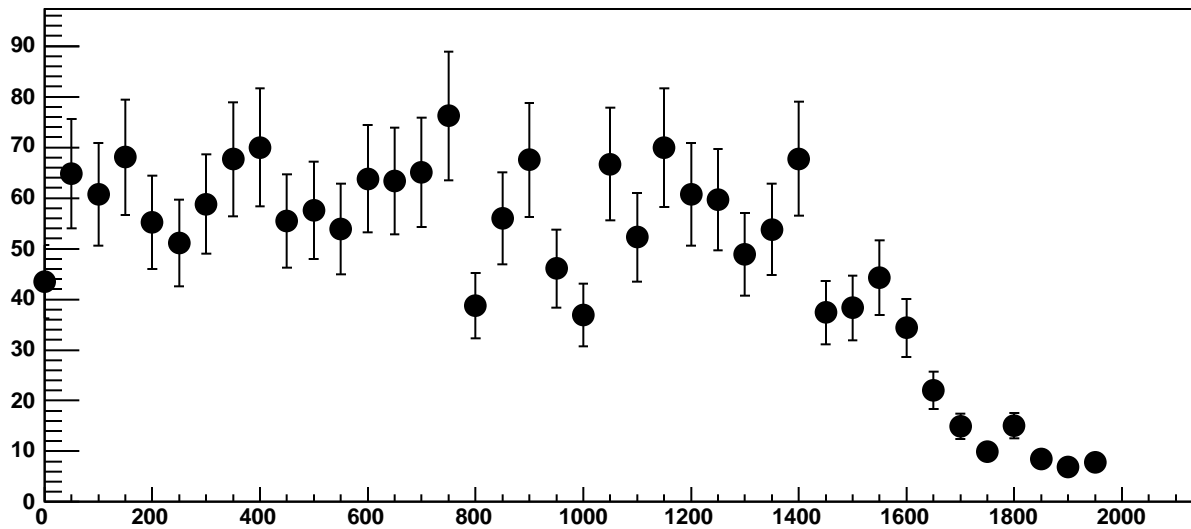


Chip 2, Channel 5, Enable 1!, Hold=30, ADC Mean vs DAC

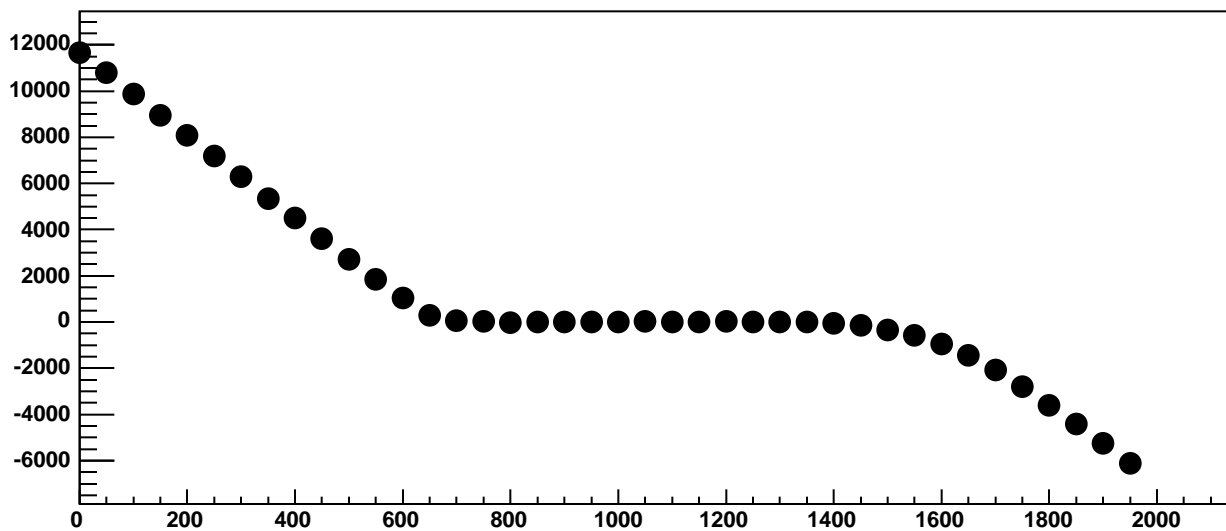


$\chi^2 / \text{ndf}$  36.02 / 11  
p0 -1232 ± 19.05  
p1 18.28 ± 0.01761

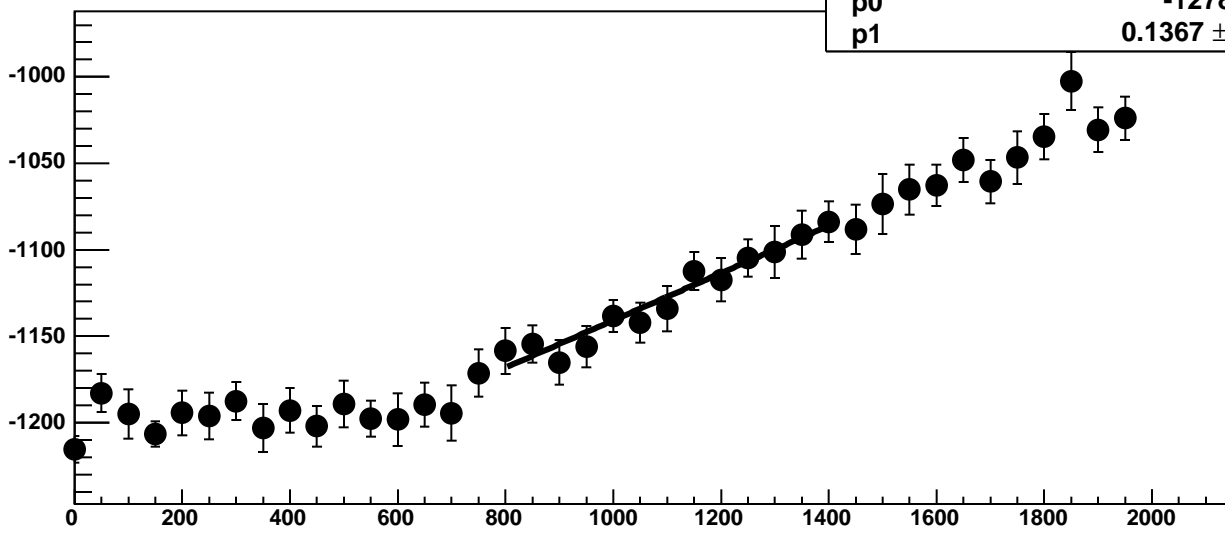
Chip 2, Channel 5, Enable 1!, Hold=30, ADC Noise vs DAC



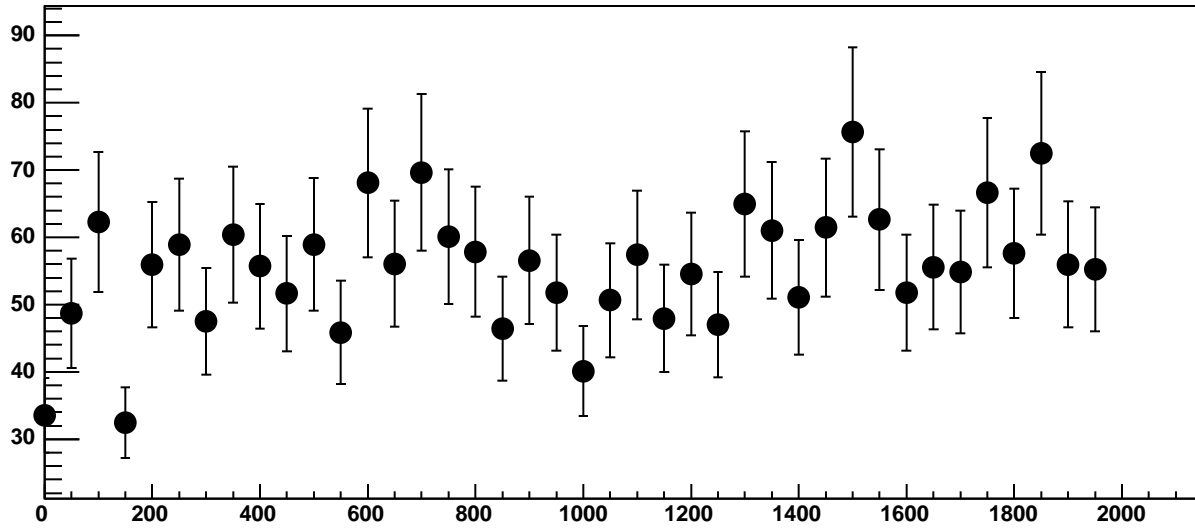
Chip 2, Channel 5, Enable 1!, Hold=30, ADC Residuals vs DAC



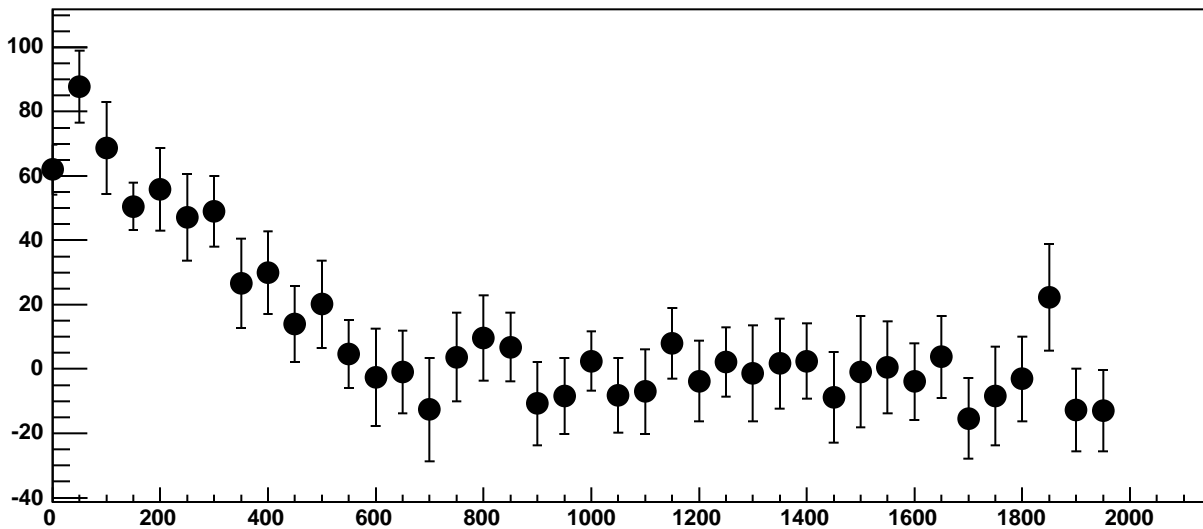
Chip 2, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC



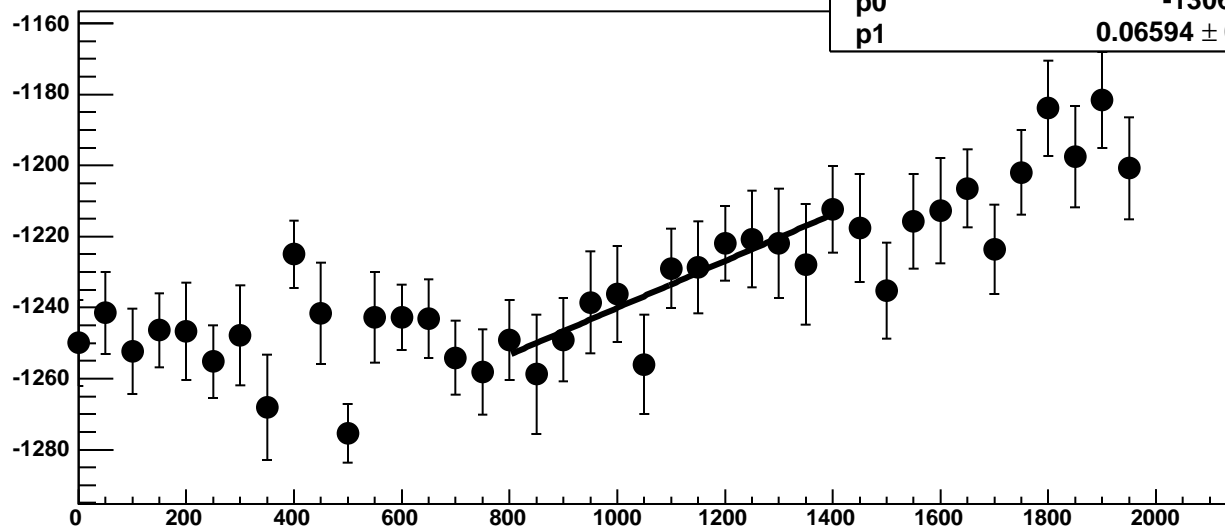
Chip 2, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC



Chip 2, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 2, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

3.417 / 11

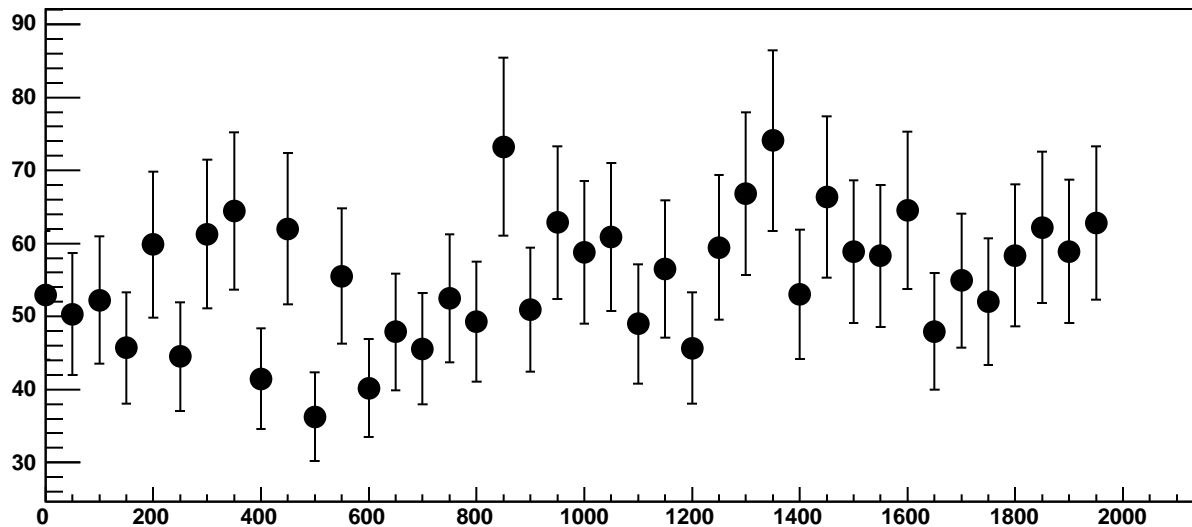
p0

$-1306 \pm 21.7$

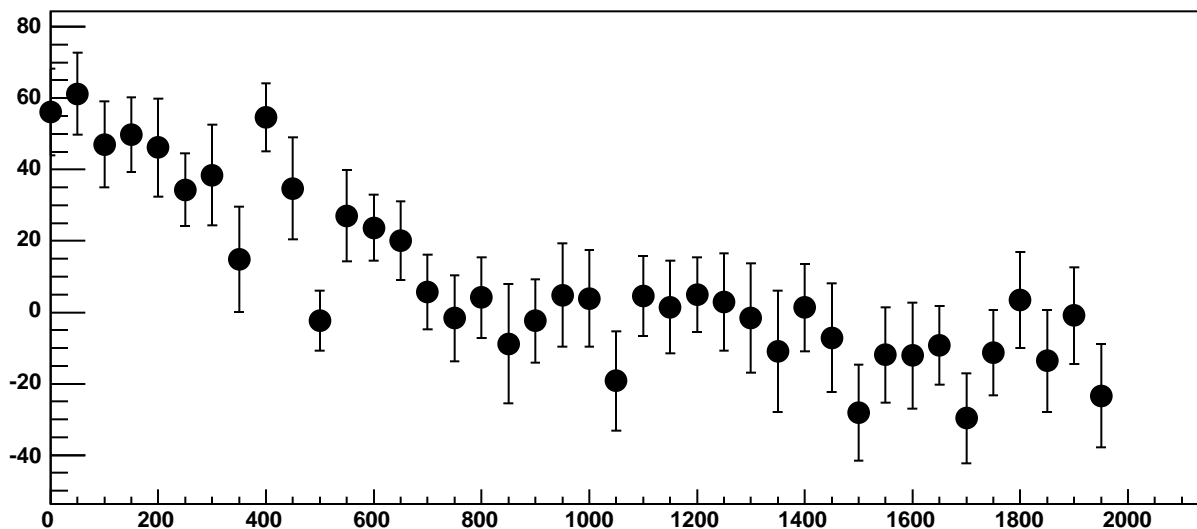
p1

$0.06594 \pm 0.01956$

Chip 2, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC

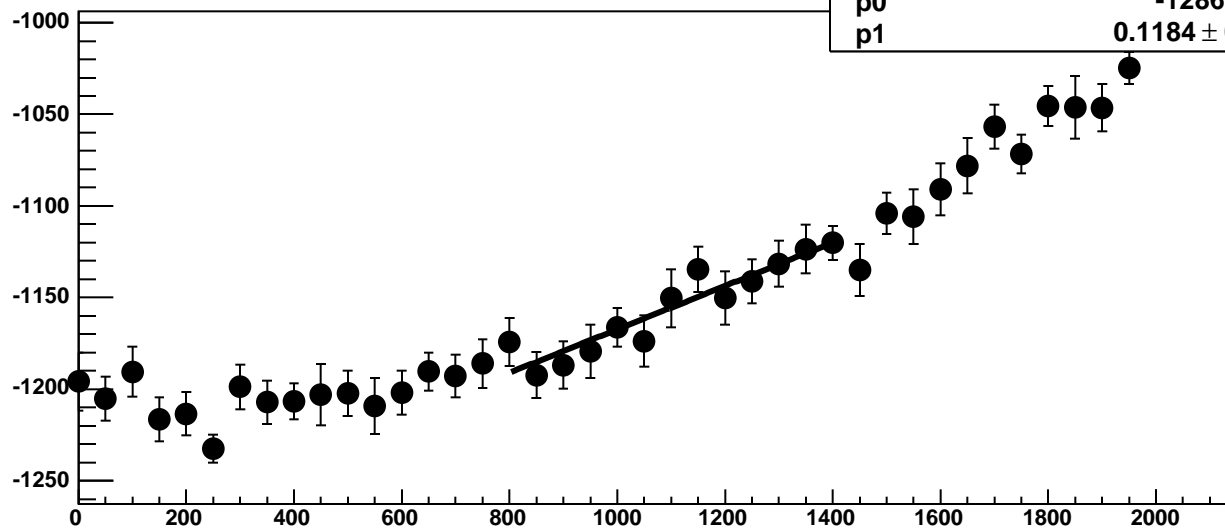


Chip 2, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC





Chip 2, Channel 5, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

5.145 / 11

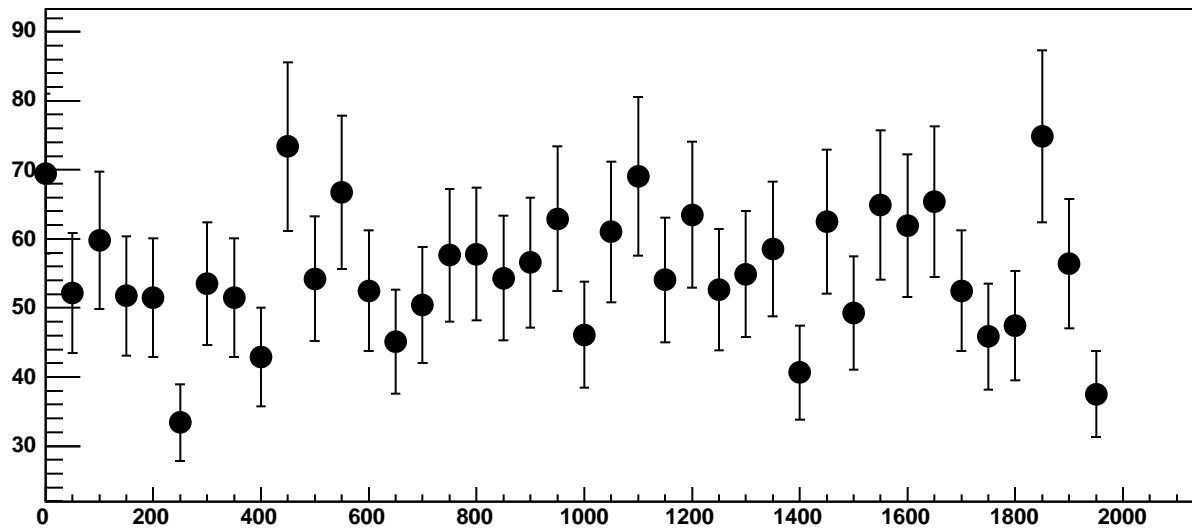
p0

$-1286 \pm 20.13$

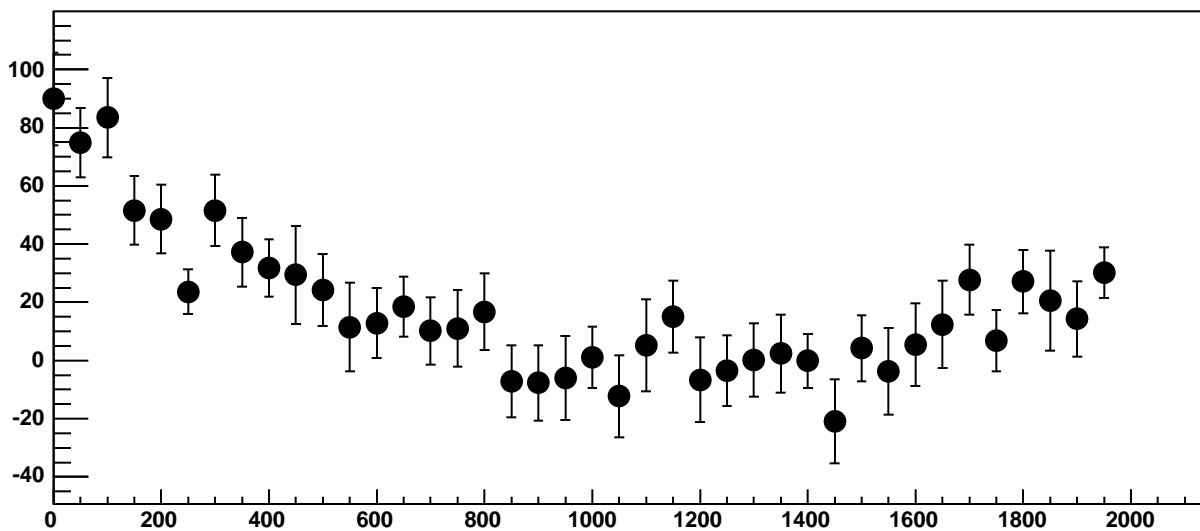
p1

$0.1184 \pm 0.01772$

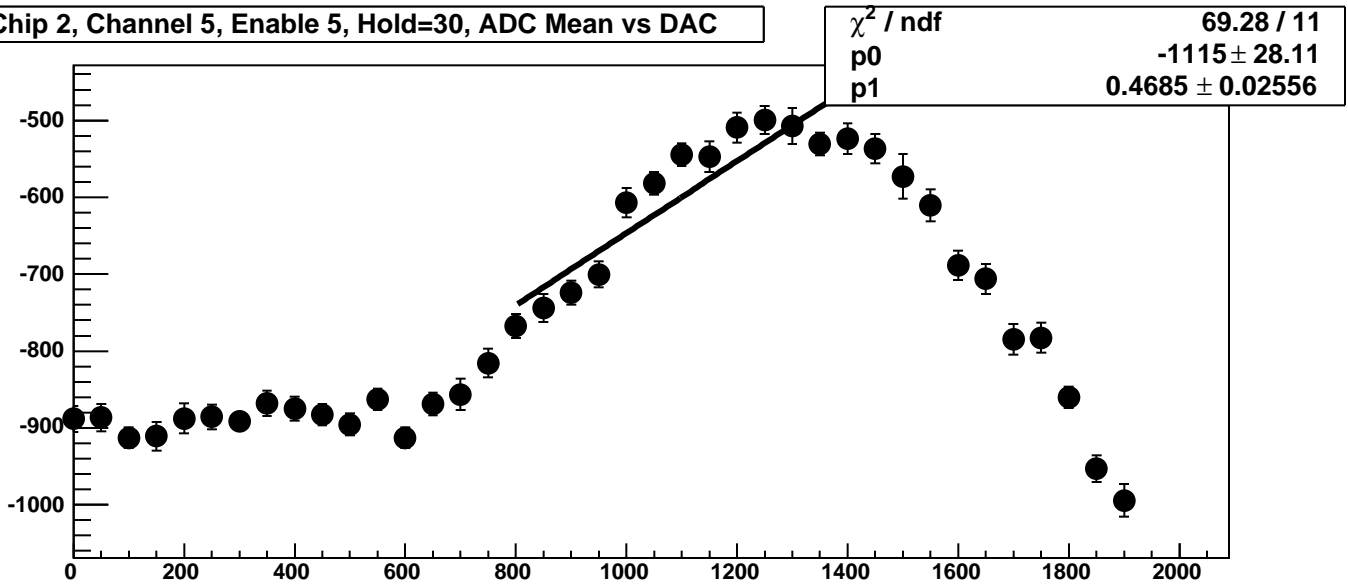
Chip 2, Channel 5, Enable 4, Hold=30, ADC Noise vs DAC



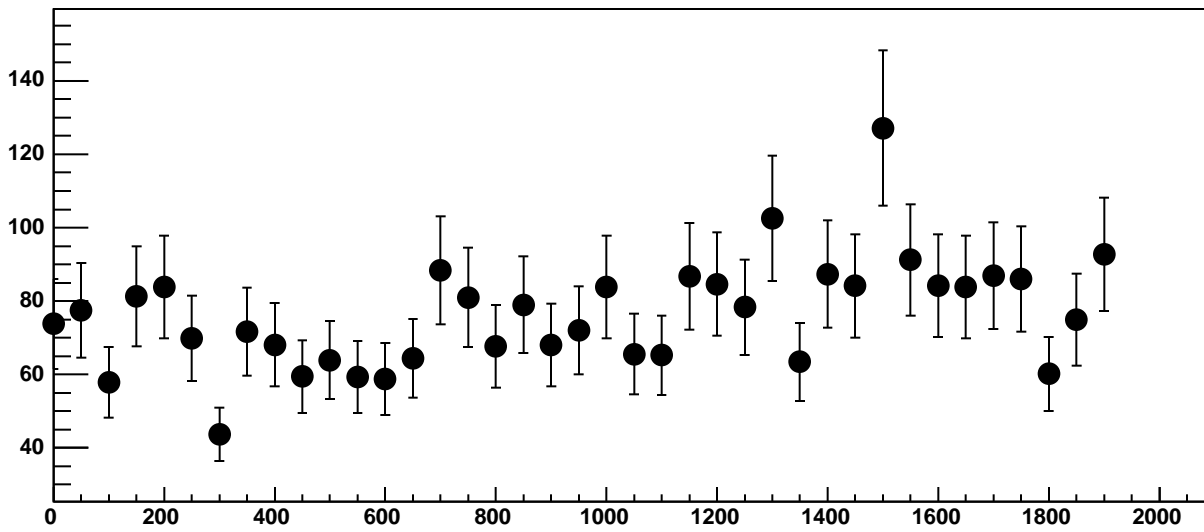
Chip 2, Channel 5, Enable 4, Hold=30, ADC Residuals vs DAC



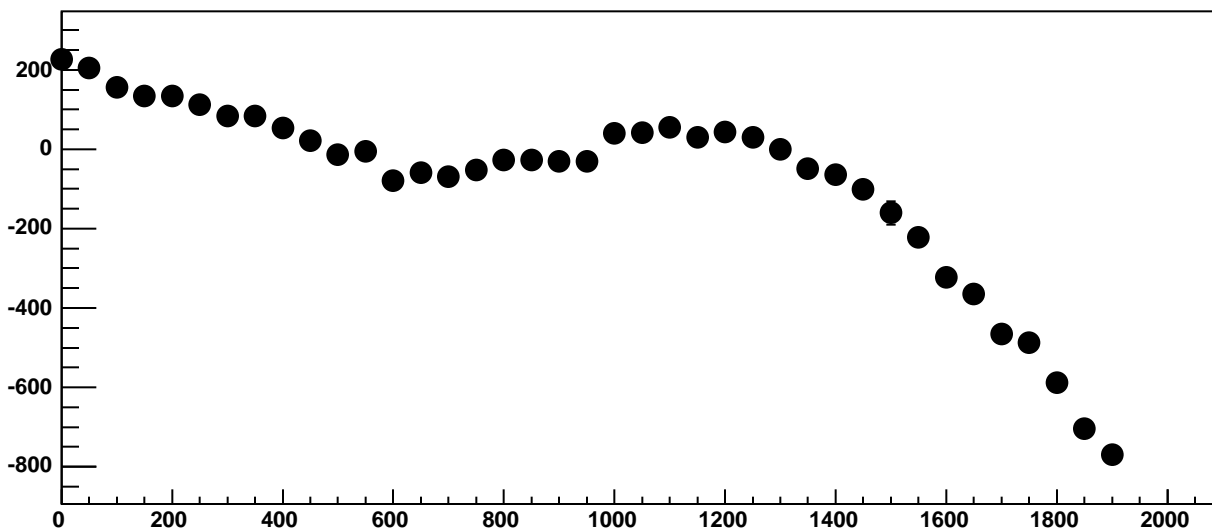
Chip 2, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC



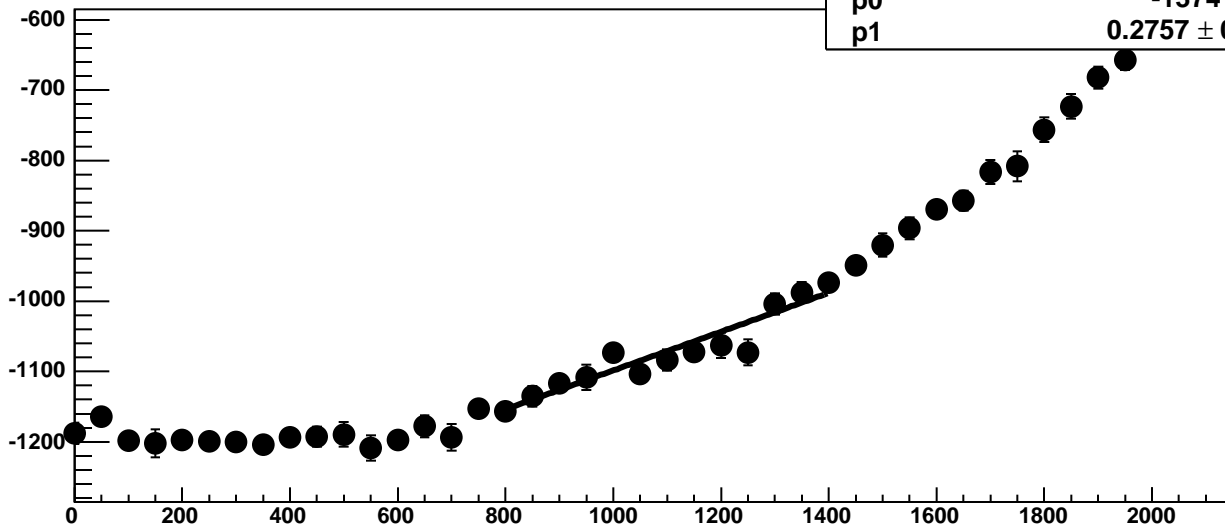
Chip 2, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC



Chip 2, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC

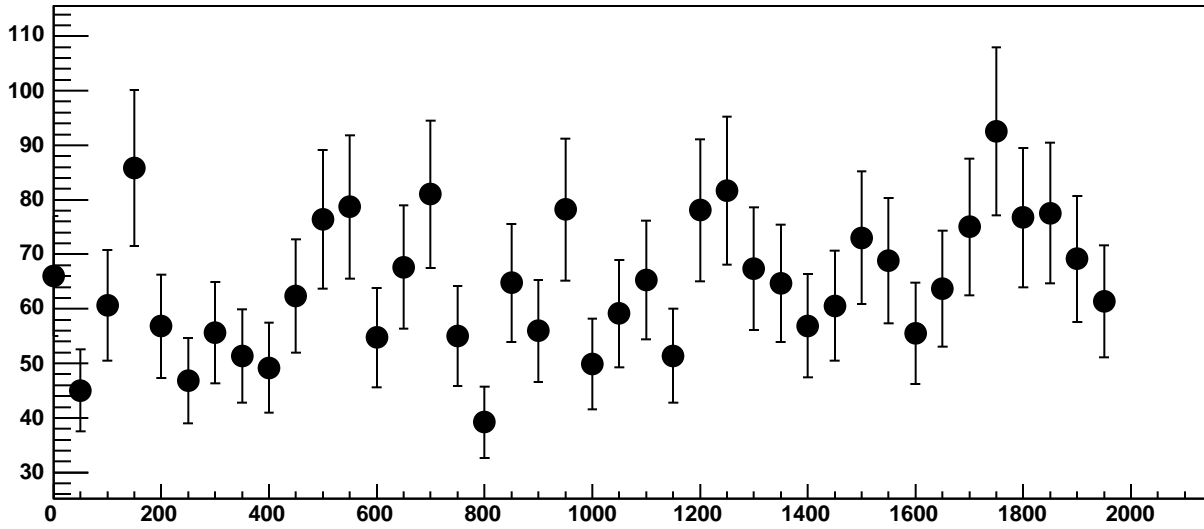


Chip 2, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC

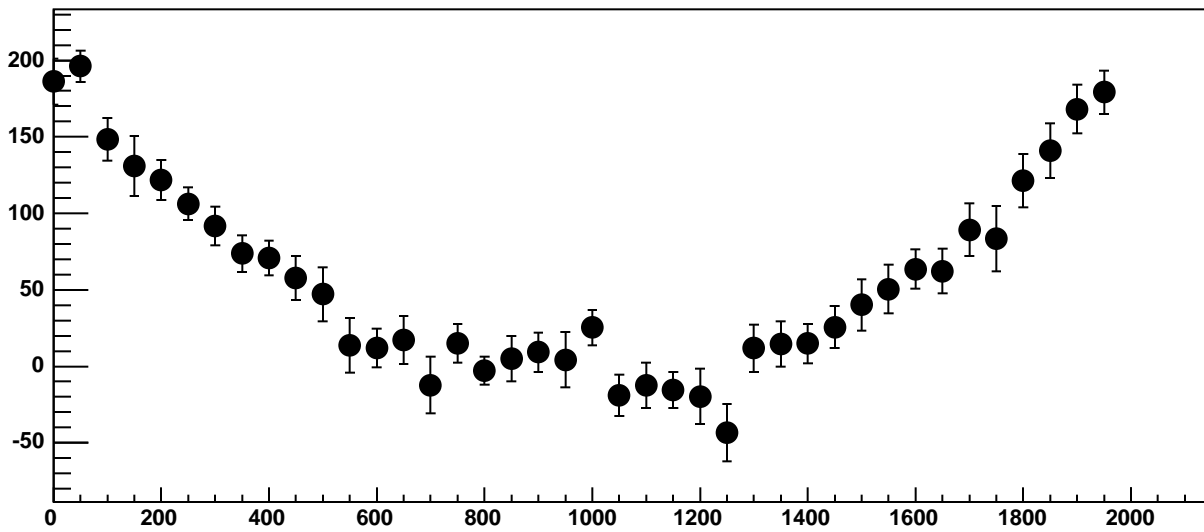


$\chi^2 / \text{ndf}$  19.38 / 11  
p0  $-1374 \pm 20.46$   
p1  $0.2757 \pm 0.01894$

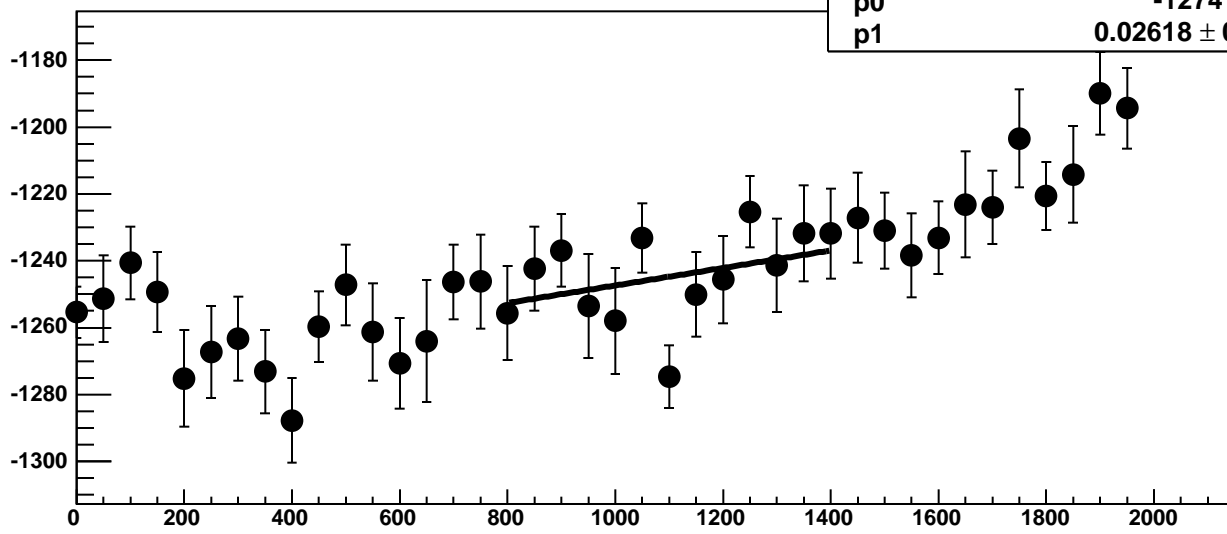
Chip 2, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC



Chip 2, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC

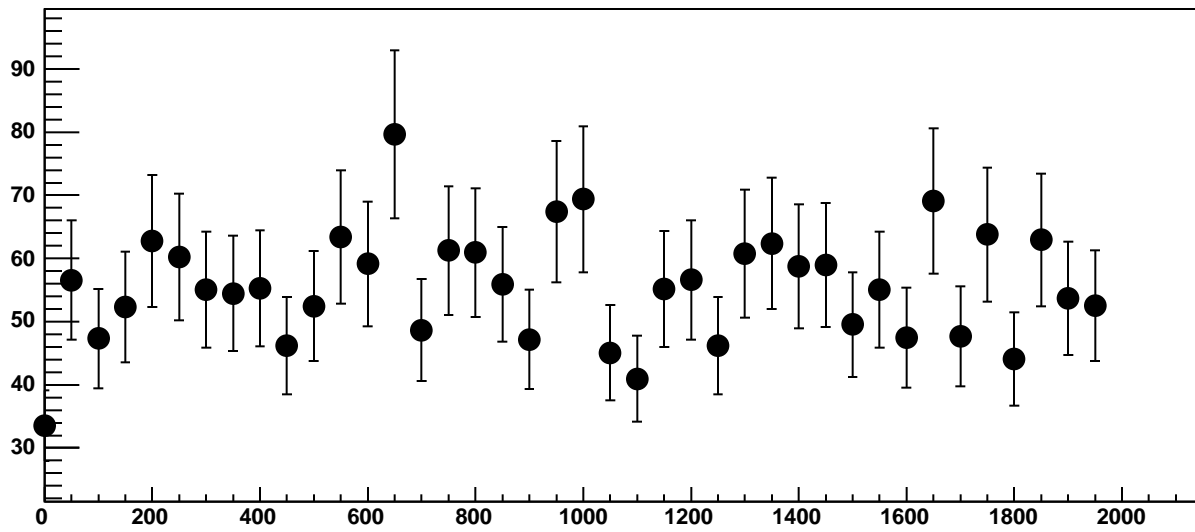


Chip 2, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC

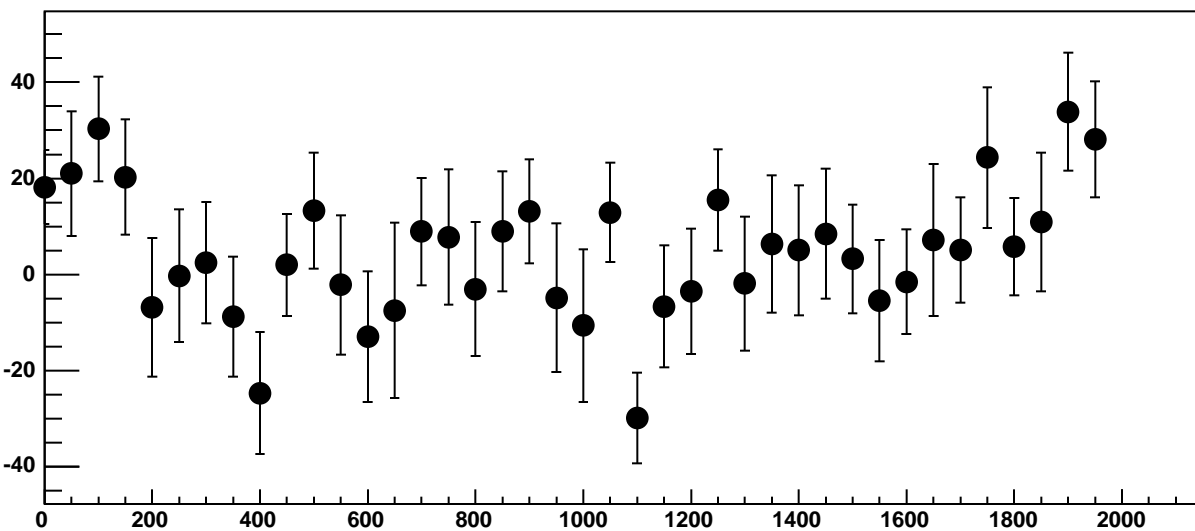


$\chi^2 / \text{ndf}$  17.05 / 11  
p0  $-1274 \pm 21.64$   
p1  $0.02618 \pm 0.01947$

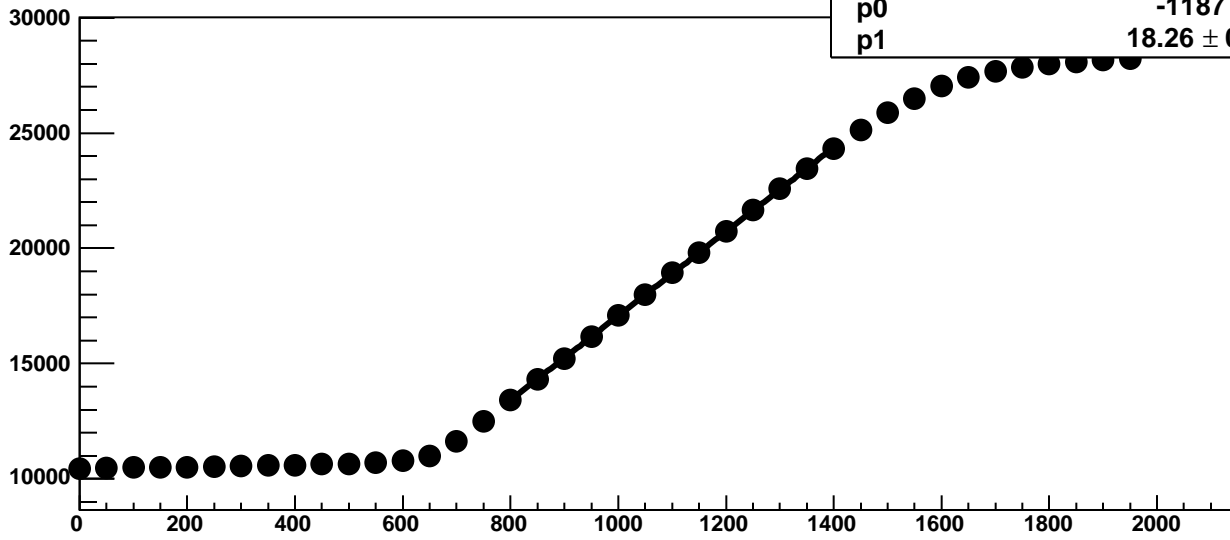
Chip 2, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC



Chip 2, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC

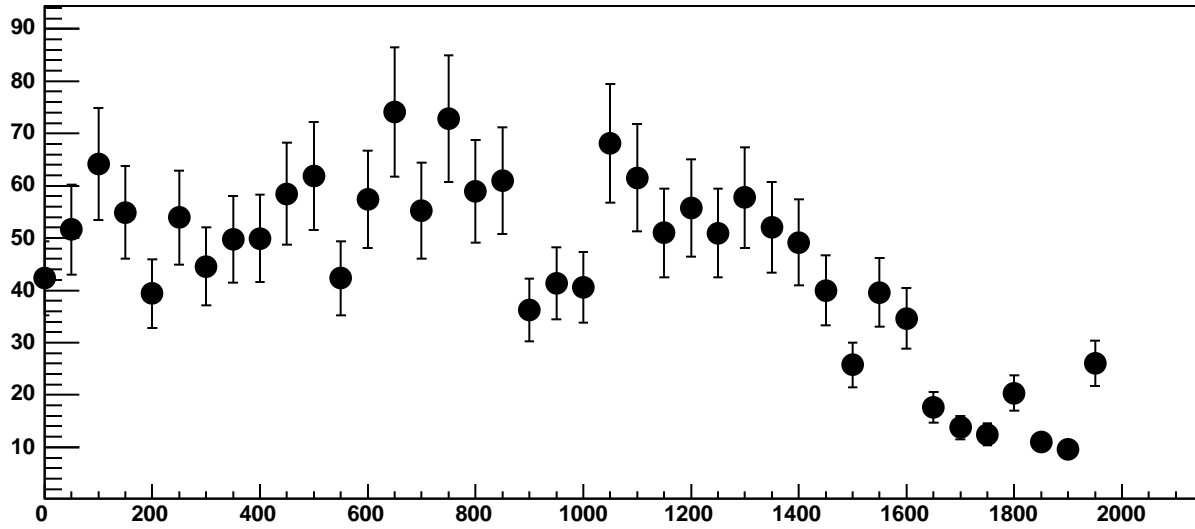


Chip 2, Channel 6, Enable 2!, Hold=30, ADC Mean vs DAC

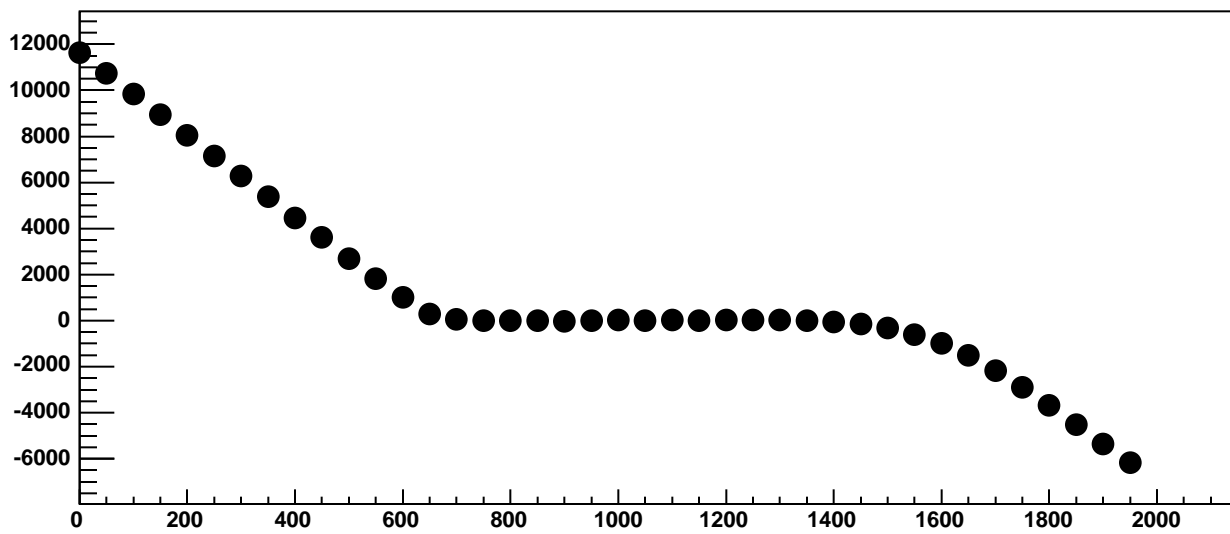


$\chi^2 / \text{ndf}$  69.03 / 11  
p0  $-1187 \pm 18.98$   
p1  $18.26 \pm 0.01725$

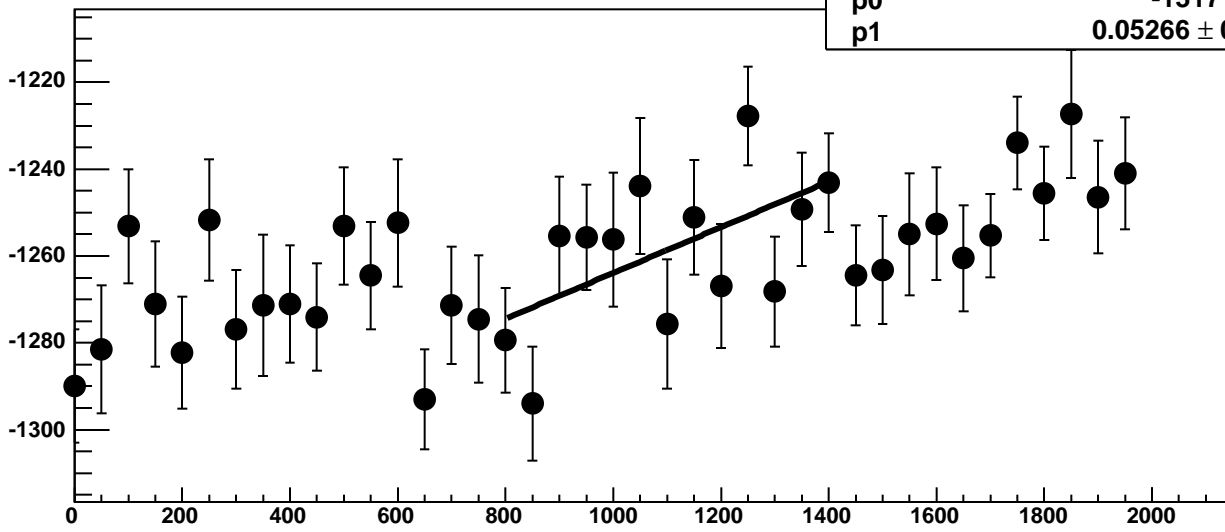
Chip 2, Channel 6, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 2, Channel 6, Enable 2!, Hold=30, ADC Residuals vs DAC

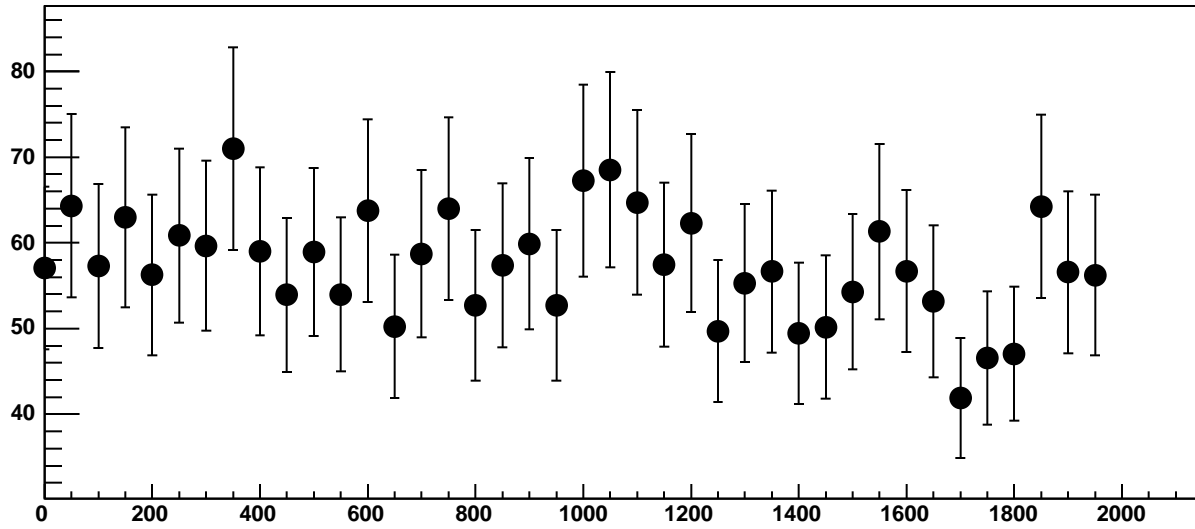


Chip 2, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC

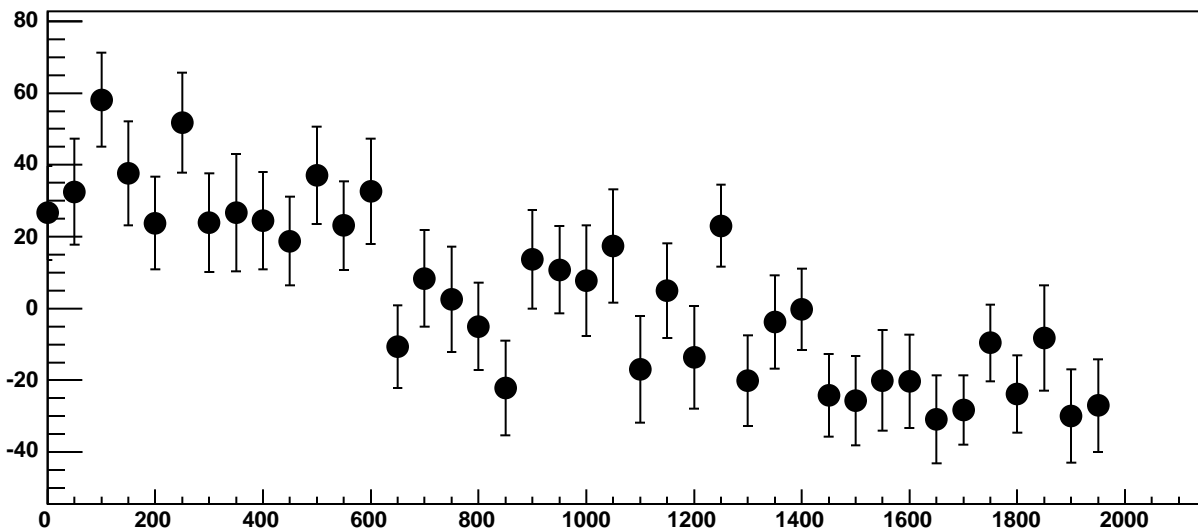


$\chi^2 / \text{ndf}$  15.29 / 11  
p0  $-1317 \pm 20.79$   
p1  $0.05266 \pm 0.01844$

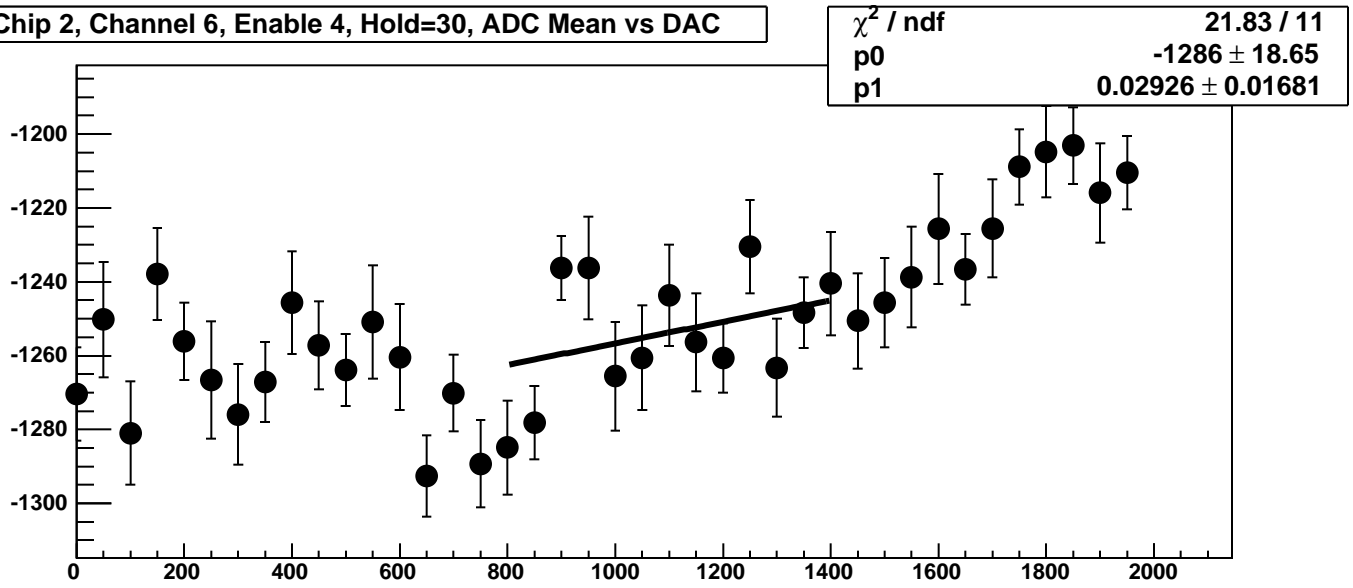
Chip 2, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC



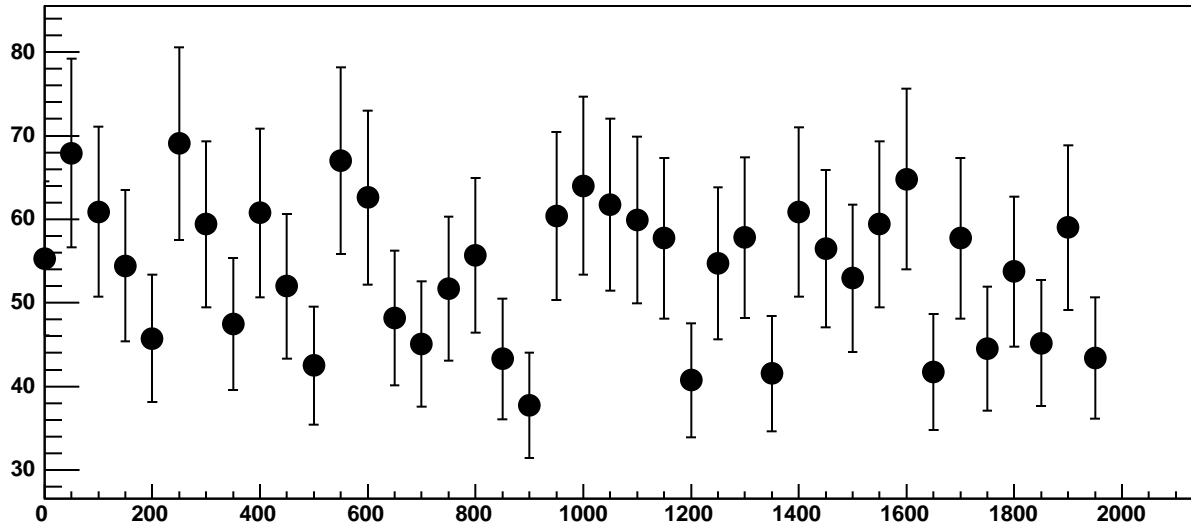
Chip 2, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC



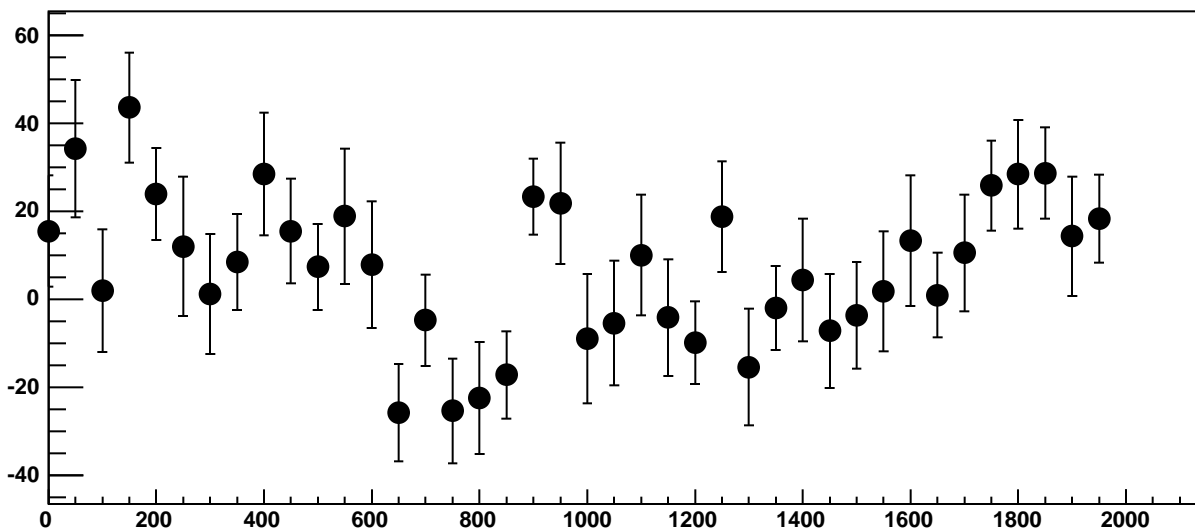
Chip 2, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC



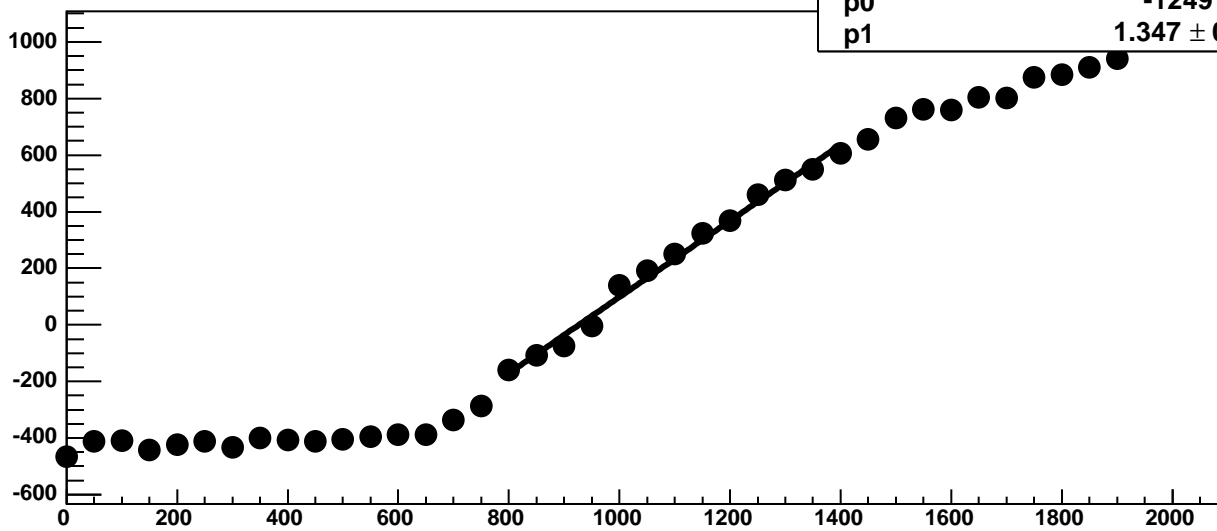
Chip 2, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC



Chip 2, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC

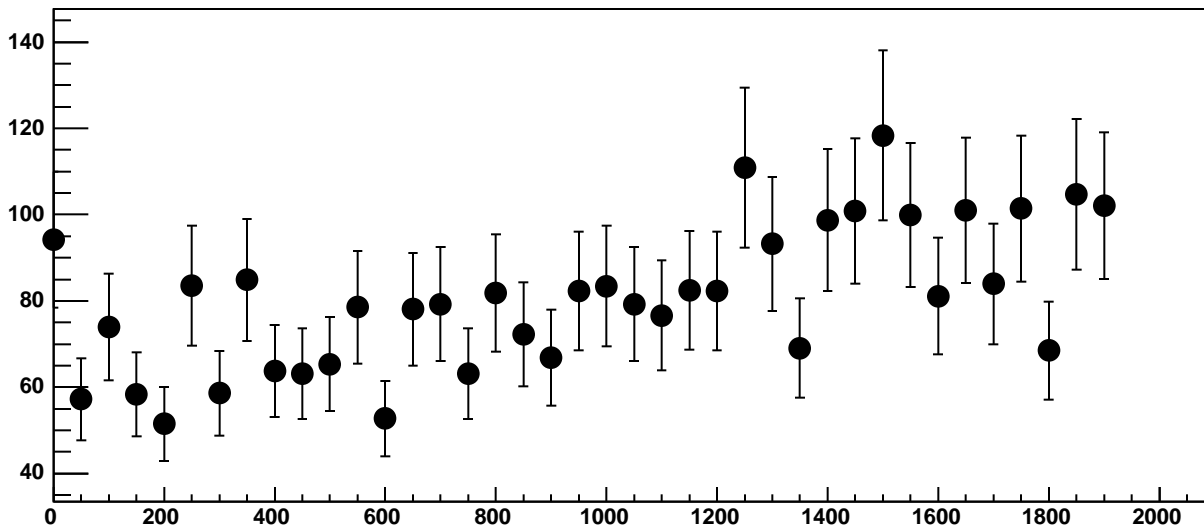


Chip 2, Channel 6, Enable 5, Hold=30, ADC Mean vs DAC

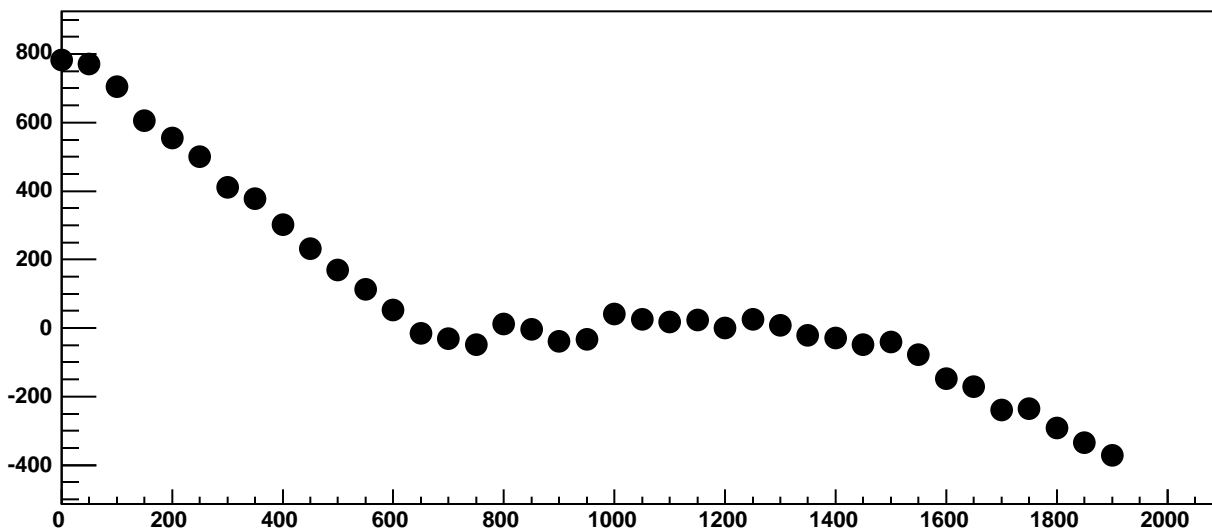


$\chi^2 / \text{ndf}$  23.81 / 11  
p0  $-1249 \pm 30.19$   
p1  $1.347 \pm 0.02756$

Chip 2, Channel 6, Enable 5, Hold=30, ADC Noise vs DAC

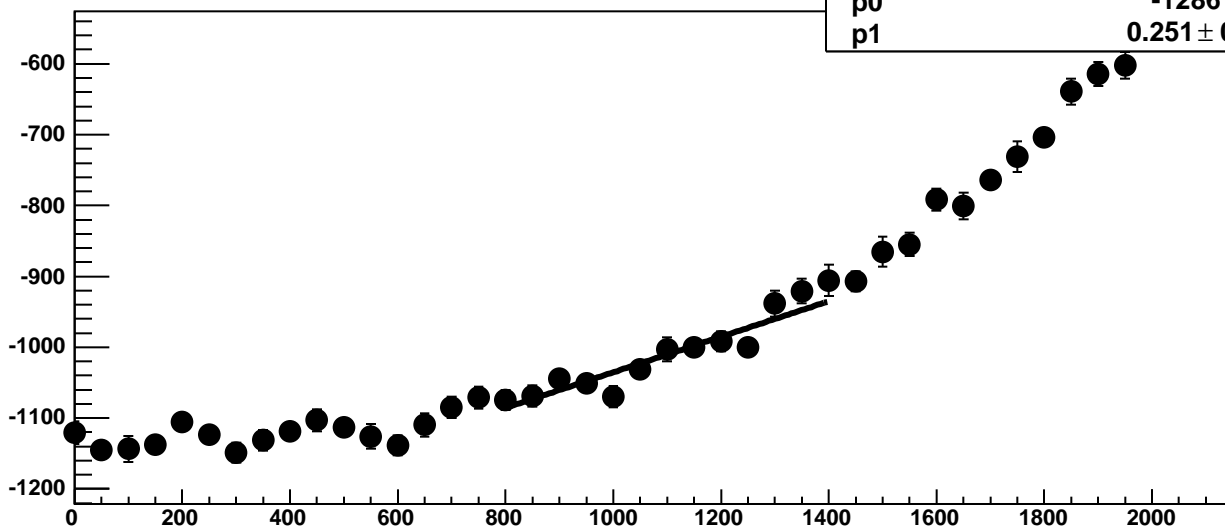


Chip 2, Channel 6, Enable 5, Hold=30, ADC Residuals vs DAC



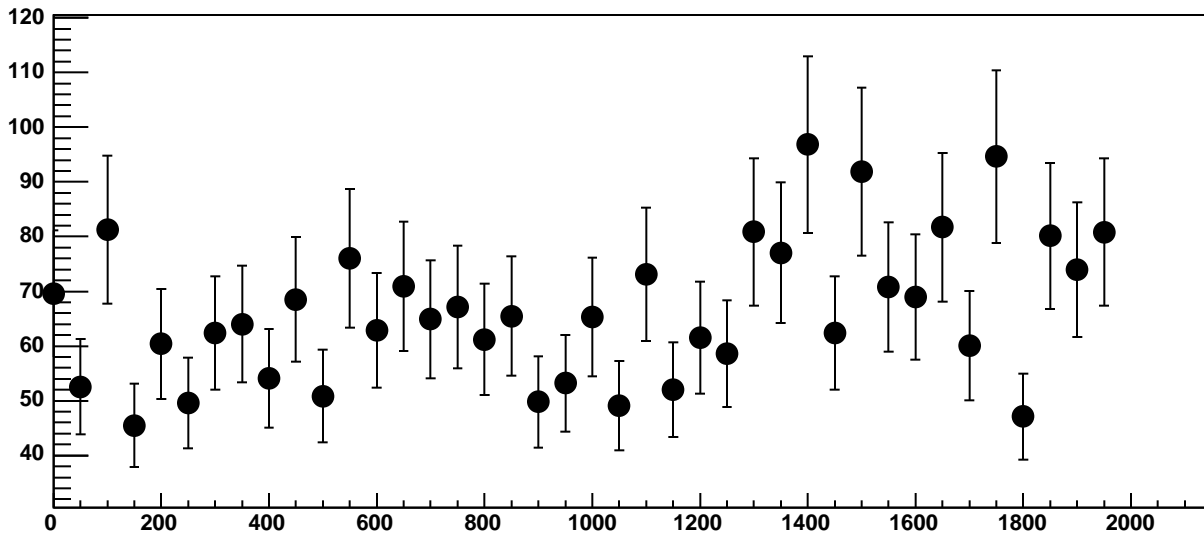


Chip 2, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC

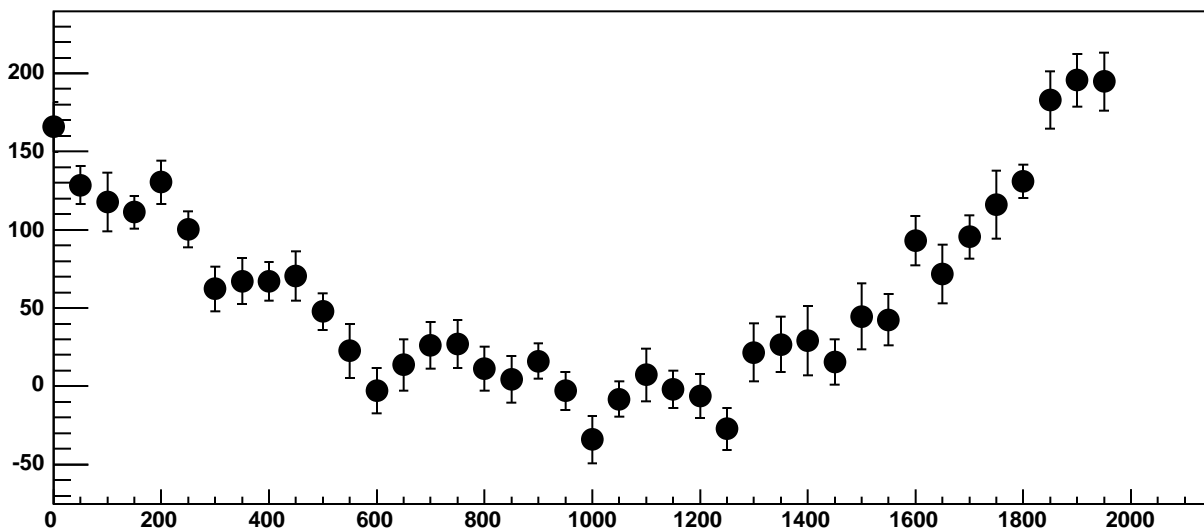


$\chi^2 / \text{ndf}$  18.41 / 11  
p0  $-1286 \pm 24.97$   
p1  $0.251 \pm 0.02317$

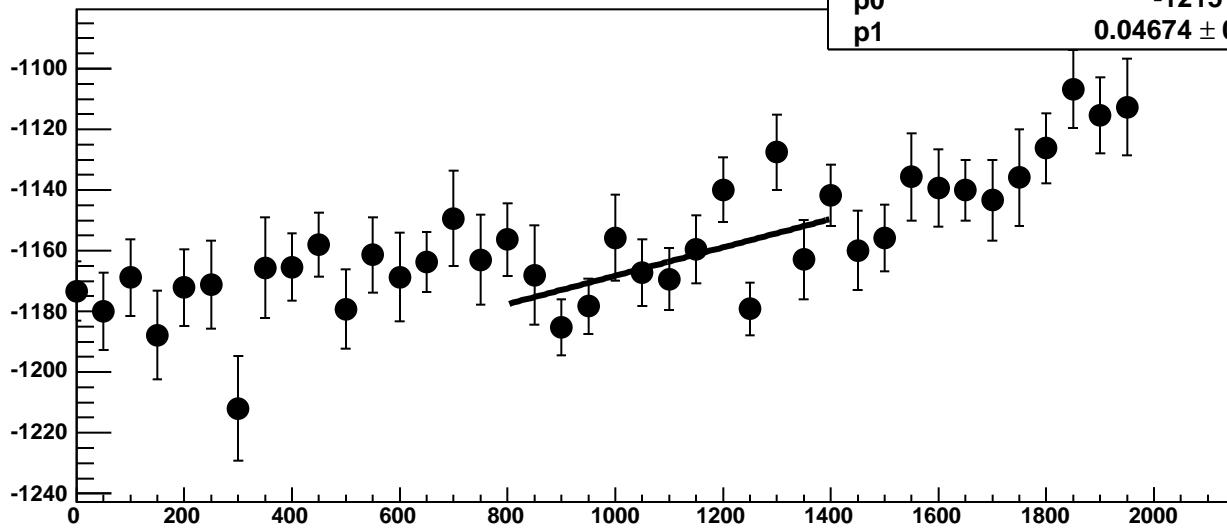
Chip 2, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC



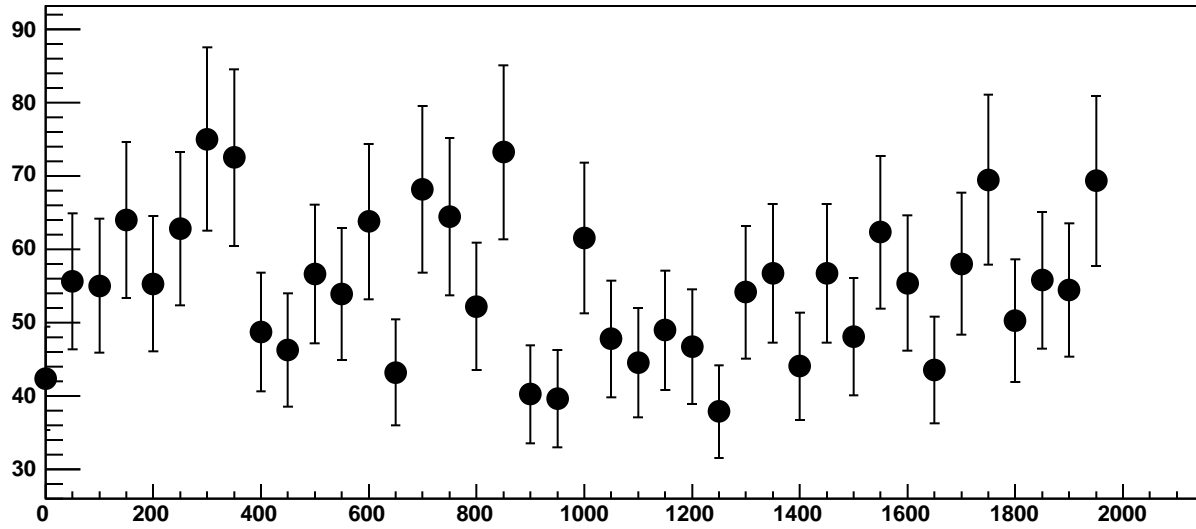
Chip 2, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC



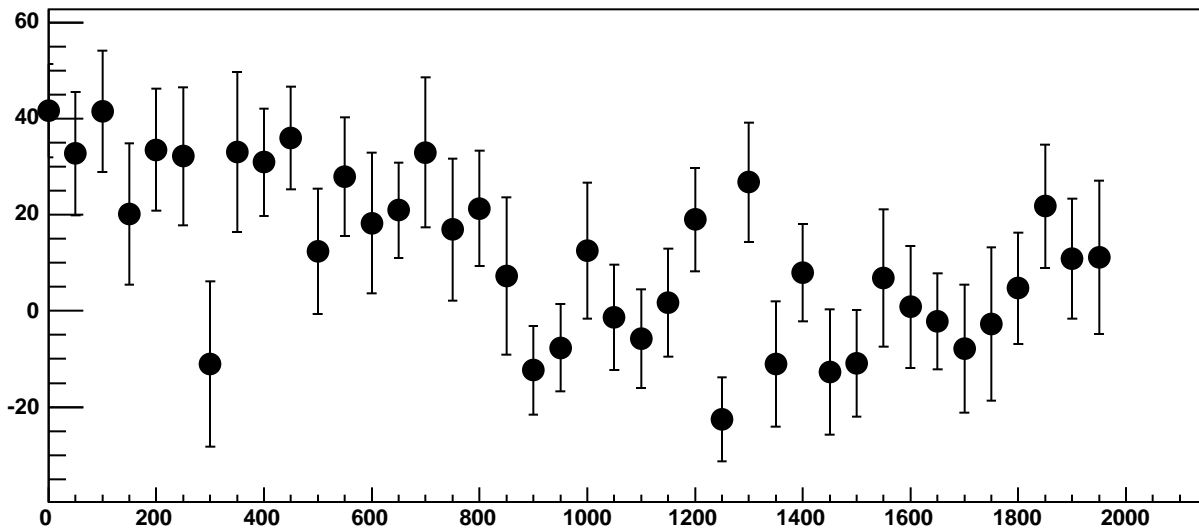
Chip 2, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC



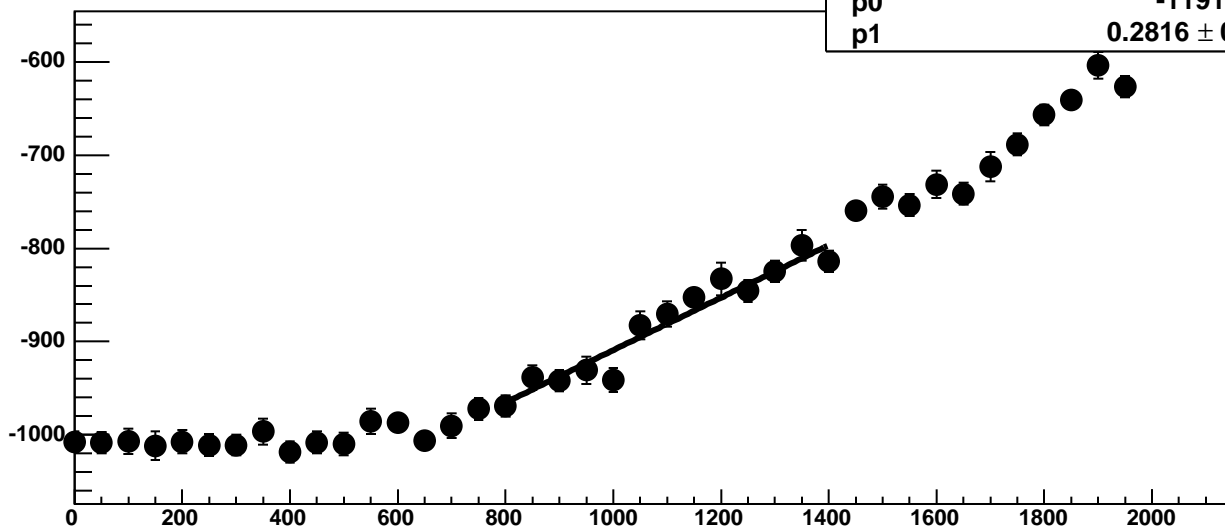
Chip 2, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC



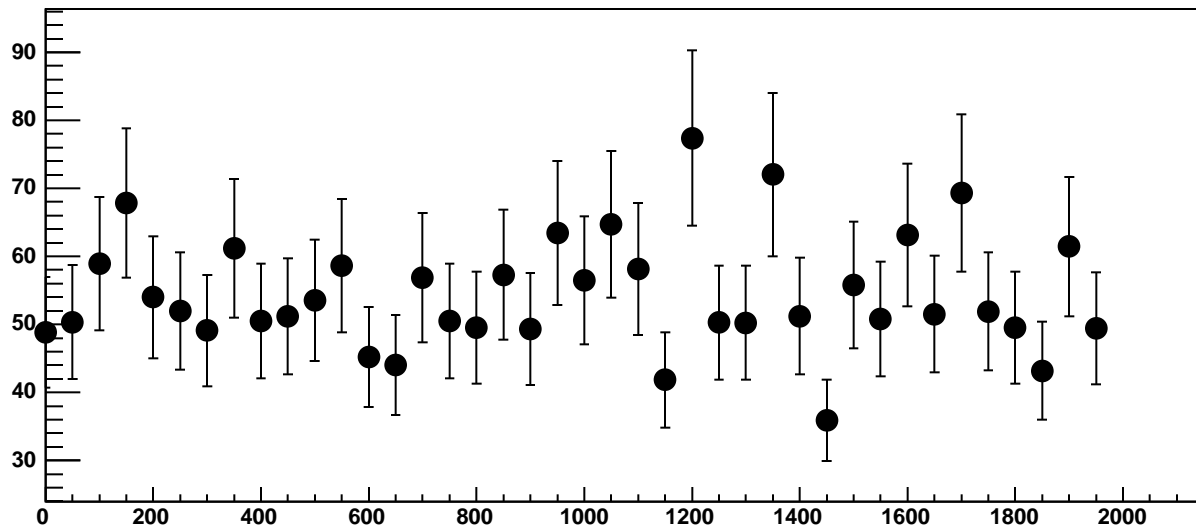
Chip 2, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC



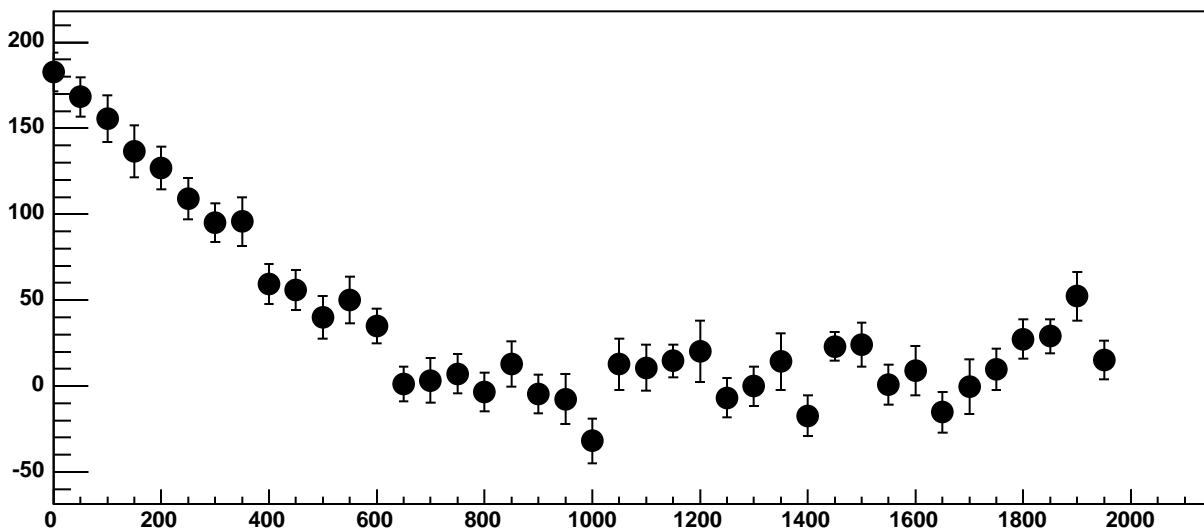
Chip 2, Channel 7, Enable 2, Hold=30, ADC Mean vs DAC



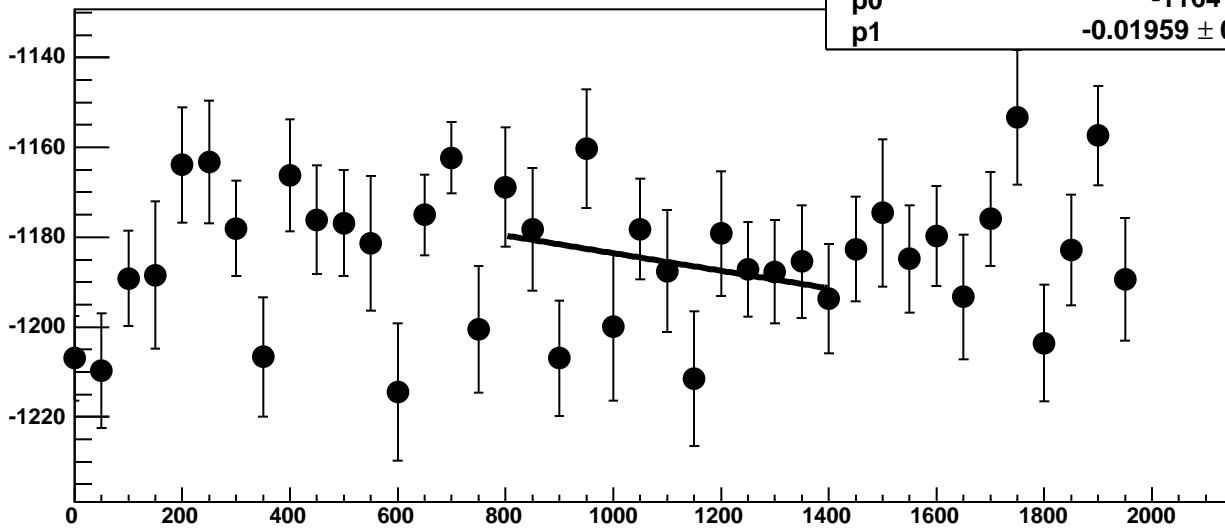
Chip 2, Channel 7, Enable 2, Hold=30, ADC Noise vs DAC



Chip 2, Channel 7, Enable 2, Hold=30, ADC Residuals vs DAC

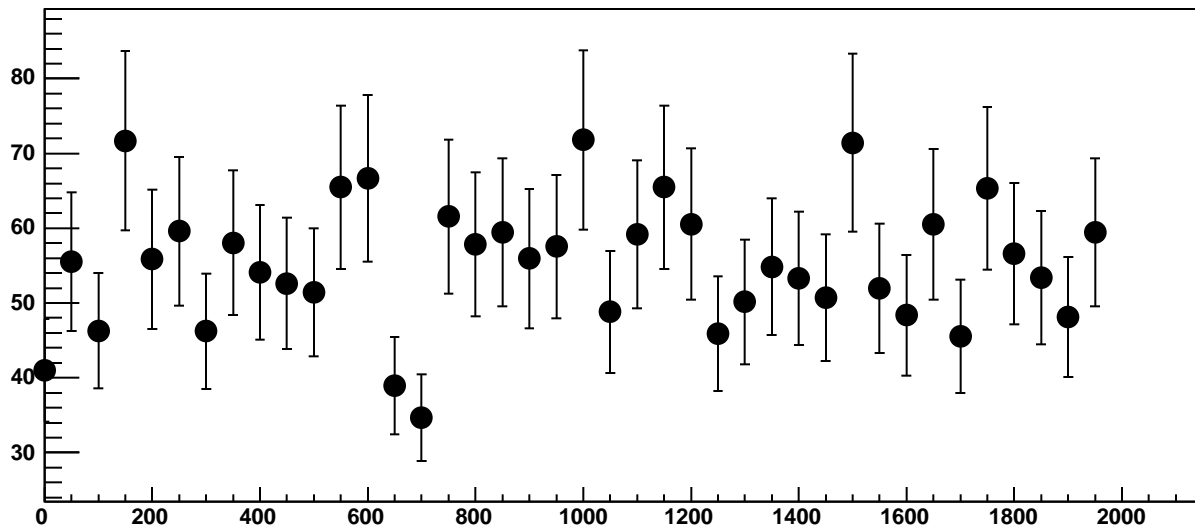


Chip 2, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC

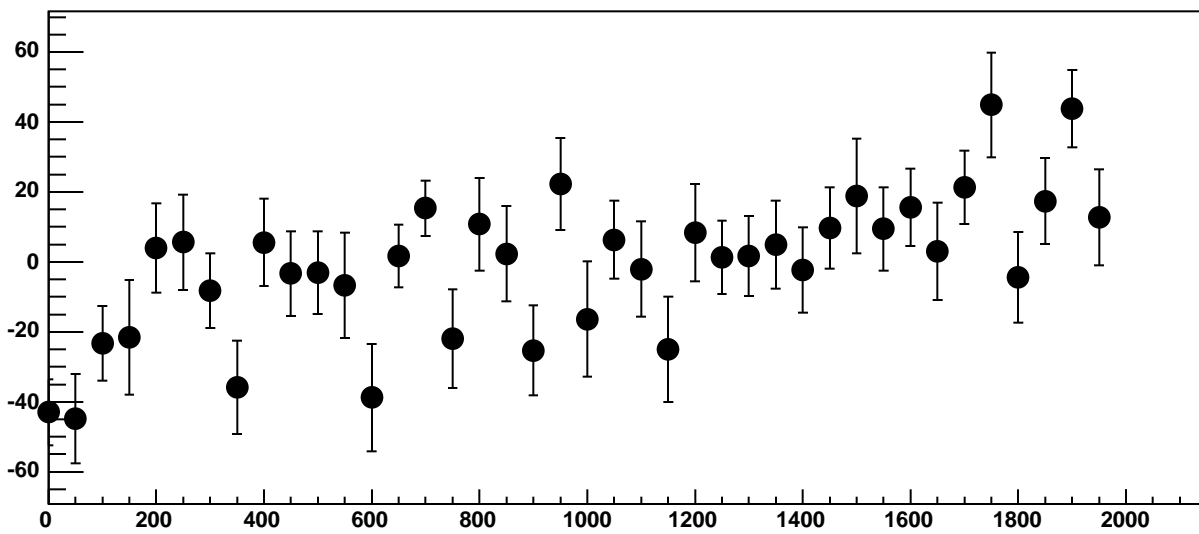


$\chi^2 / \text{ndf}$  12.11 / 11  
p0  $-1164 \pm 21.32$   
p1  $-0.01959 \pm 0.01883$

Chip 2, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC

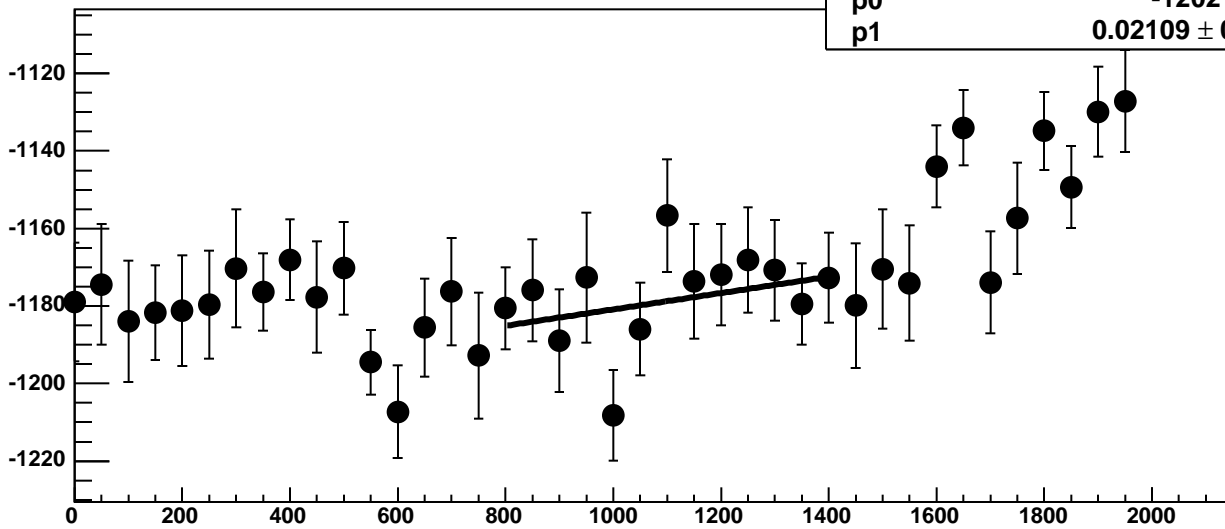


Chip 2, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC

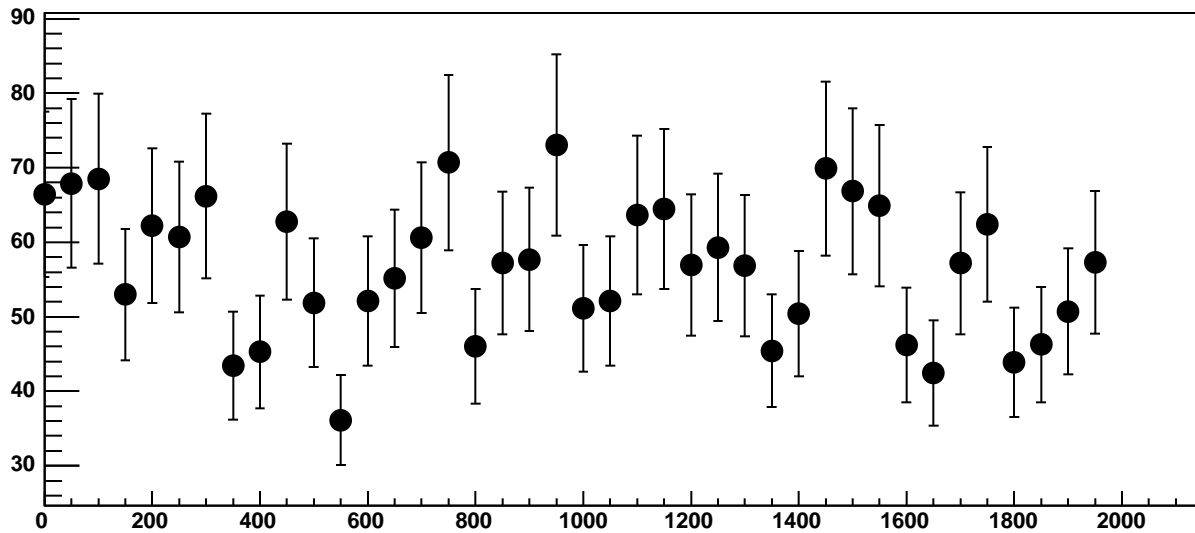


Chip 2, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC

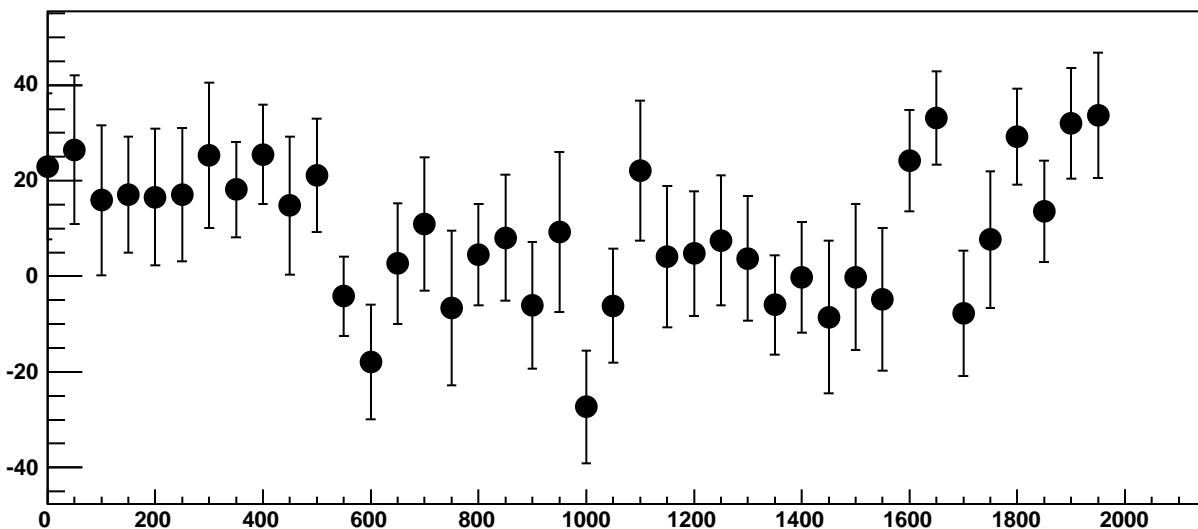
$\chi^2 / \text{ndf}$  9.992 / 11  
p0  $-1202 \pm 19.73$   
p1  $0.02109 \pm 0.01757$



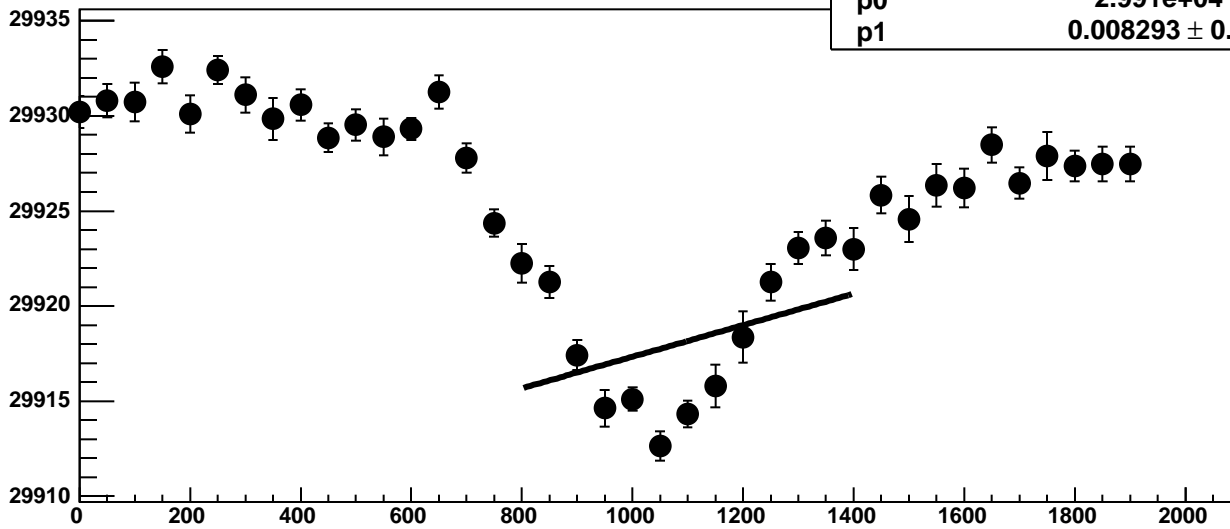
Chip 2, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC



Chip 2, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC



Chip 2, Channel 7, Enable 5!, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

218 / 11

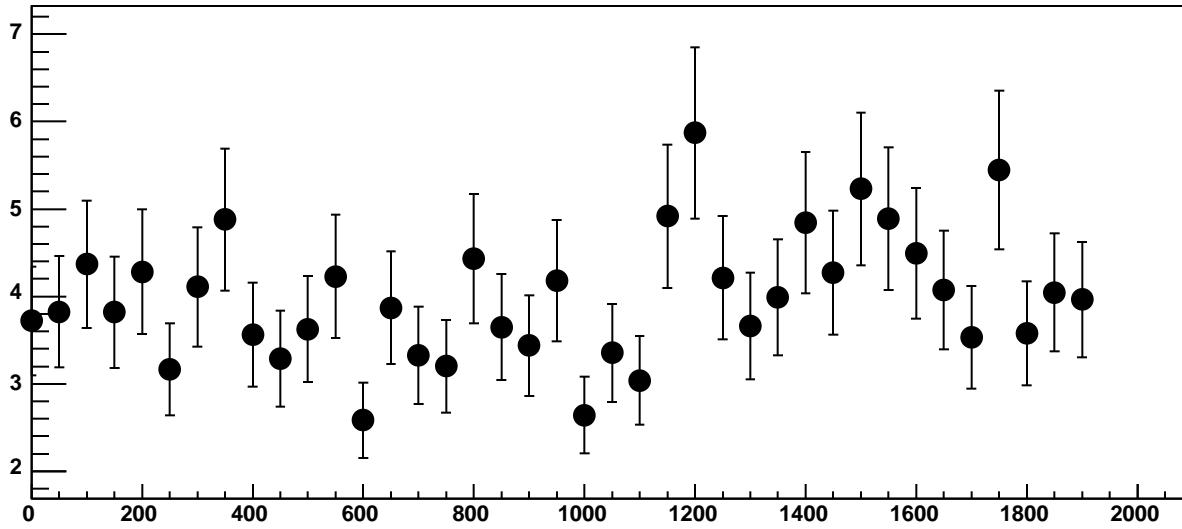
p0

$2.991\text{e}+04 \pm 1.508$

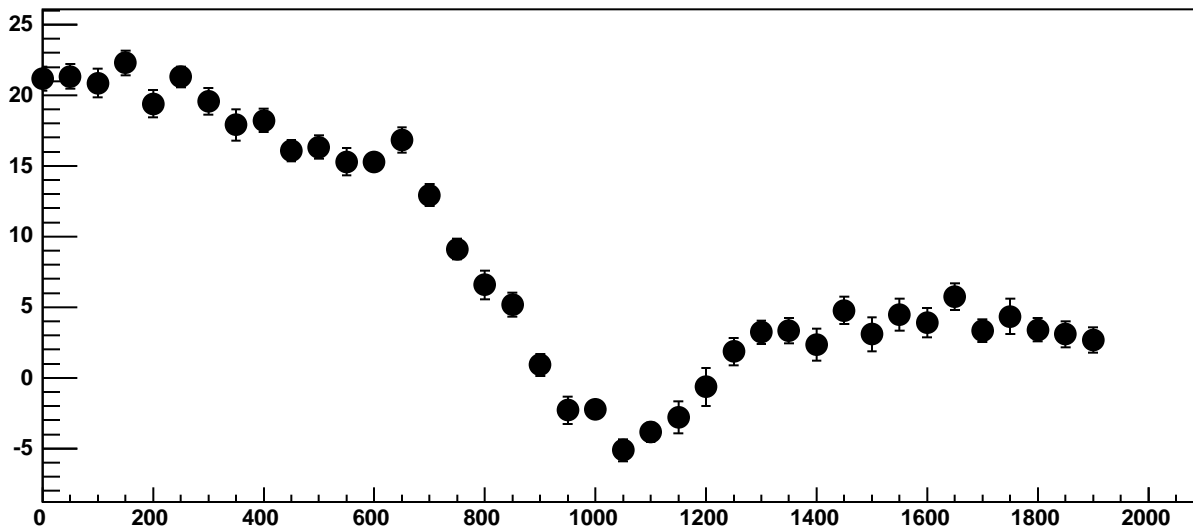
p1

$0.008293 \pm 0.001383$

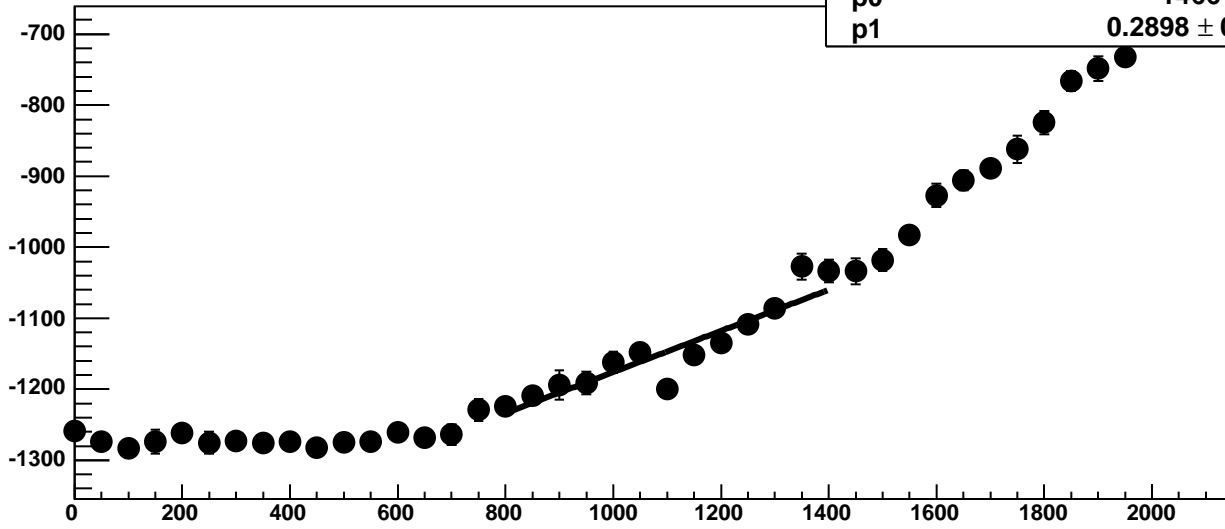
Chip 2, Channel 7, Enable 5!, Hold=30, ADC Noise vs DAC



Chip 2, Channel 7, Enable 5!, Hold=30, ADC Residuals vs DAC

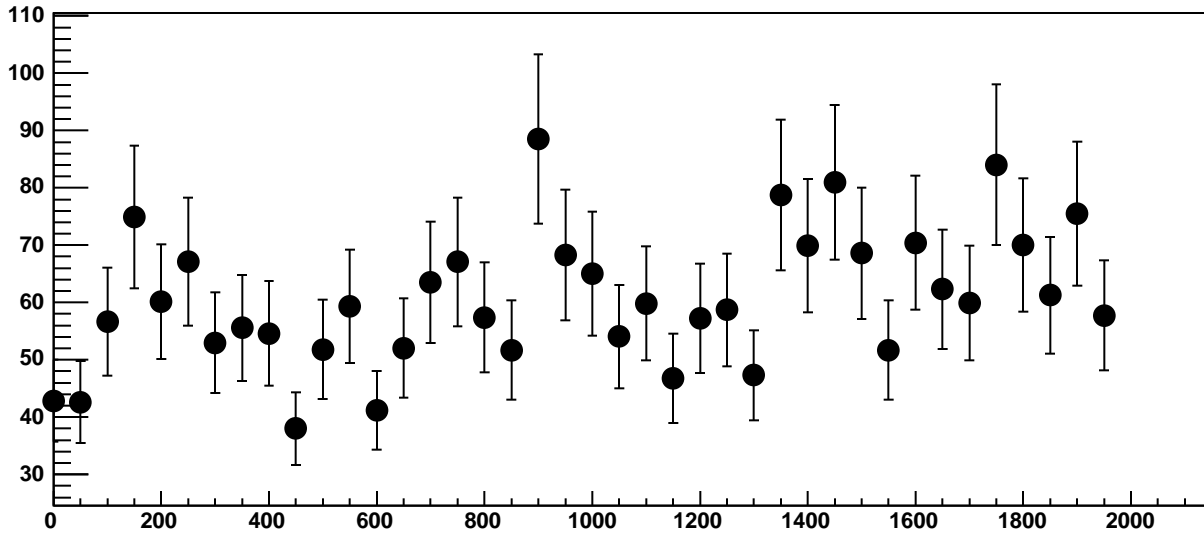


Chip 2, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC

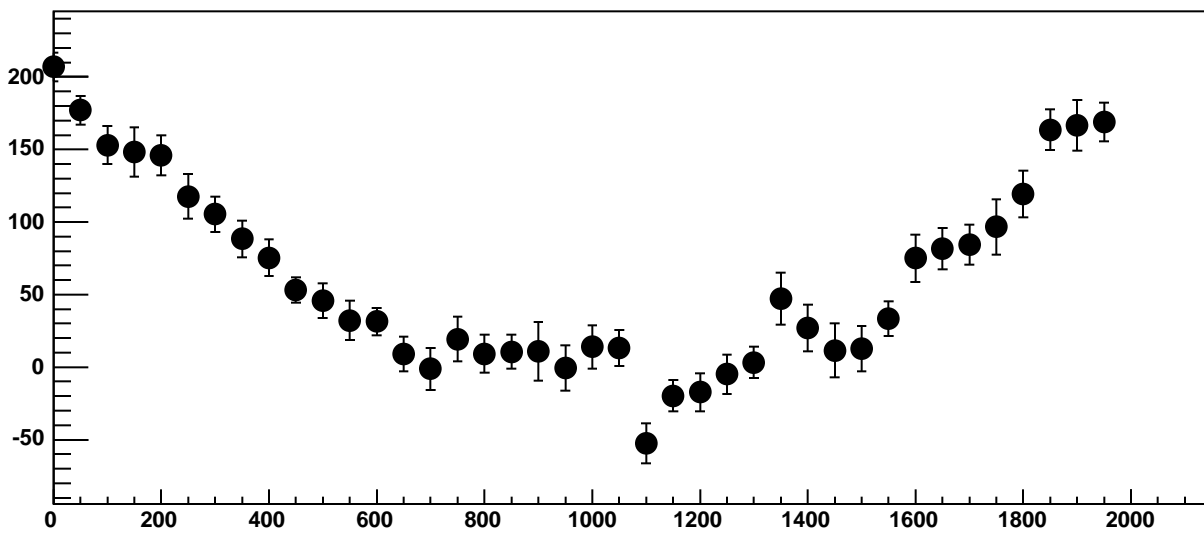


$\chi^2 / \text{ndf}$  33.08 / 11  
p0  $-1466 \pm 23.28$   
p1  $0.2898 \pm 0.02086$

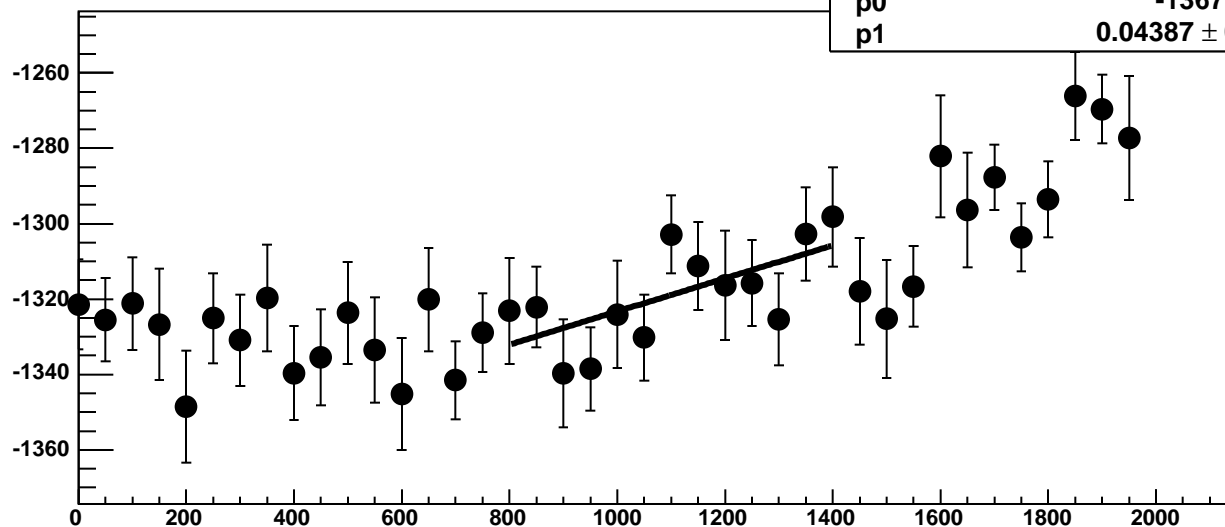
Chip 2, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC



Chip 2, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 2, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

8.487 / 11

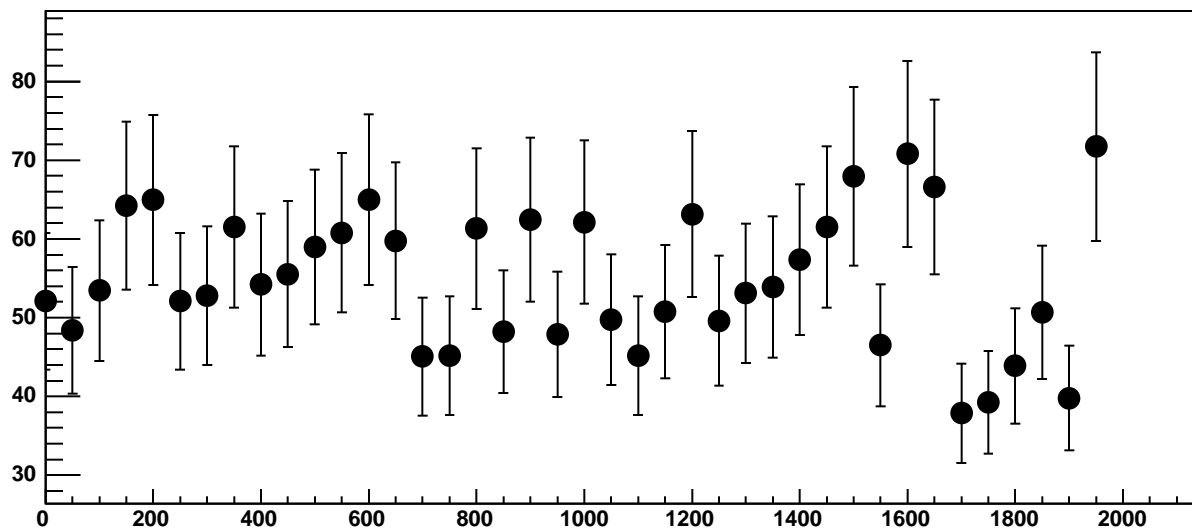
p0

$-1367 \pm 20.77$

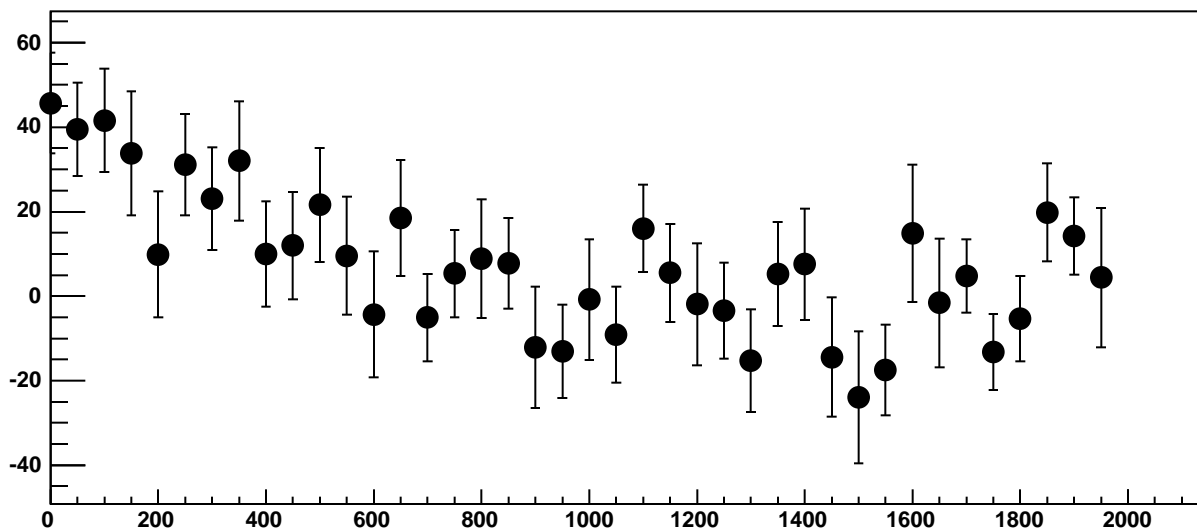
p1

$0.04387 \pm 0.01864$

Chip 2, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC

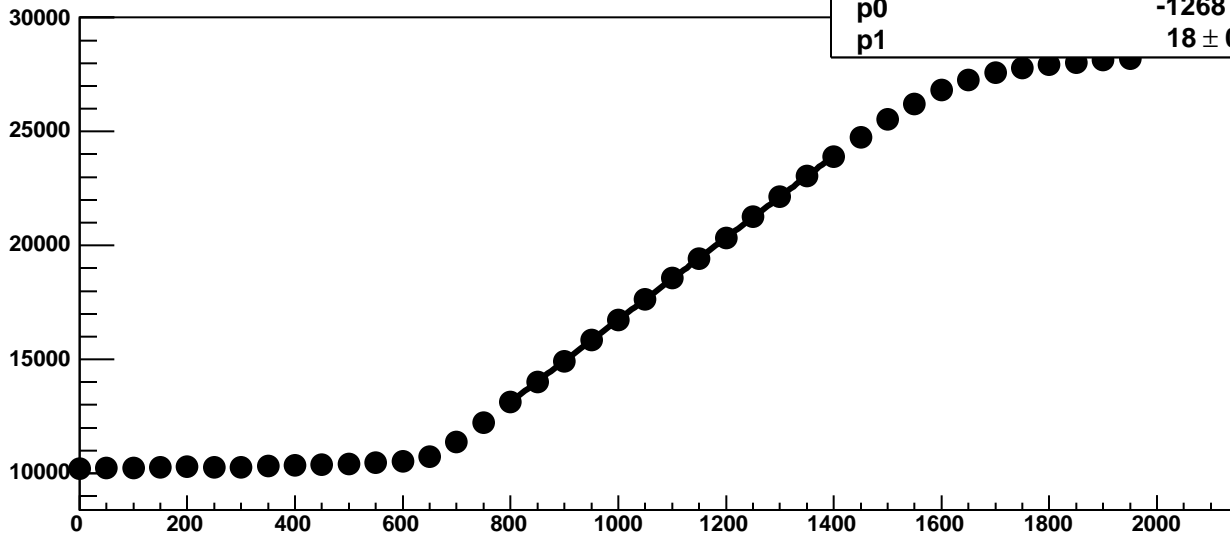


Chip 2, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC



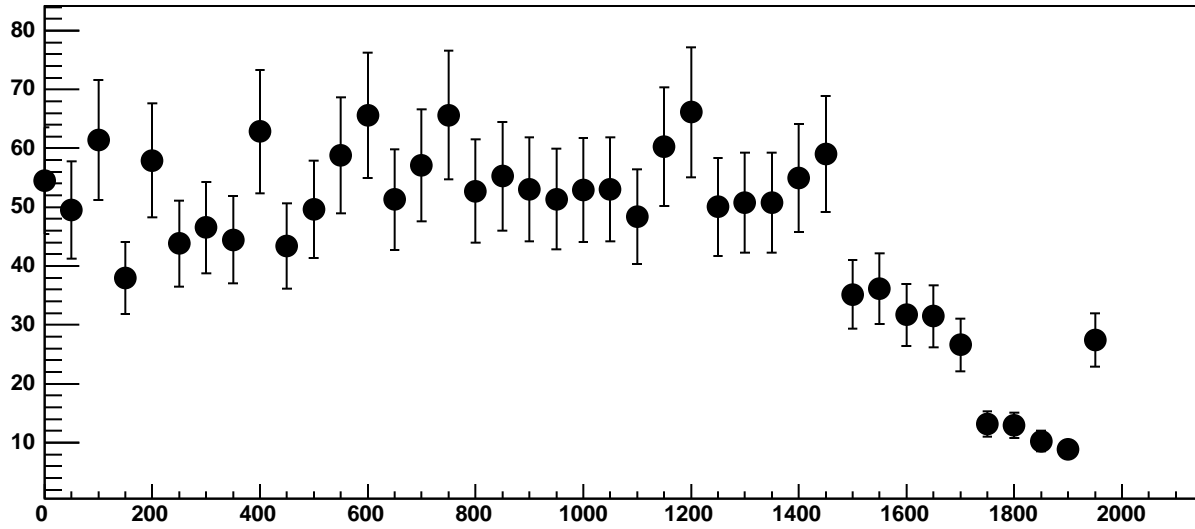


Chip 2, Channel 8, Enable 2!, Hold=30, ADC Mean vs DAC

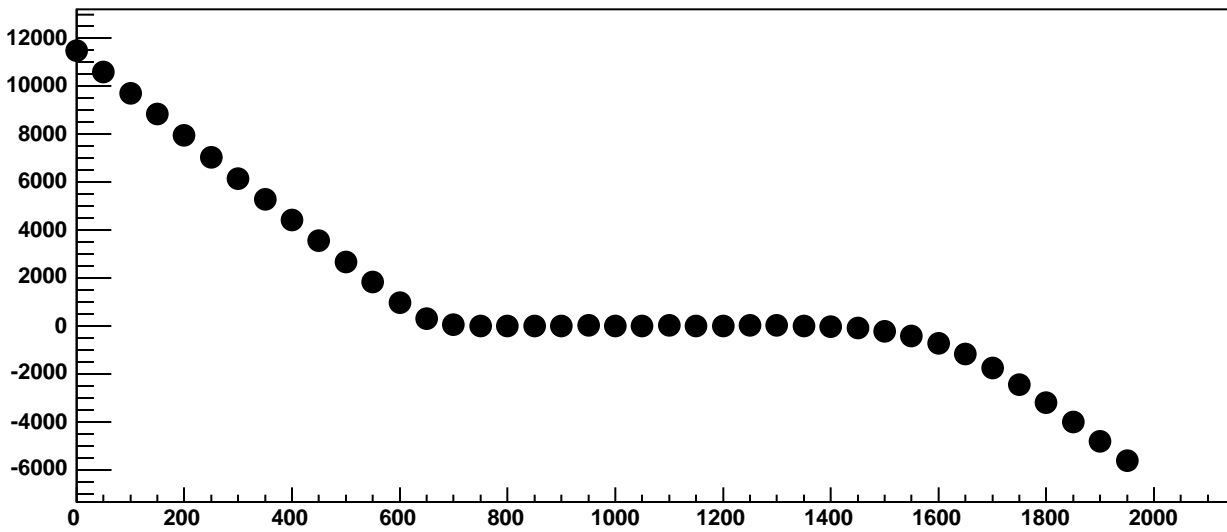


$\chi^2 / \text{ndf}$	25.74 / 11
p0	$-1268 \pm 20.14$
p1	$18 \pm 0.01805$

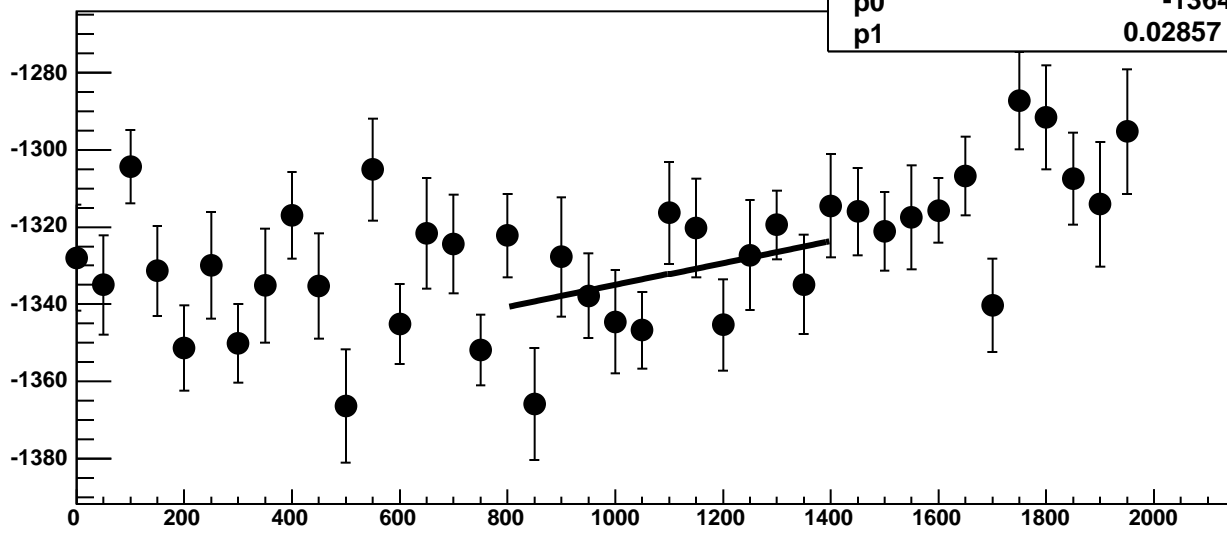
Chip 2, Channel 8, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 2, Channel 8, Enable 2!, Hold=30, ADC Residuals vs DAC

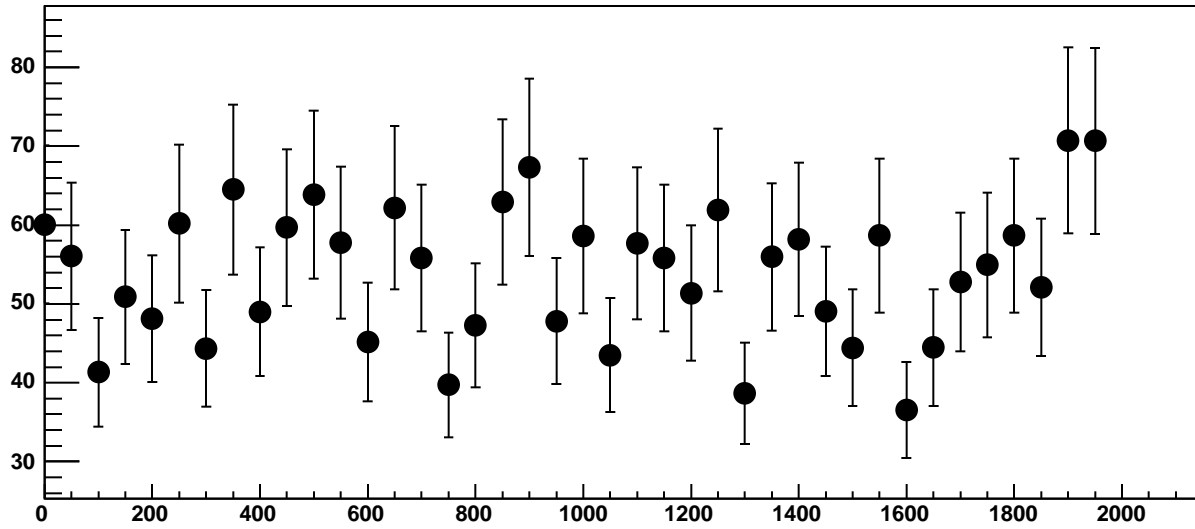


Chip 2, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC

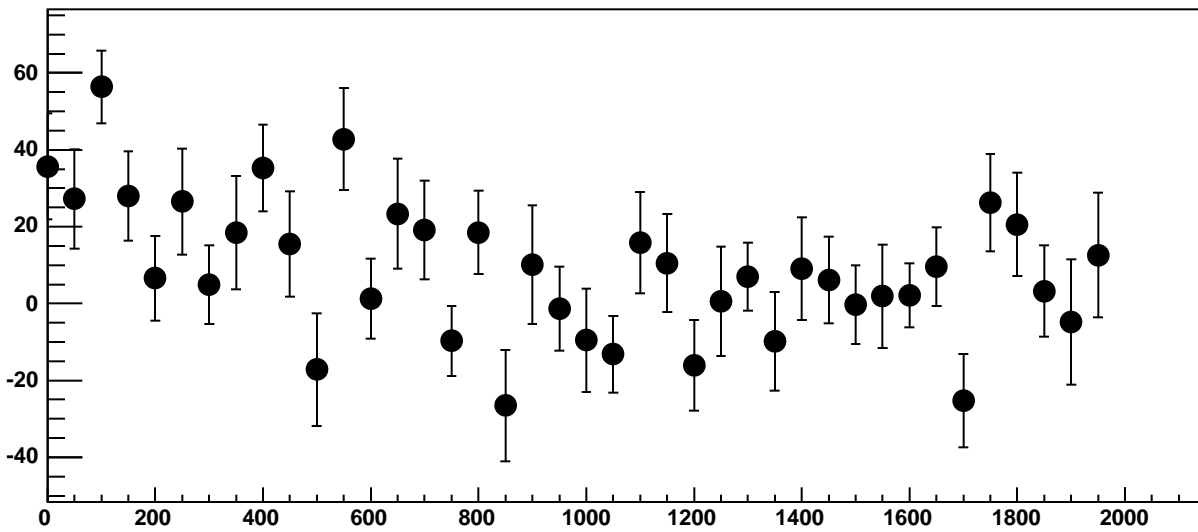


$\chi^2 / \text{ndf}$  14.62 / 11  
p0  $-1364 \pm 20.2$   
p1  $0.02857 \pm 0.018$

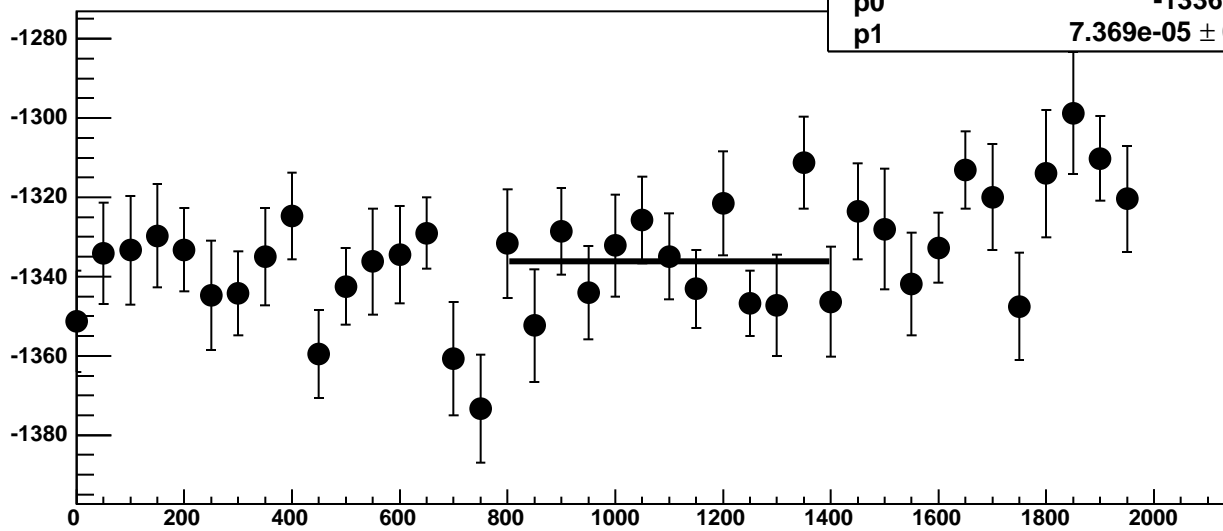
Chip 2, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC



Chip 2, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC

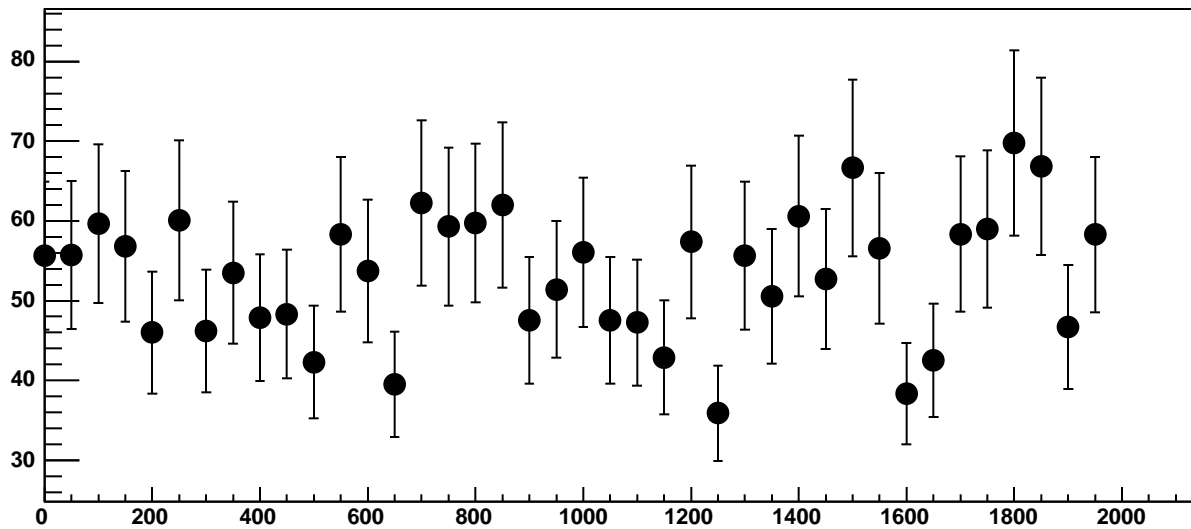


Chip 2, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC

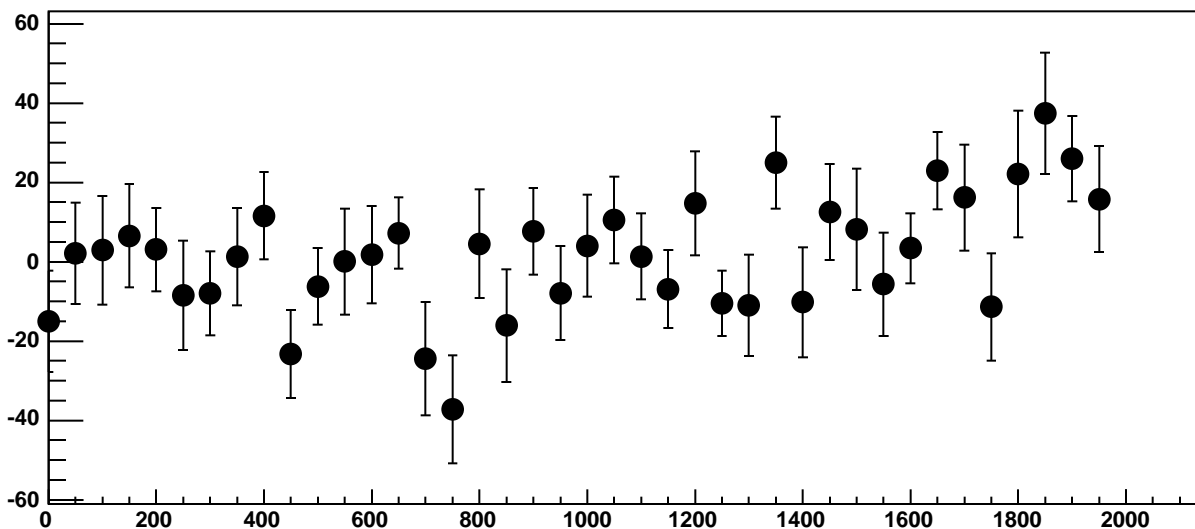


$\chi^2 / \text{ndf}$  12.65 / 11  
p0  $-1336 \pm 20.78$   
p1  $7.369\text{e-}05 \pm 0.01844$

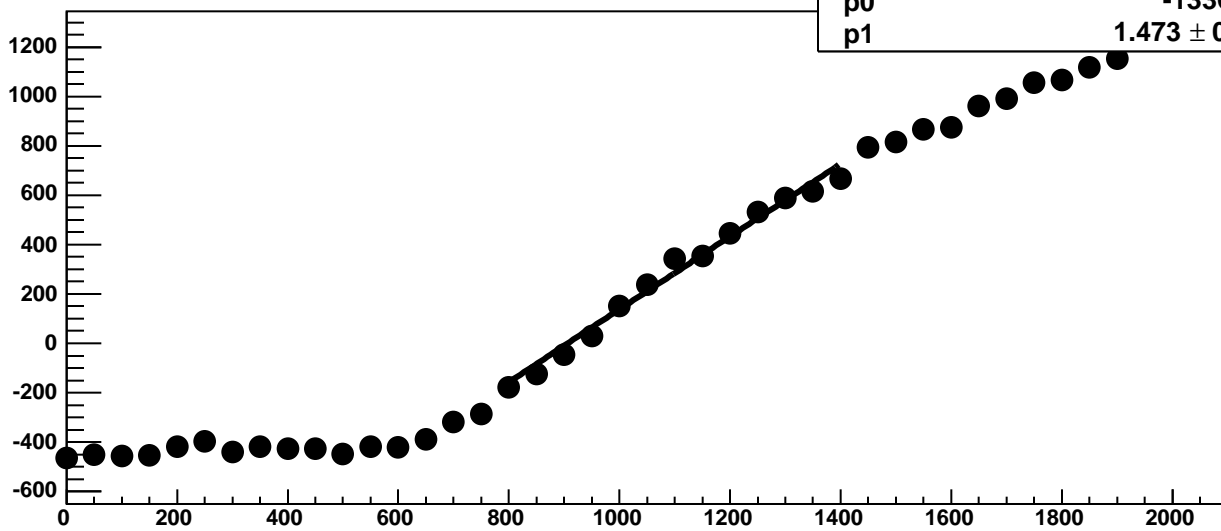
Chip 2, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC



Chip 2, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC

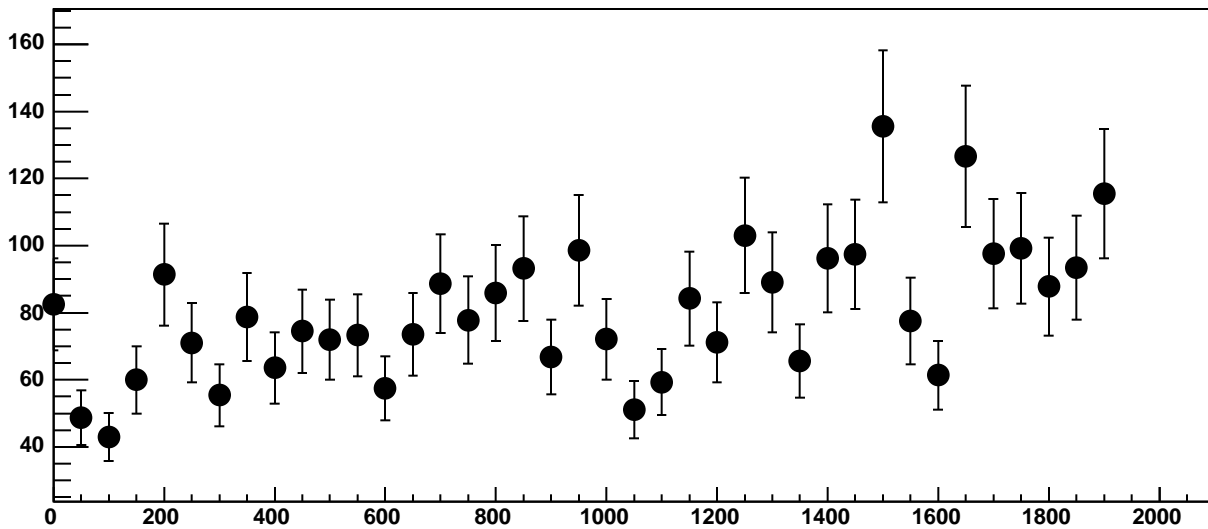


Chip 2, Channel 8, Enable 5, Hold=30, ADC Mean vs DAC

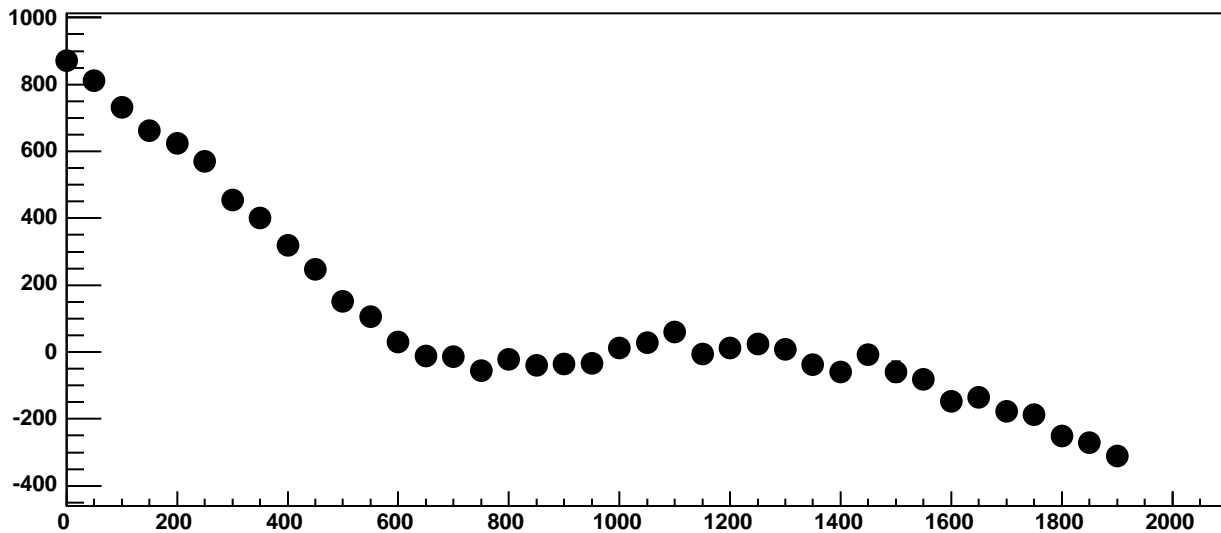


$\chi^2 / \text{ndf}$  52.73 / 11  
p0 -1336 ± 31  
p1 1.473 ± 0.02797

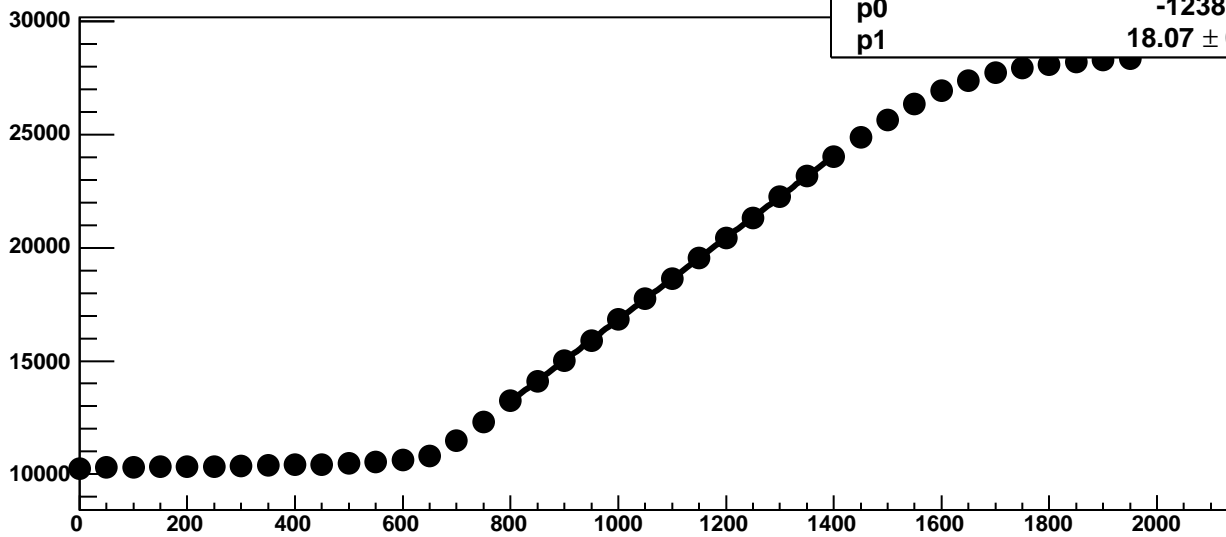
Chip 2, Channel 8, Enable 5, Hold=30, ADC Noise vs DAC



Chip 2, Channel 8, Enable 5, Hold=30, ADC Residuals vs DAC

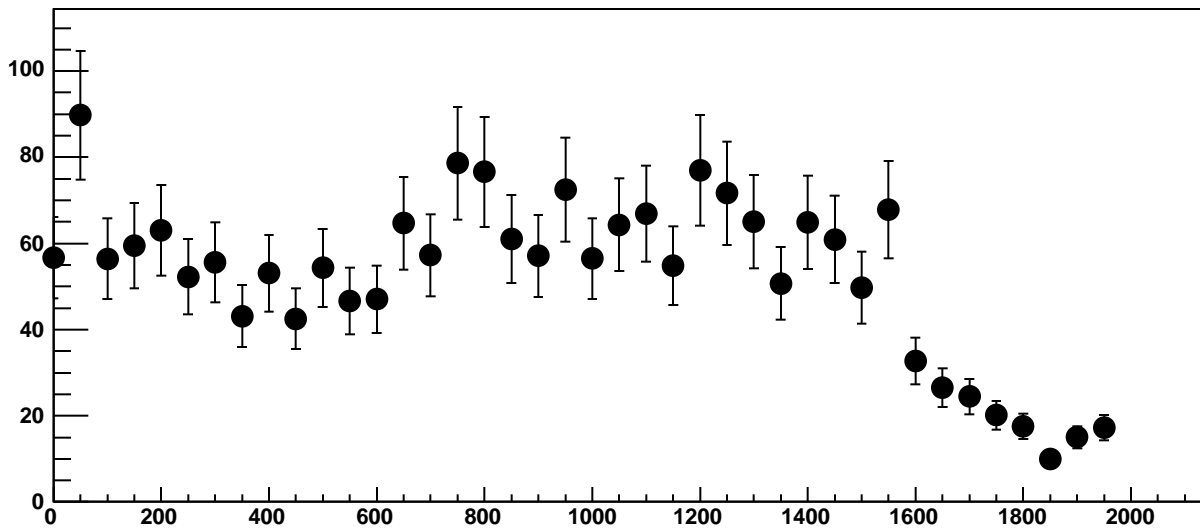


Chip 2, Channel 9, Enable 0!, Hold=30, ADC Mean vs DAC

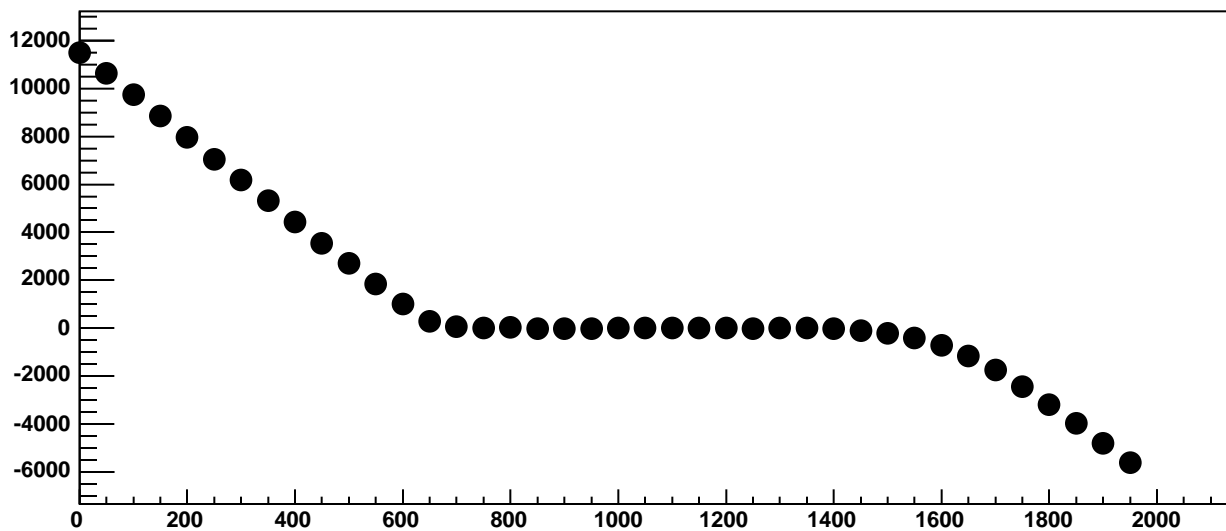


$\chi^2 / \text{ndf}$  19.46 / 11  
p0  $-1238 \pm 24.13$   
p1  $18.07 \pm 0.02148$

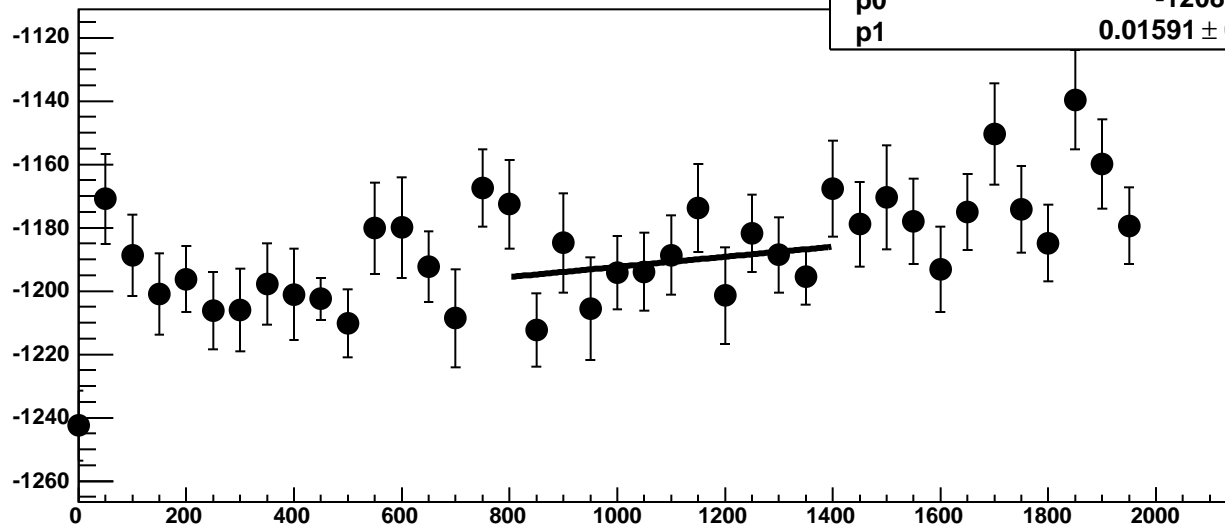
Chip 2, Channel 9, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 2, Channel 9, Enable 0!, Hold=30, ADC Residuals vs DAC

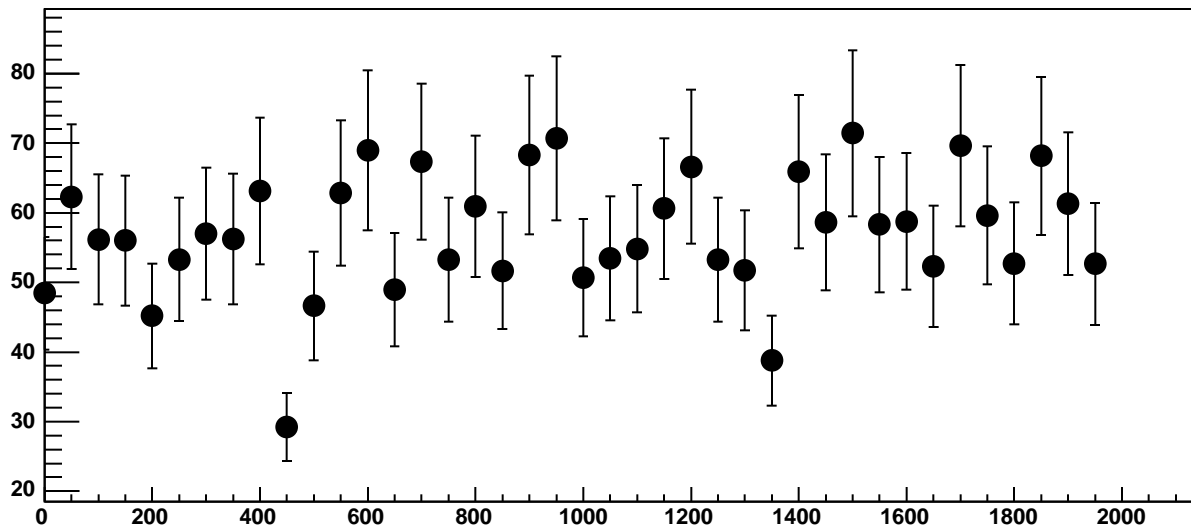


Chip 2, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC

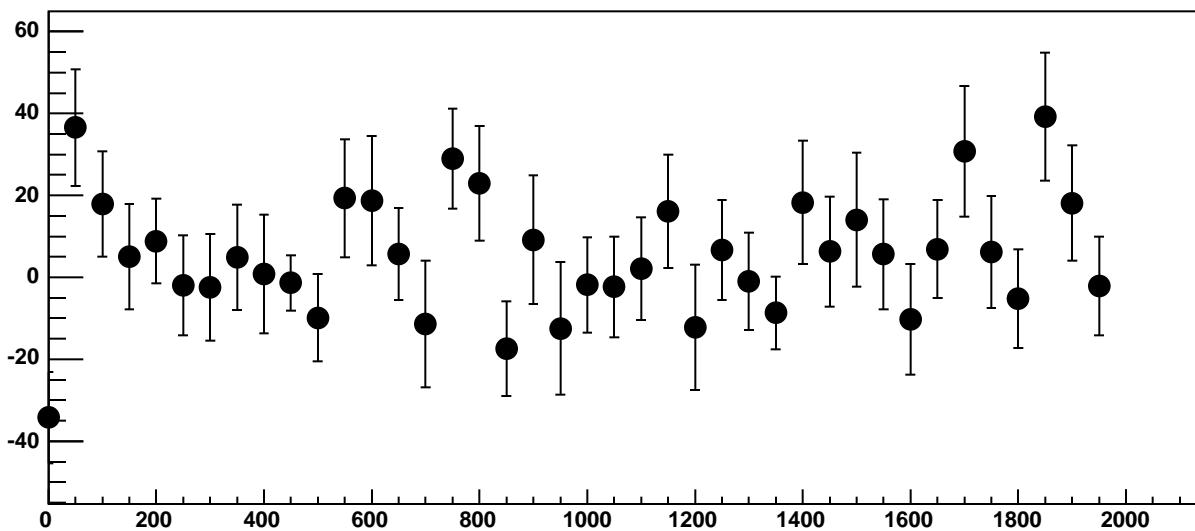


$\chi^2 / \text{ndf}$  10.73 / 11  
p0  $-1208 \pm 21.09$   
p1  $0.01591 \pm 0.01855$

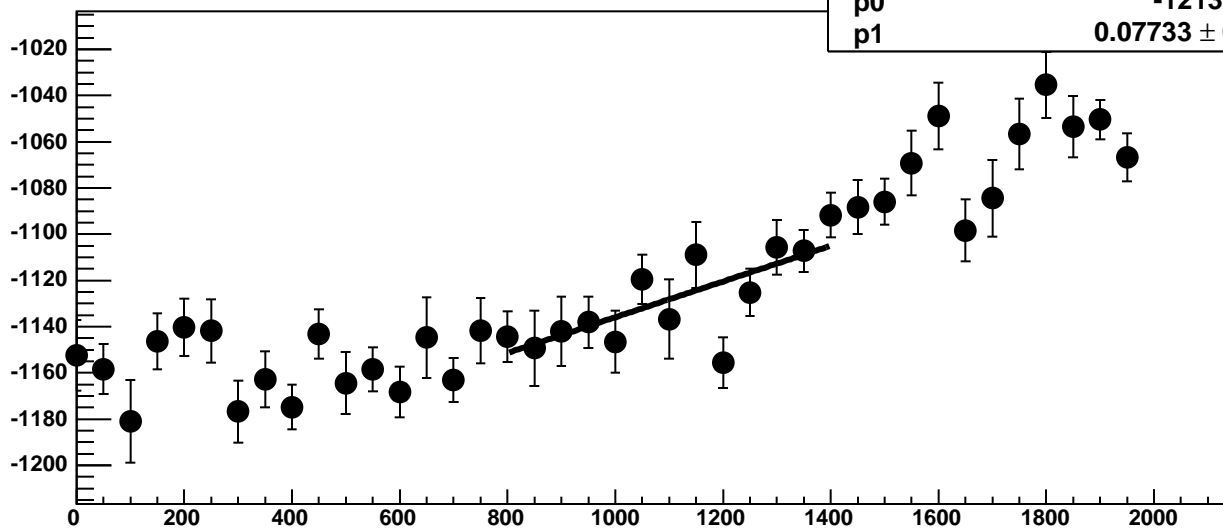
Chip 2, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC



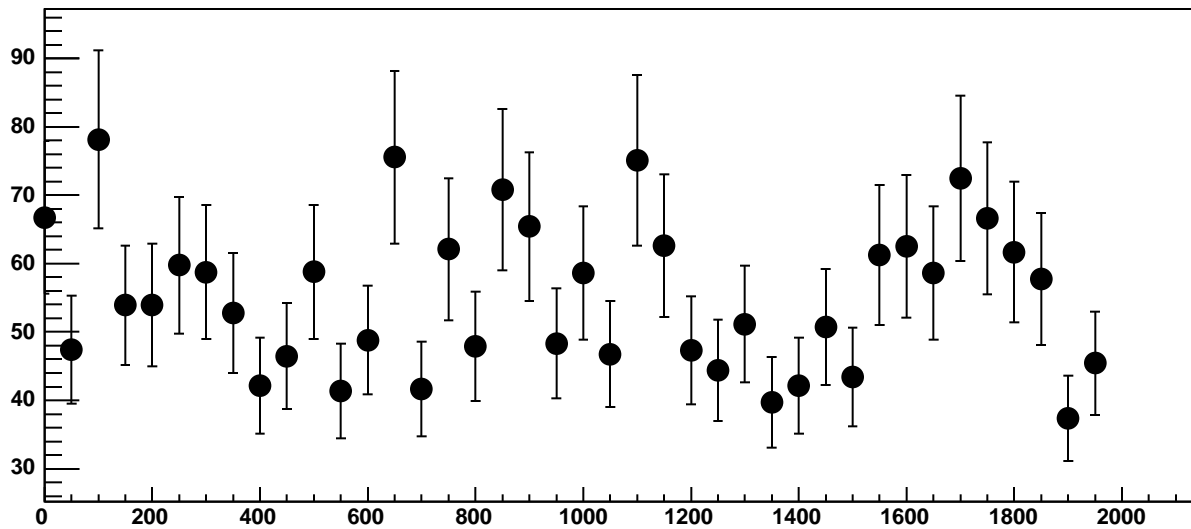
Chip 2, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC



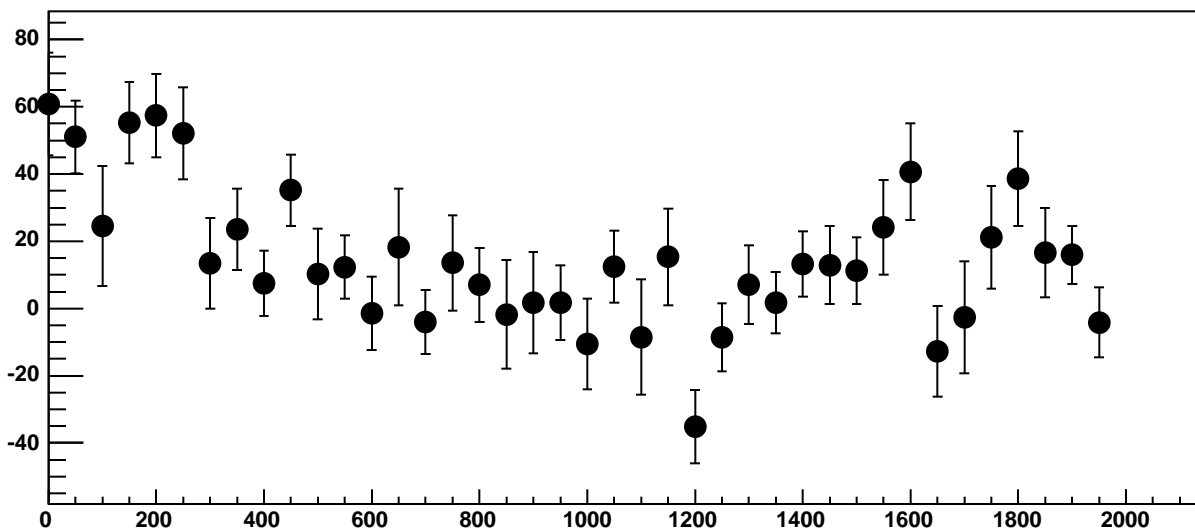
Chip 2, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC



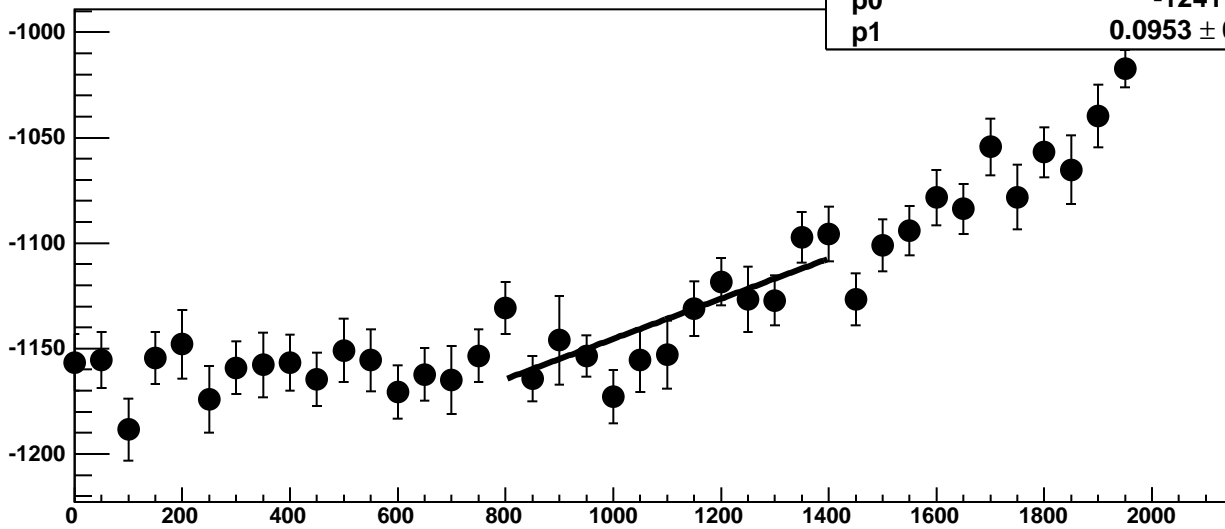
Chip 2, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC



Chip 2, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 2, Channel 9, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

18.36 / 11

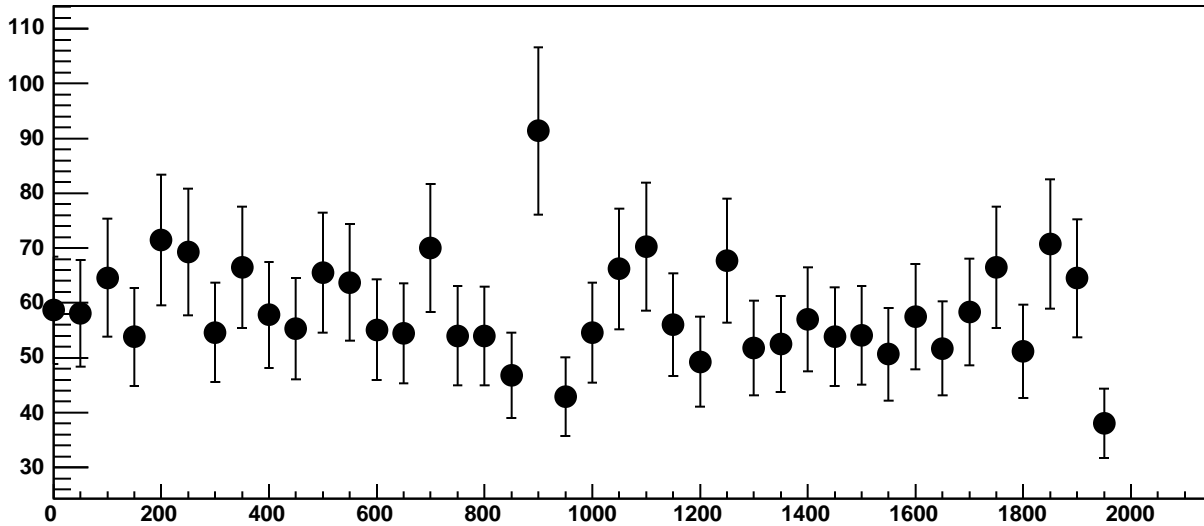
p0

$-1241 \pm 20.46$

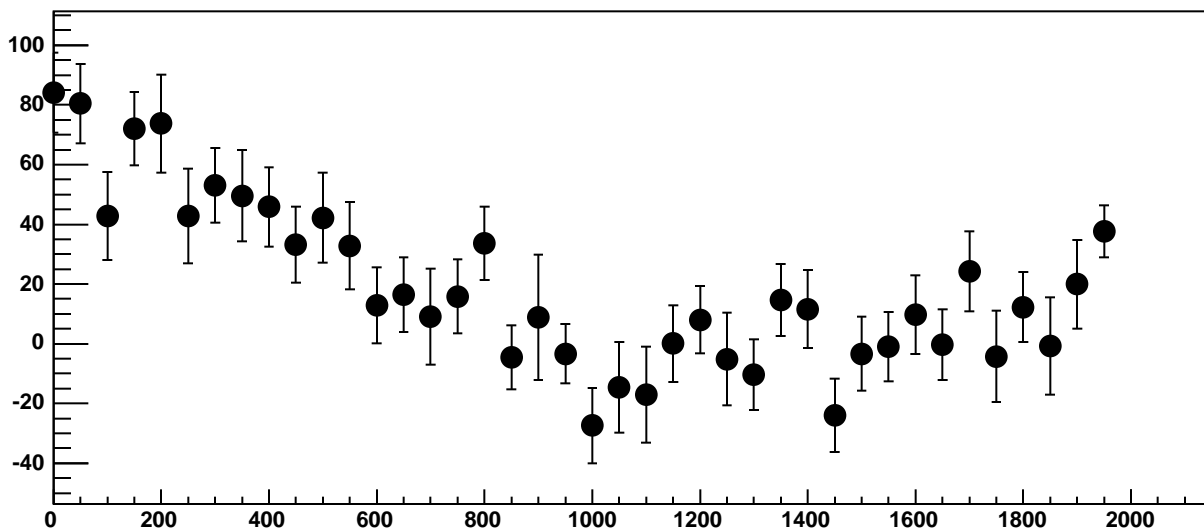
p1

$0.0953 \pm 0.01841$

Chip 2, Channel 9, Enable 3, Hold=30, ADC Noise vs DAC

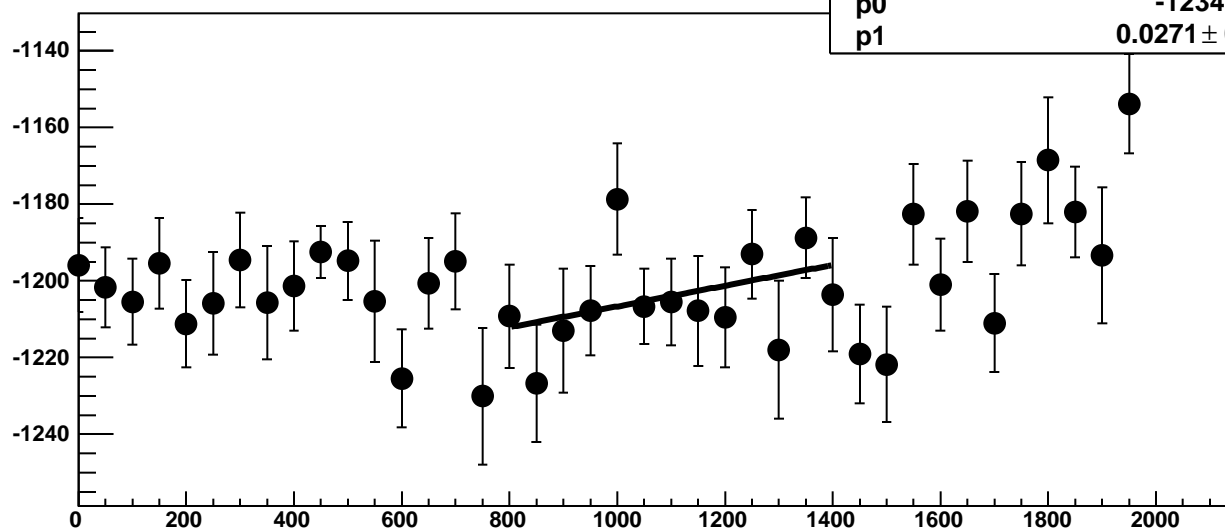


Chip 2, Channel 9, Enable 3, Hold=30, ADC Residuals vs DAC

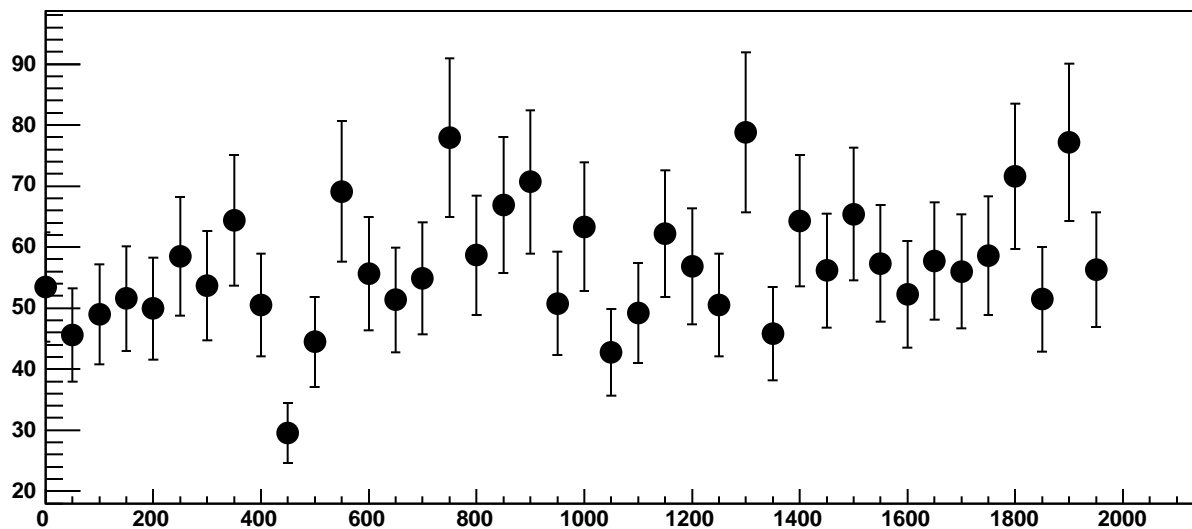




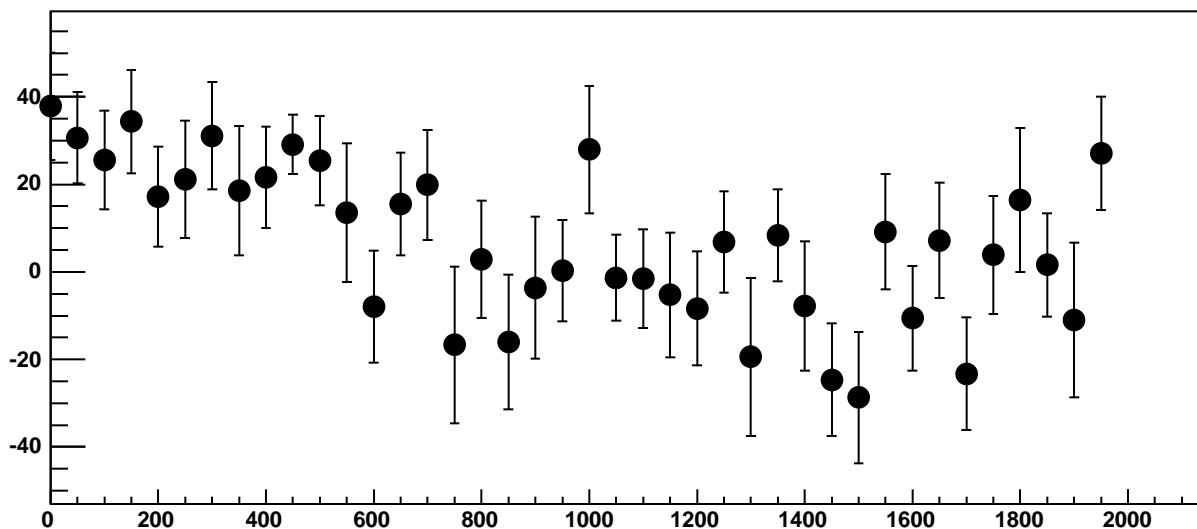
Chip 2, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC



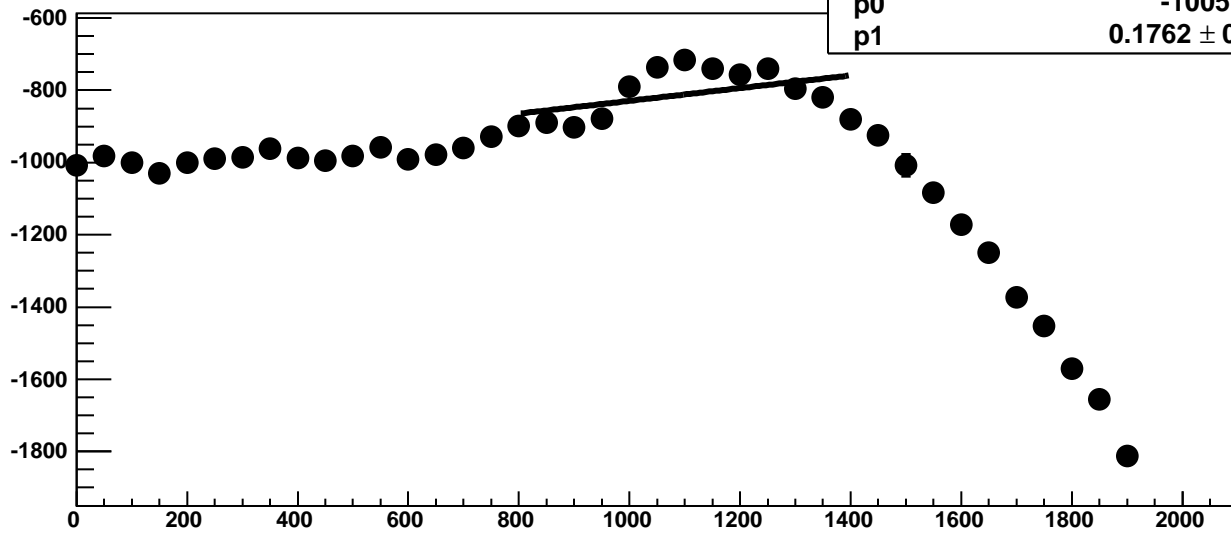
Chip 2, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC



Chip 2, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC



Chip 2, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

158.3 / 11

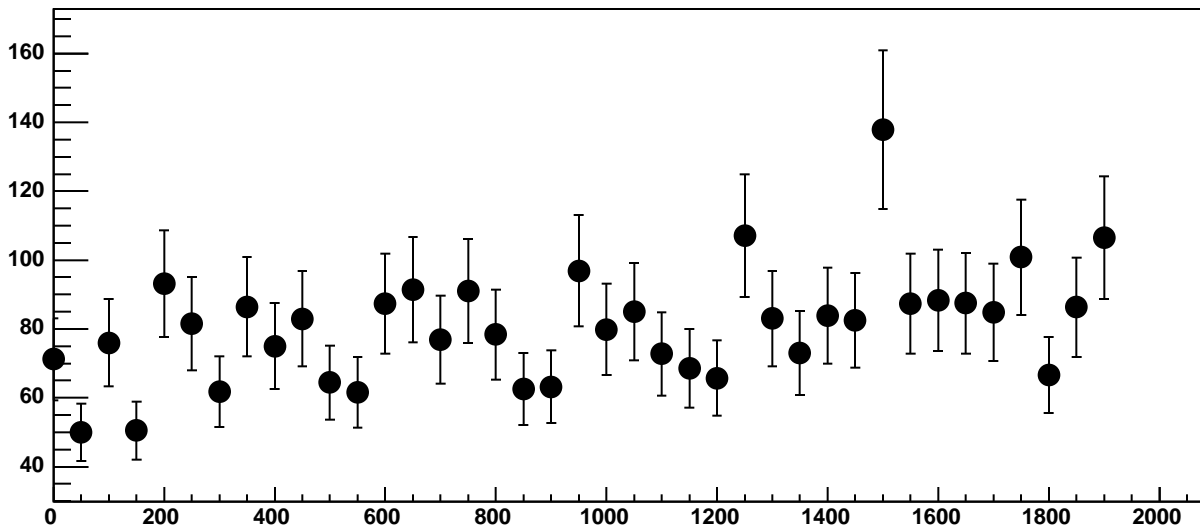
p0

$-1005 \pm 28.3$

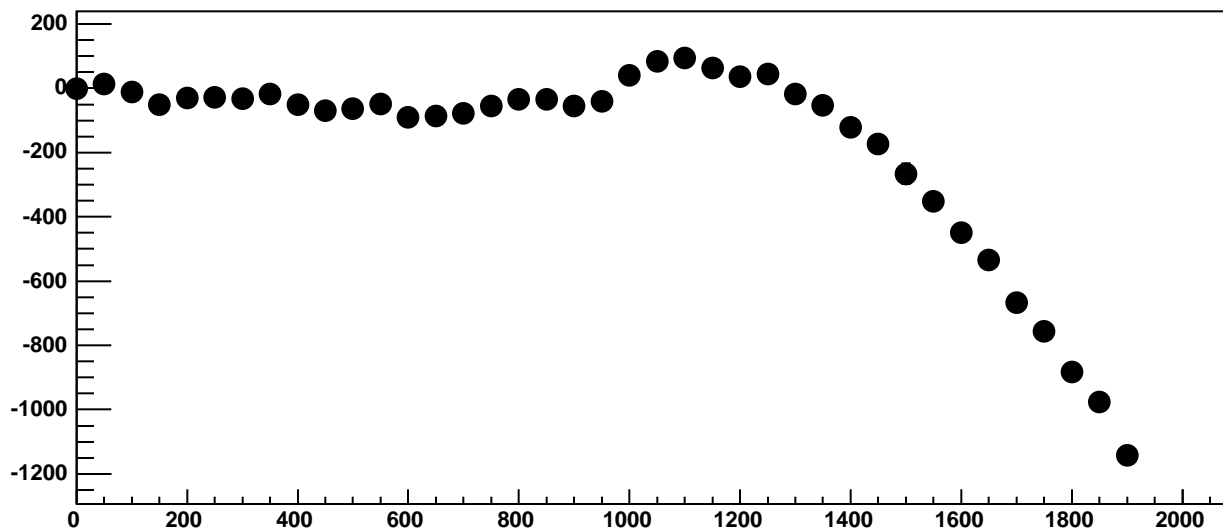
p1

$0.1762 \pm 0.02573$

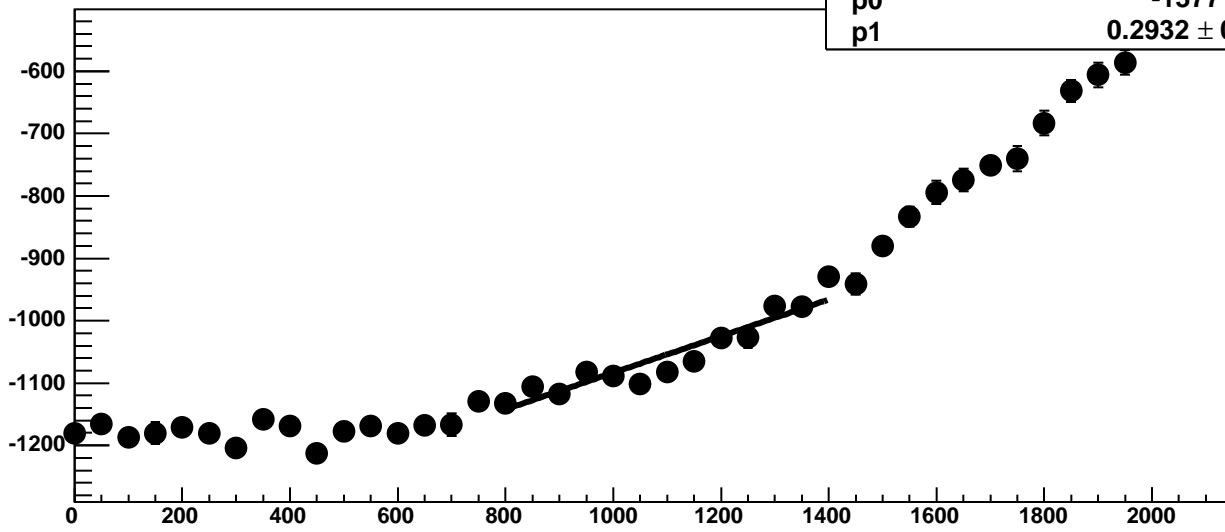
Chip 2, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC



Chip 2, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC

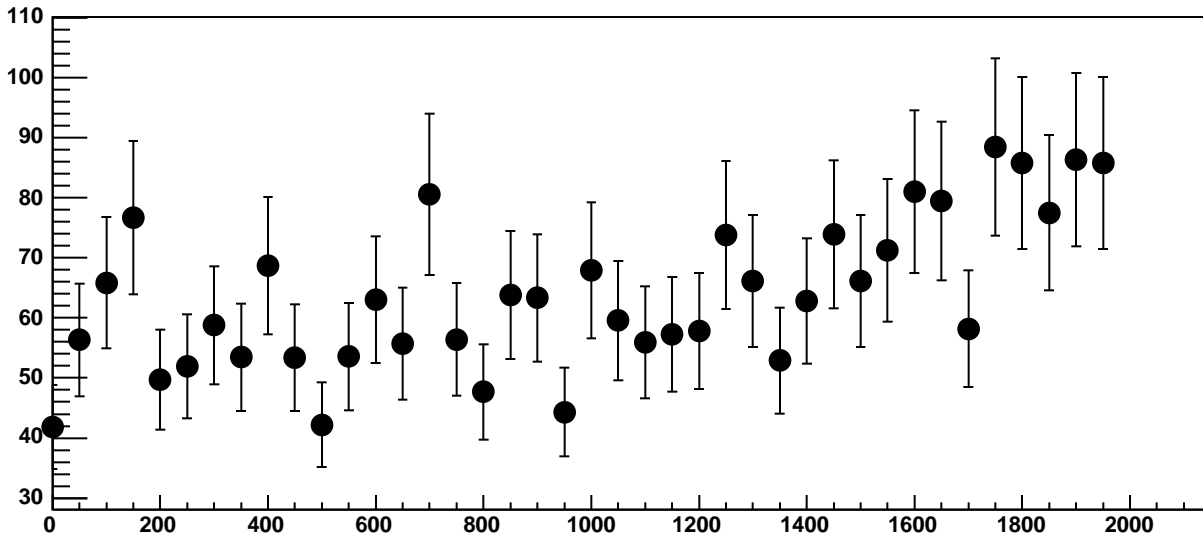


Chip 2, Channel 10, Enable 0, Hold=30, ADC Mean vs DAC

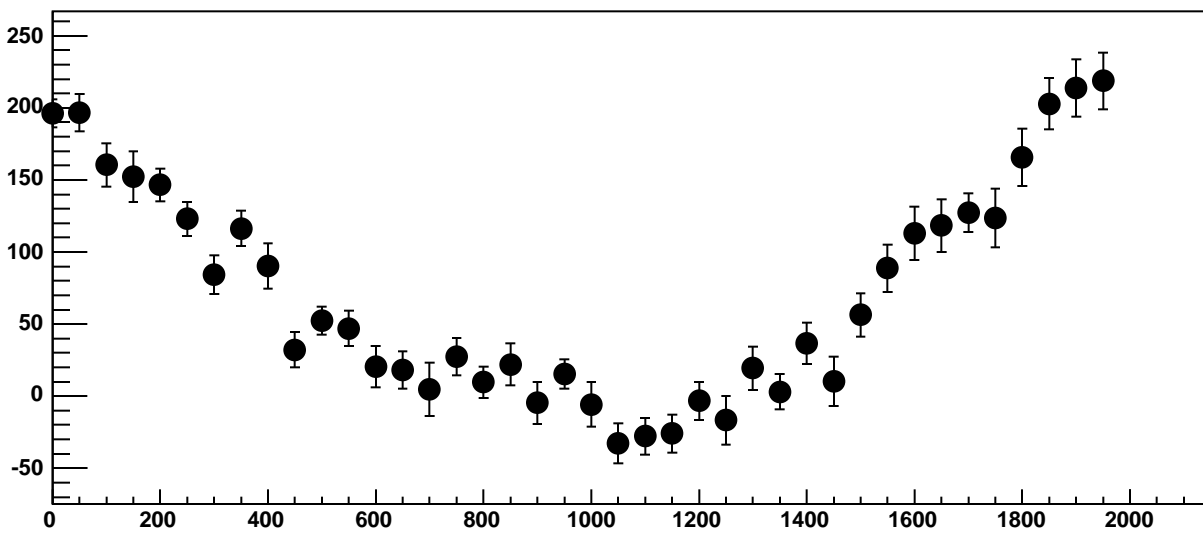


$\chi^2 / \text{ndf}$  29.17 / 11  
p0  $-1377 \pm 21.28$   
p1  $0.2932 \pm 0.01938$

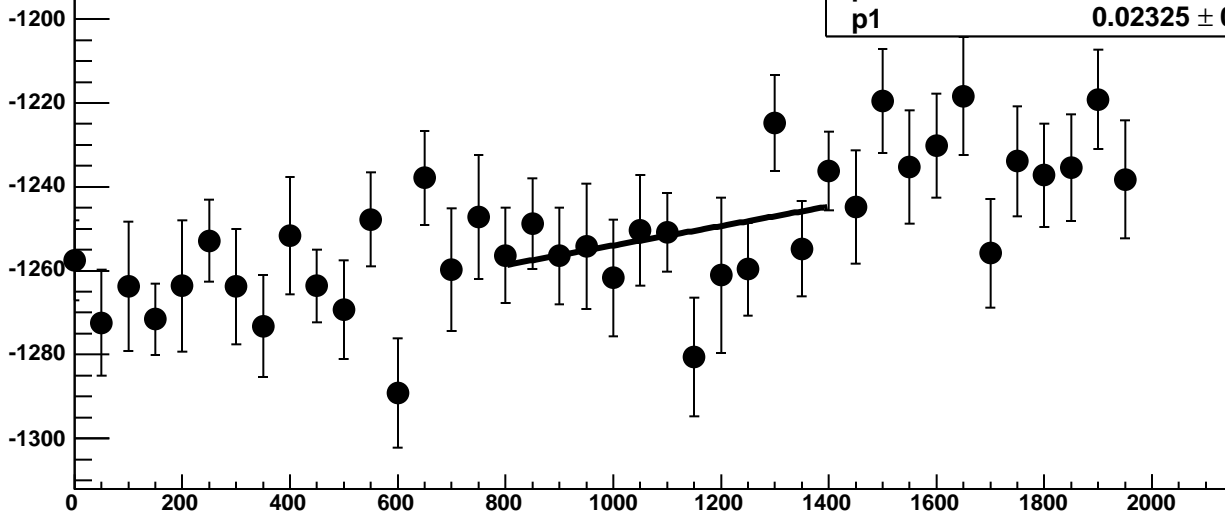
Chip 2, Channel 10, Enable 0, Hold=30, ADC Noise vs DAC



Chip 2, Channel 10, Enable 0, Hold=30, ADC Residuals vs DAC

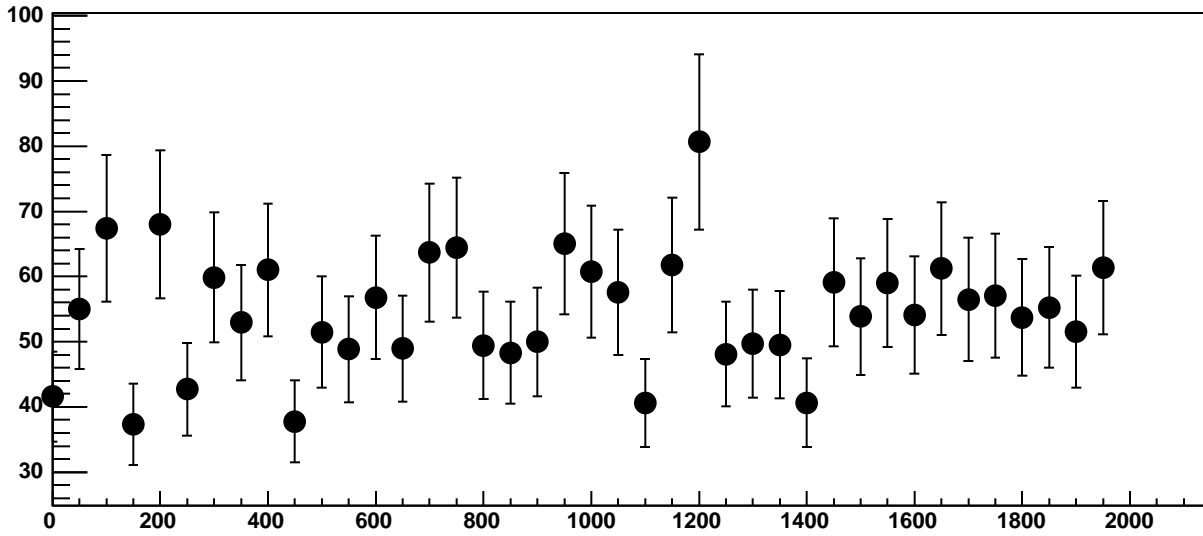


Chip 2, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC

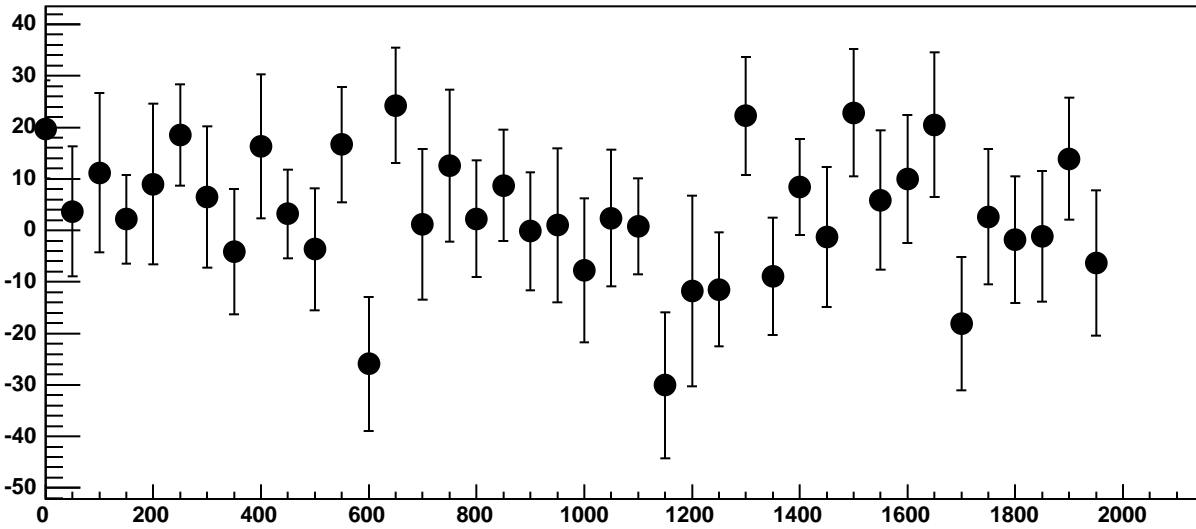


$\chi^2 / \text{ndf}$  12.26 / 11  
p0  $-1277 \pm 18.48$   
p1  $0.02325 \pm 0.01634$

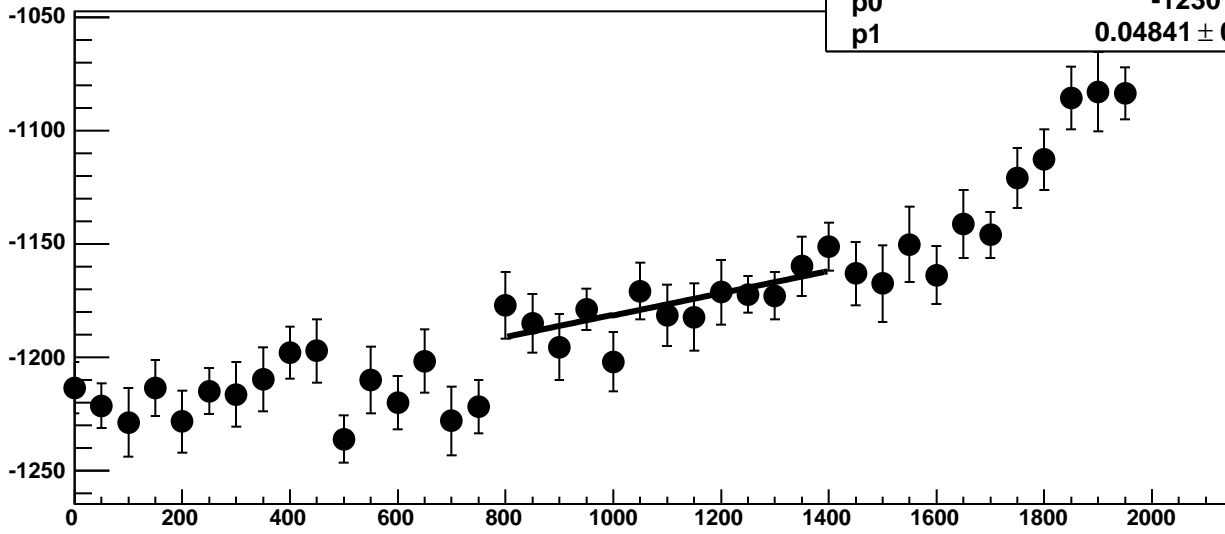
Chip 2, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC



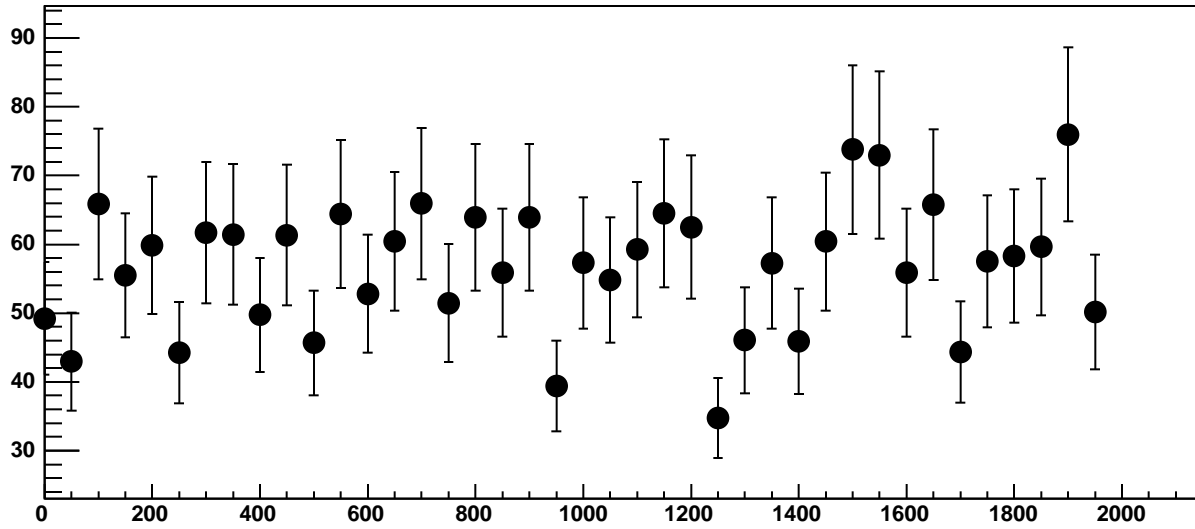
Chip 2, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC



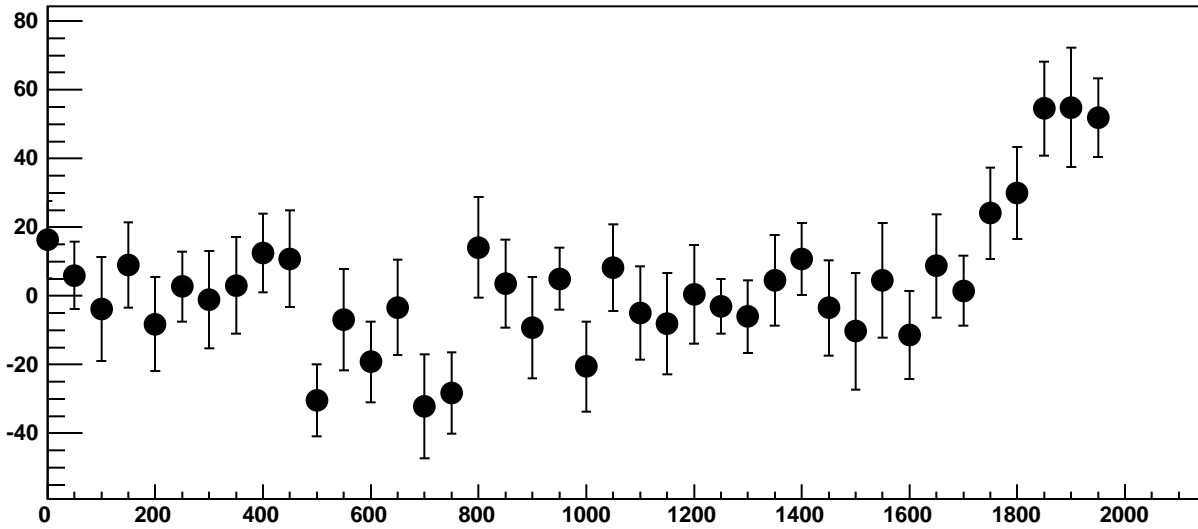
Chip 2, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC



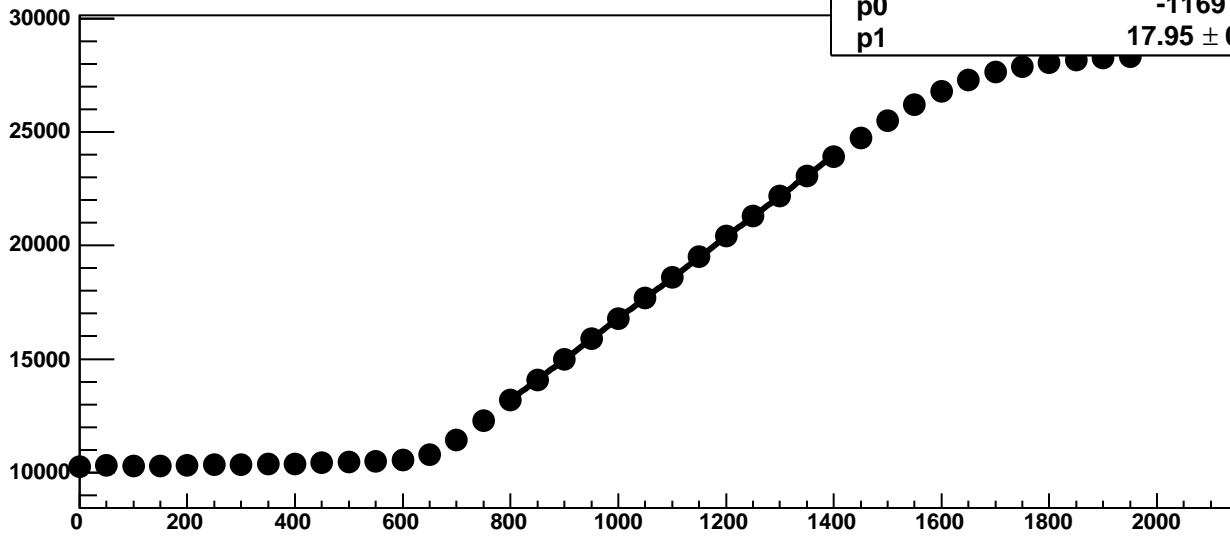
Chip 2, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC



Chip 2, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC

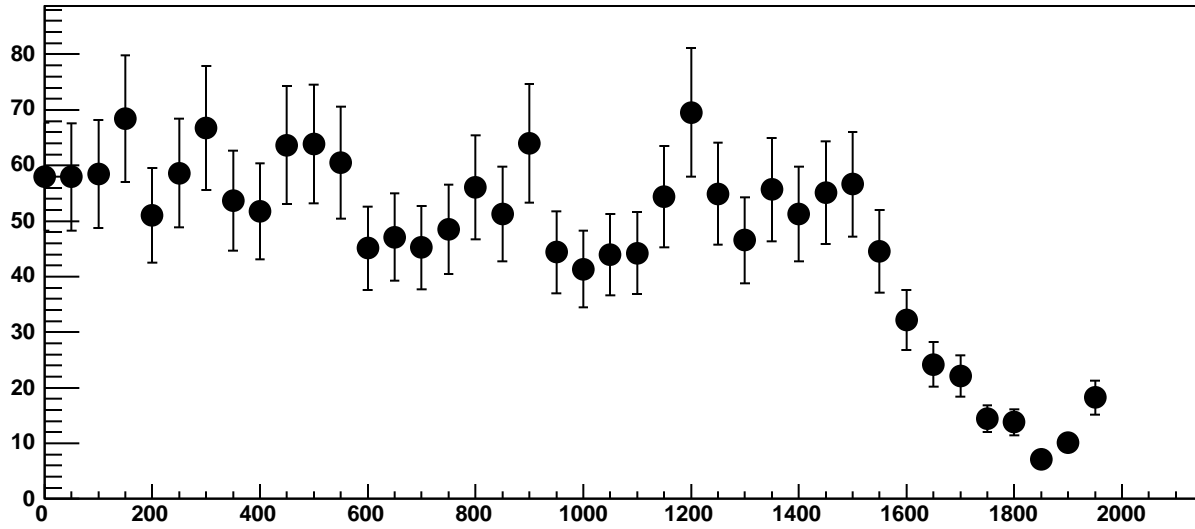


Chip 2, Channel 10, Enable 3!, Hold=30, ADC Mean vs DAC

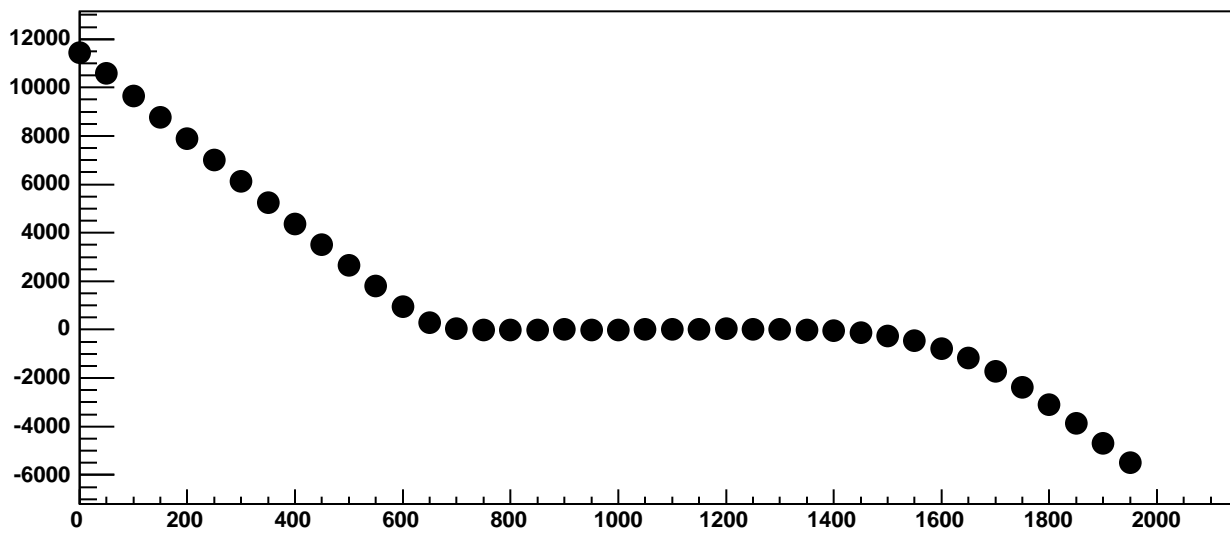


$\chi^2 / \text{ndf}$  39.69 / 11  
p0  $-1169 \pm 19.89$   
p1  $17.95 \pm 0.01792$

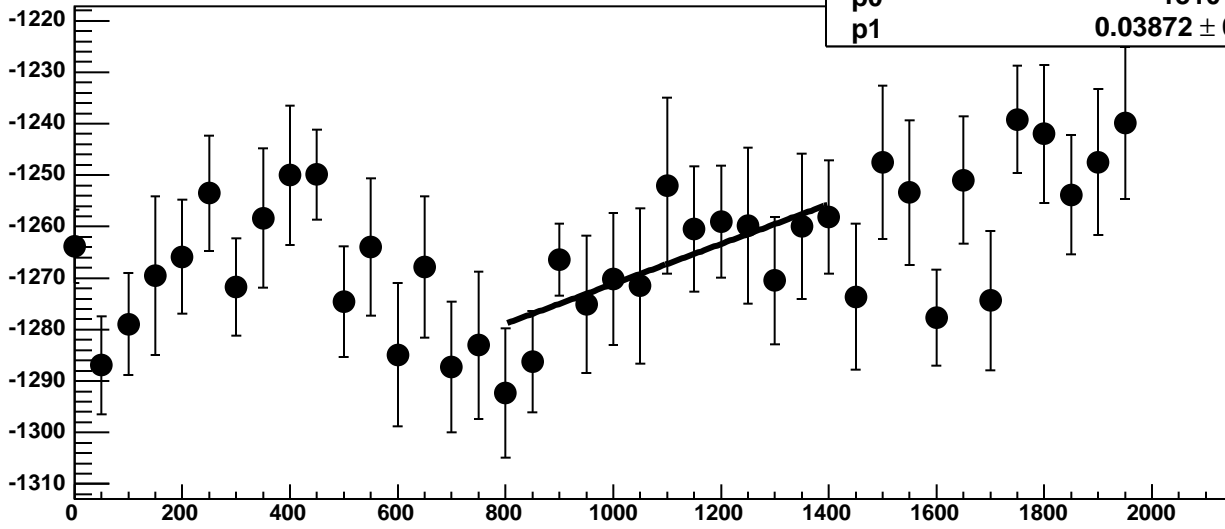
Chip 2, Channel 10, Enable 3!, Hold=30, ADC Noise vs DAC



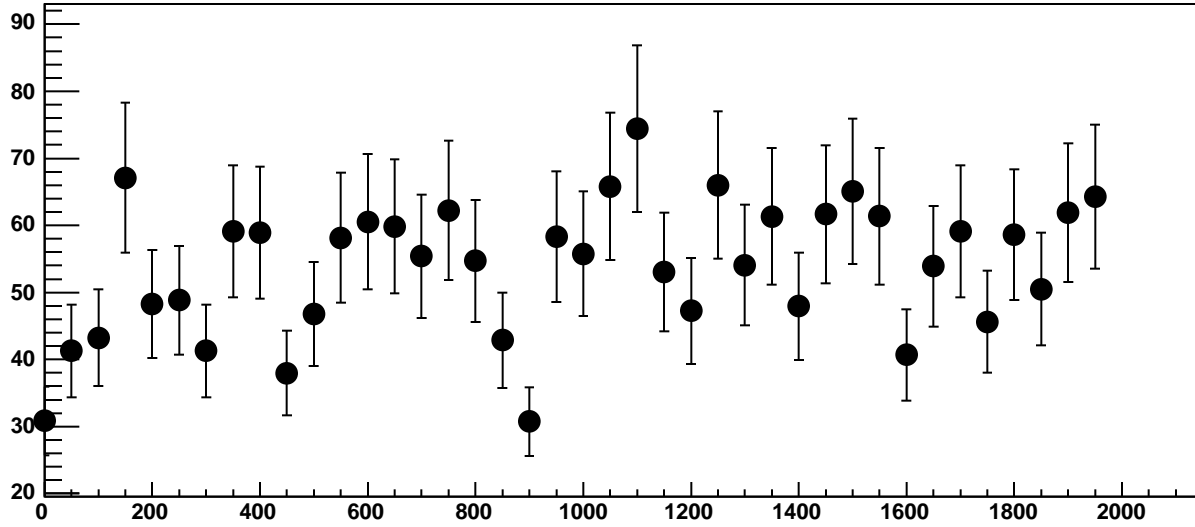
Chip 2, Channel 10, Enable 3!, Hold=30, ADC Residuals vs DAC



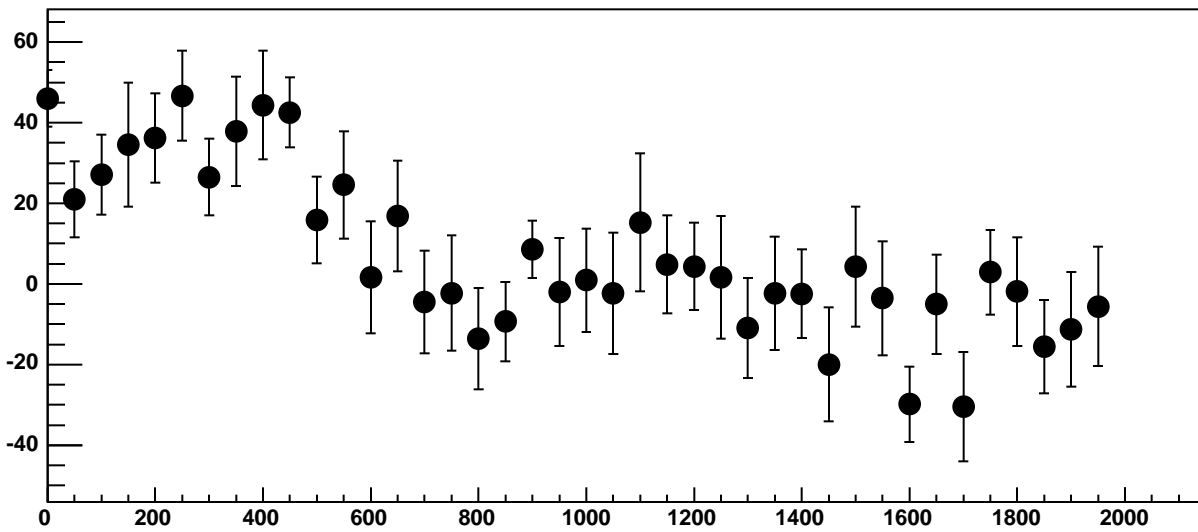
Chip 2, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC



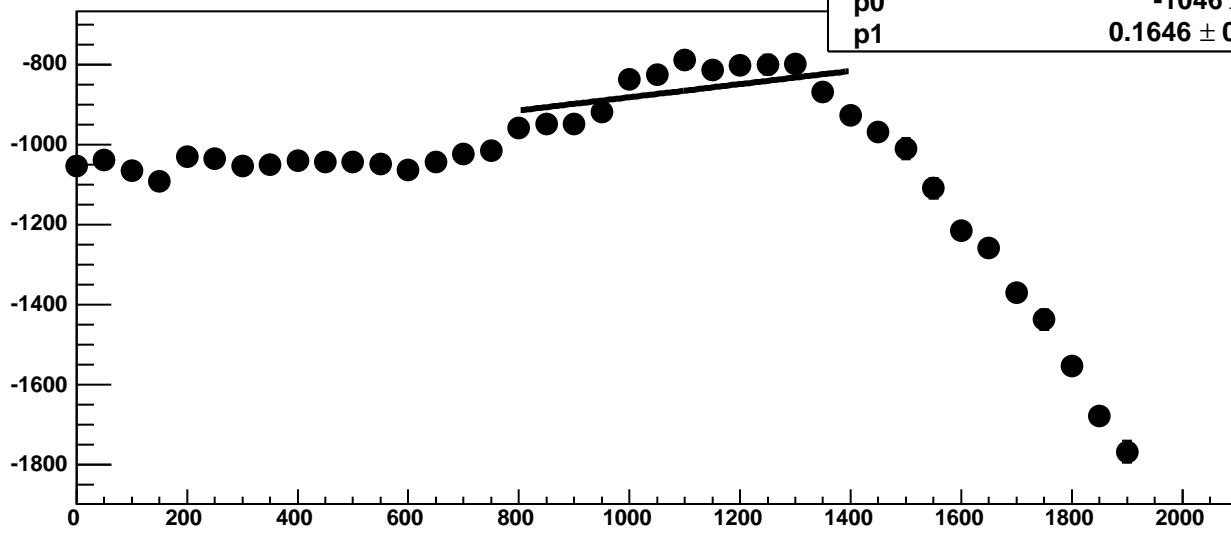
Chip 2, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC



Chip 2, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC

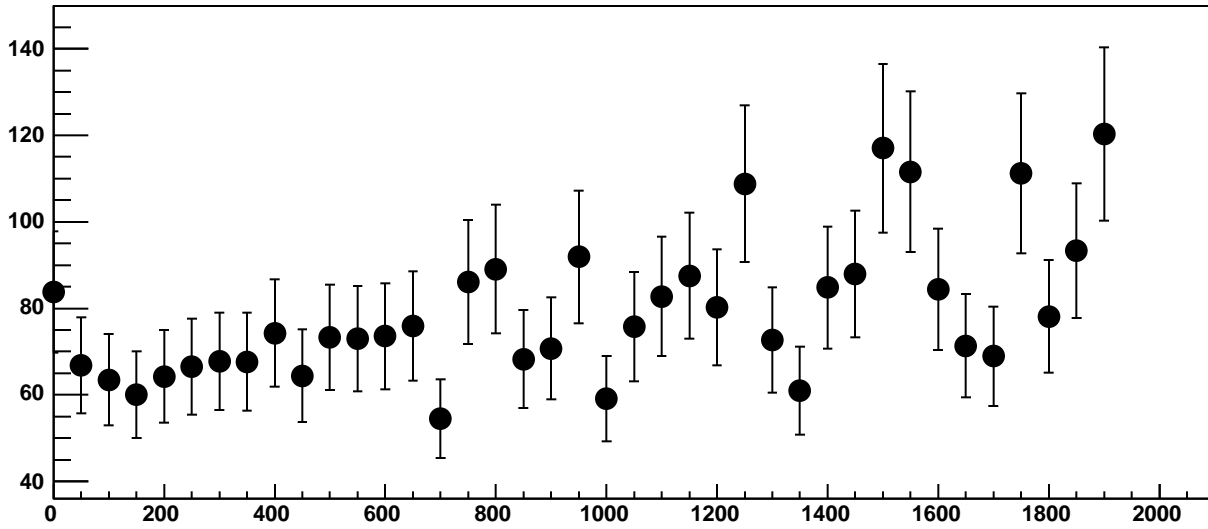


Chip 2, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC

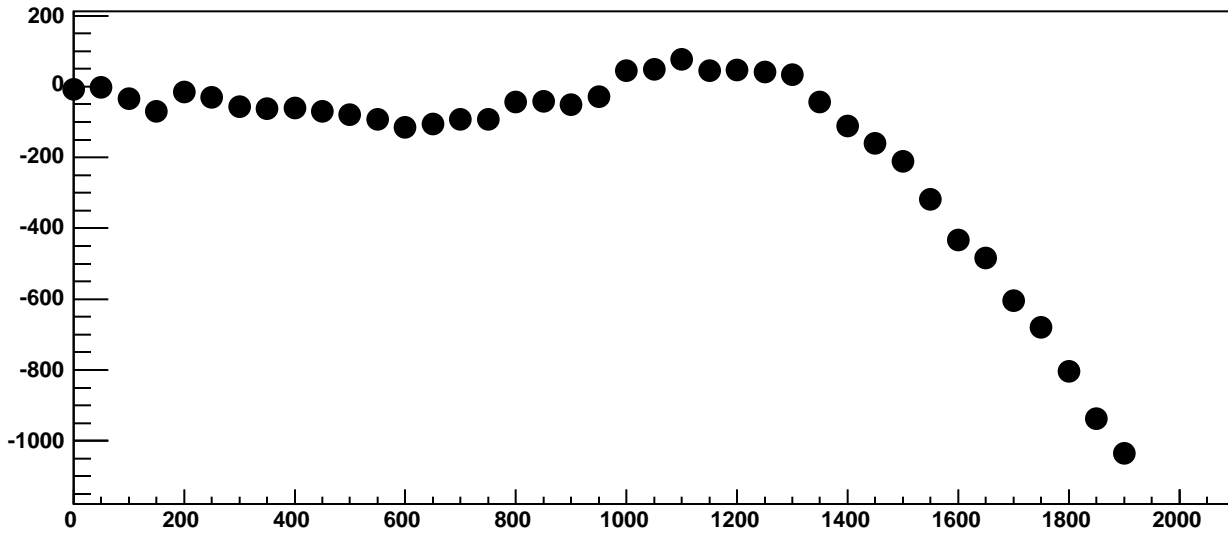


$\chi^2 / \text{ndf}$  119.6 / 11  
p0  $-1046 \pm 28.68$   
p1  $0.1646 \pm 0.02575$

Chip 2, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC

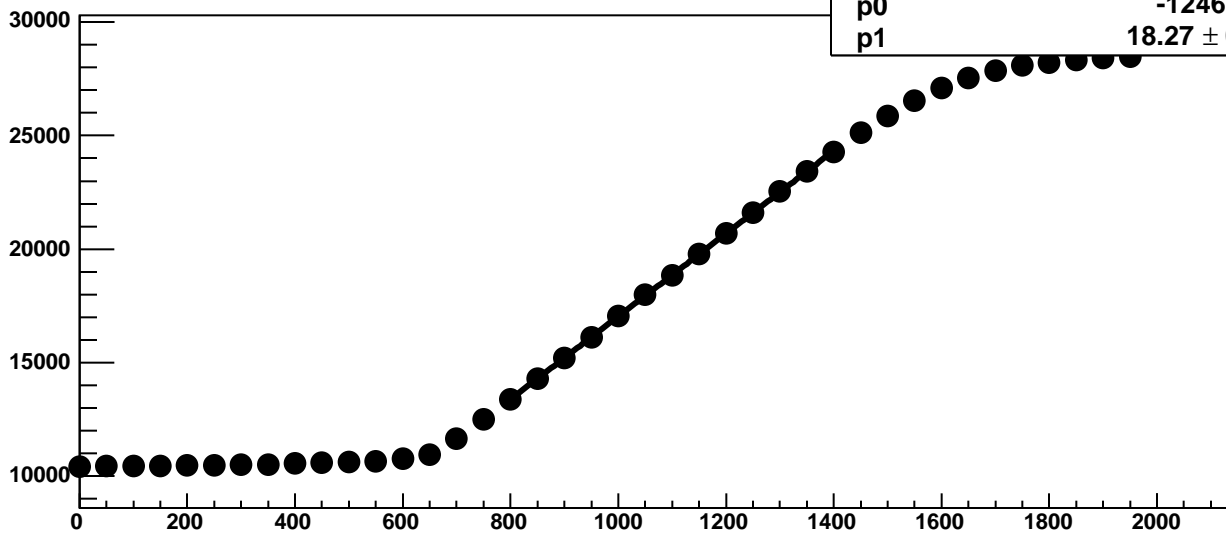


Chip 2, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC



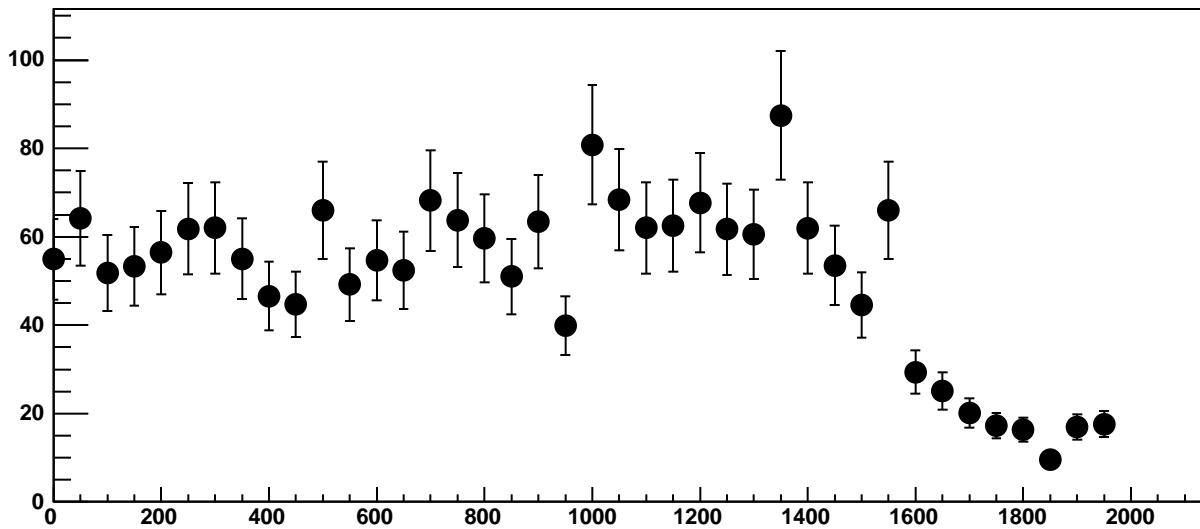


Chip 2, Channel 11, Enable 0!, Hold=30, ADC Mean vs DAC

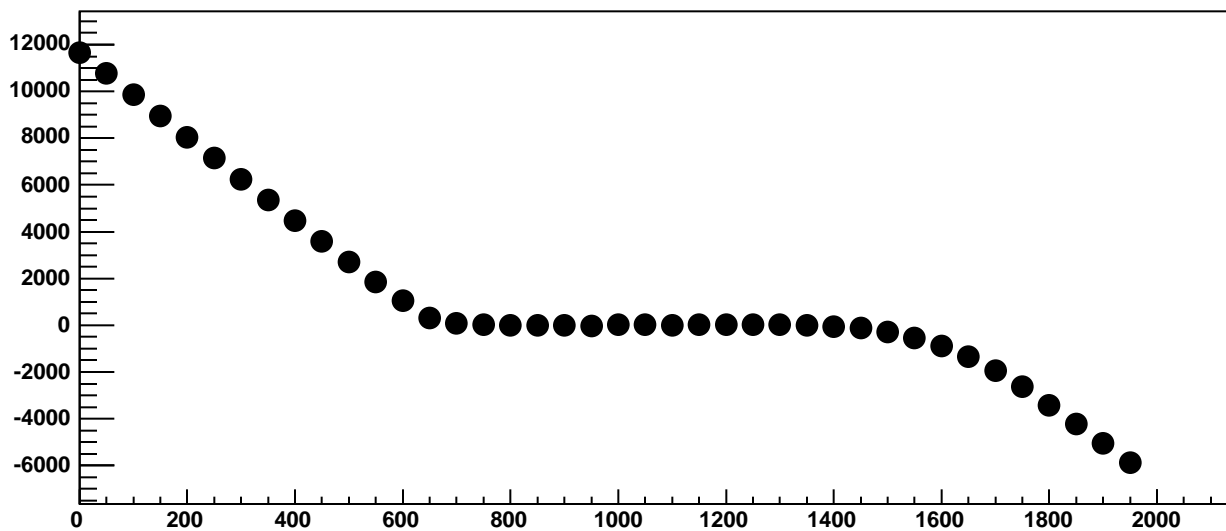


$\chi^2 / \text{ndf}$  29 / 11  
p0  $-1246 \pm 22.42$   
p1  $18.27 \pm 0.02067$

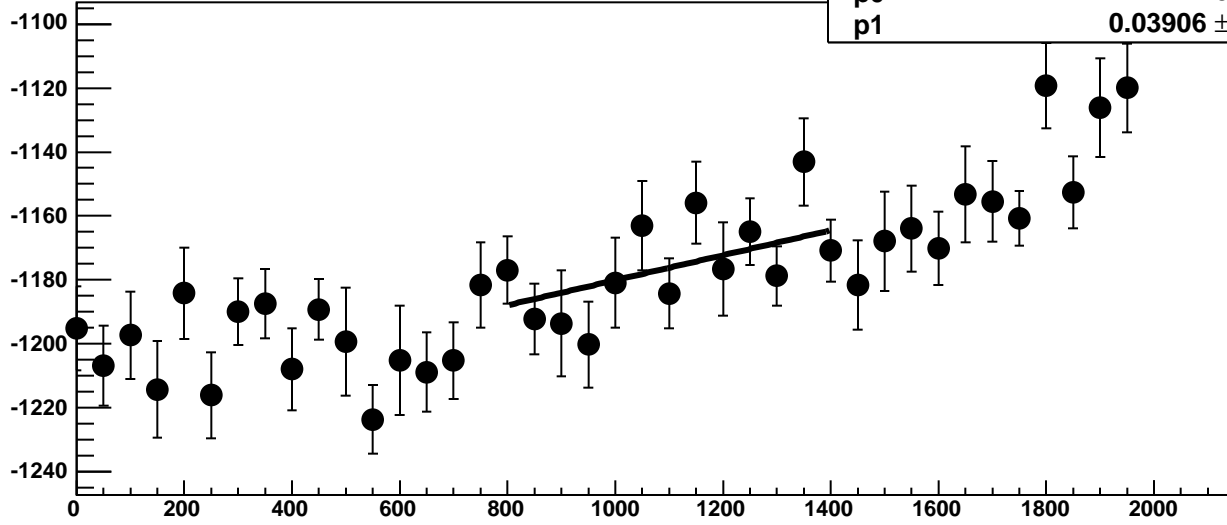
Chip 2, Channel 11, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 2, Channel 11, Enable 0!, Hold=30, ADC Residuals vs DAC

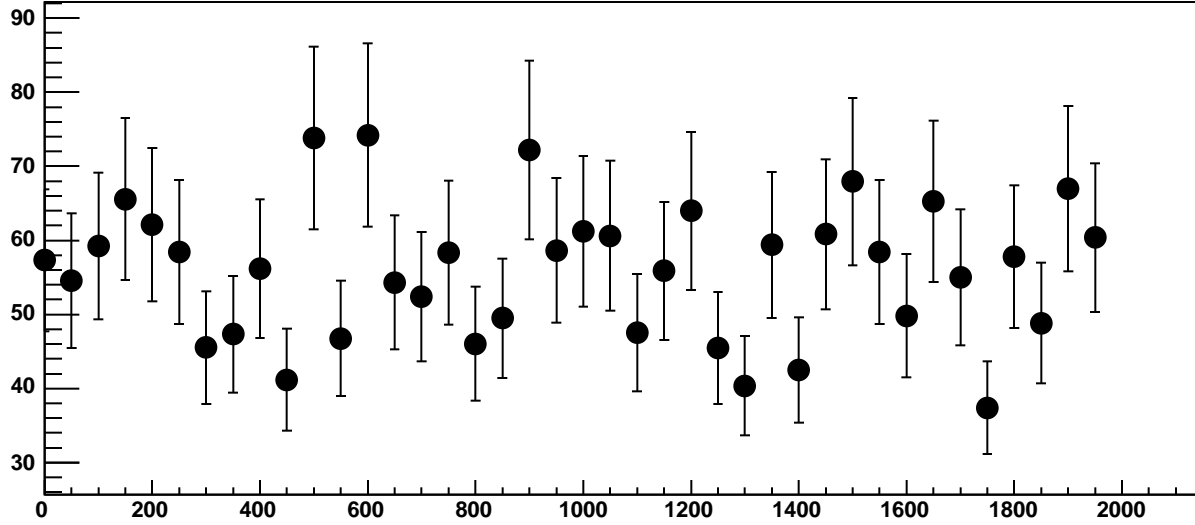


Chip 2, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC

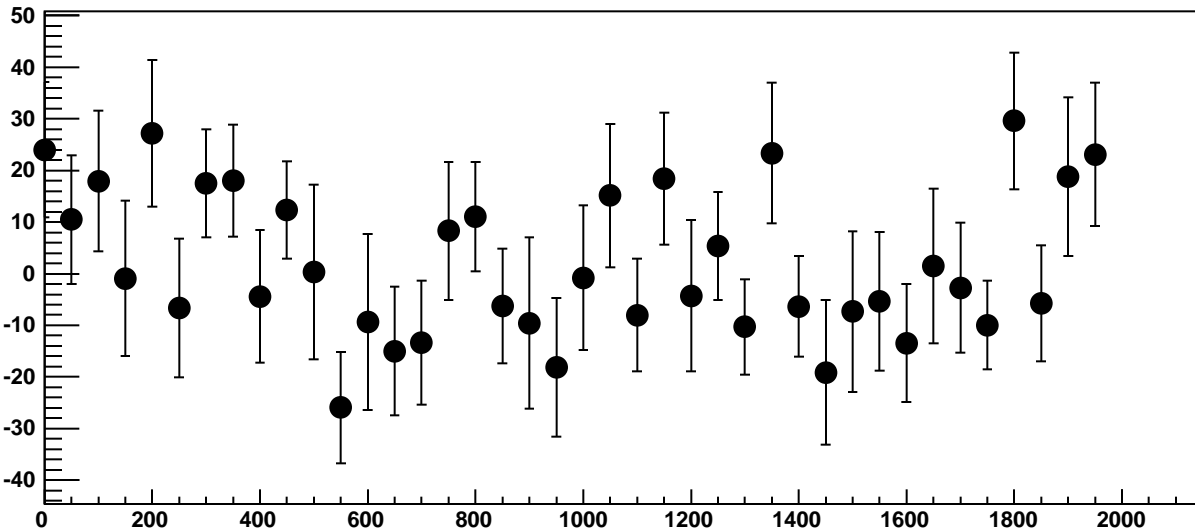


$\chi^2 / \text{ndf}$  12.3 / 11  
p0  $-1219 \pm 18.9$   
p1  $0.03906 \pm 0.0166$

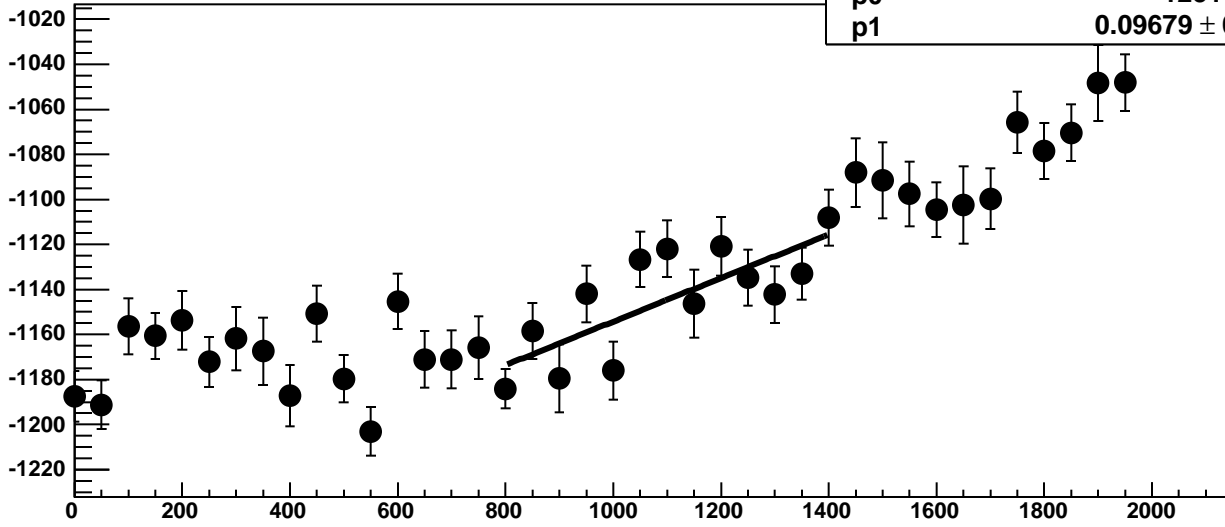
Chip 2, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC



Chip 2, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC

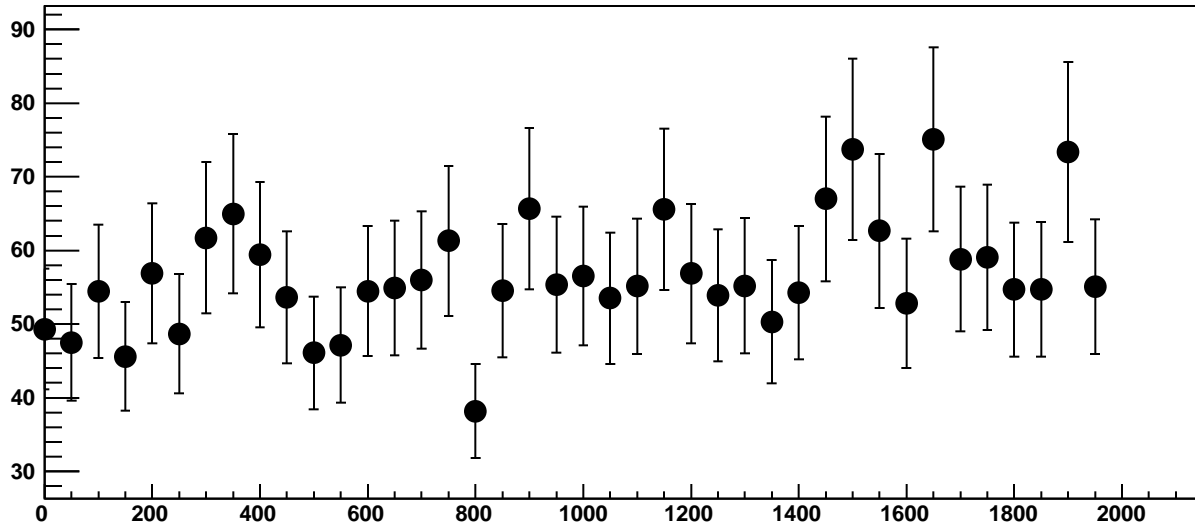


Chip 2, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC

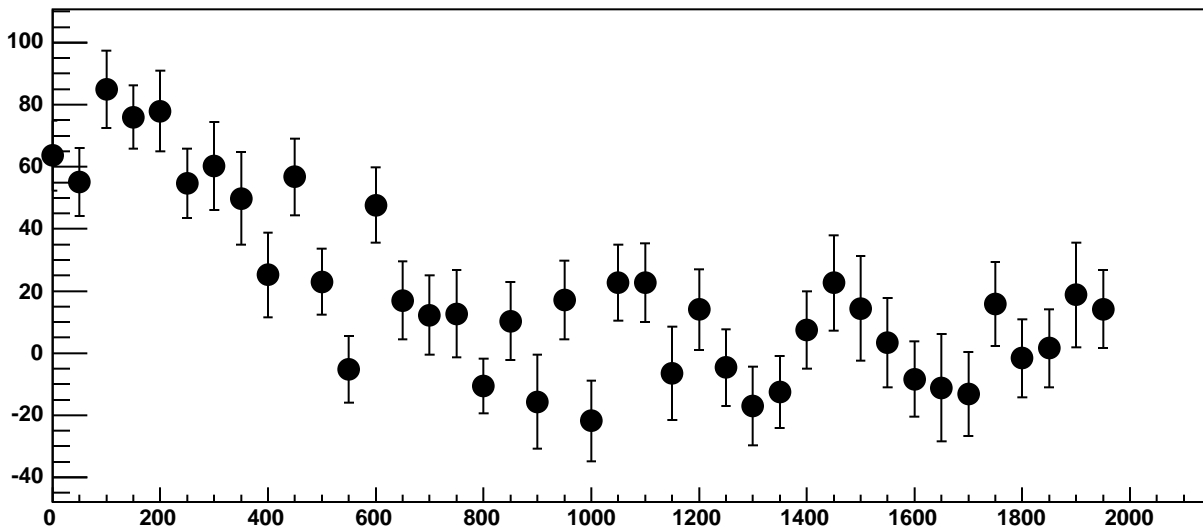


$\chi^2 / \text{ndf}$  19.31 / 11  
p0  $-1251 \pm 18.75$   
p1  $0.09679 \pm 0.01701$

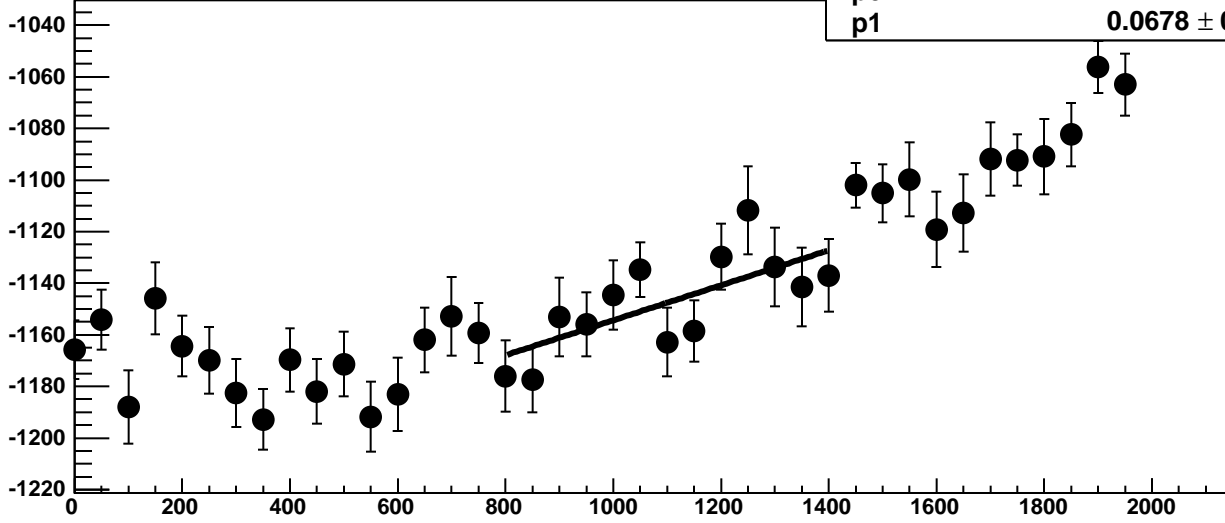
Chip 2, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC



Chip 2, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC

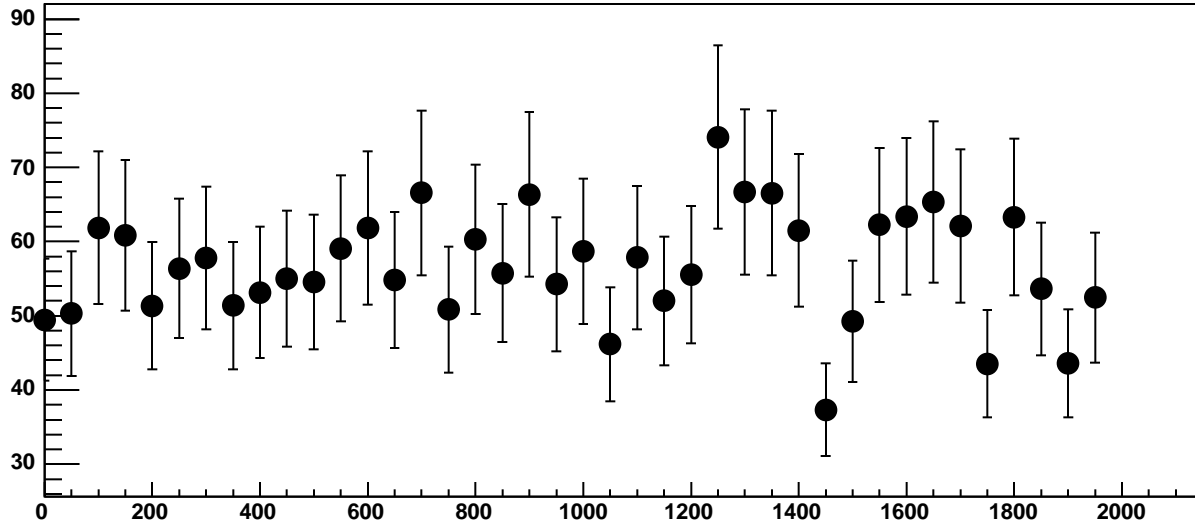


Chip 2, Channel 11, Enable 3, Hold=30, ADC Mean vs DAC

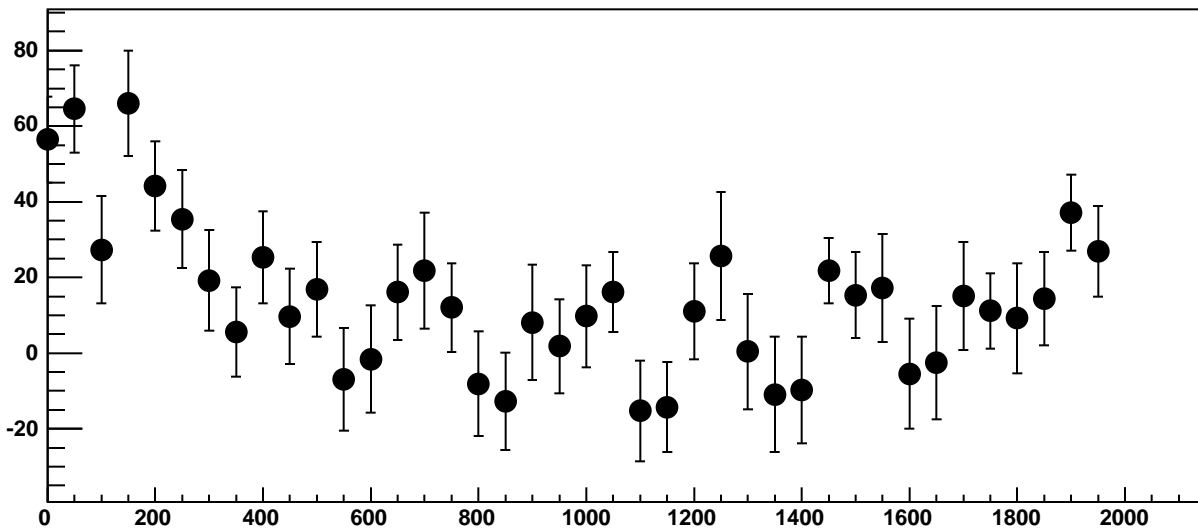


$\chi^2 / \text{ndf}$  11.25 / 11  
p0  $-1222 \pm 23.04$   
p1  $0.0678 \pm 0.02093$

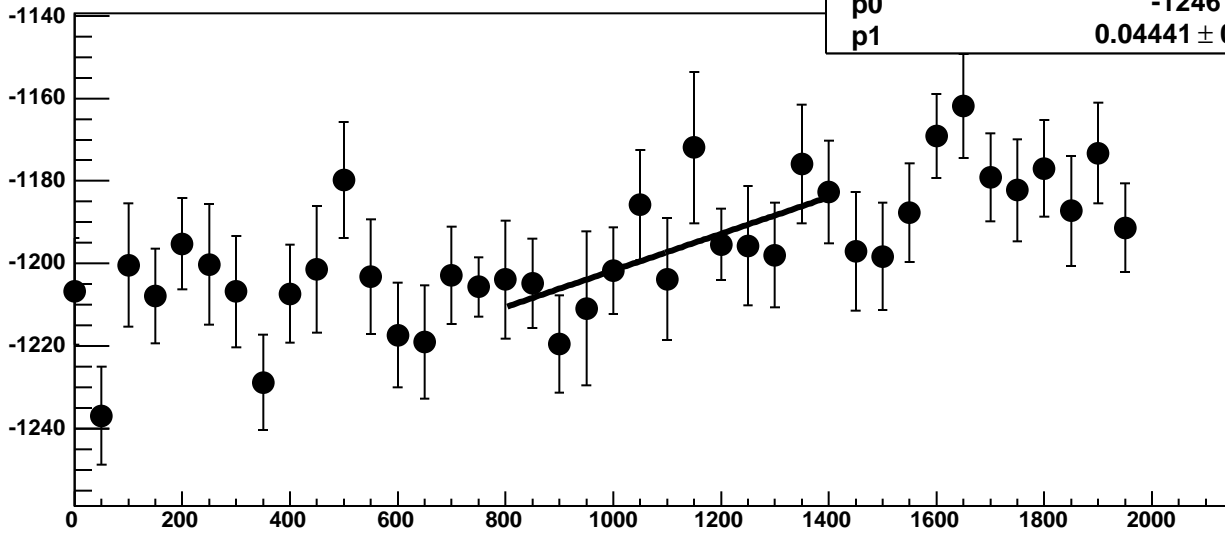
Chip 2, Channel 11, Enable 3, Hold=30, ADC Noise vs DAC



Chip 2, Channel 11, Enable 3, Hold=30, ADC Residuals vs DAC

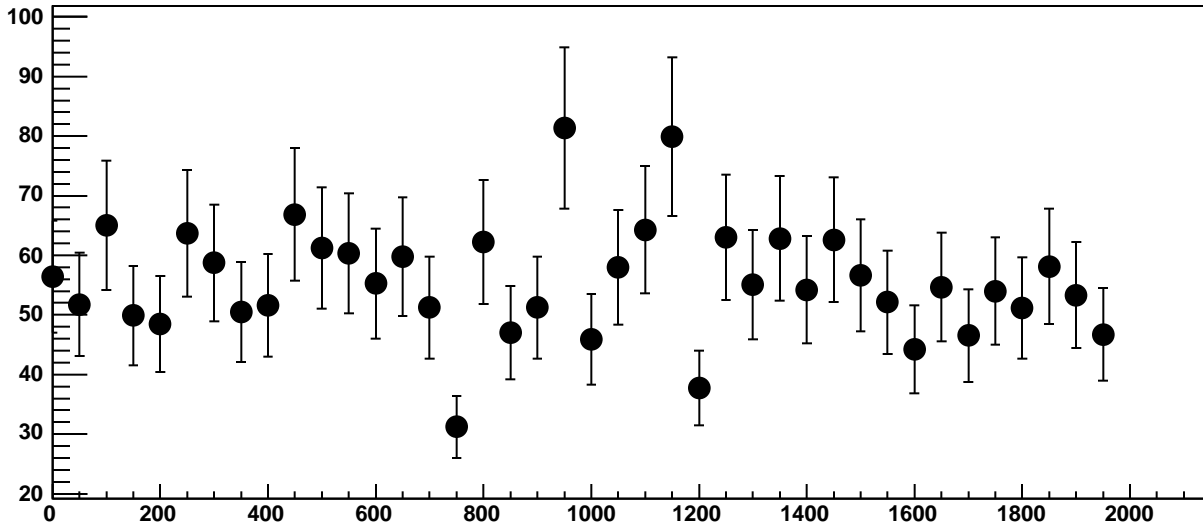


Chip 2, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC

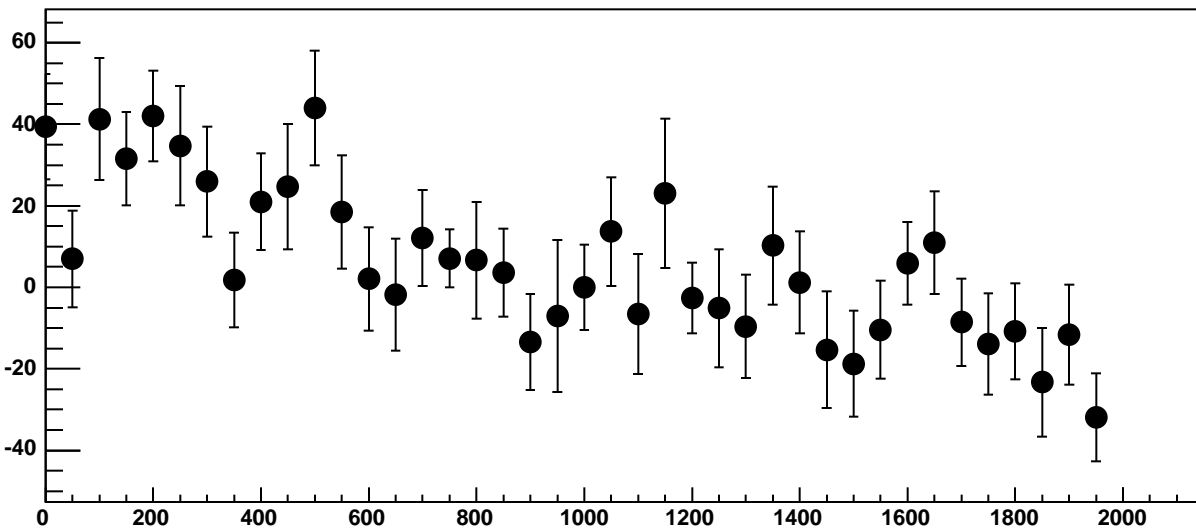


$\chi^2 / \text{ndf}$  5.91 / 11  
p0  $-1246 \pm 20.99$   
p1  $0.04441 \pm 0.01883$

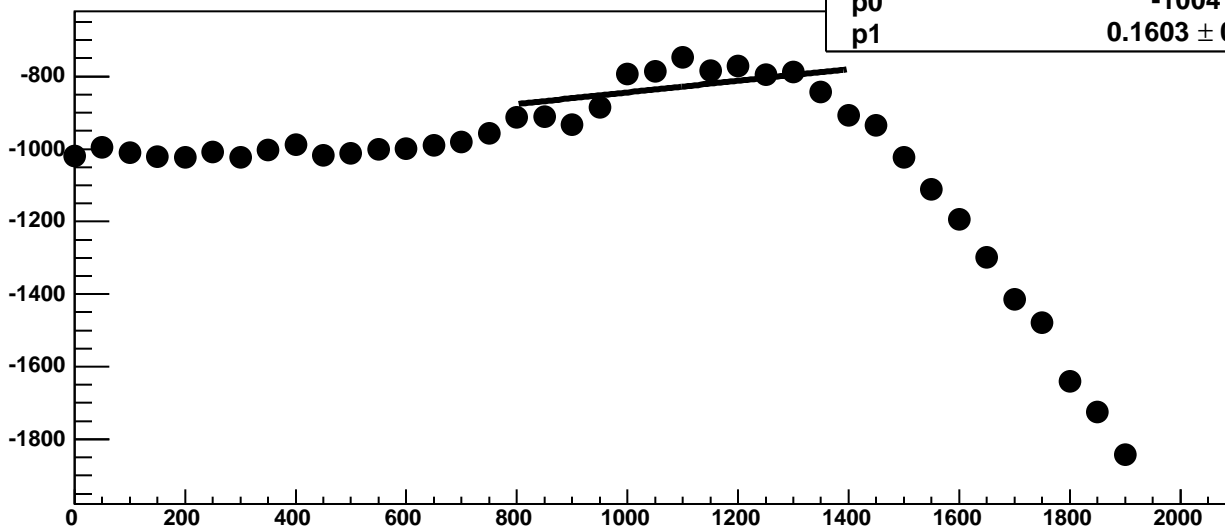
Chip 2, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC



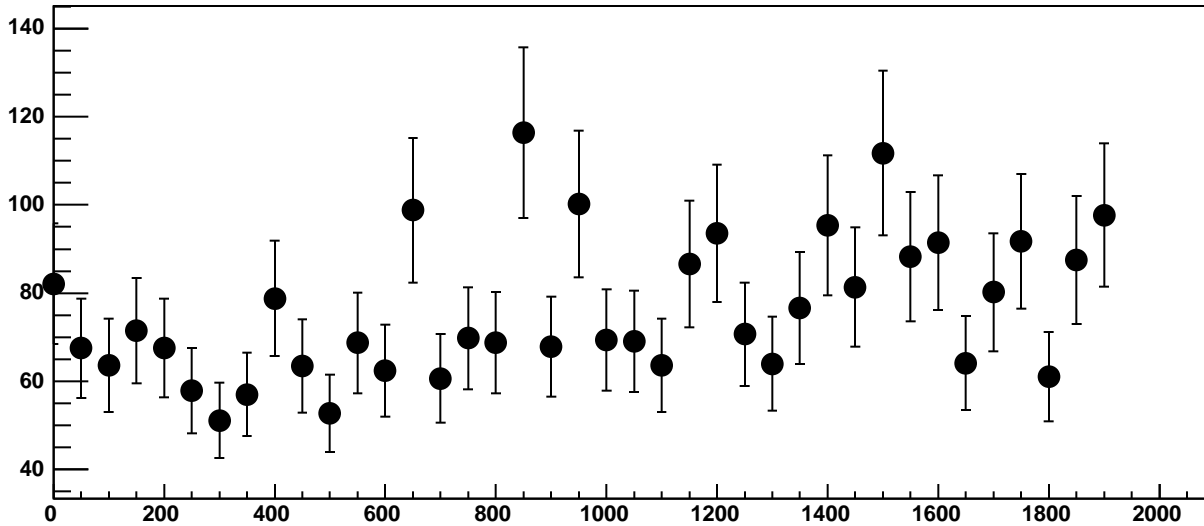
Chip 2, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC



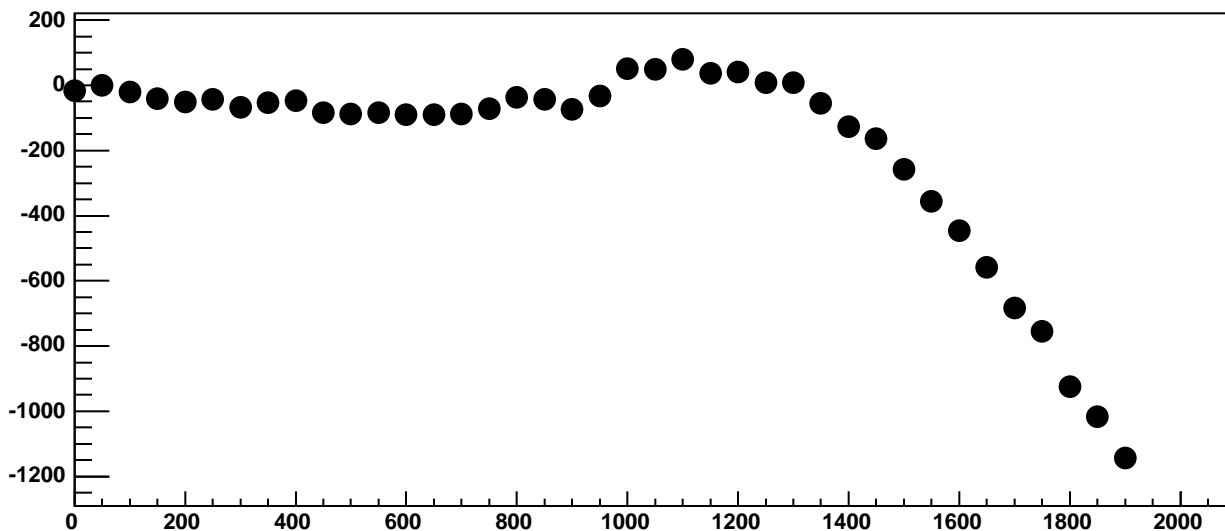
Chip 2, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC



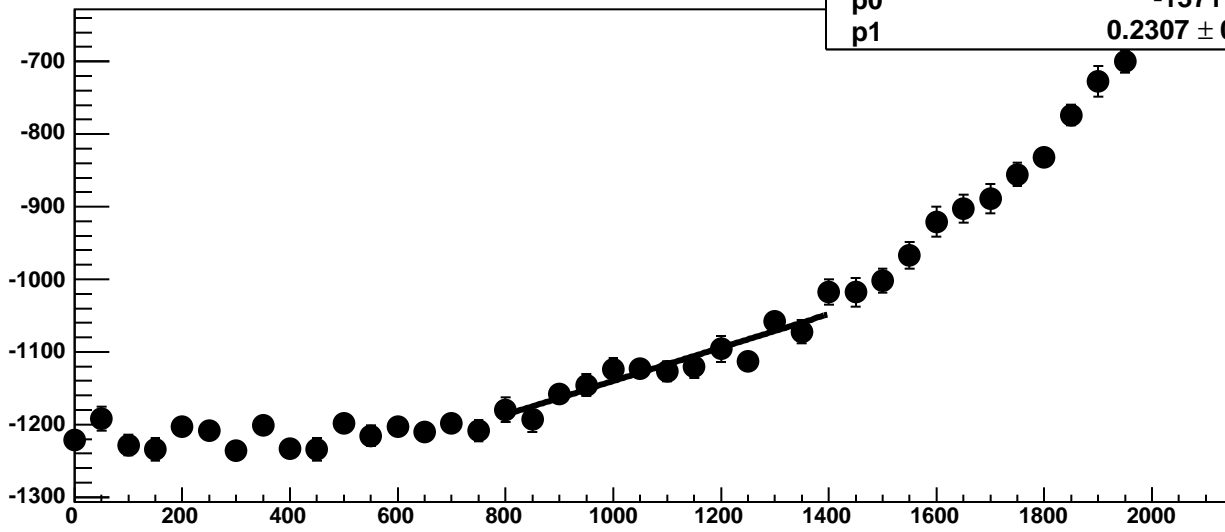
Chip 2, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC



Chip 2, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC

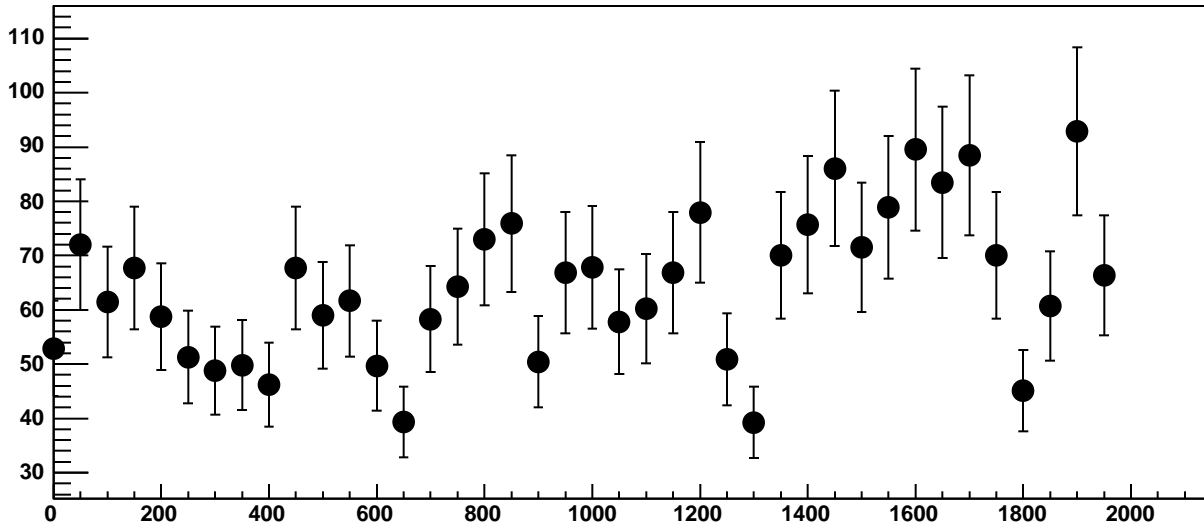


Chip 2, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC

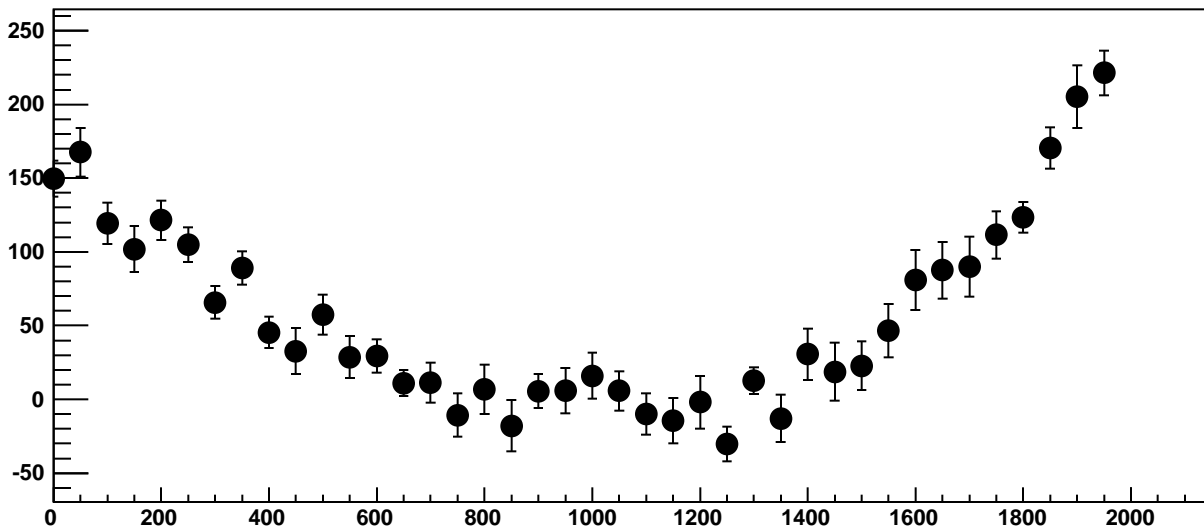


$\chi^2 / \text{ndf}$  16.7 / 11  
p0  $-1371 \pm 24.22$   
p1  $0.2307 \pm 0.02136$

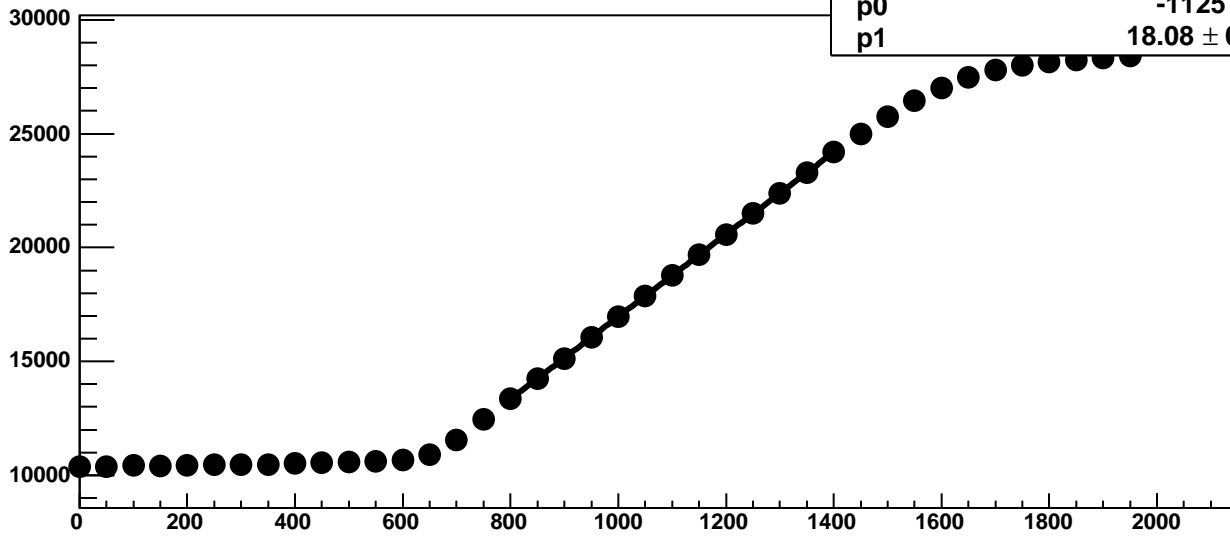
Chip 2, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC



Chip 2, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC

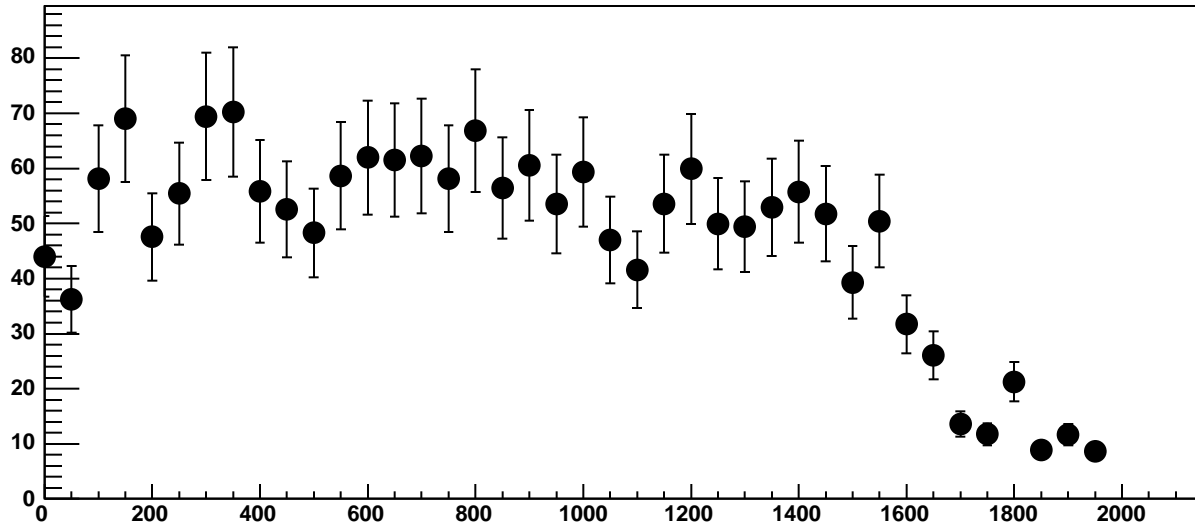


Chip 2, Channel 12, Enable 1!, Hold=30, ADC Mean vs DAC

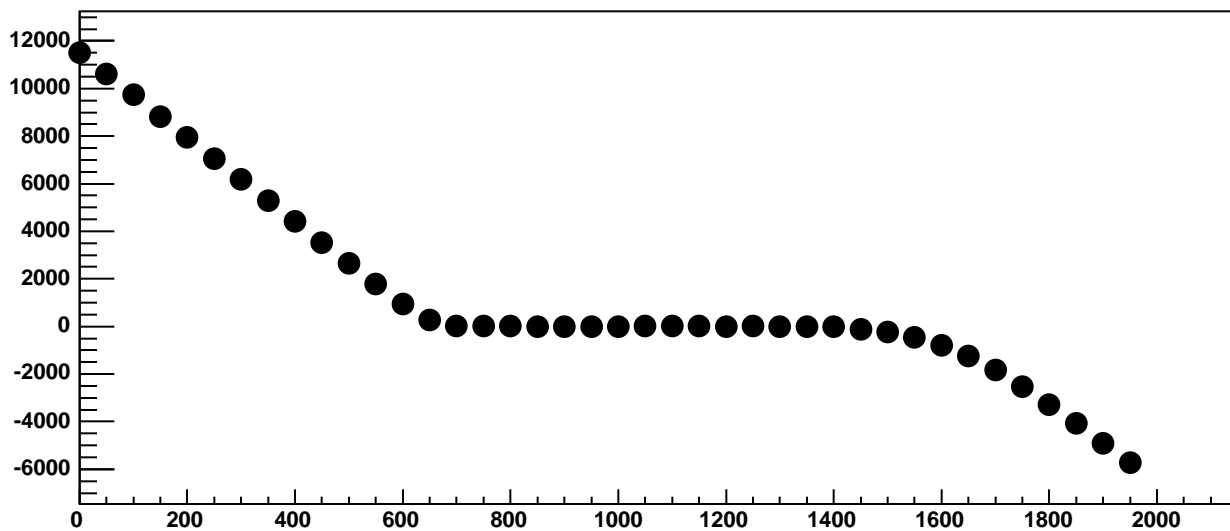


$\chi^2 / \text{ndf}$  12.56 / 11  
p0  $-1125 \pm 21.65$   
p1  $18.08 \pm 0.01919$

Chip 2, Channel 12, Enable 1!, Hold=30, ADC Noise vs DAC

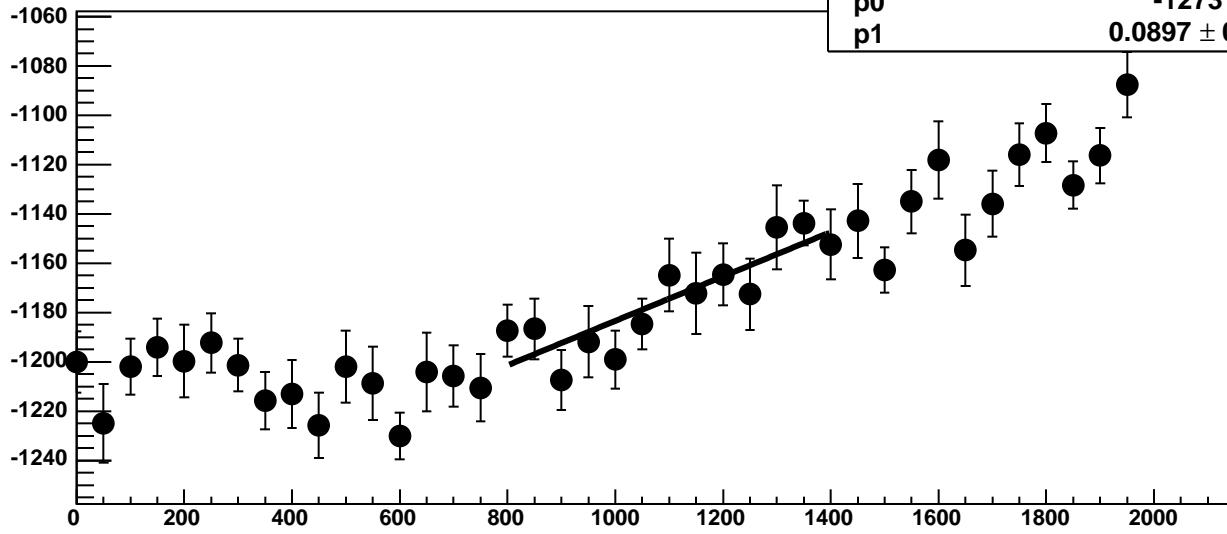


Chip 2, Channel 12, Enable 1!, Hold=30, ADC Residuals vs DAC



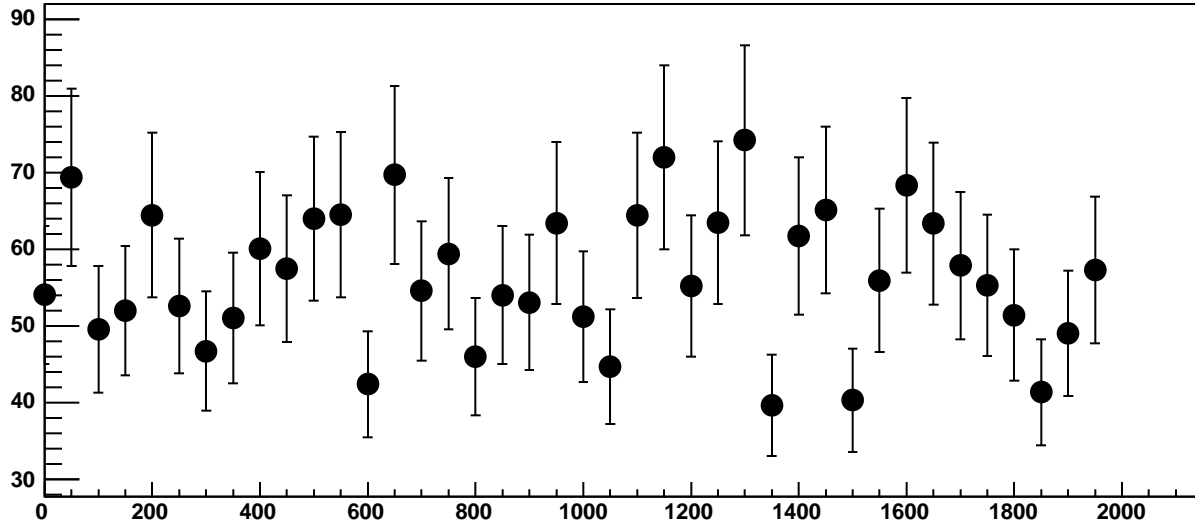


Chip 2, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC

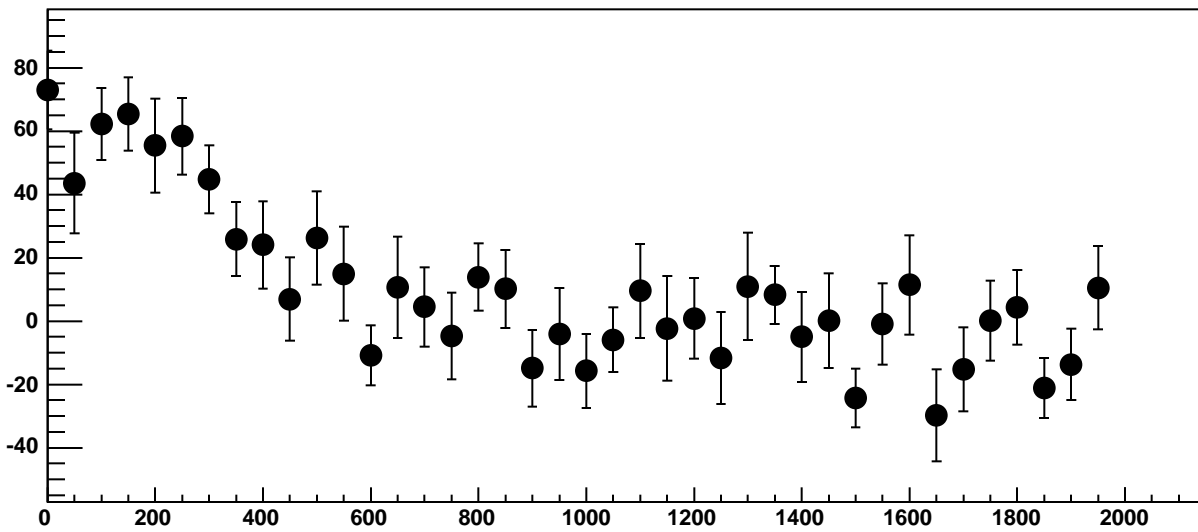


$\chi^2 / \text{ndf}$  8.546 / 11  
p0  $-1273 \pm 19.58$   
p1  $0.0897 \pm 0.01768$

Chip 2, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC

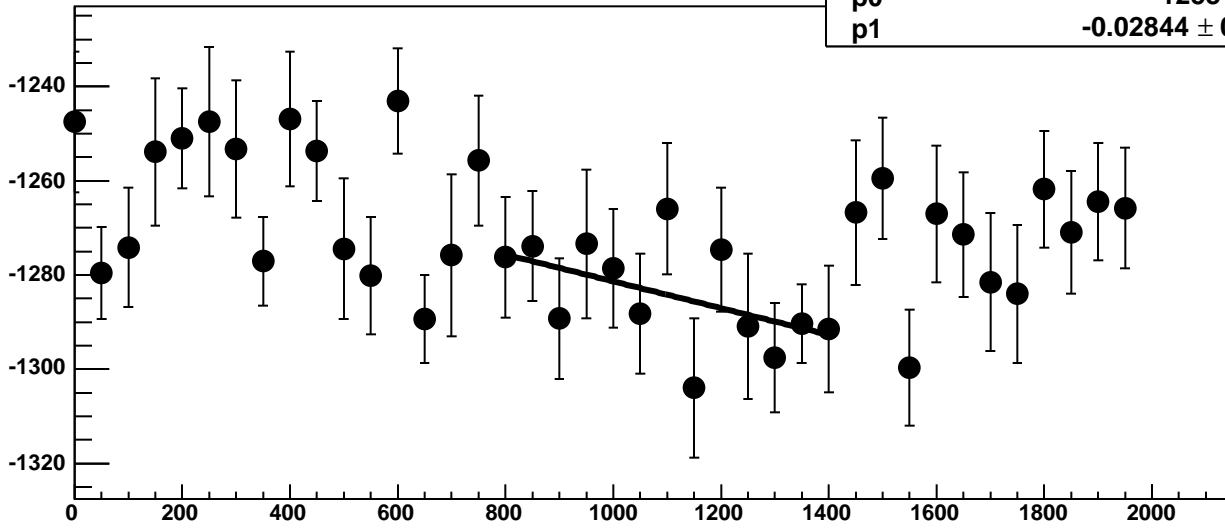


Chip 2, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC

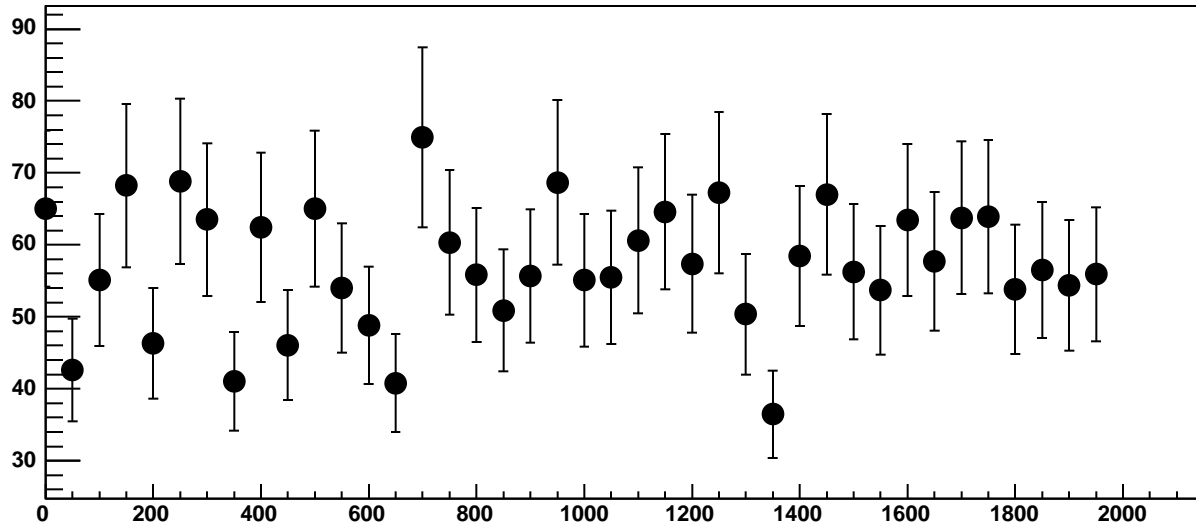


Chip 2, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC

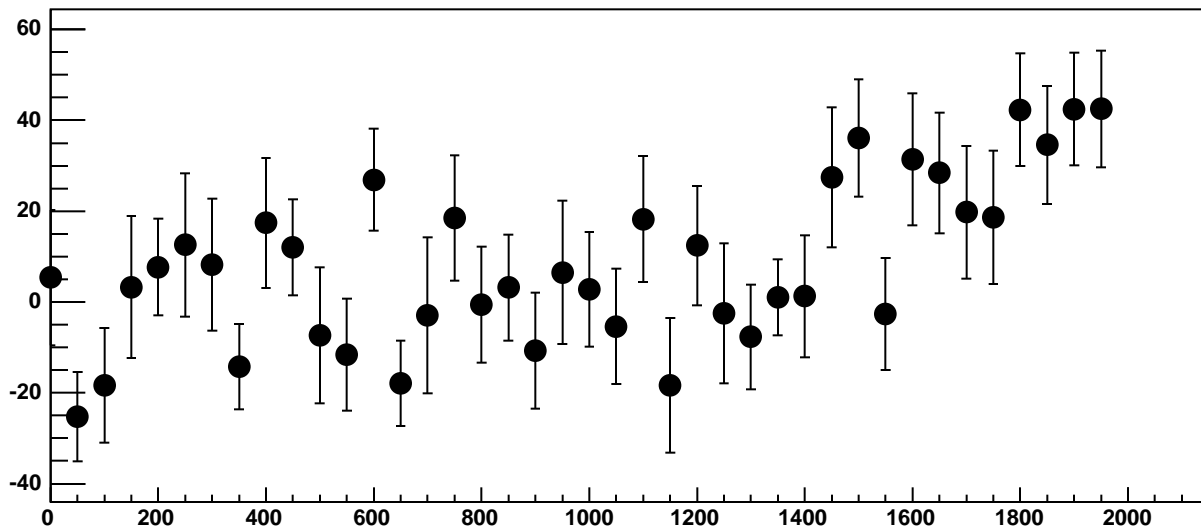
$\chi^2 / \text{ndf}$  5.823 / 11  
p0  $-1253 \pm 19.99$   
p1  $-0.02844 \pm 0.01756$



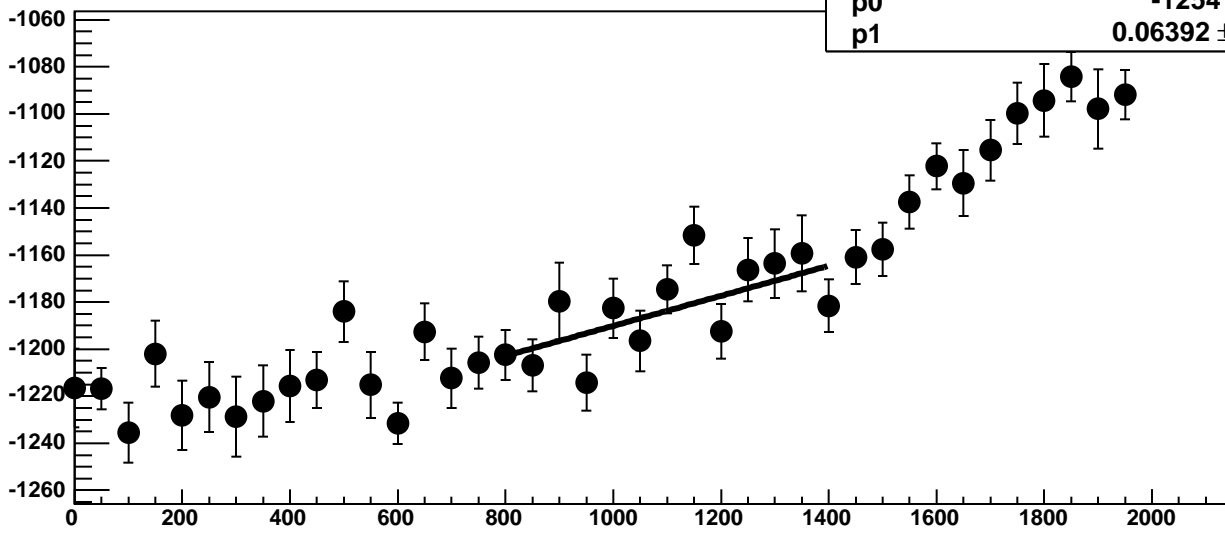
Chip 2, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC



Chip 2, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC

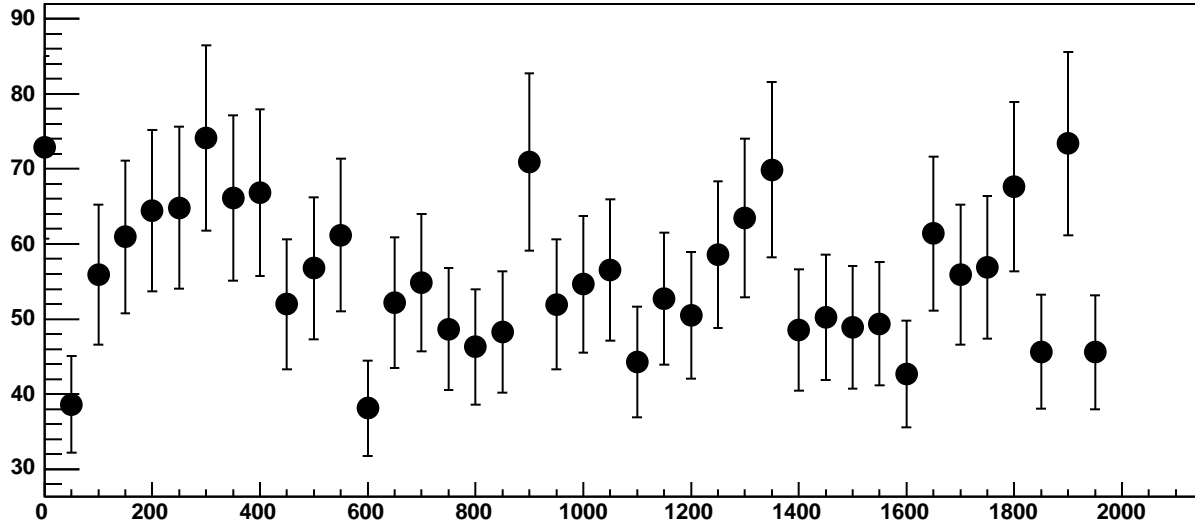


Chip 2, Channel 12, Enable 4, Hold=30, ADC Mean vs DAC

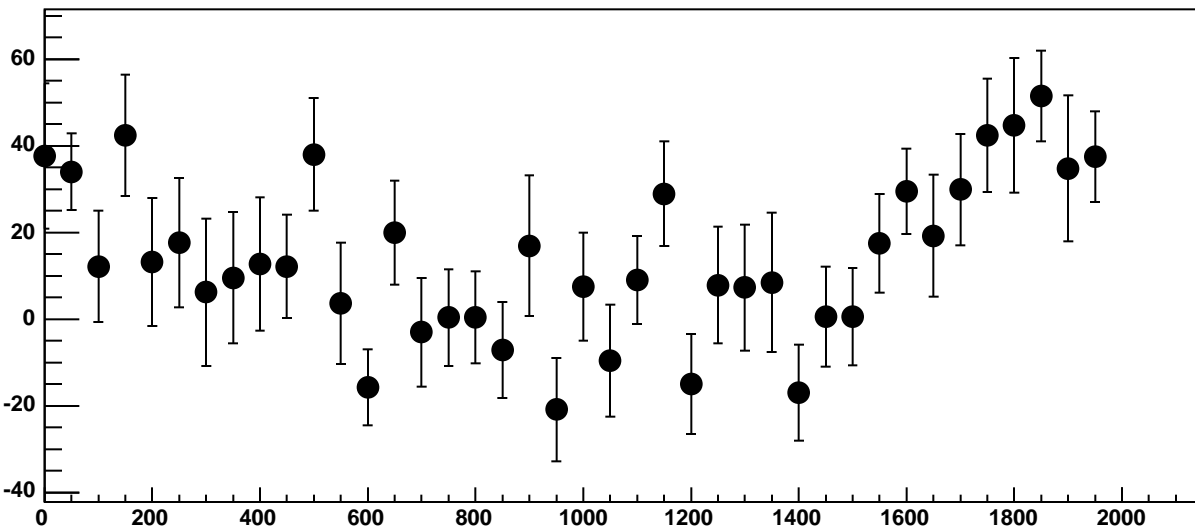


$\chi^2 / \text{ndf}$  16.85 / 11  
p0  $-1254 \pm 19.95$   
p1  $0.06392 \pm 0.0181$

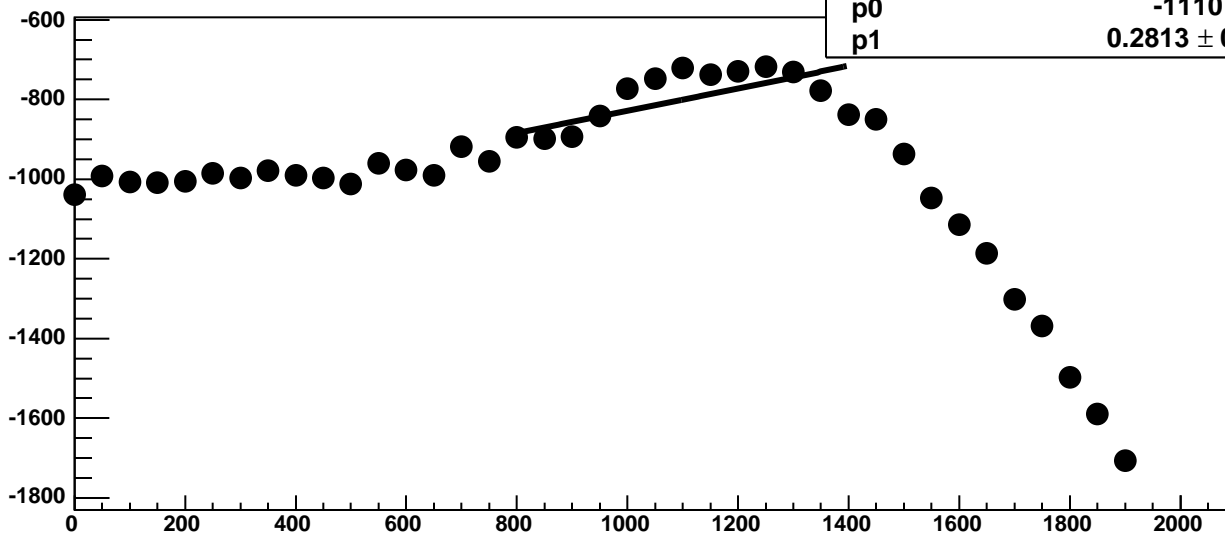
Chip 2, Channel 12, Enable 4, Hold=30, ADC Noise vs DAC



Chip 2, Channel 12, Enable 4, Hold=30, ADC Residuals vs DAC

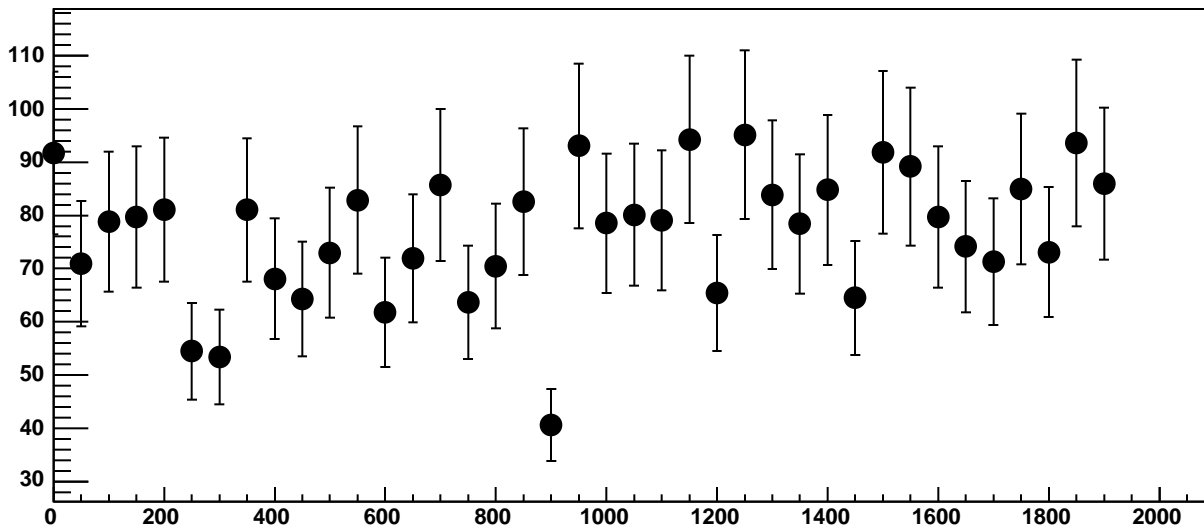


Chip 2, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC

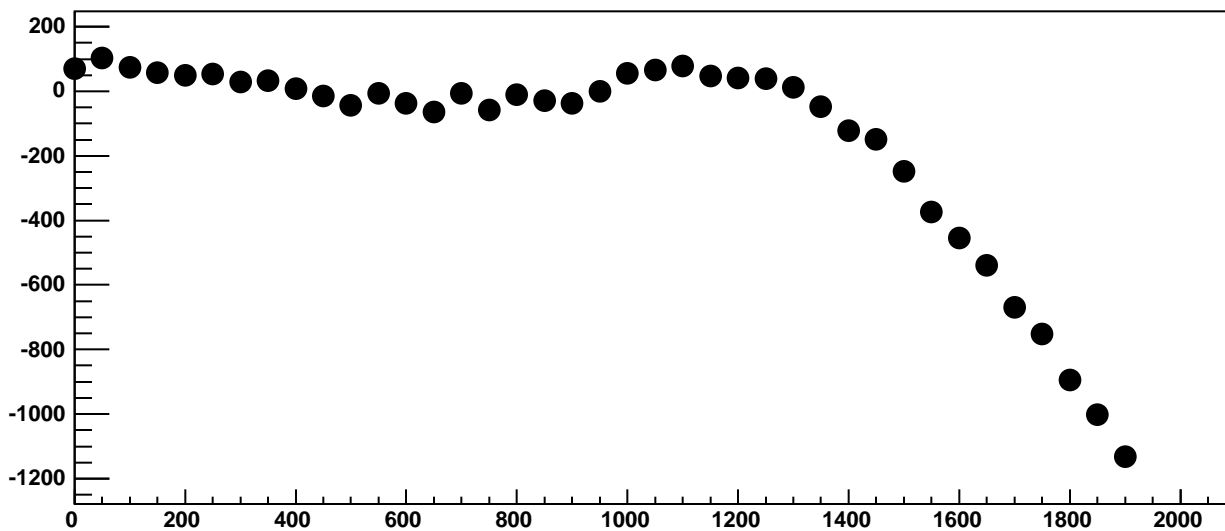


$\chi^2 / \text{ndf}$  122.4 / 11  
p0  $-1110 \pm 26.69$   
p1  $0.2813 \pm 0.02485$

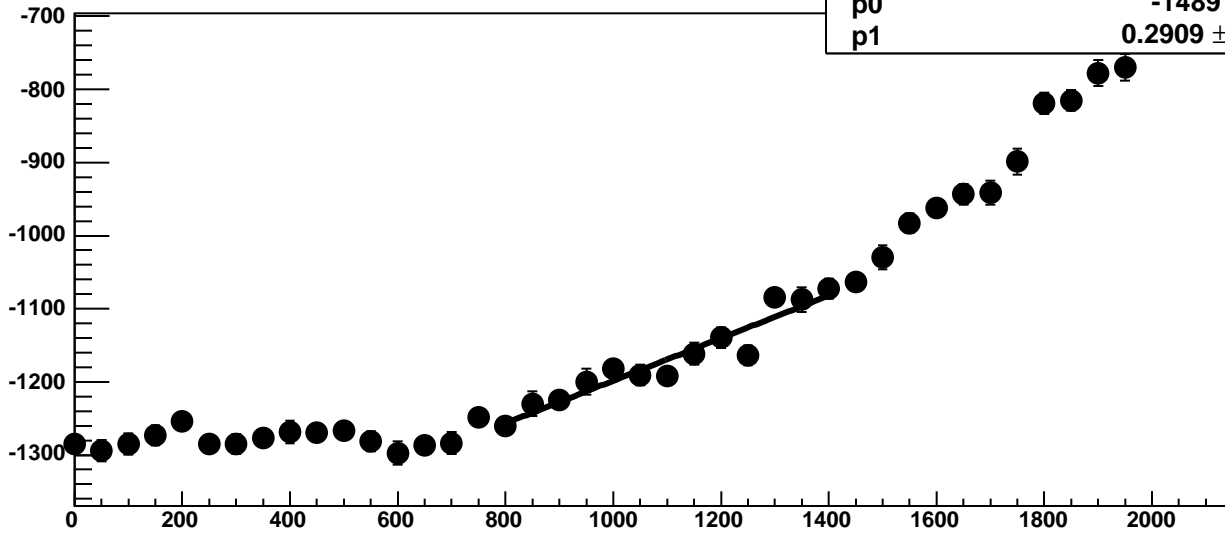
Chip 2, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC



Chip 2, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC

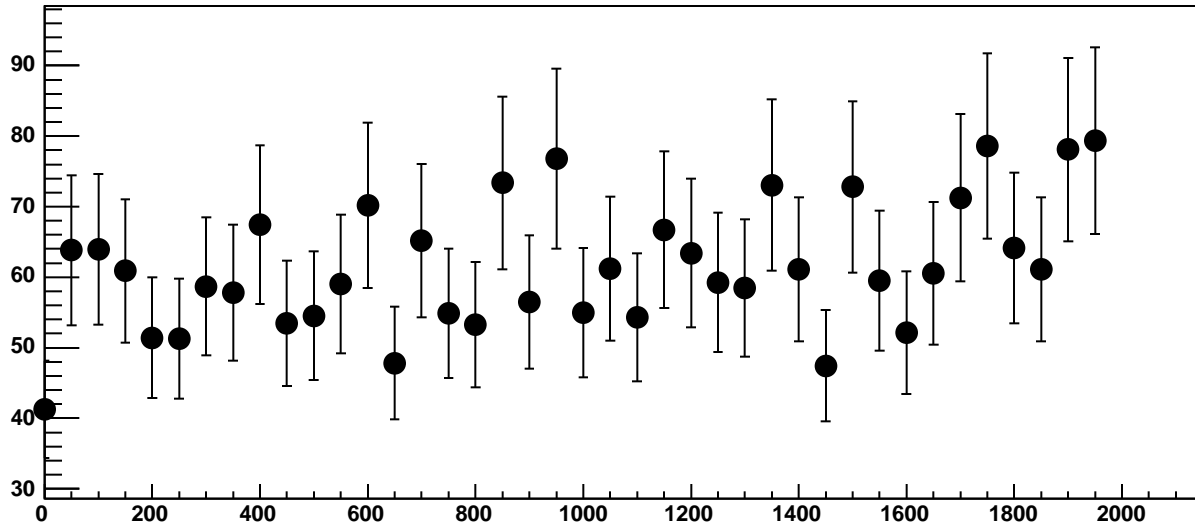


Chip 2, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC

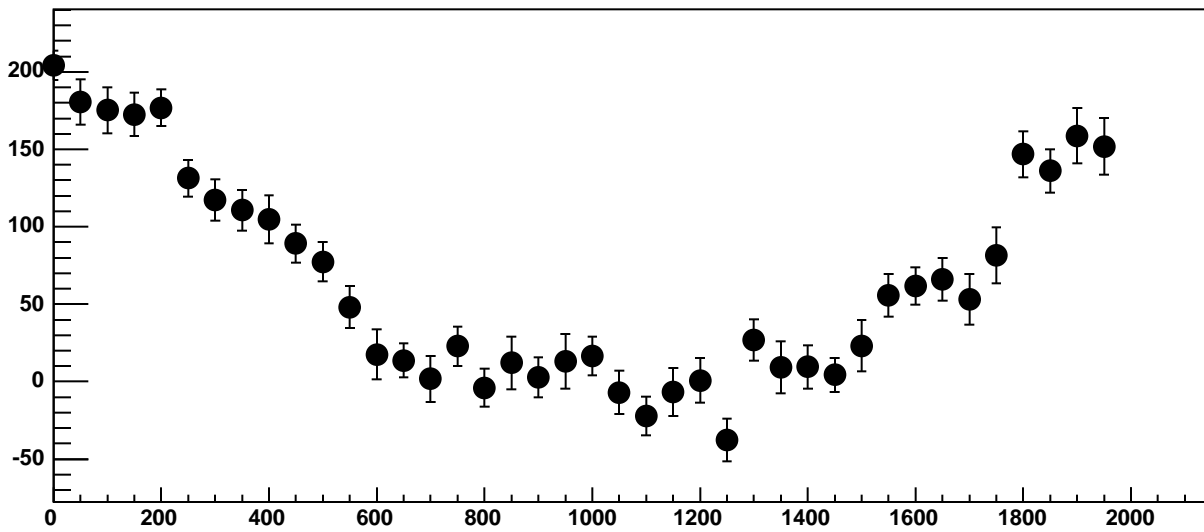


$\chi^2 / \text{ndf}$  19.01 / 11  
p0  $-1489 \pm 23.19$   
p1  $0.2909 \pm 0.0209$

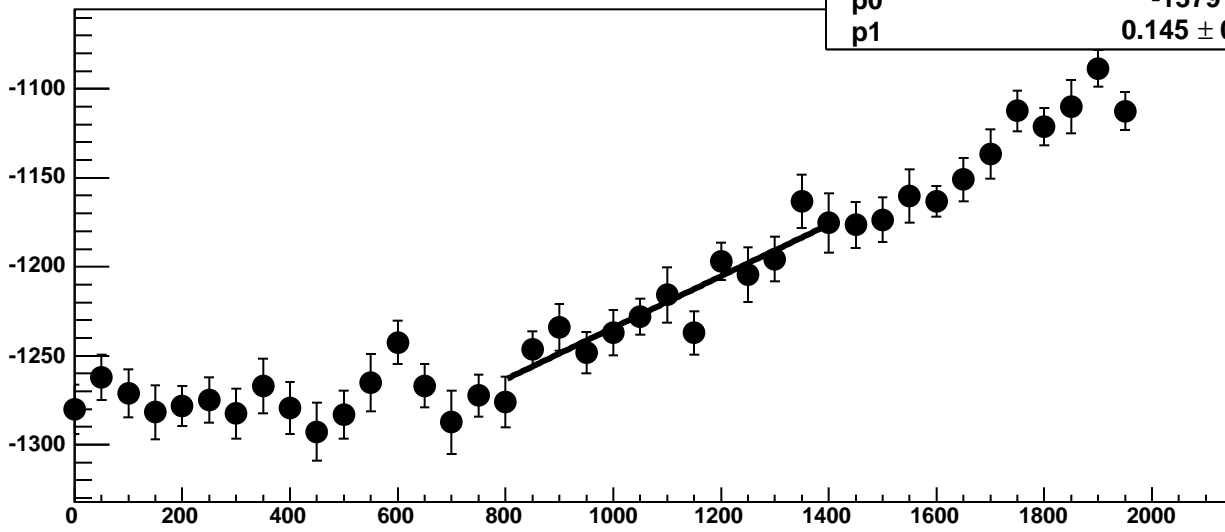
Chip 2, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC



Chip 2, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC

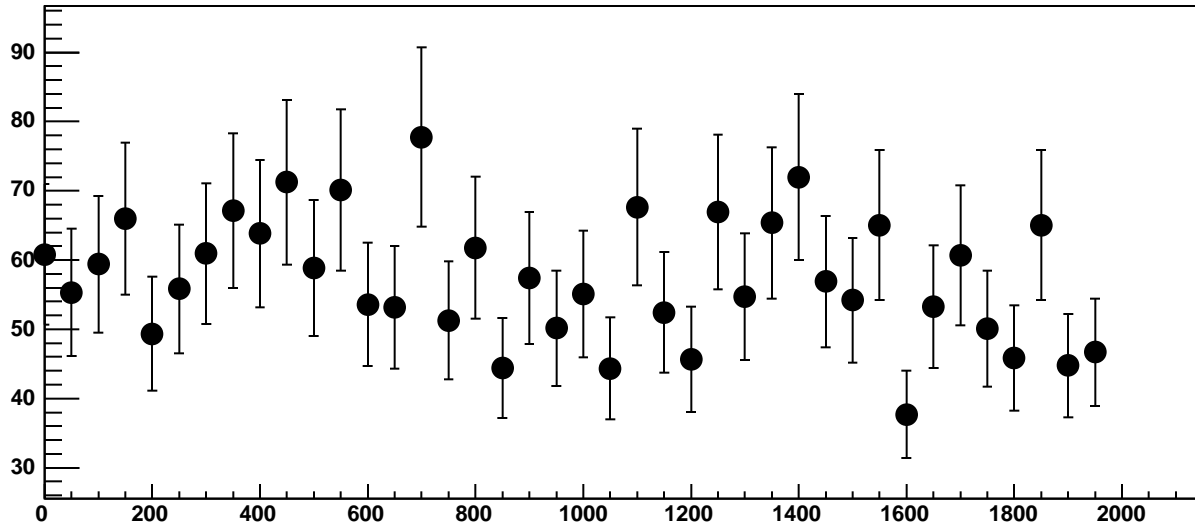


Chip 2, Channel 13, Enable 1, Hold=30, ADC Mean vs DAC

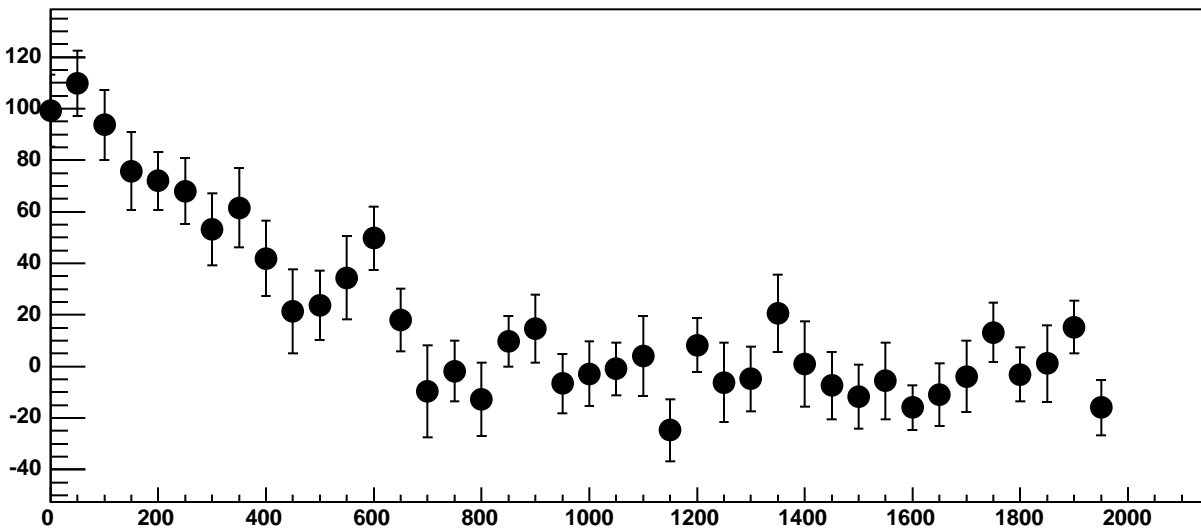


$\chi^2 / \text{ndf}$  10.47 / 11  
p0  $-1379 \pm 21.44$   
p1  $0.145 \pm 0.01968$

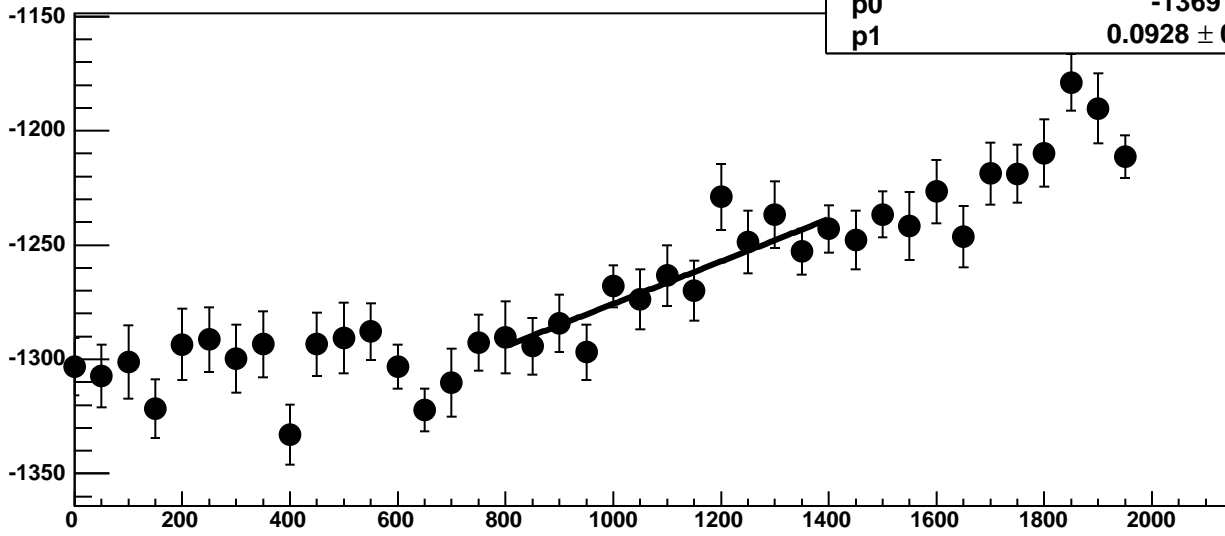
Chip 2, Channel 13, Enable 1, Hold=30, ADC Noise vs DAC



Chip 2, Channel 13, Enable 1, Hold=30, ADC Residuals vs DAC

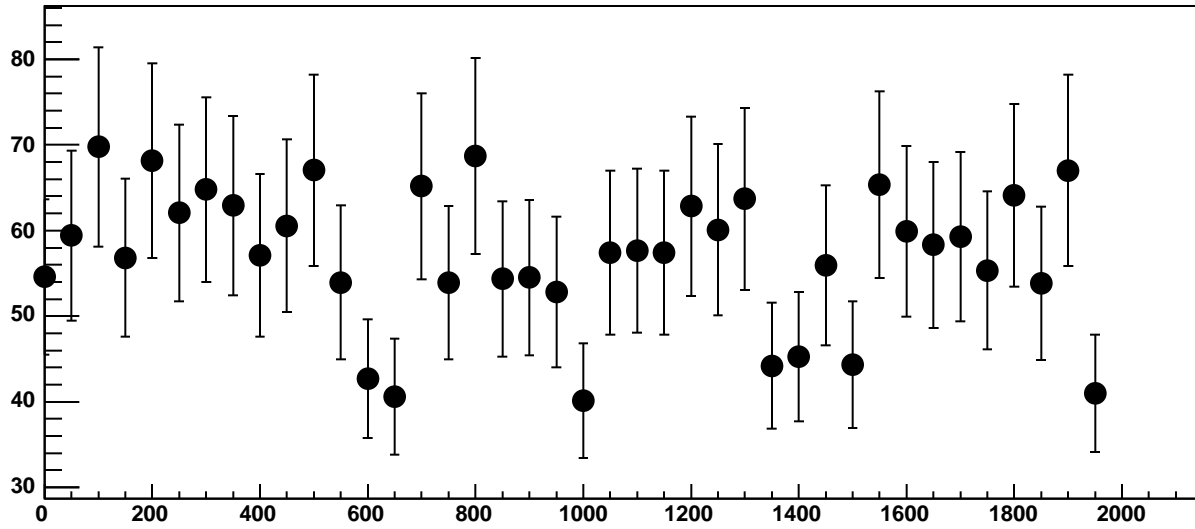


Chip 2, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC

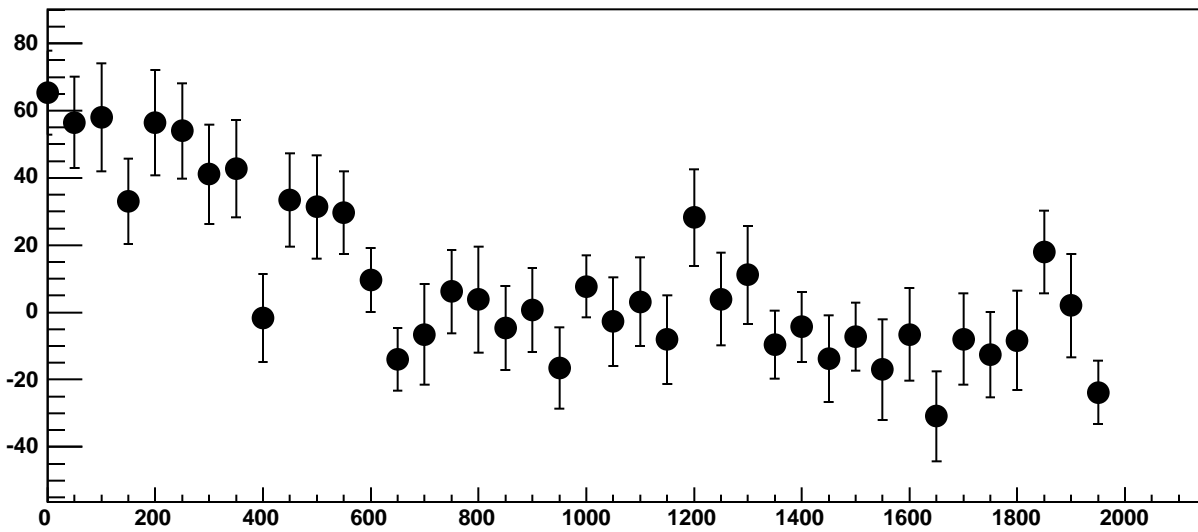


$\chi^2 / \text{ndf}$  8.804 / 11  
p0  $-1369 \pm 20.27$   
p1  $0.0928 \pm 0.01795$

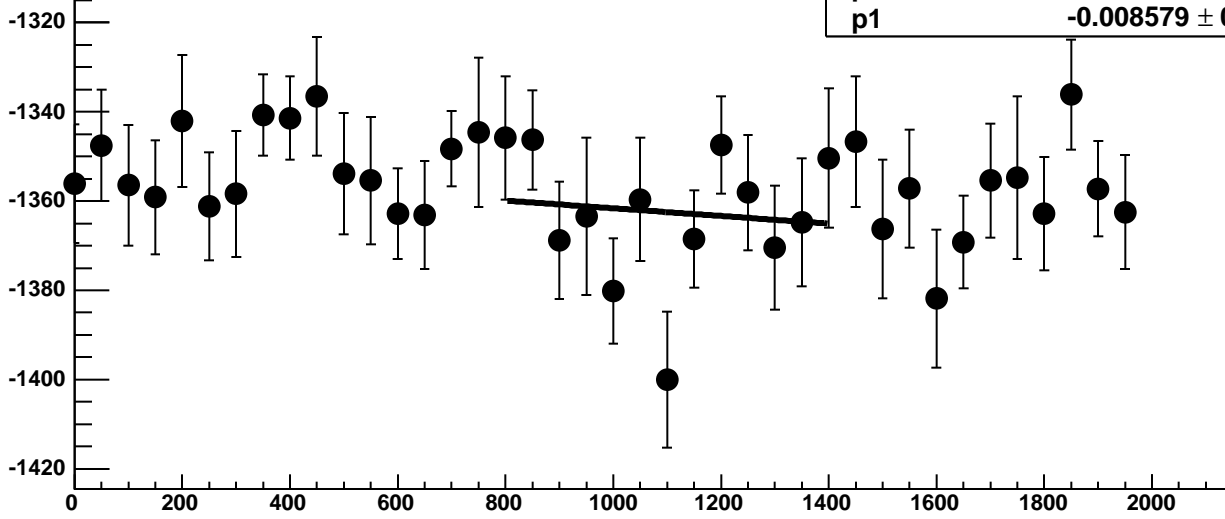
Chip 2, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC



Chip 2, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC

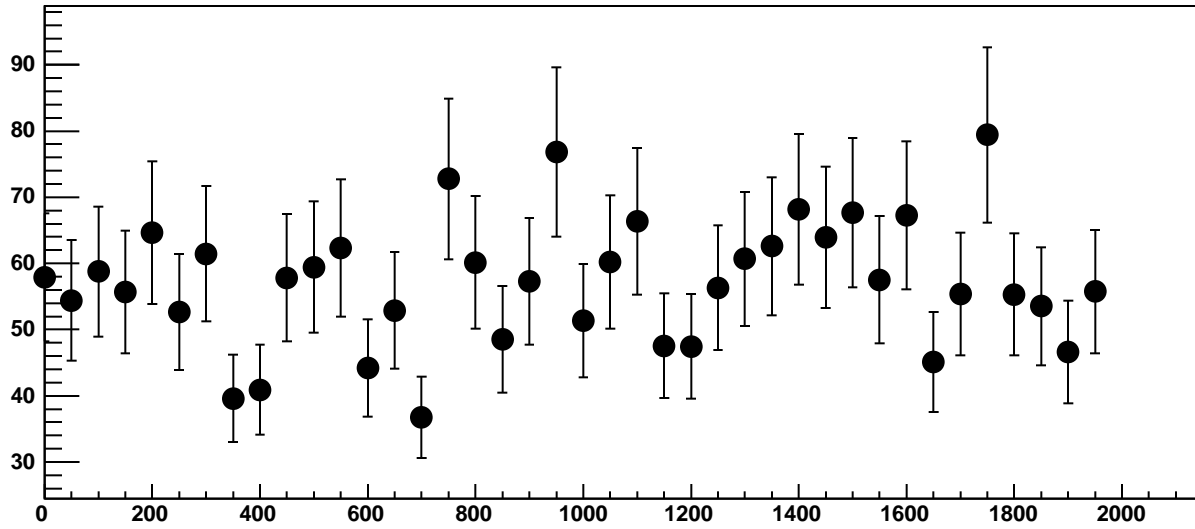


Chip 2, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC

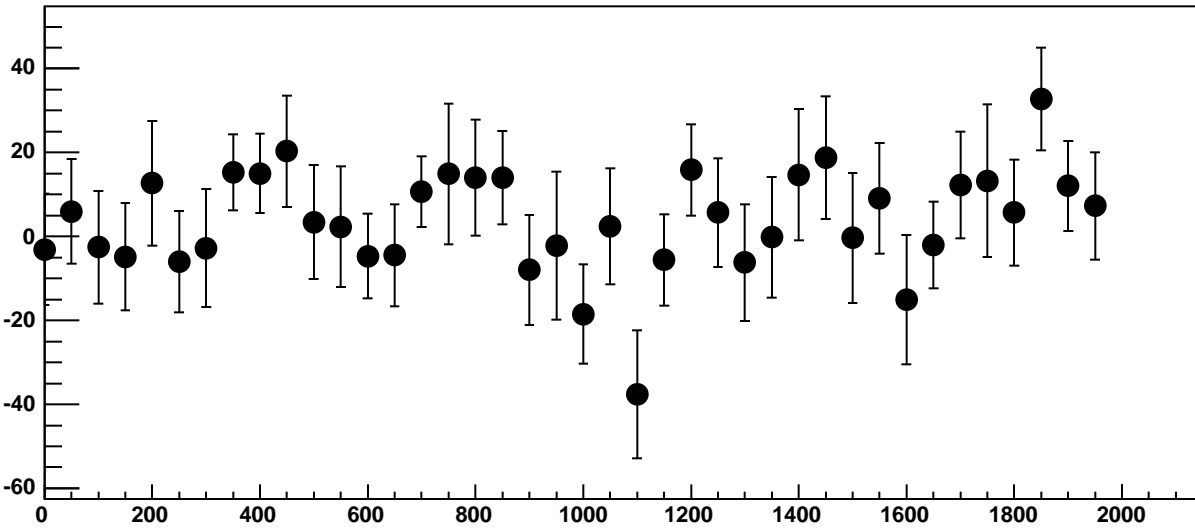


$\chi^2 / \text{ndf}$  15.26 / 11  
p0  $-1353 \pm 22.24$   
p1  $-0.008579 \pm 0.02009$

Chip 2, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC

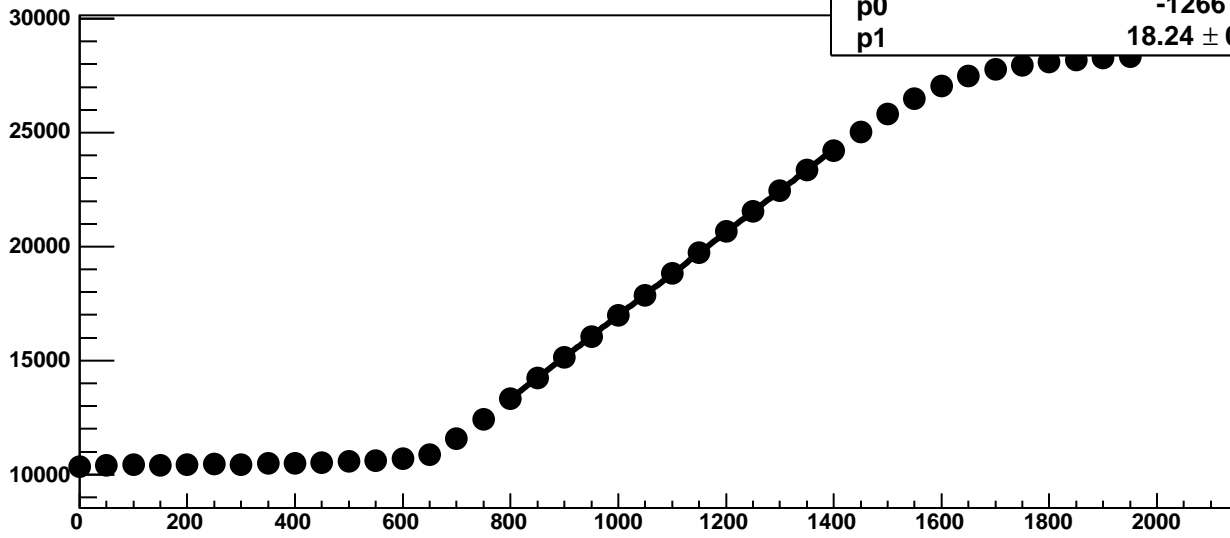


Chip 2, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC



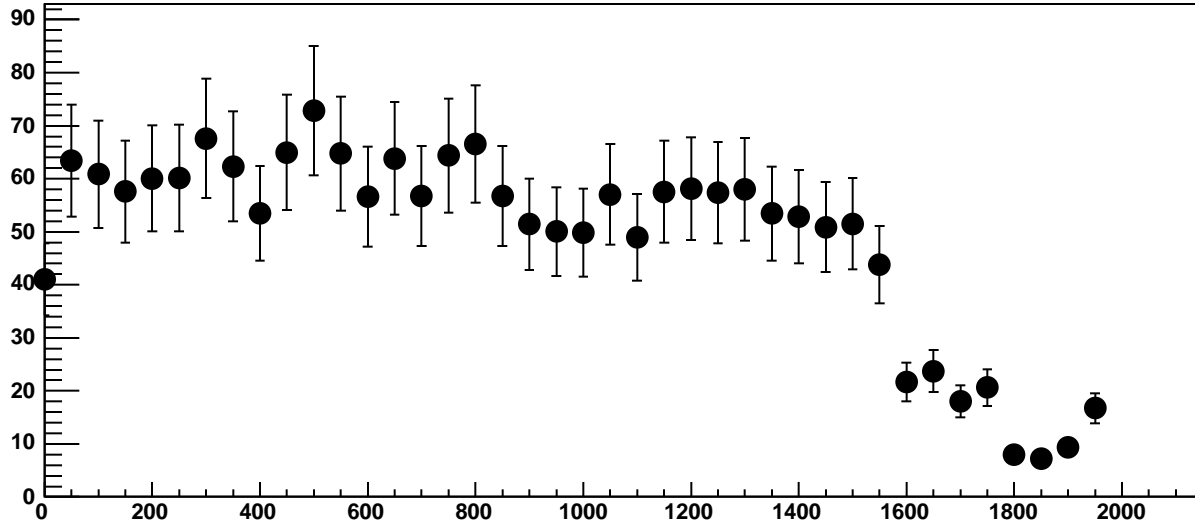


Chip 2, Channel 13, Enable 4!, Hold=30, ADC Mean vs DAC

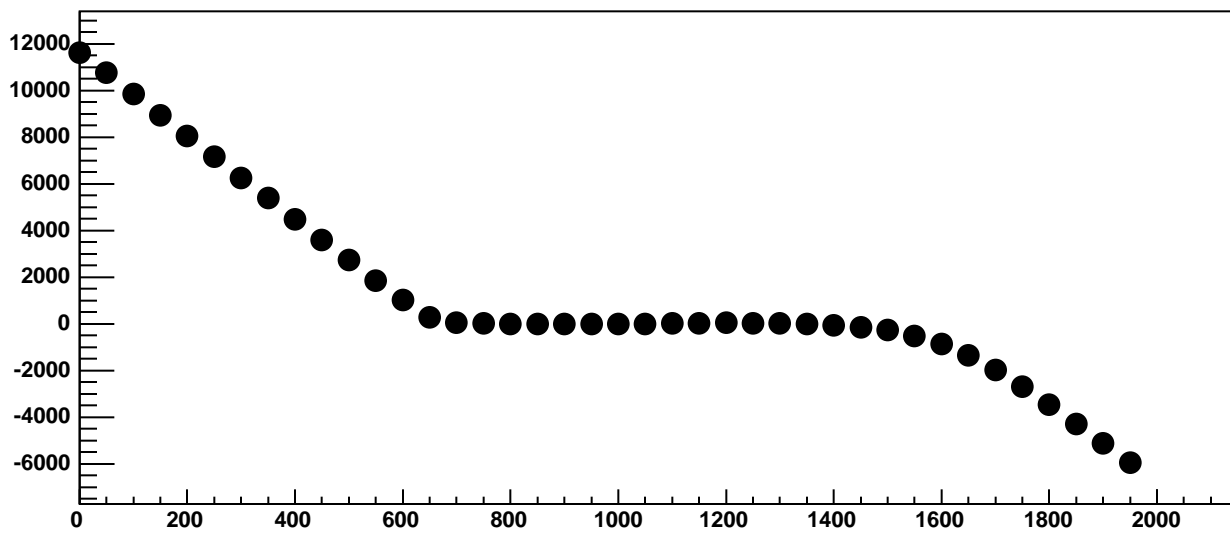


$\chi^2 / \text{ndf}$	38.92 / 11
p0	$-1266 \pm 21.26$
p1	$18.24 \pm 0.01904$

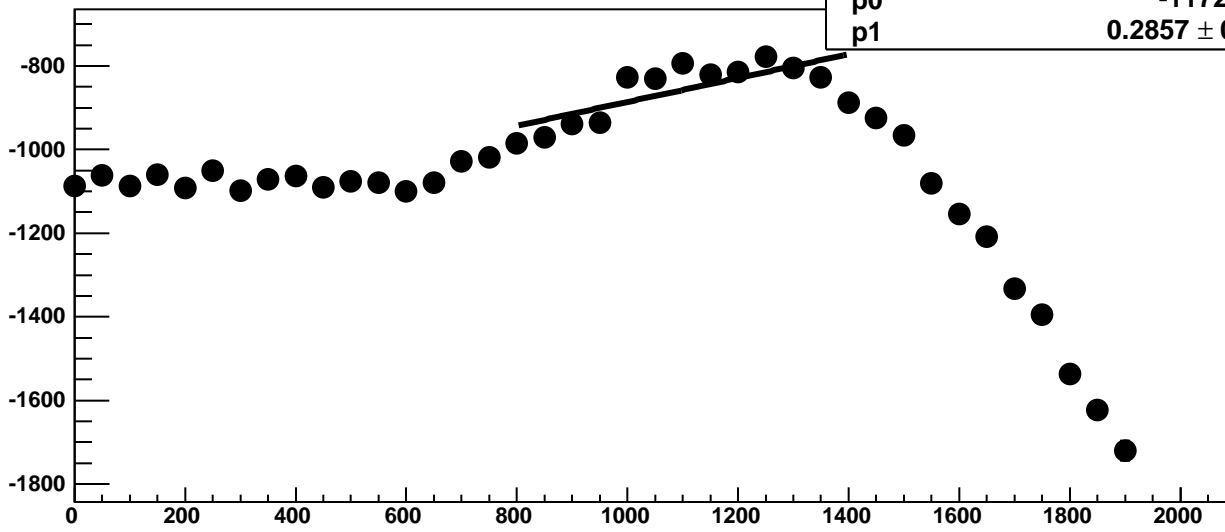
Chip 2, Channel 13, Enable 4!, Hold=30, ADC Noise vs DAC



Chip 2, Channel 13, Enable 4!, Hold=30, ADC Residuals vs DAC

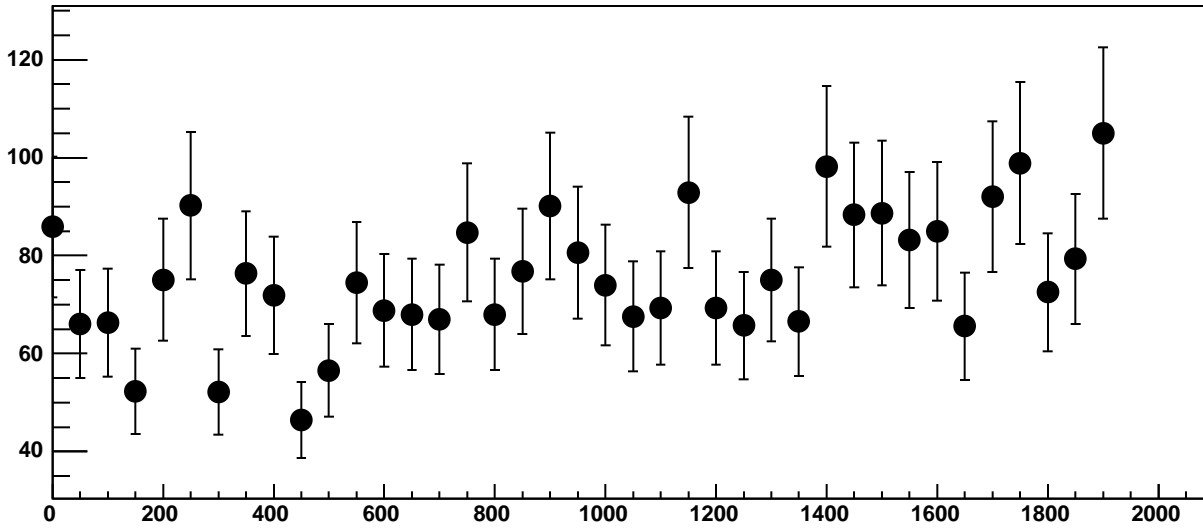


Chip 2, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC

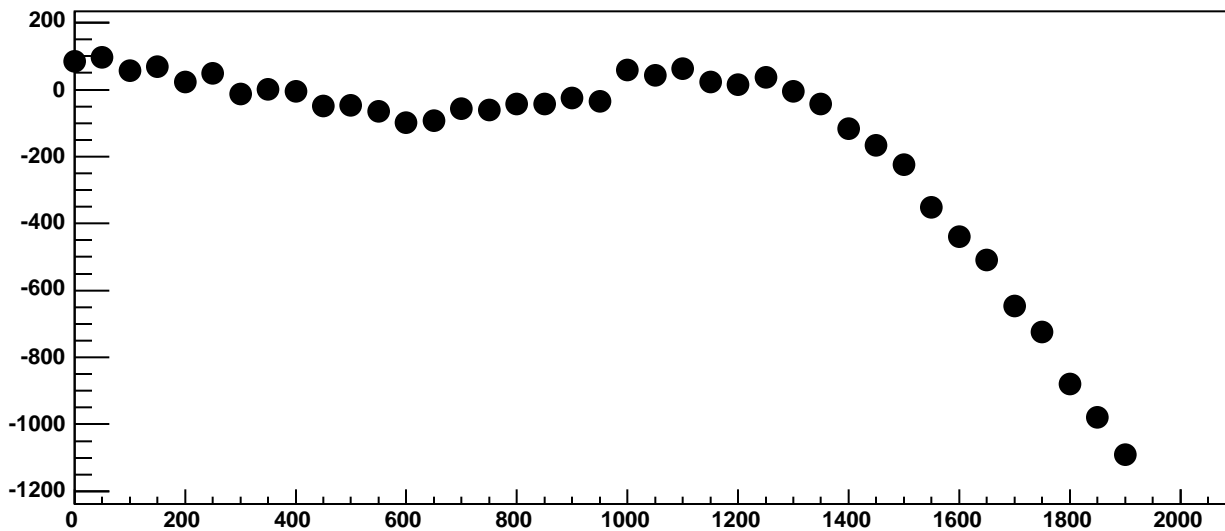


$\chi^2 / \text{ndf}$  95.29 / 11  
p0  $-1172 \pm 28.8$   
p1  $0.2857 \pm 0.02582$

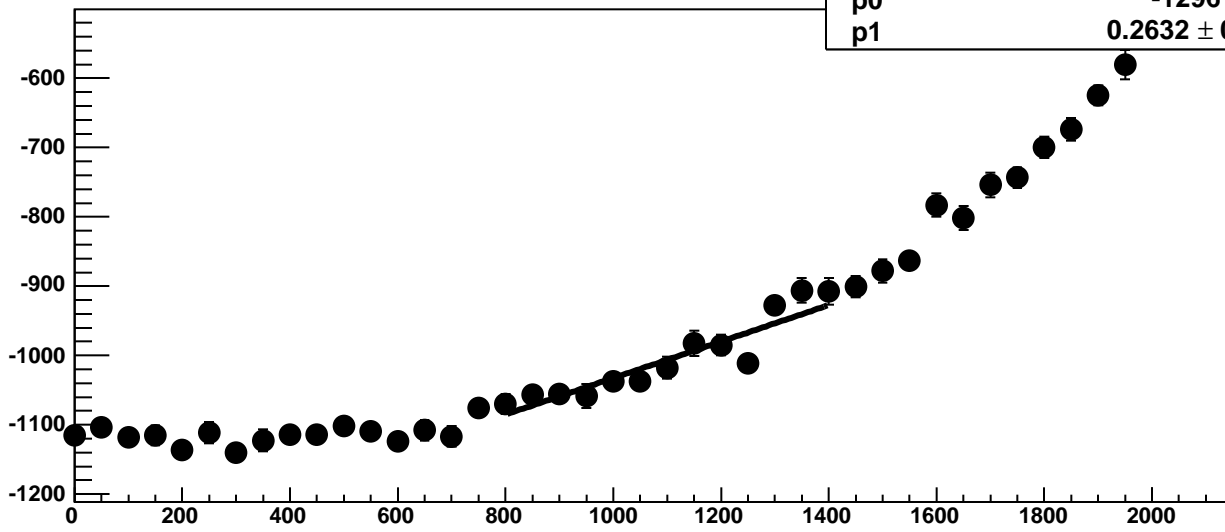
Chip 2, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC



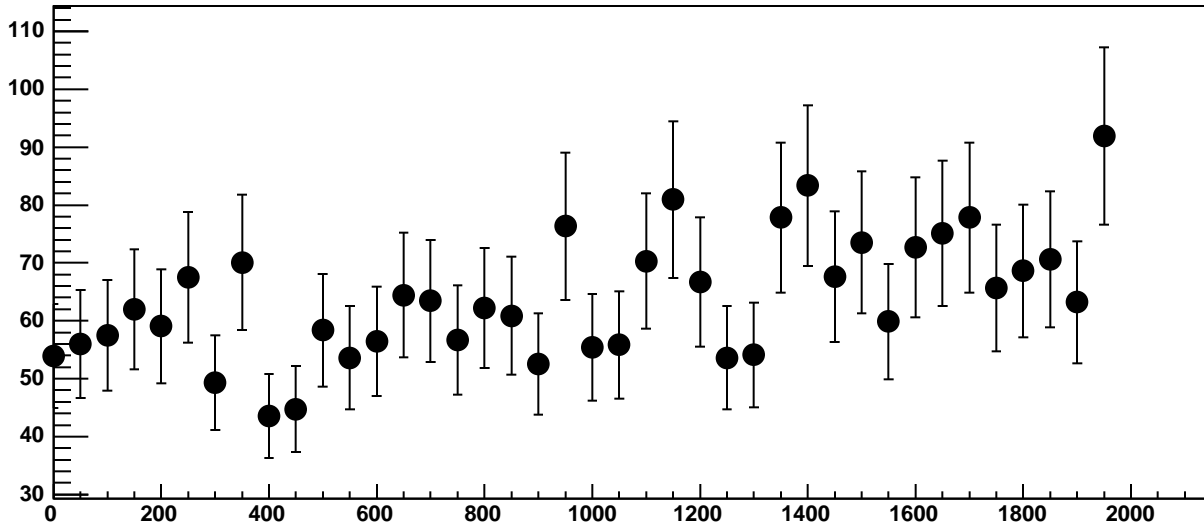
Chip 2, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC



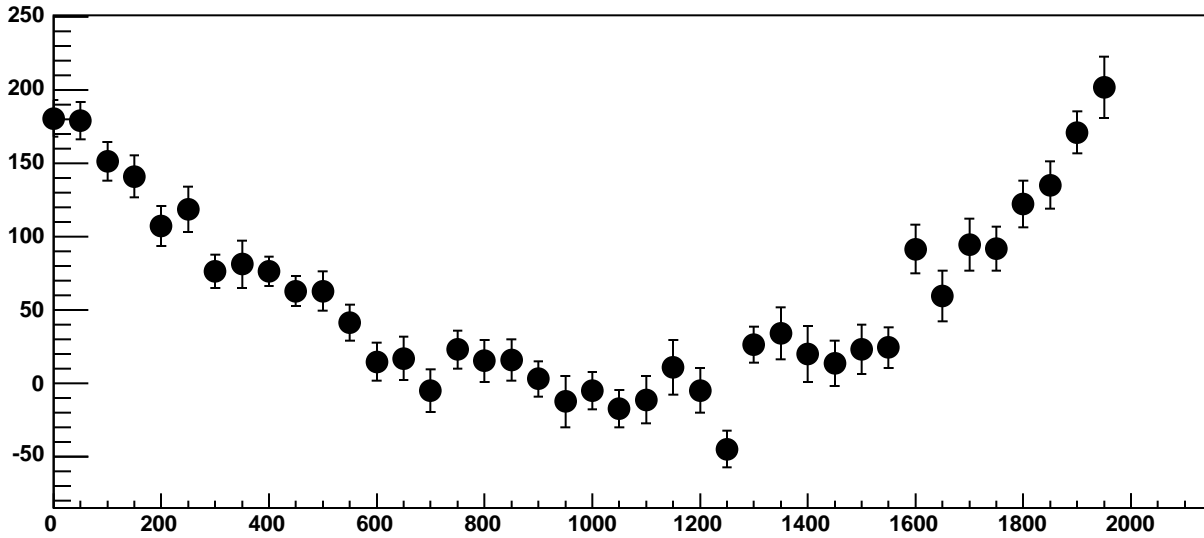
Chip 2, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC



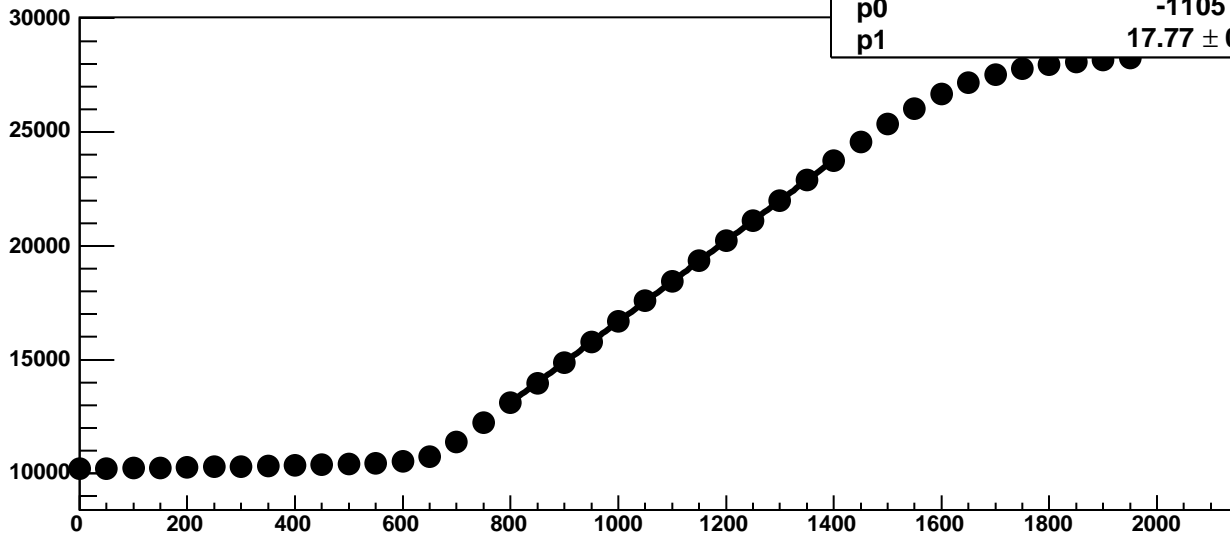
Chip 2, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC



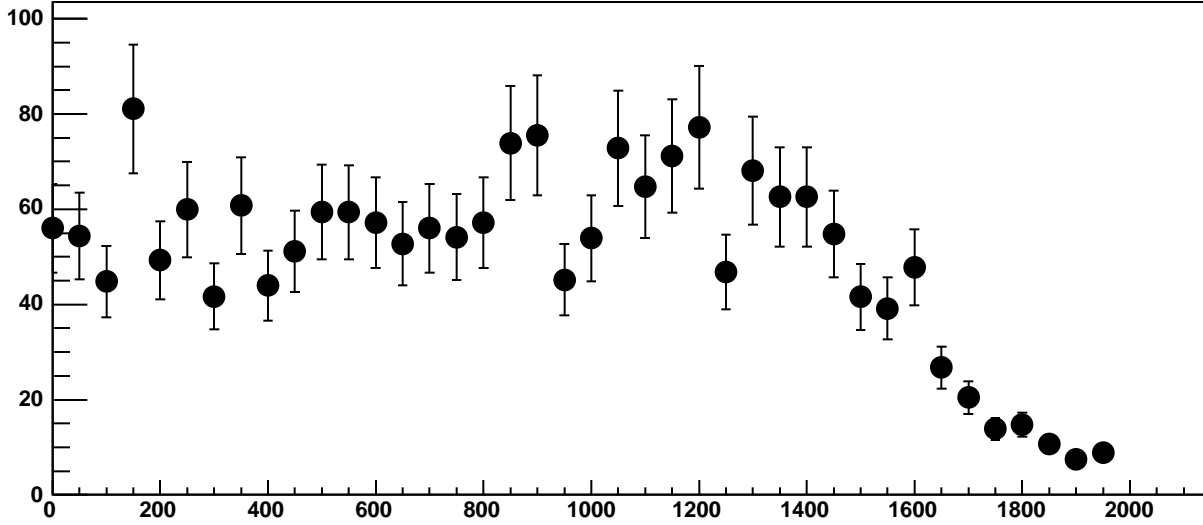
Chip 2, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC



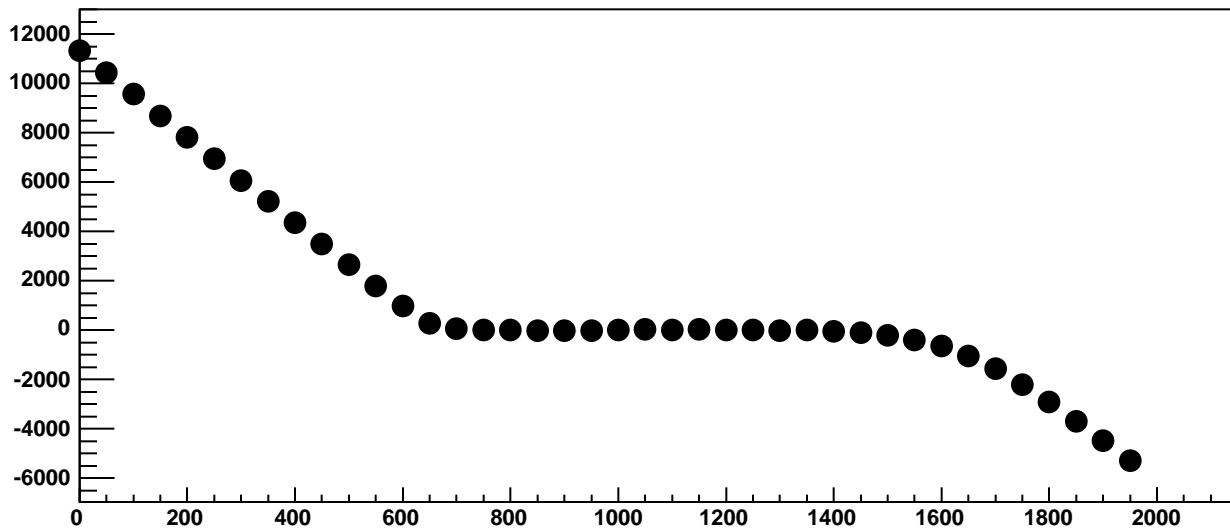
Chip 2, Channel 14, Enable 1!, Hold=30, ADC Mean vs DAC



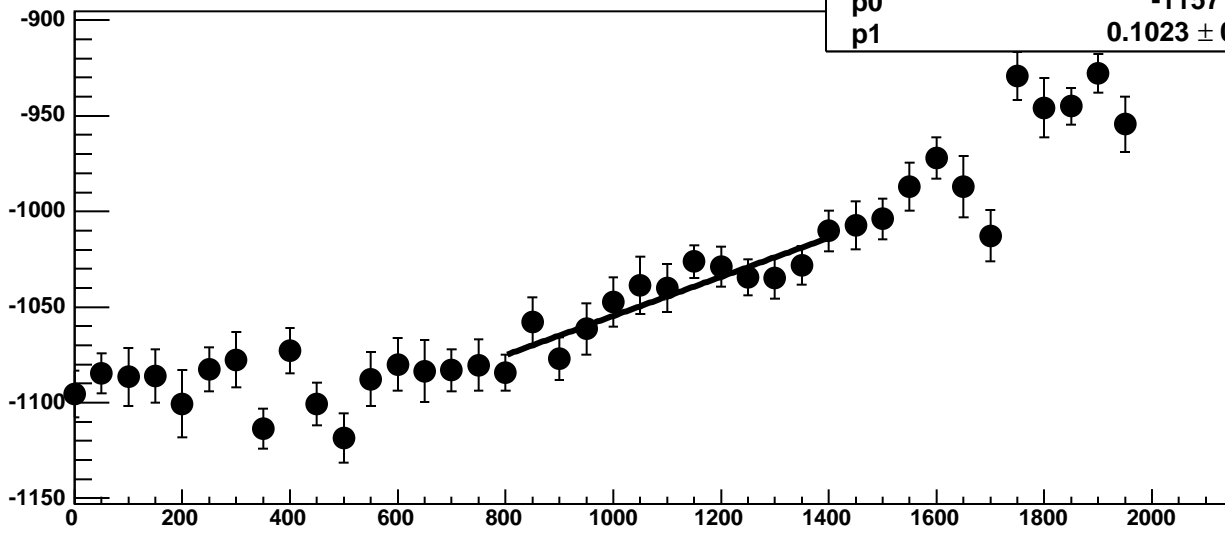
Chip 2, Channel 14, Enable 1!, Hold=30, ADC Noise vs DAC



Chip 2, Channel 14, Enable 1!, Hold=30, ADC Residuals vs DAC

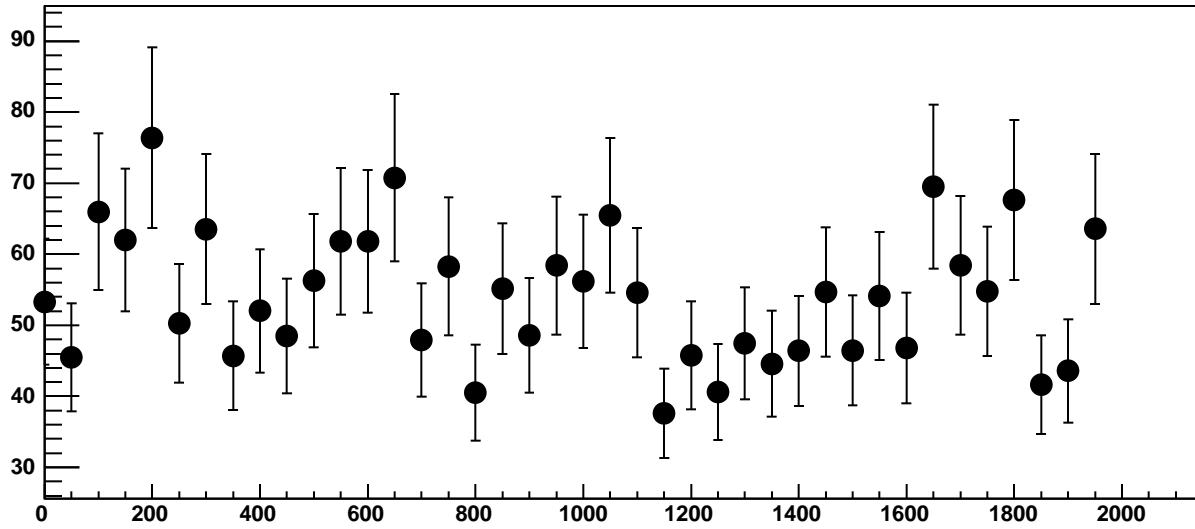


Chip 2, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC

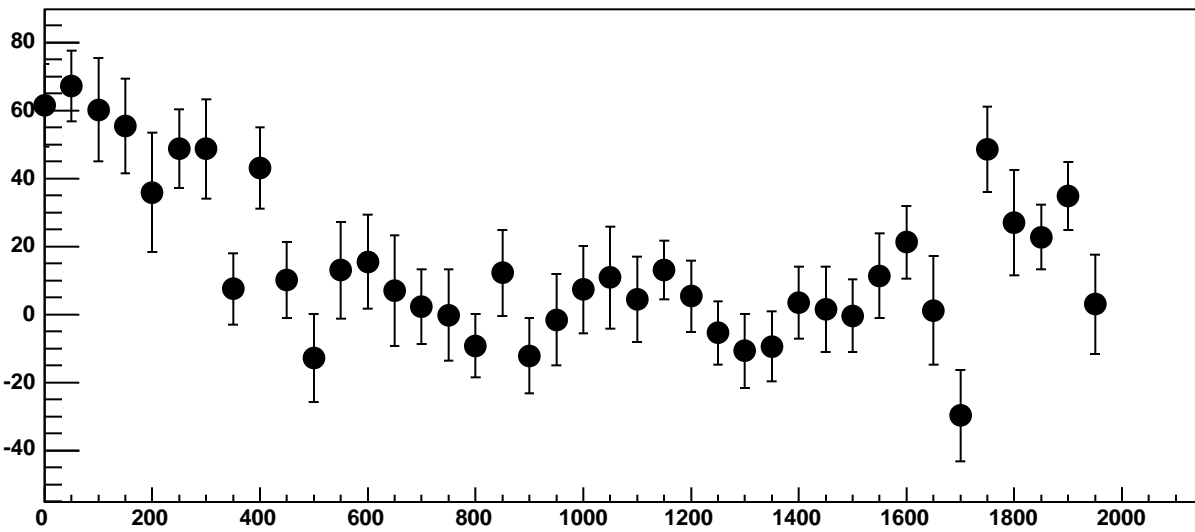


$\chi^2 / \text{ndf}$  8.917 / 11  
p0  $-1157 \pm 17.83$   
p1  $0.1023 \pm 0.01574$

Chip 2, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC

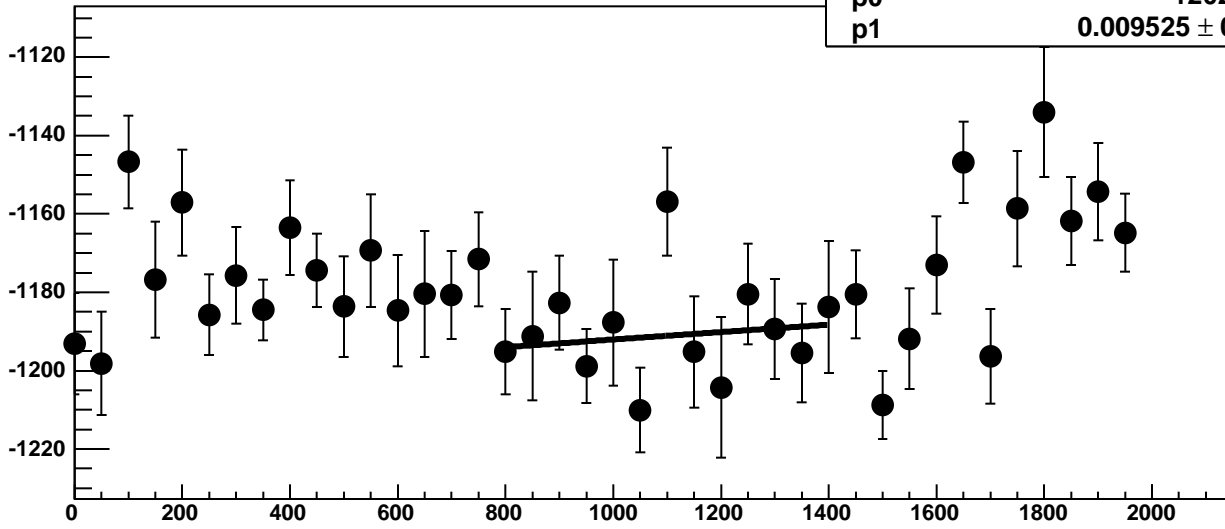


Chip 2, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC

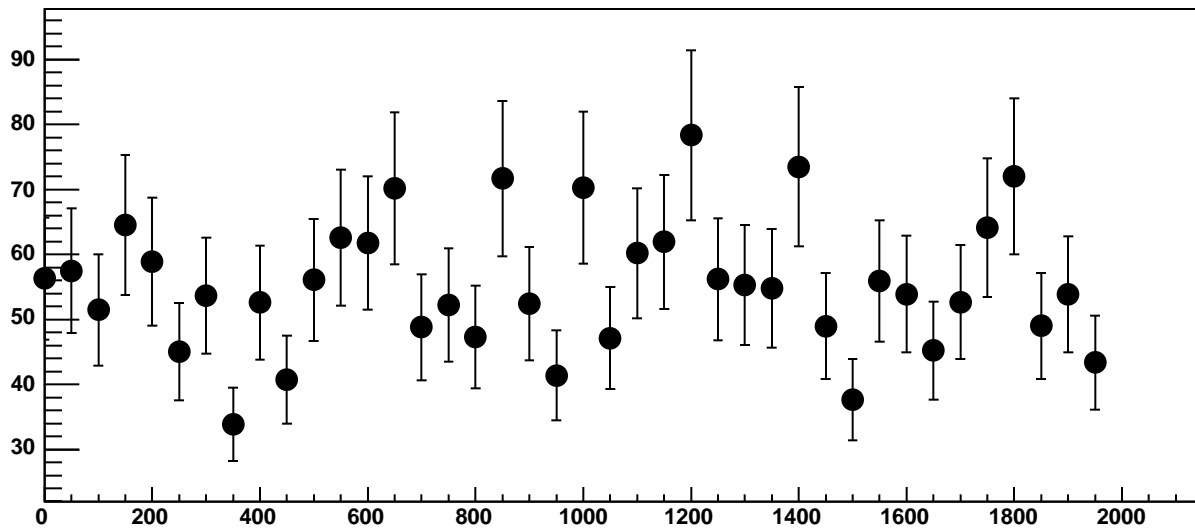


Chip 2, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC

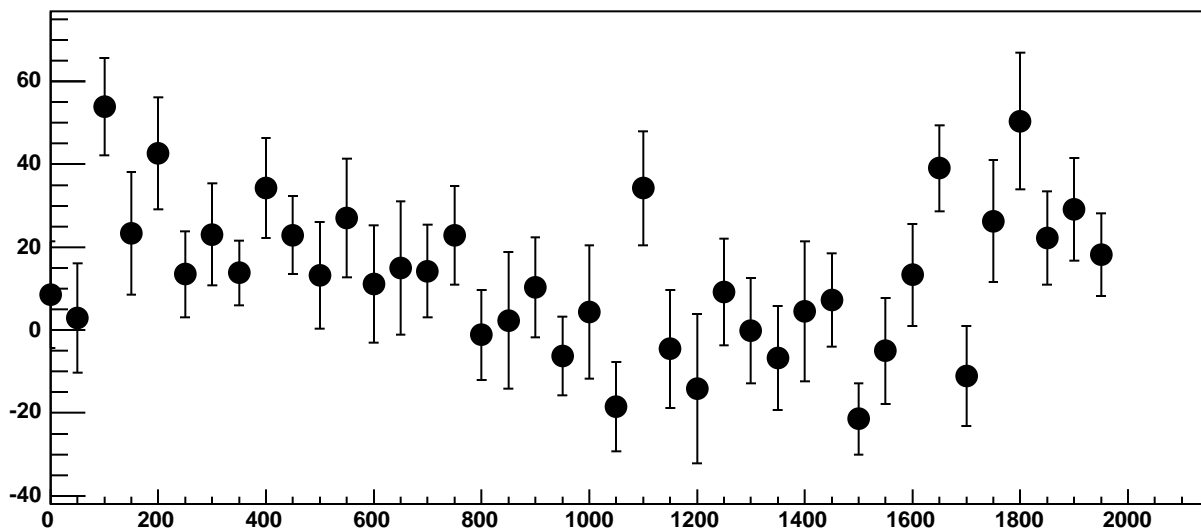
$\chi^2 / \text{ndf}$  11.95 / 11  
p0  $-1202 \pm 21.1$   
p1  $0.009525 \pm 0.01935$



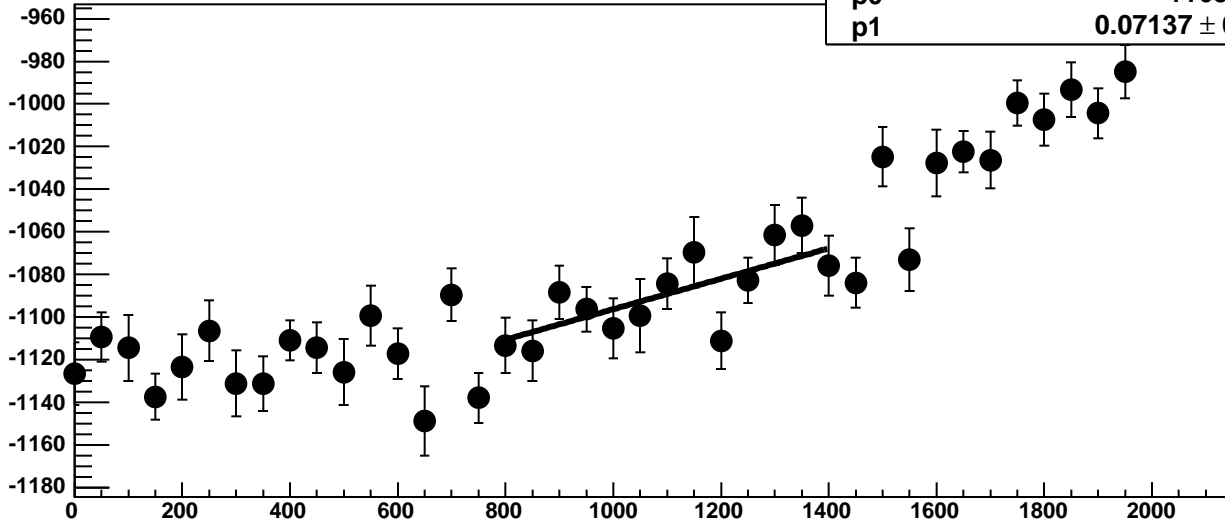
Chip 2, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC



Chip 2, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC

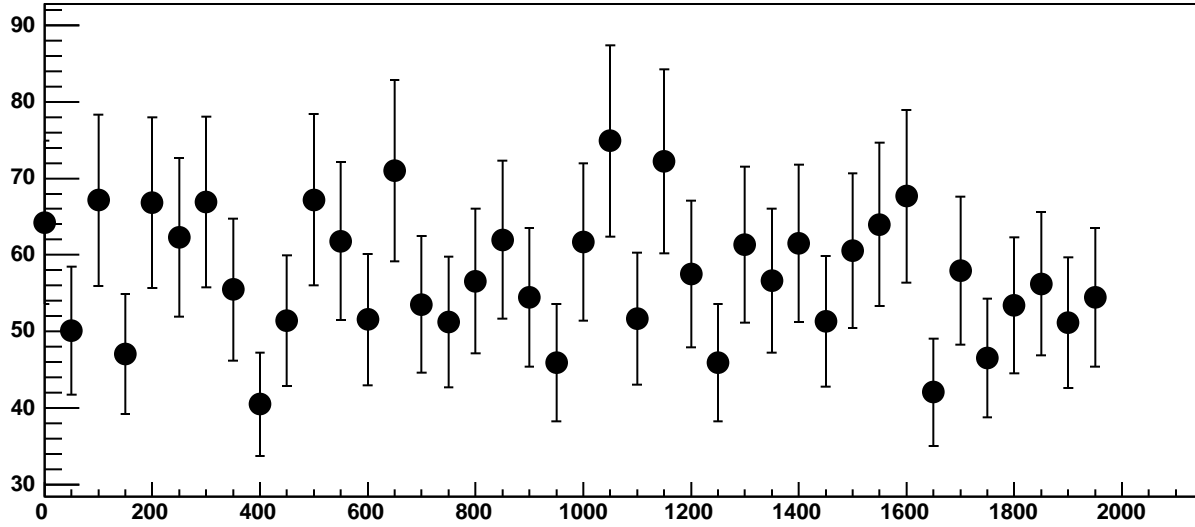


Chip 2, Channel 14, Enable 4, Hold=30, ADC Mean vs DAC

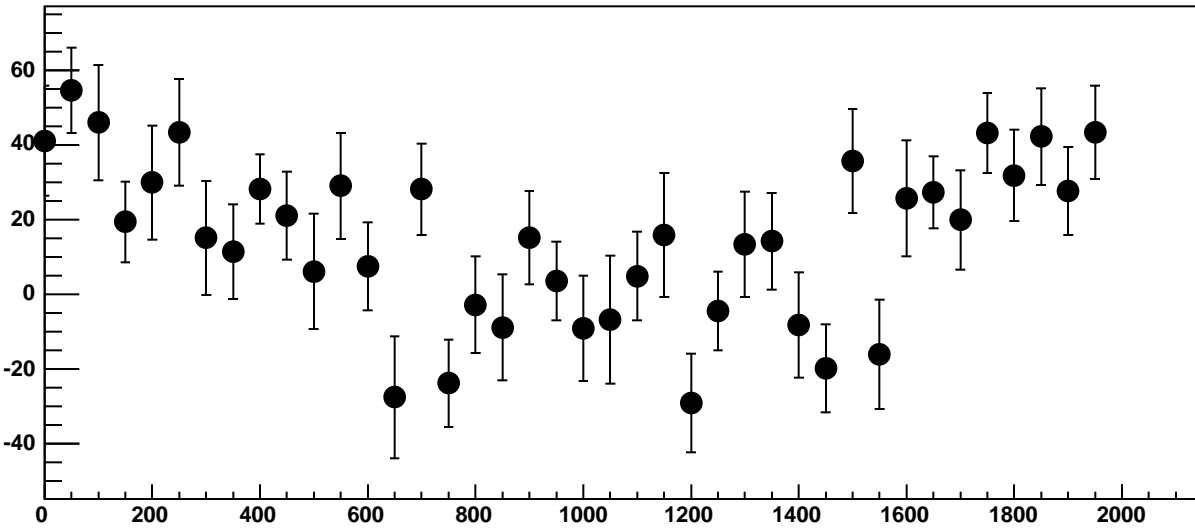


$\chi^2 / \text{ndf}$  11.17 / 11  
p0  $-1168 \pm 21.6$   
p1  $0.07137 \pm 0.01941$

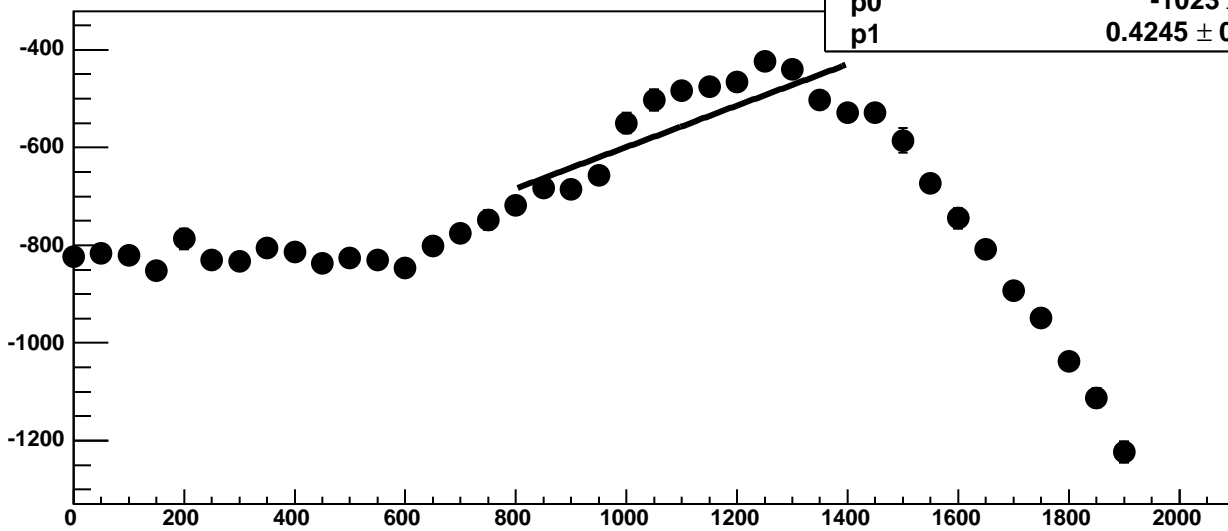
Chip 2, Channel 14, Enable 4, Hold=30, ADC Noise vs DAC



Chip 2, Channel 14, Enable 4, Hold=30, ADC Residuals vs DAC

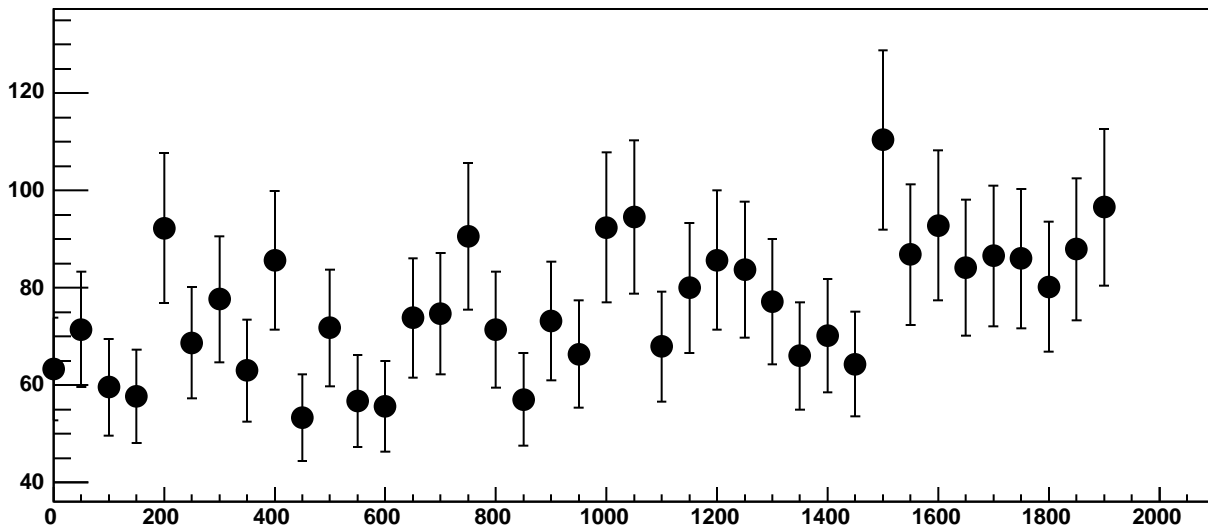


Chip 2, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC

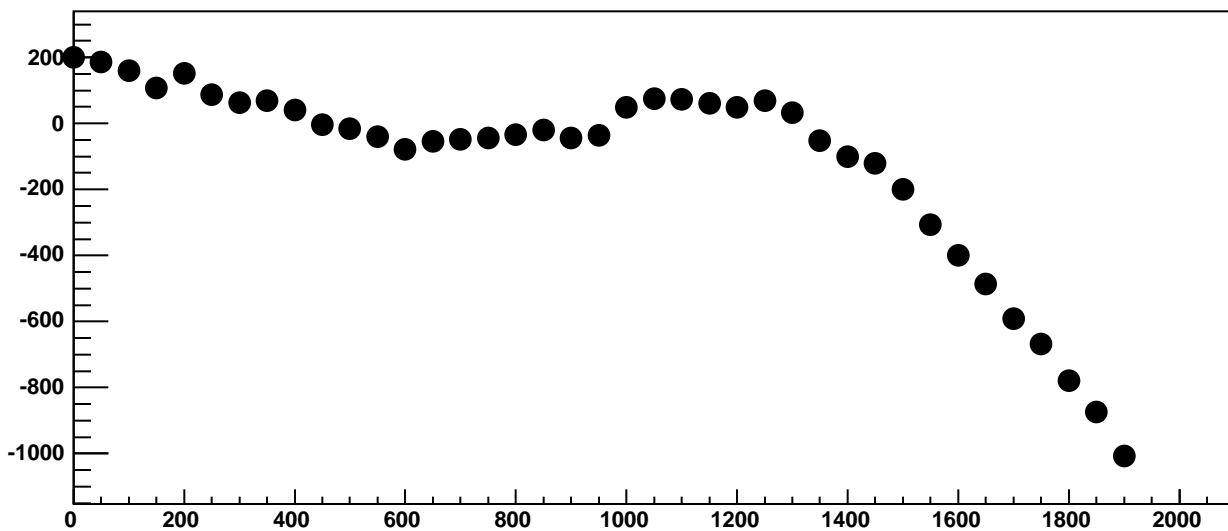


$\chi^2 / \text{ndf}$  142 / 11  
p0  $-1023 \pm 26.04$   
p1  $0.4245 \pm 0.02356$

Chip 2, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC

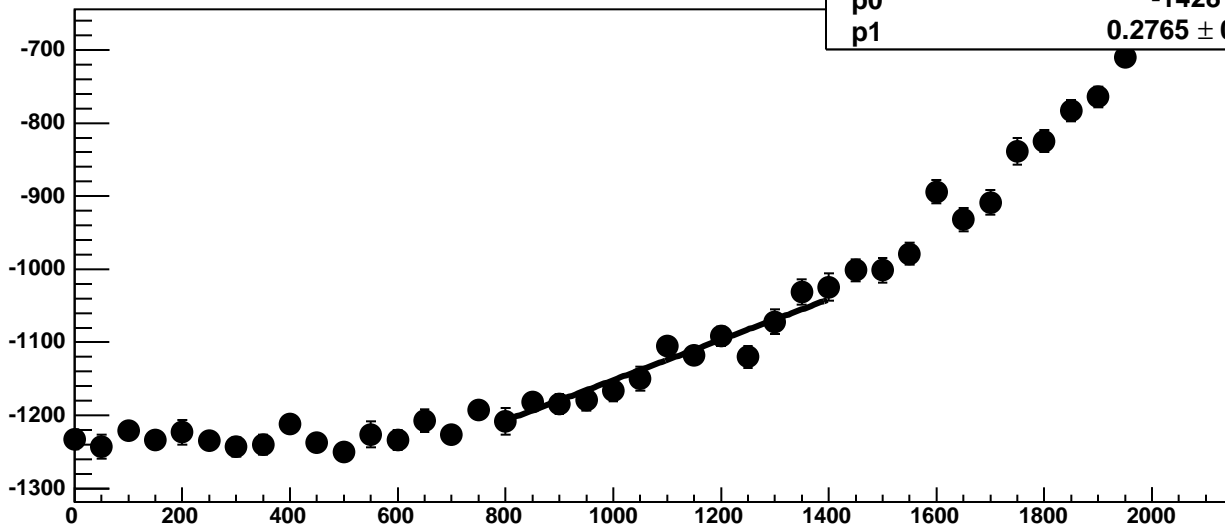


Chip 2, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC



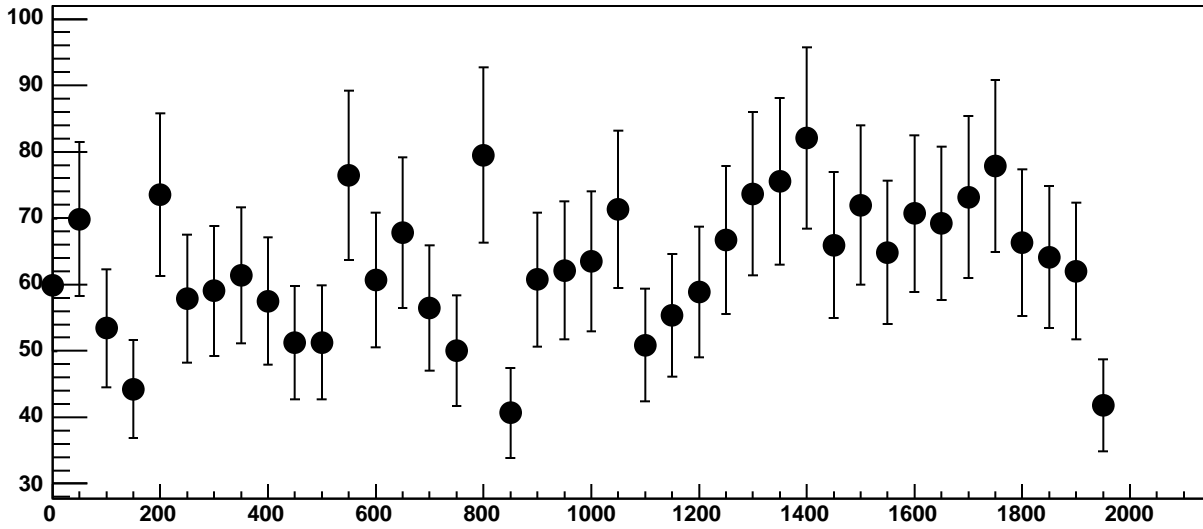


Chip 2, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC

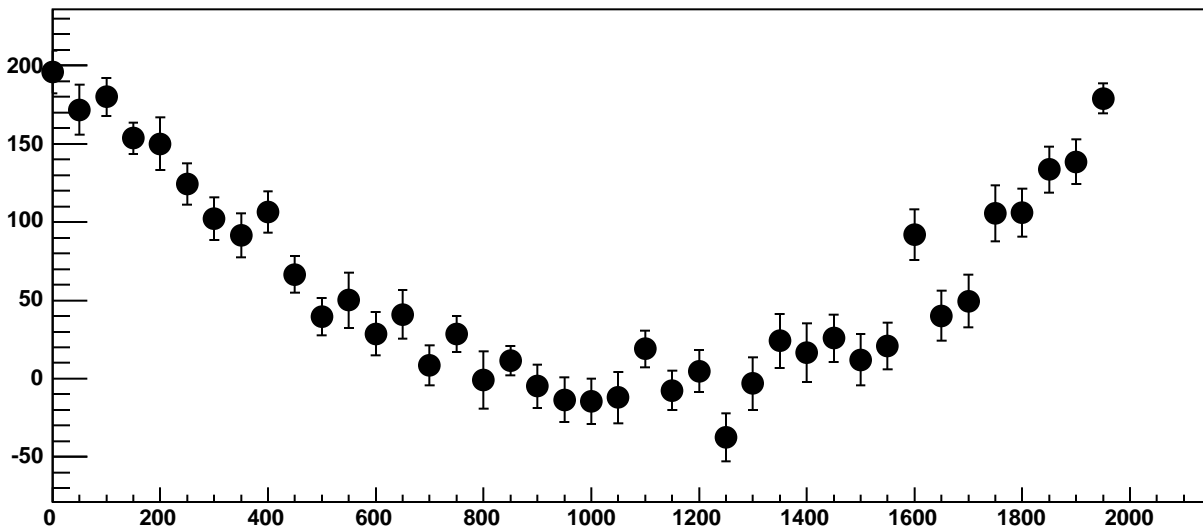


$\chi^2 / \text{ndf}$  15.9 / 11  
p0  $-1428 \pm 23.84$   
p1  $0.2765 \pm 0.02209$

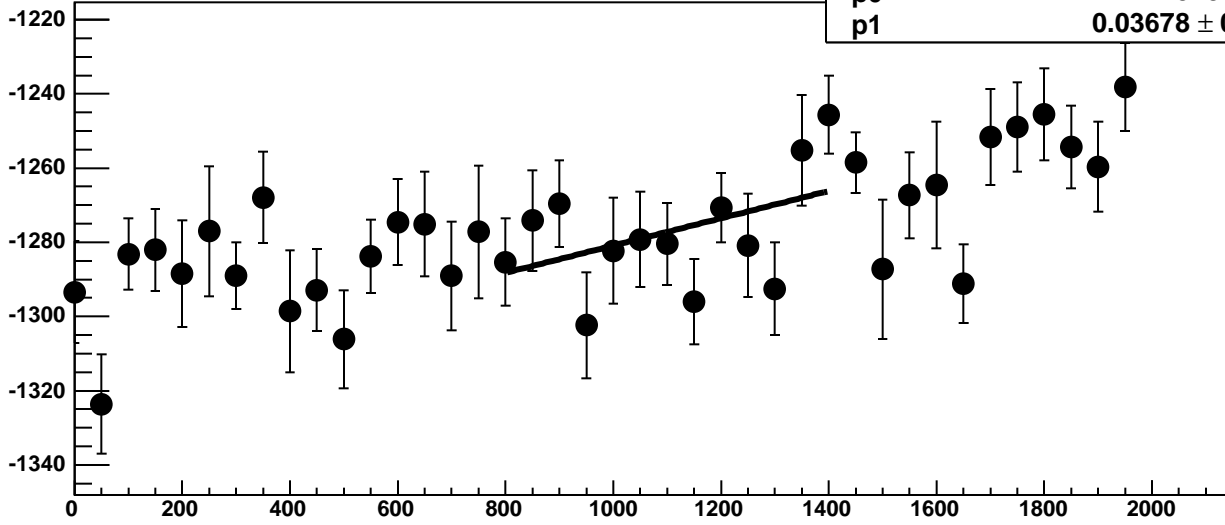
Chip 2, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC



Chip 2, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 2, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

16.14 / 11

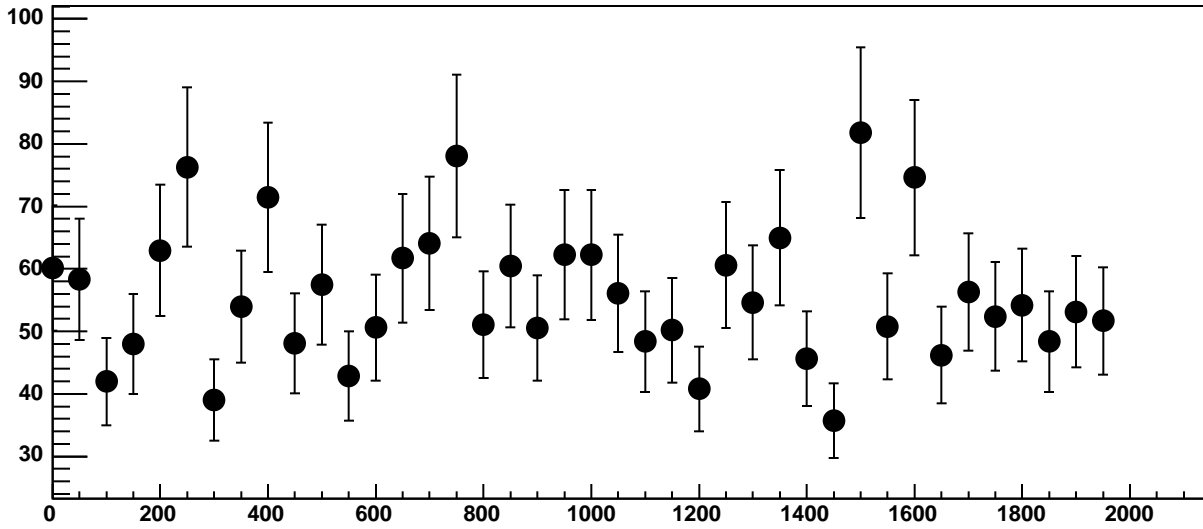
p0

$-1318 \pm 20.32$

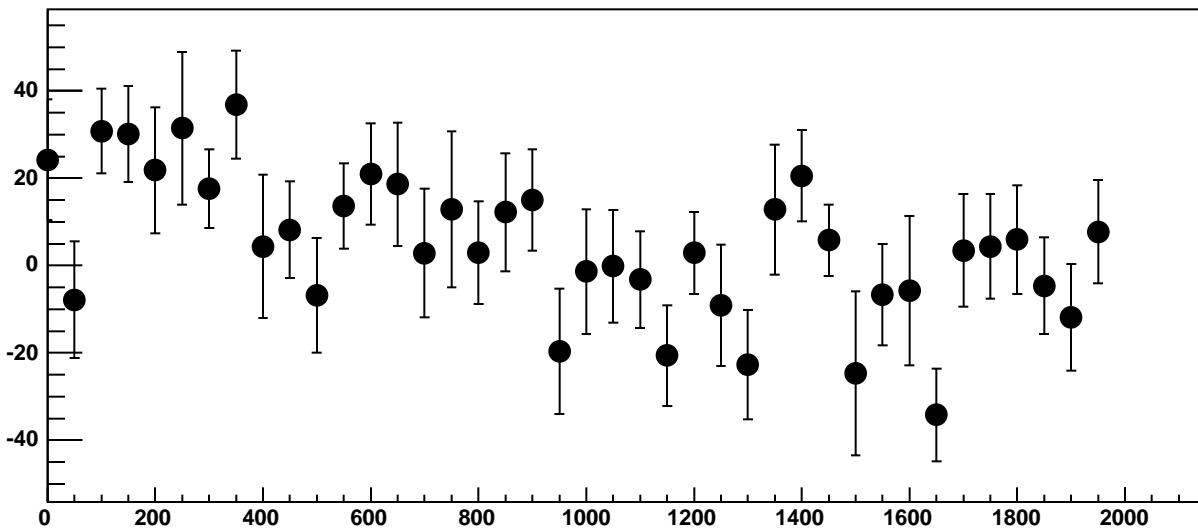
p1

$0.03678 \pm 0.01806$

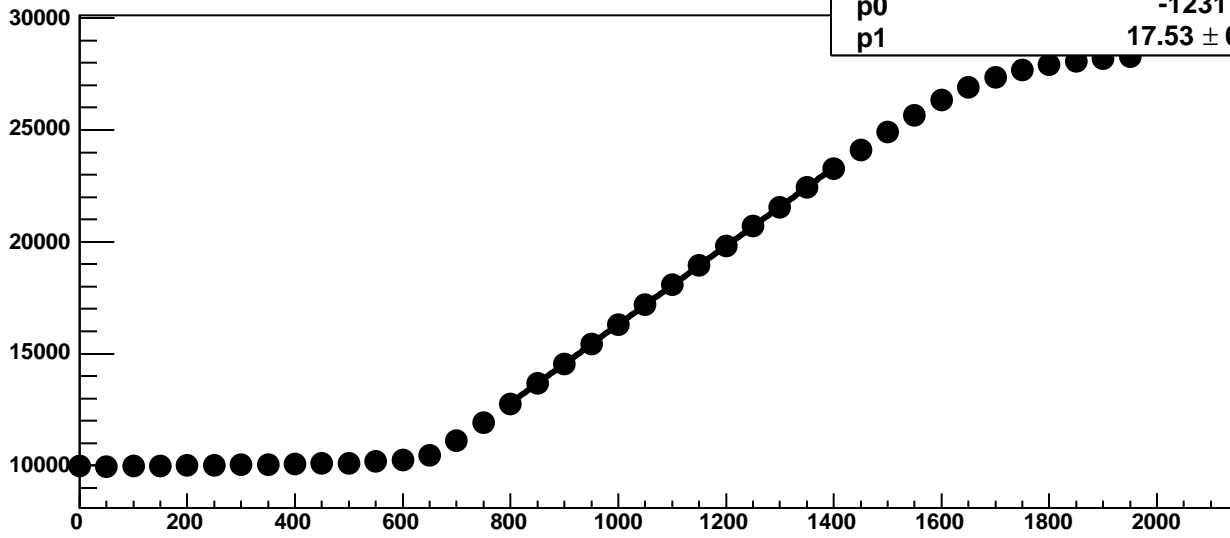
Chip 2, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC



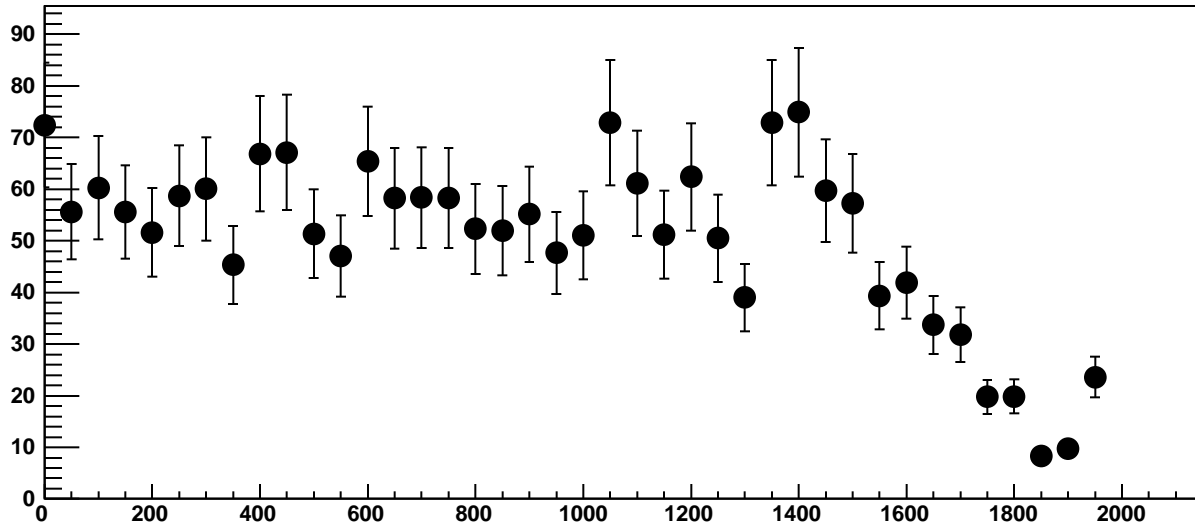
Chip 2, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC



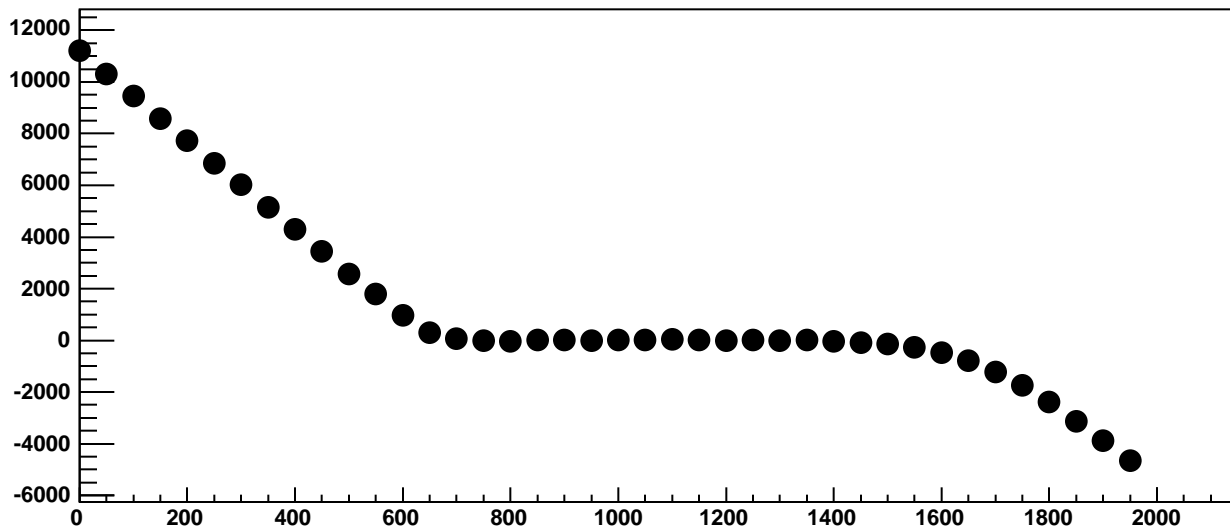
Chip 2, Channel 15, Enable 2!, Hold=30, ADC Mean vs DAC



Chip 2, Channel 15, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 2, Channel 15, Enable 2!, Hold=30, ADC Residuals vs DAC



Chip 2, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

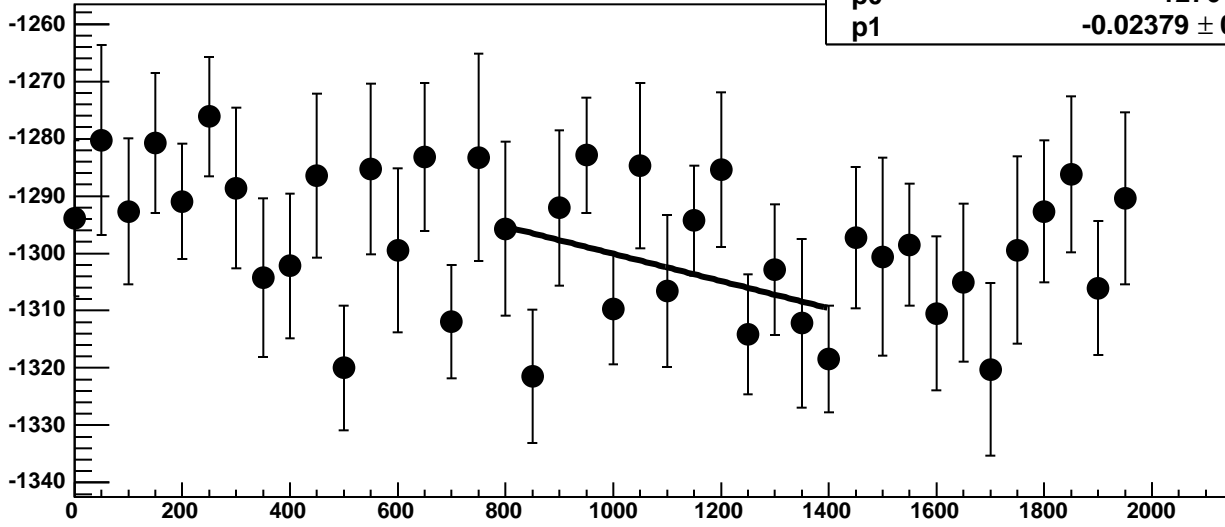
14.5 / 11

p0

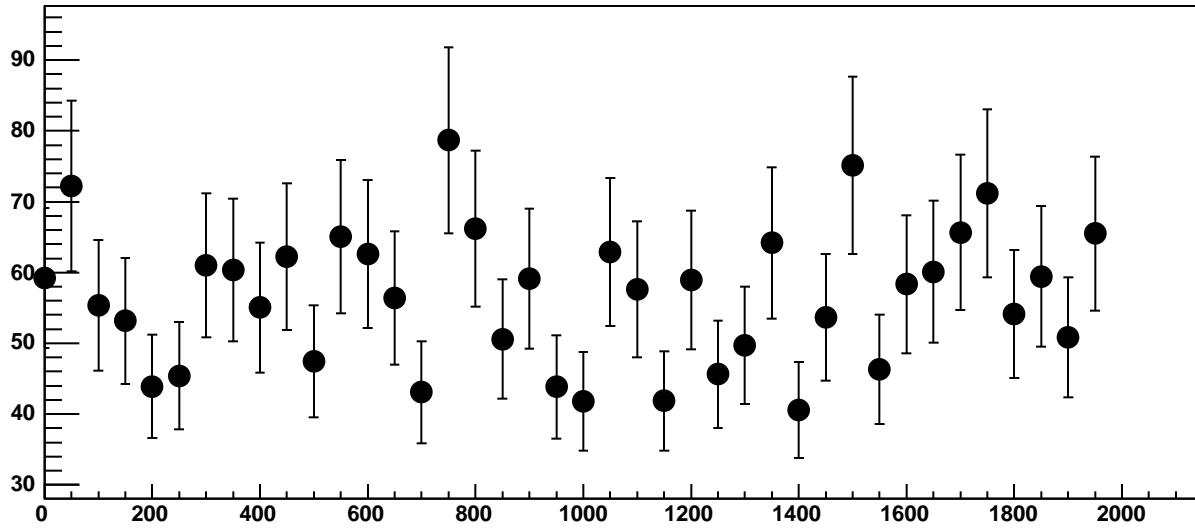
$-1276 \pm 19.65$

p1

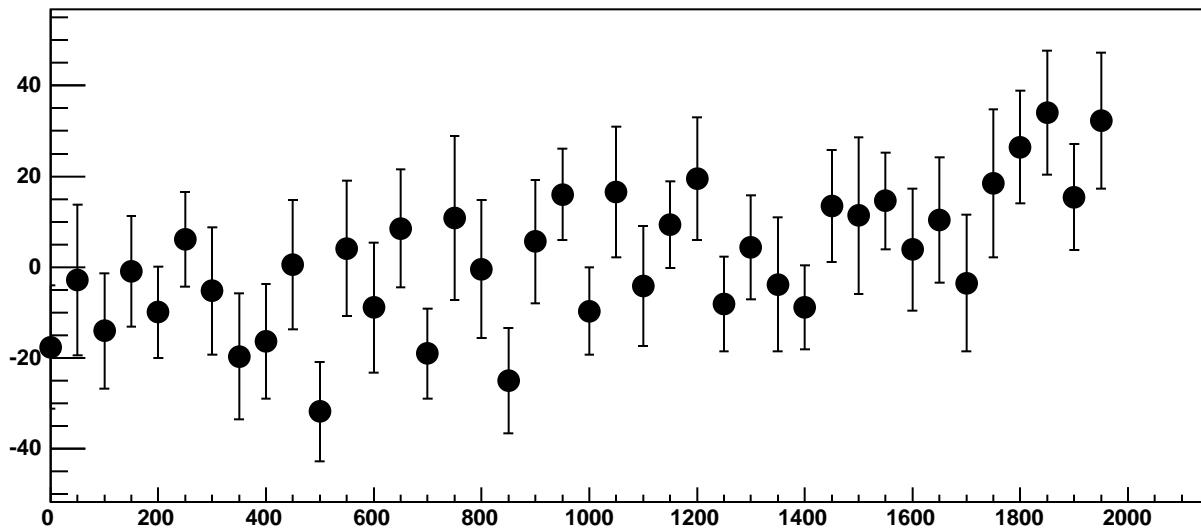
$-0.02379 \pm 0.01738$



Chip 2, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC

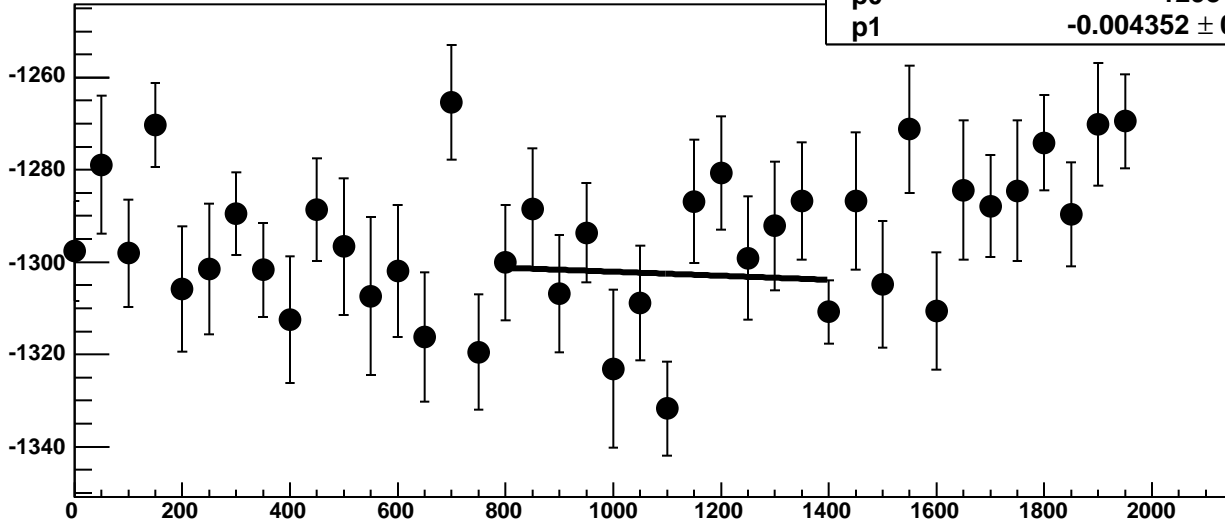


Chip 2, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC

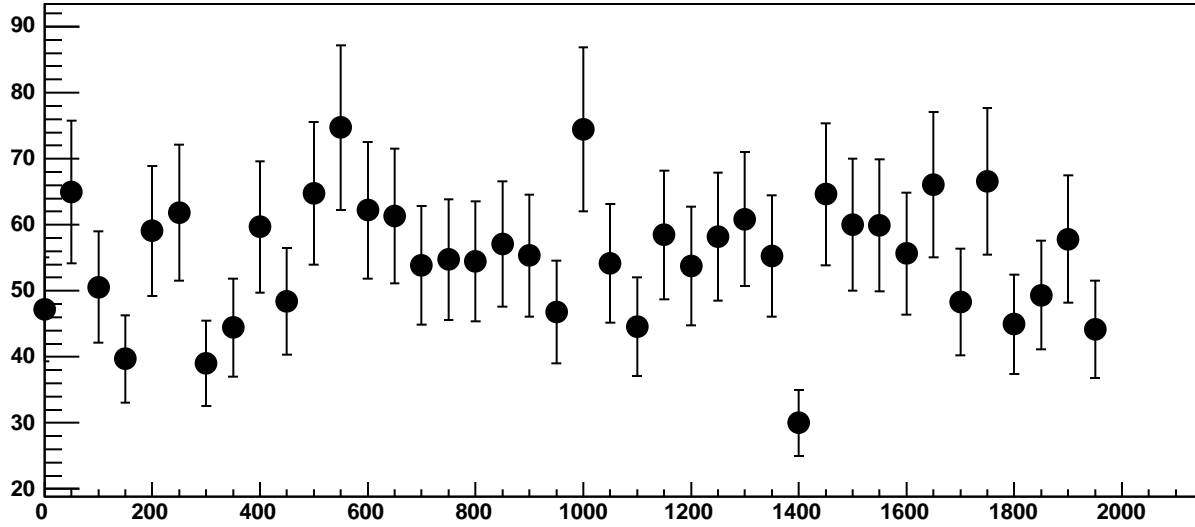


Chip 2, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC

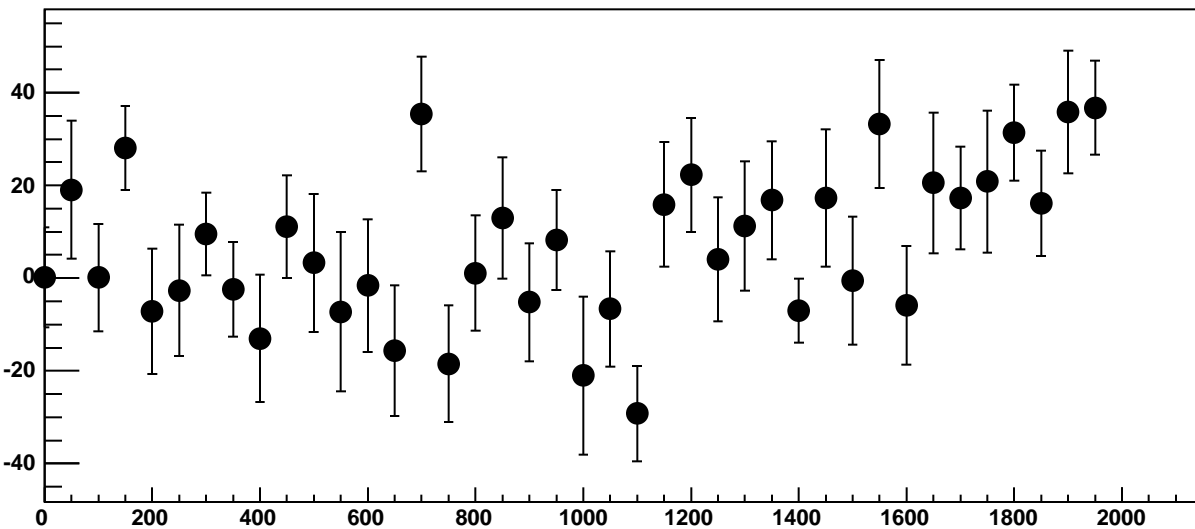
$\chi^2 / \text{ndf}$  19.89 / 11  
p0  $-1298 \pm 18.44$   
p1  $-0.004352 \pm 0.01589$



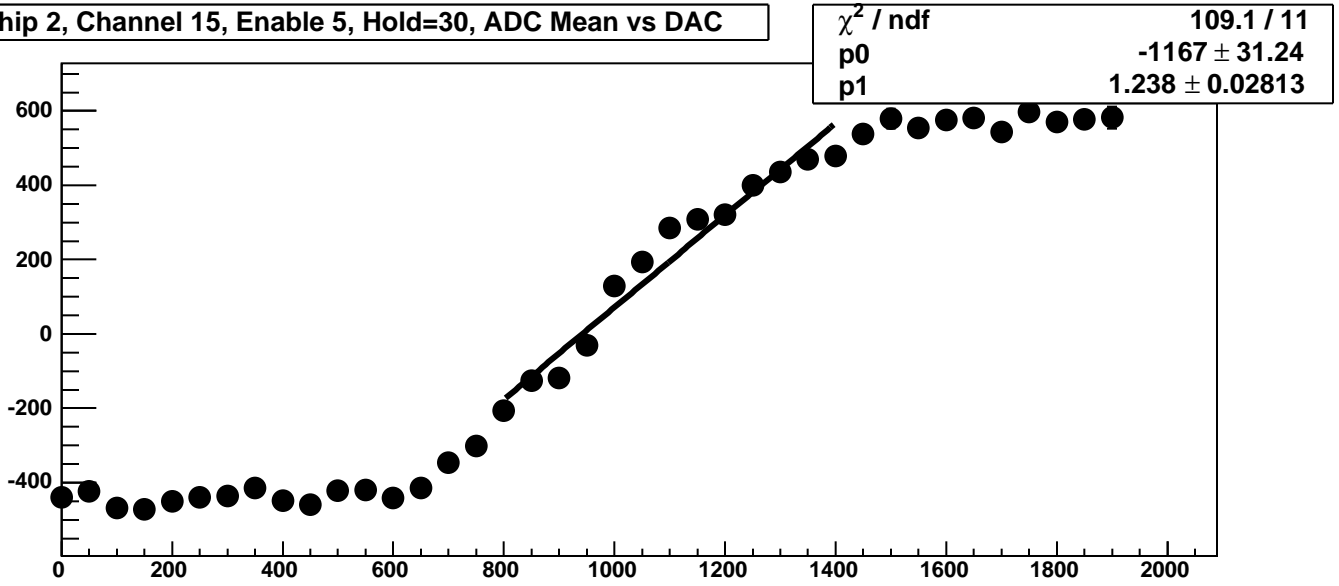
Chip 2, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC



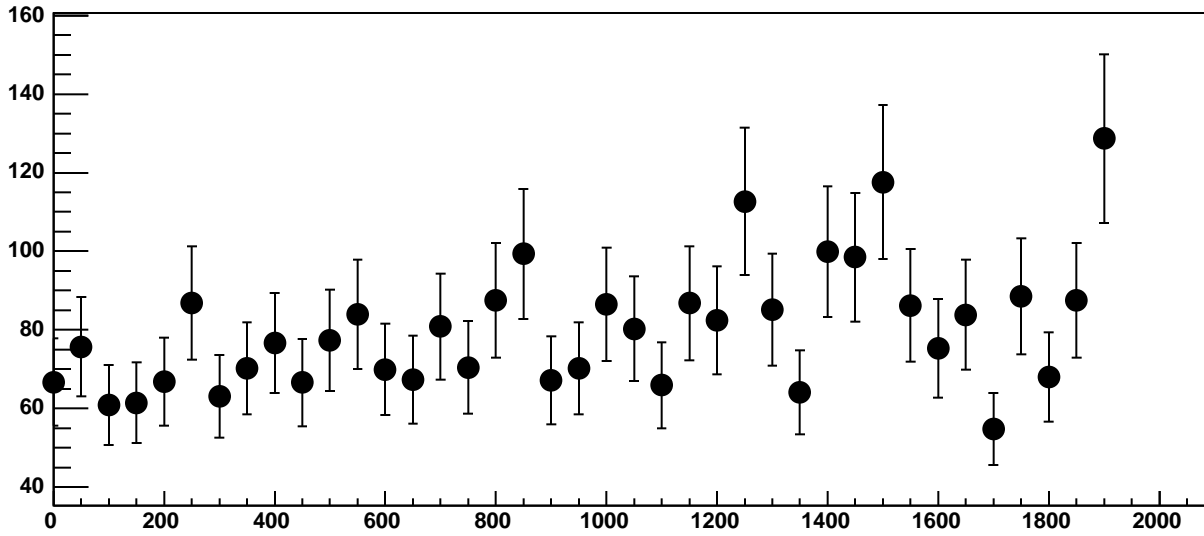
Chip 2, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC



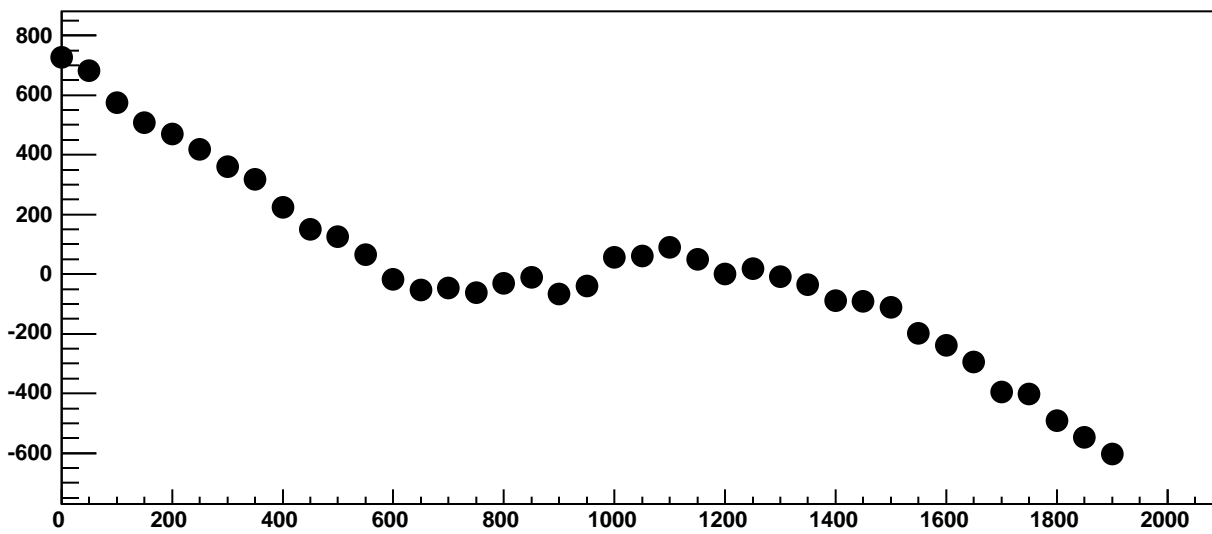
Chip 2, Channel 15, Enable 5, Hold=30, ADC Mean vs DAC



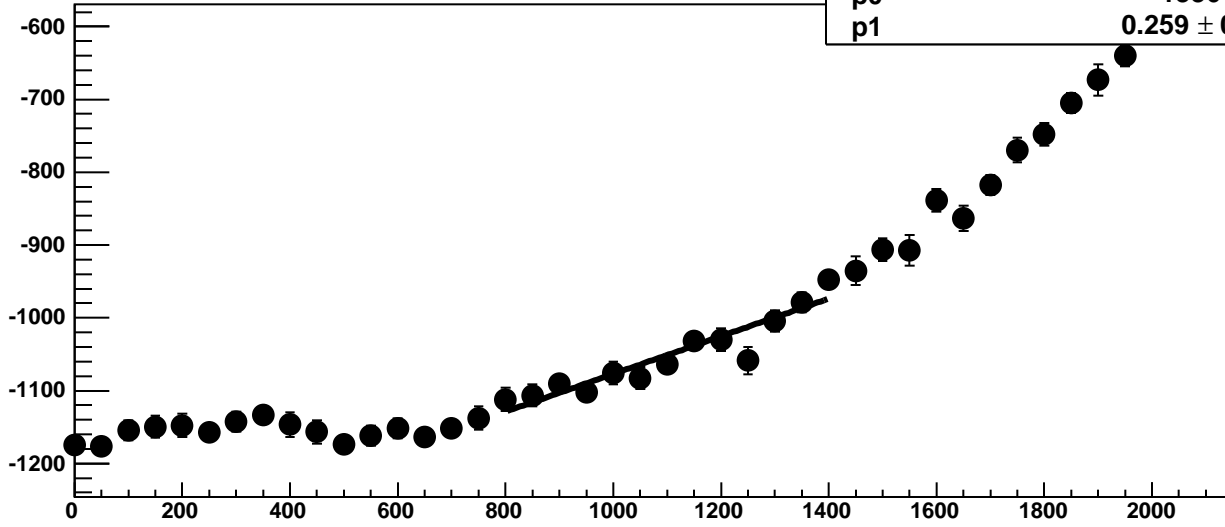
Chip 2, Channel 15, Enable 5, Hold=30, ADC Noise vs DAC



Chip 2, Channel 15, Enable 5, Hold=30, ADC Residuals vs DAC

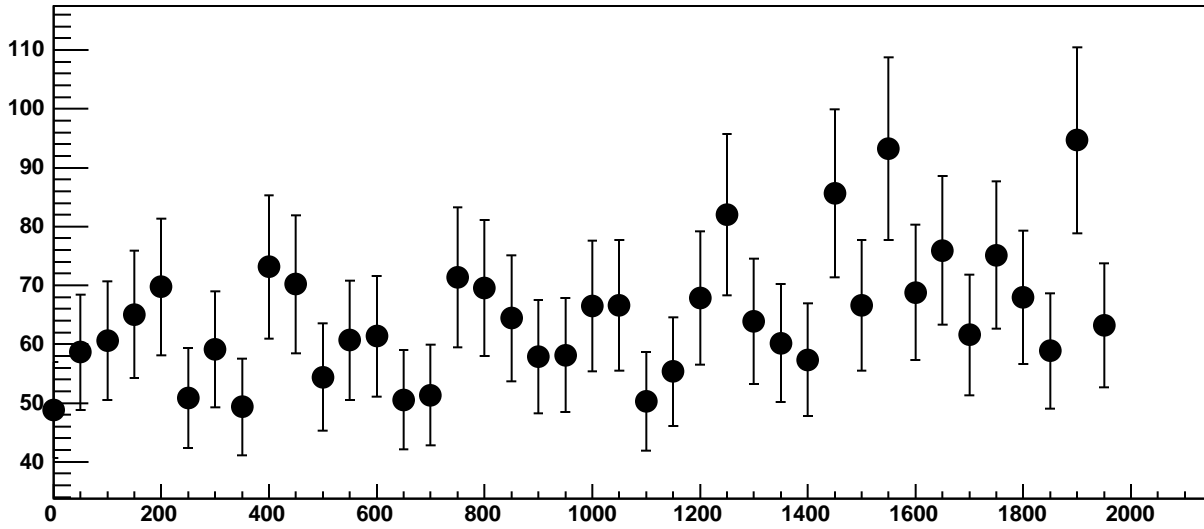


Chip 2, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC

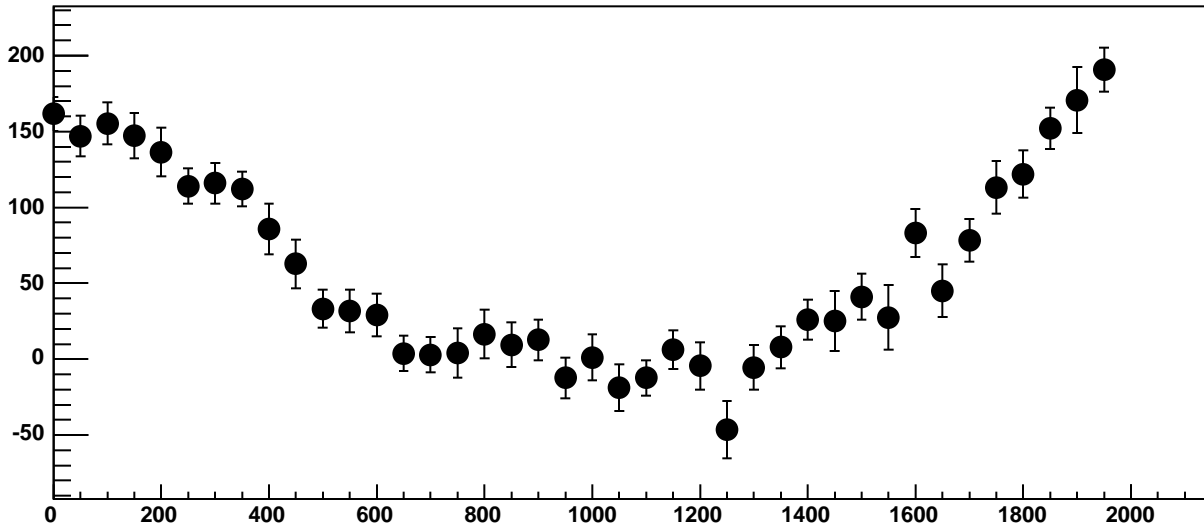


$\chi^2 / \text{ndf}$  16.63 / 11  
p0  $-1336 \pm 23.83$   
p1  $0.259 \pm 0.02132$

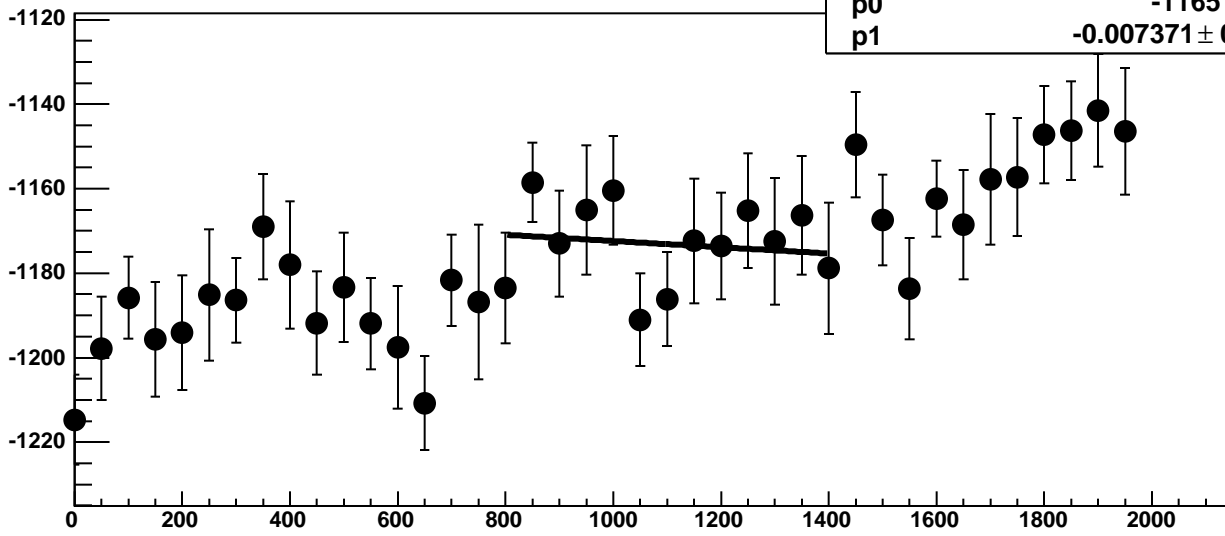
Chip 2, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC



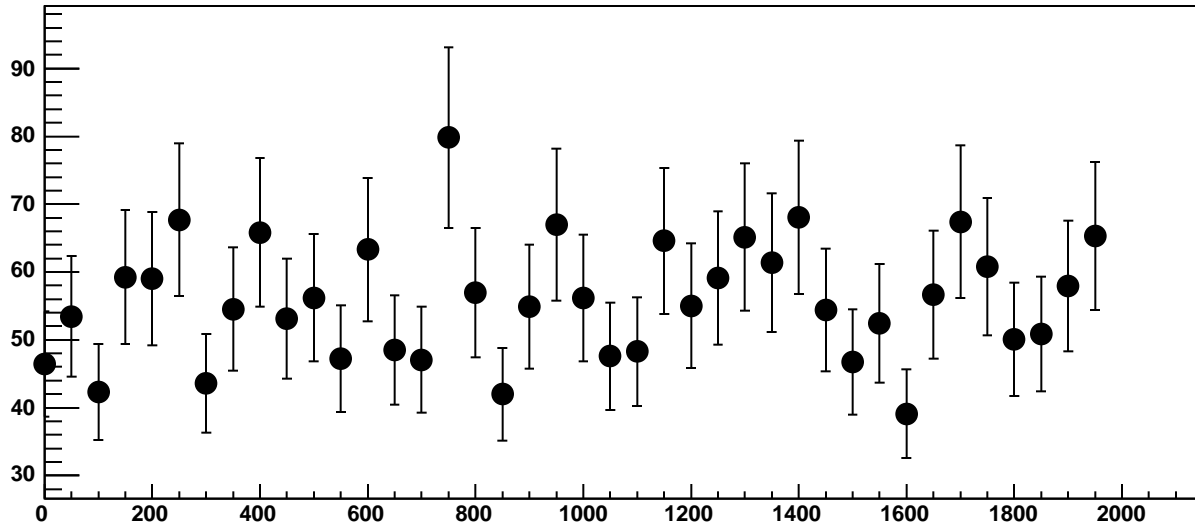
Chip 2, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC



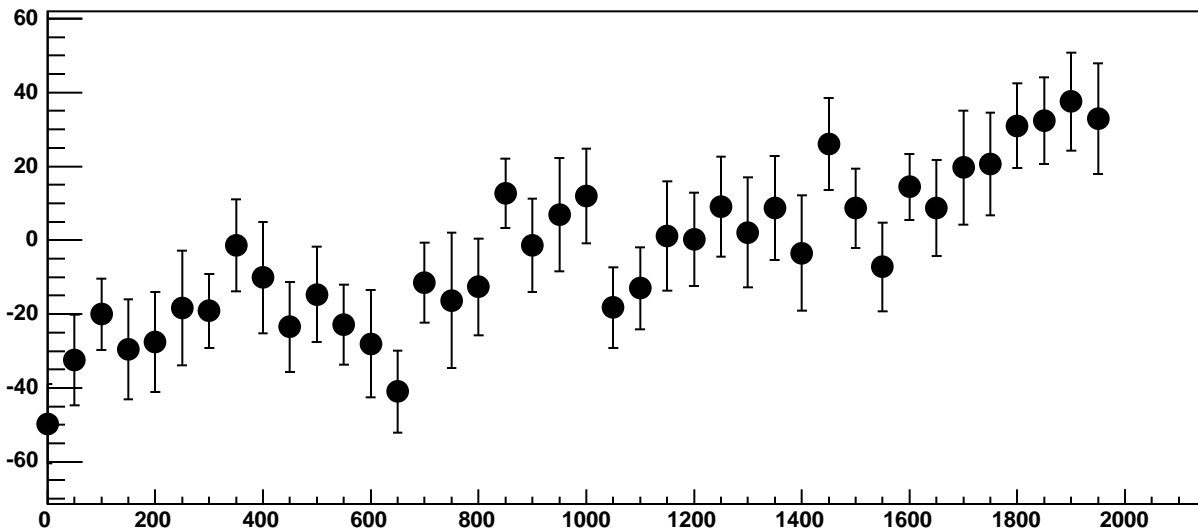
Chip 2, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC



Chip 2, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC

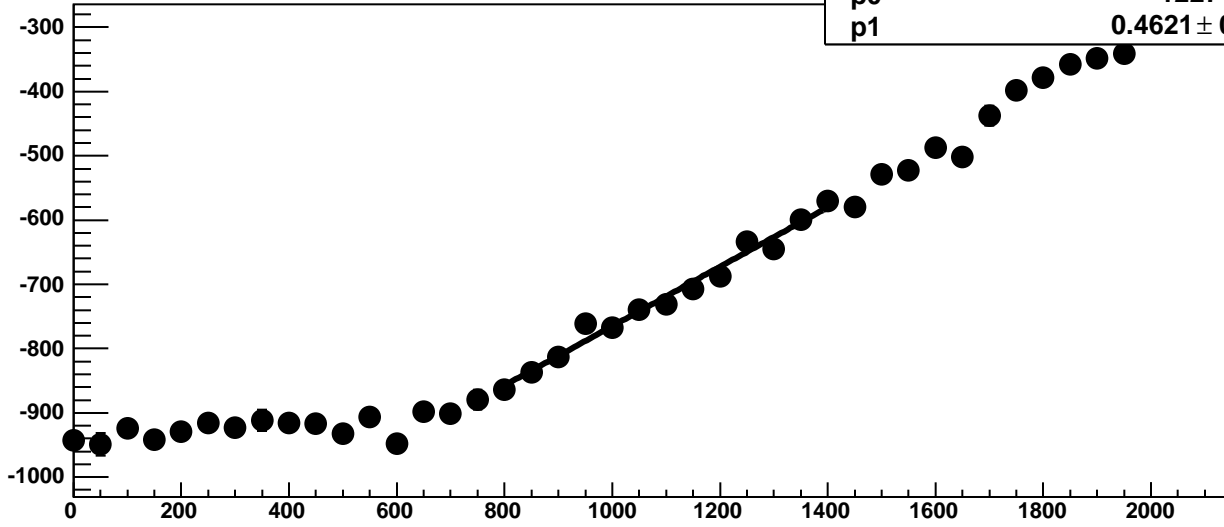


Chip 2, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC



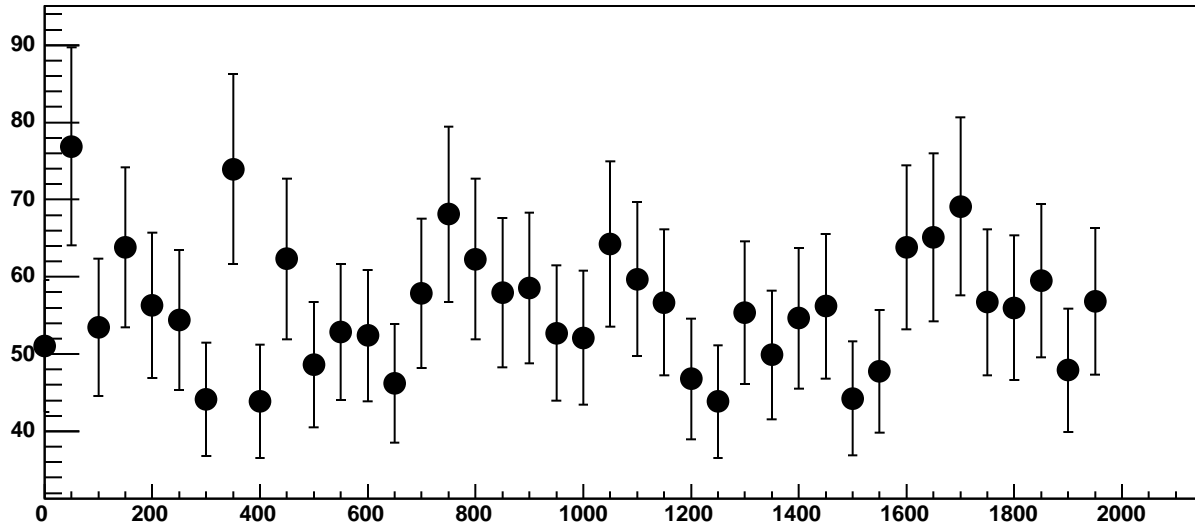


Chip 2, Channel 16, Enable 2, Hold=30, ADC Mean vs DAC

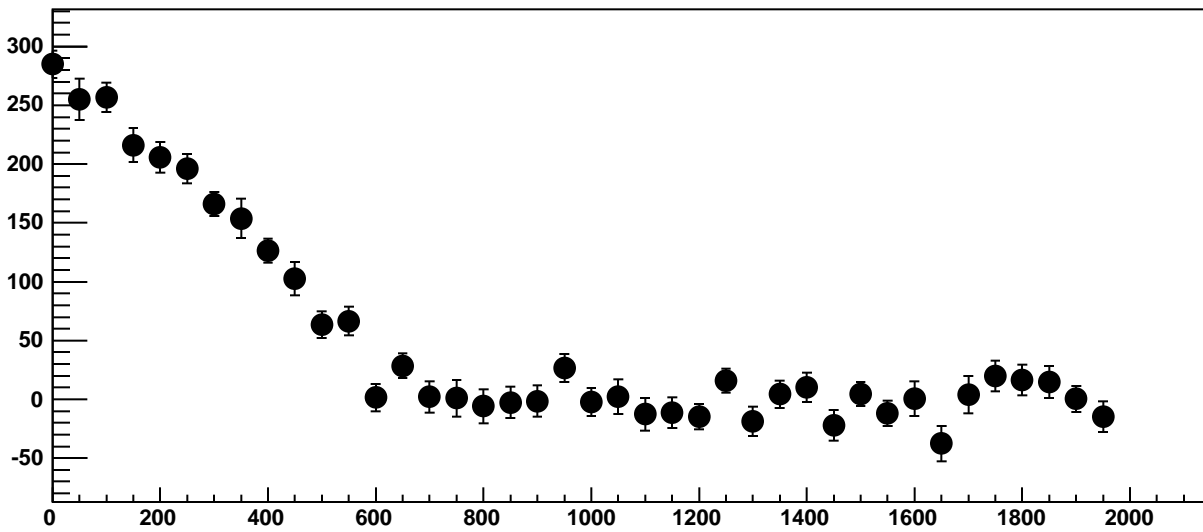


$\chi^2 / \text{ndf}$  14.12 / 11  
p0  $-1227 \pm 21.33$   
p1  $0.4621 \pm 0.01878$

Chip 2, Channel 16, Enable 2, Hold=30, ADC Noise vs DAC

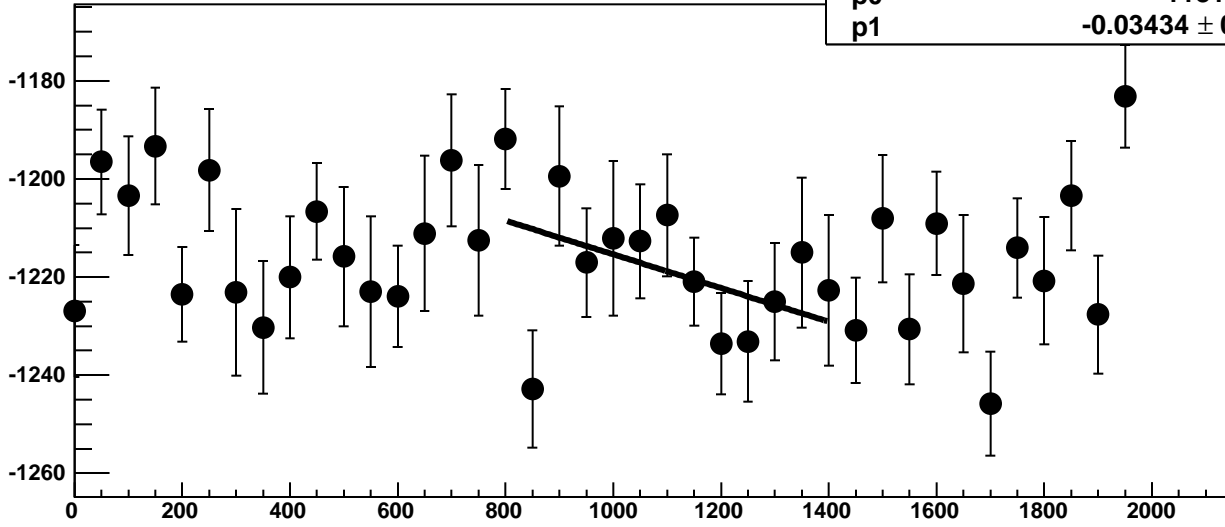


Chip 2, Channel 16, Enable 2, Hold=30, ADC Residuals vs DAC

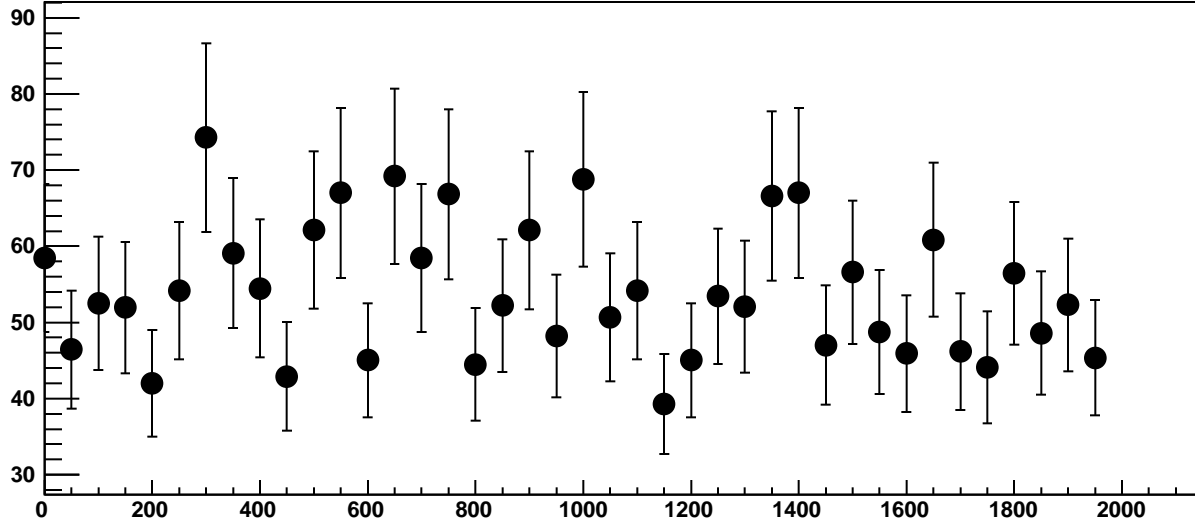


Chip 2, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC

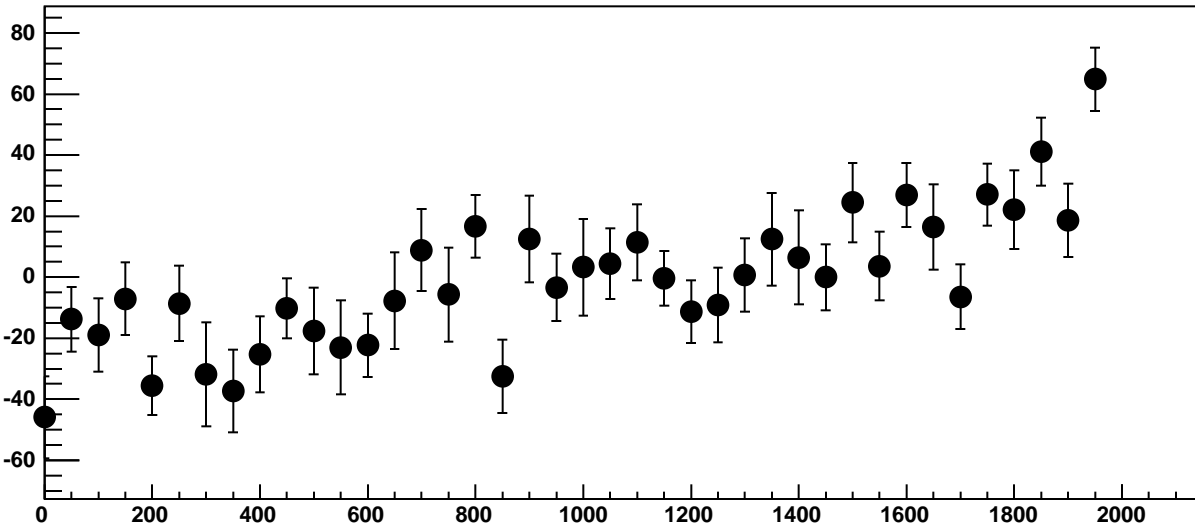
$\chi^2 / \text{ndf}$  14.56 / 11  
p0  $-1181 \pm 20.35$   
p1  $-0.03434 \pm 0.01849$



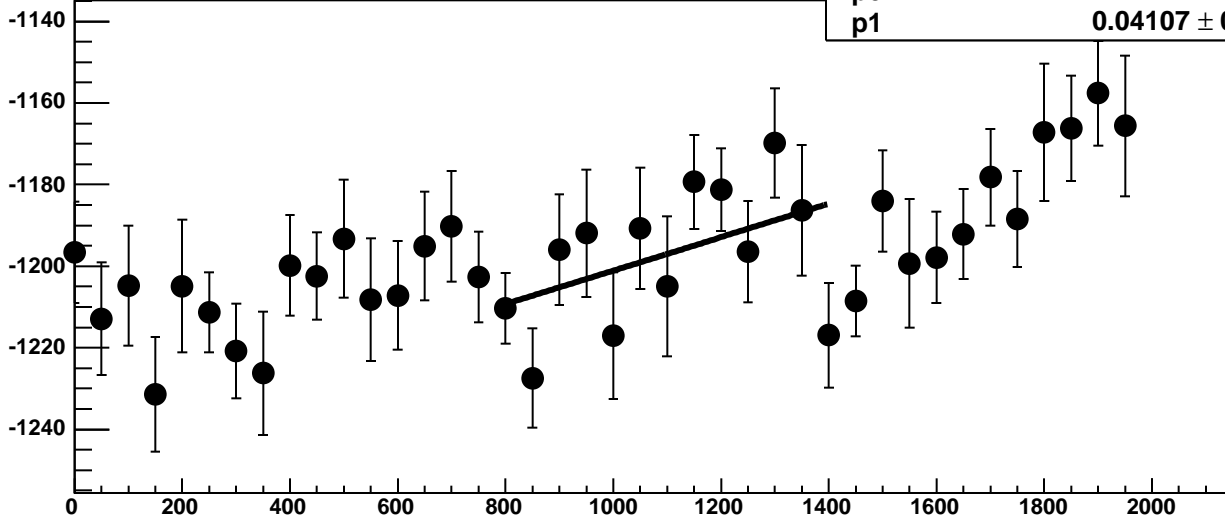
Chip 2, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC



Chip 2, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC

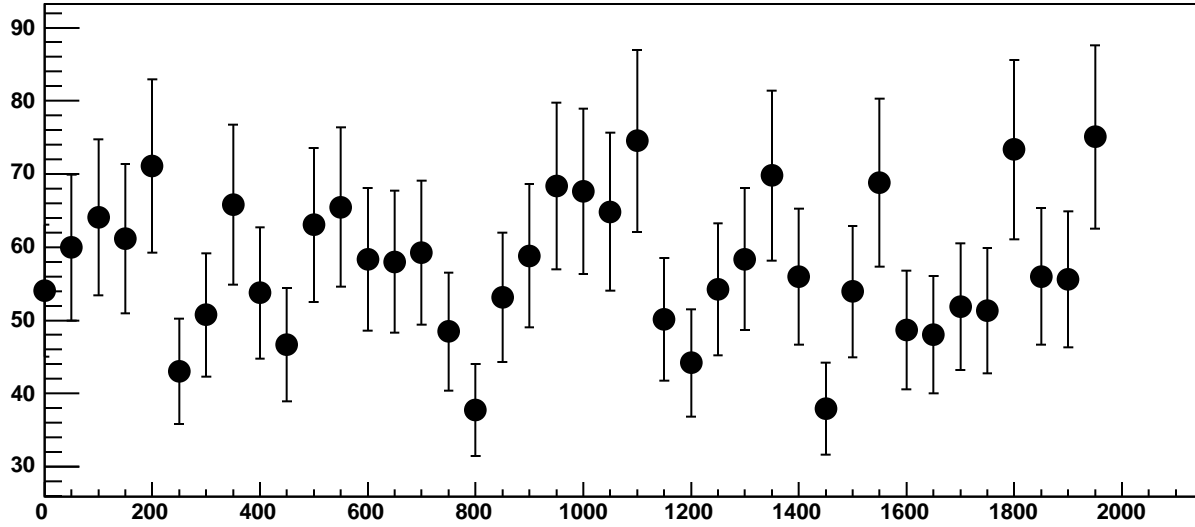


Chip 2, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC

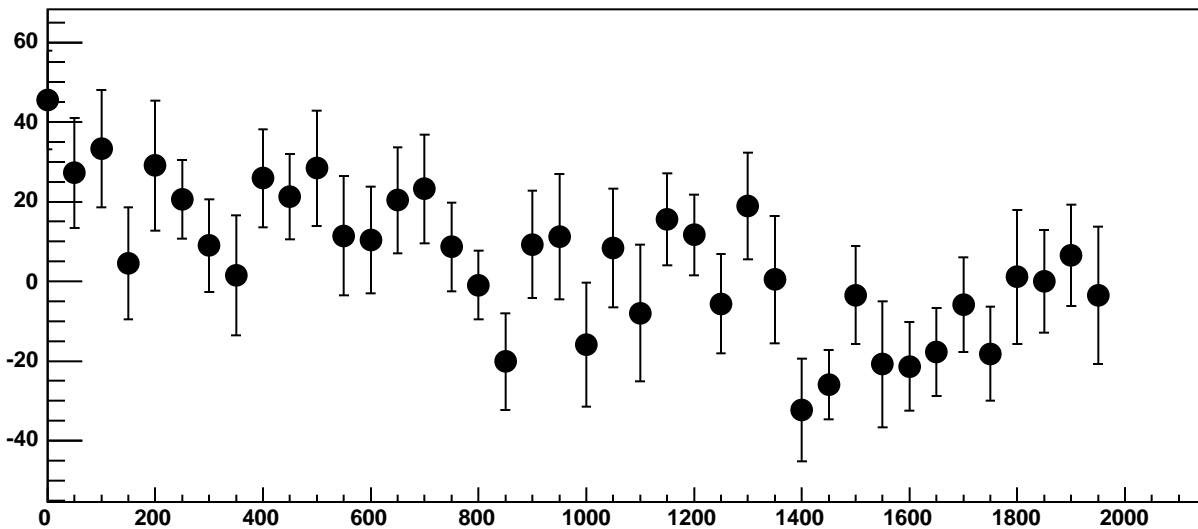


$\chi^2 / \text{ndf}$  16.99 / 11  
p0  $-1242 \pm 19.32$   
p1  $0.04107 \pm 0.01763$

Chip 2, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC

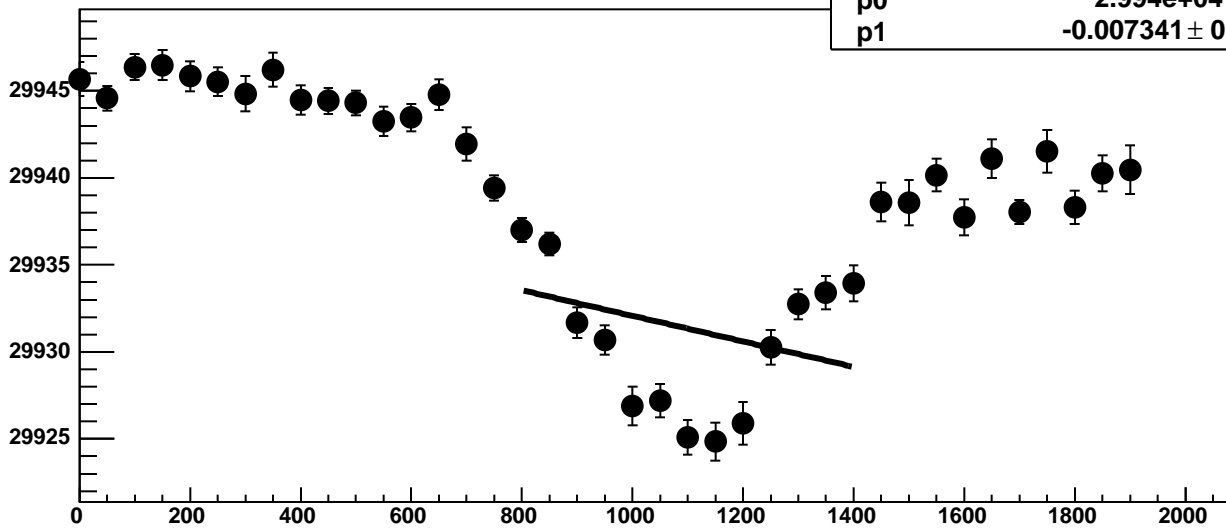


Chip 2, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC

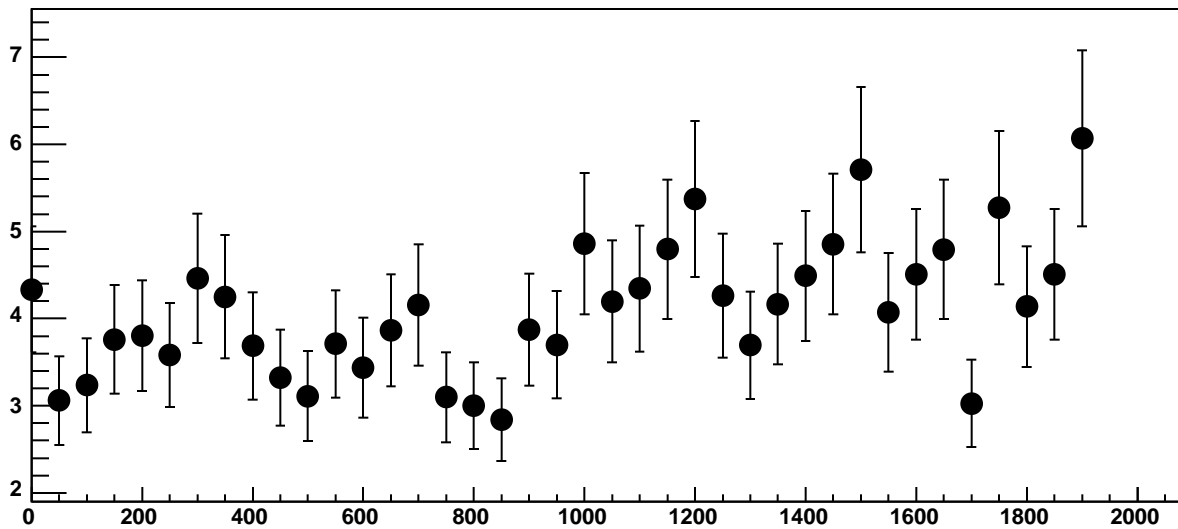


Chip 2, Channel 16, Enable 5!, Hold=30, ADC Mean vs DAC

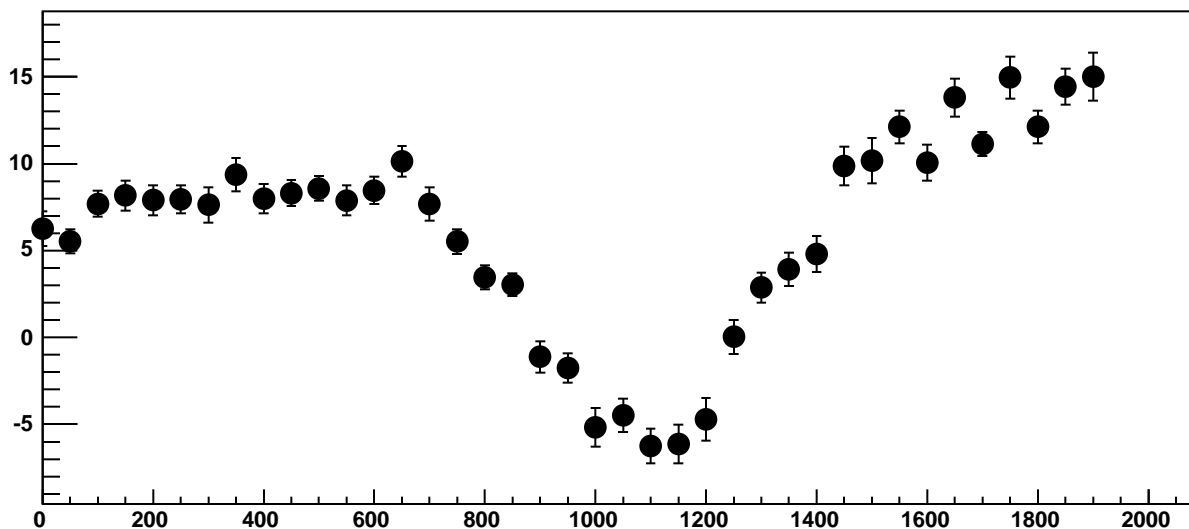
$\chi^2 / \text{ndf}$  230.9 / 11  
p0  $2.994\text{e}+04 \pm 1.344$   
p1  $-0.007341 \pm 0.001252$



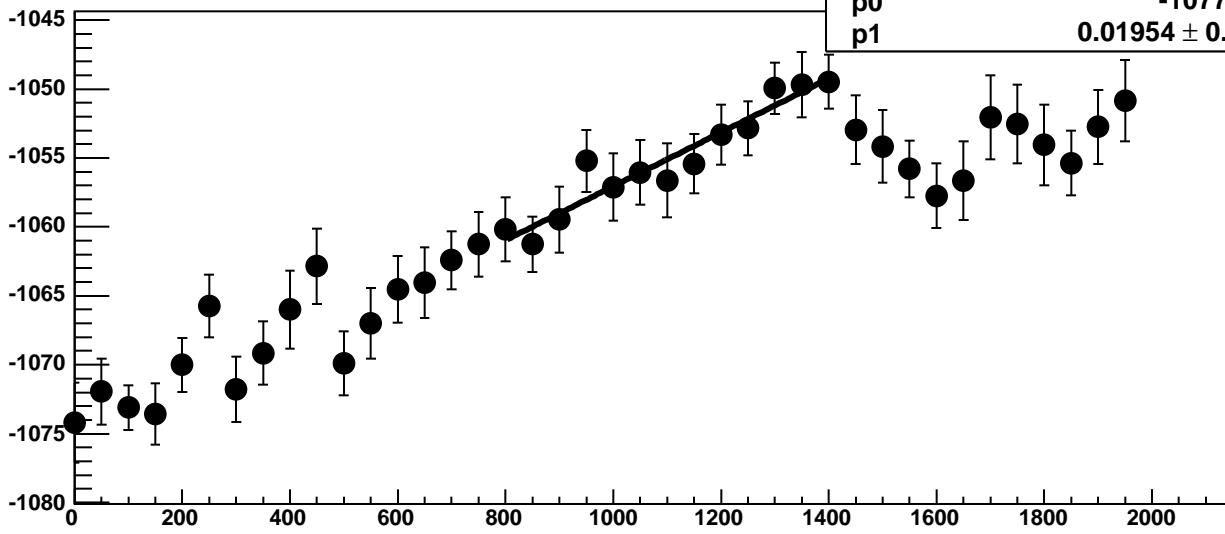
Chip 2, Channel 16, Enable 5!, Hold=30, ADC Noise vs DAC



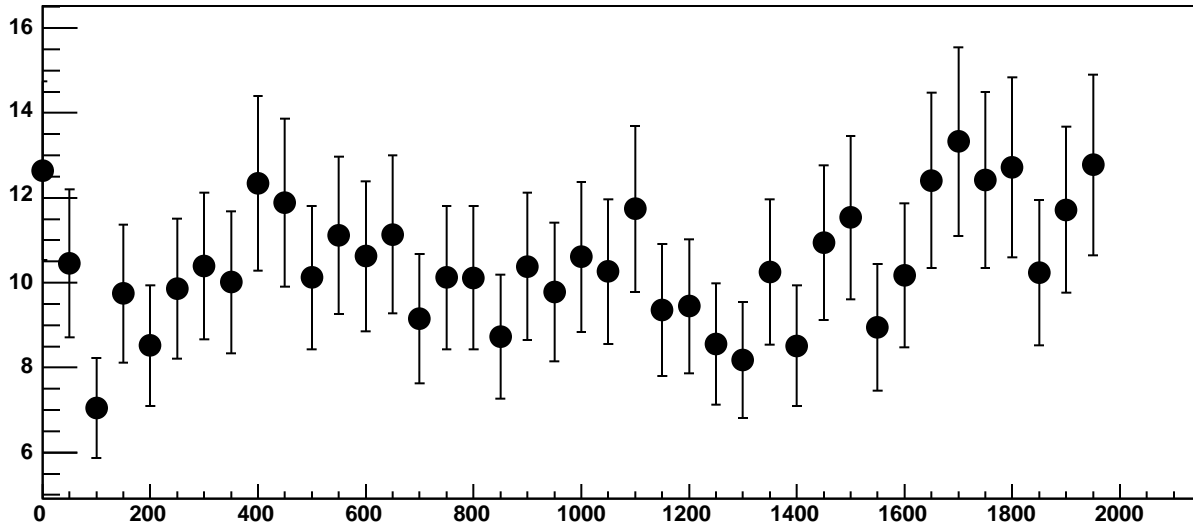
Chip 2, Channel 16, Enable 5!, Hold=30, ADC Residuals vs DAC



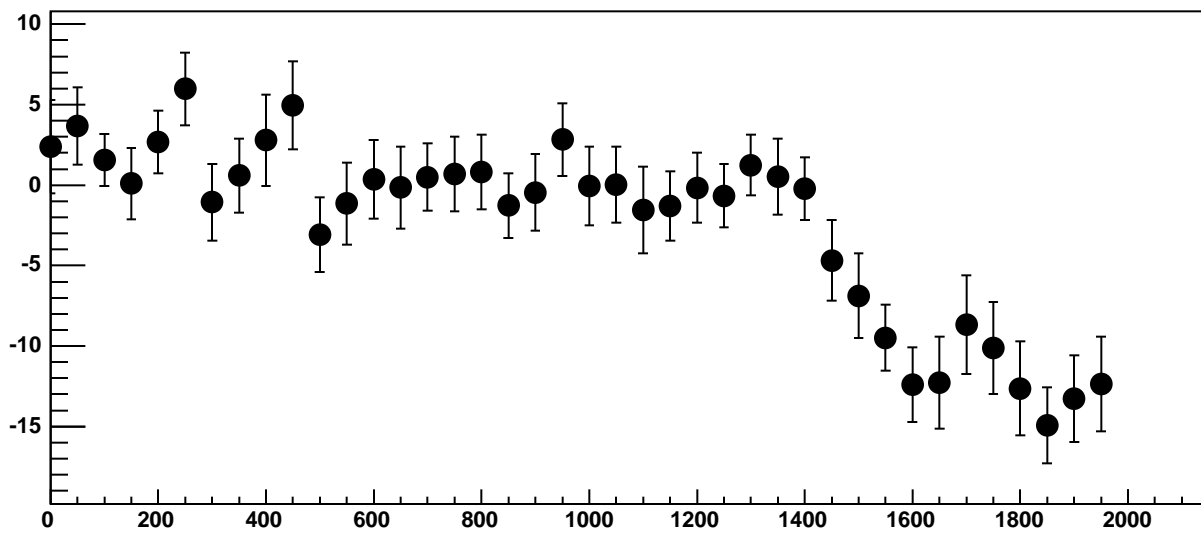
Chip 2, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC



Chip 2, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC

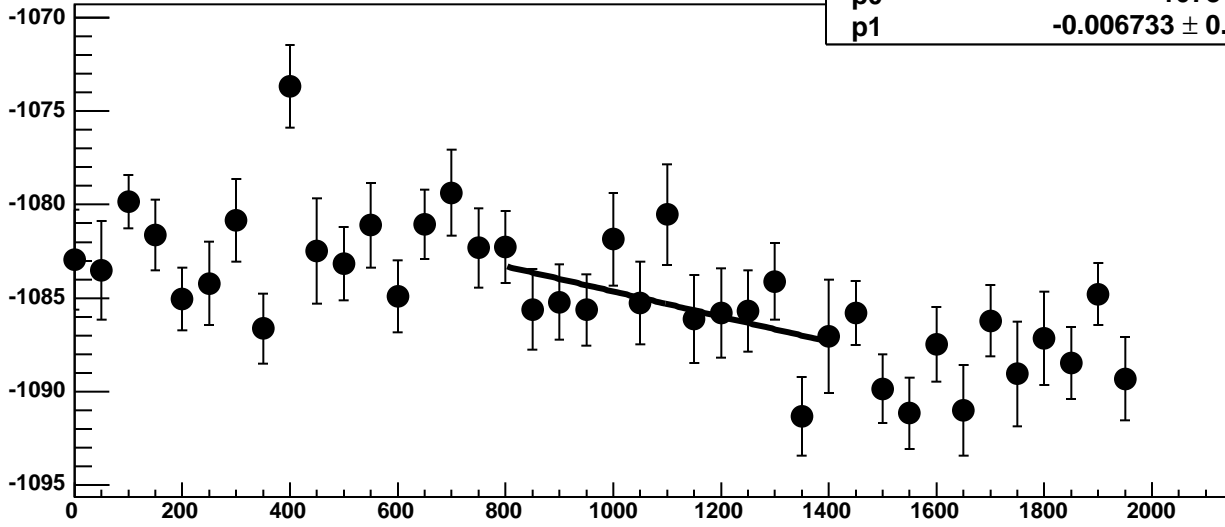


Chip 2, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC

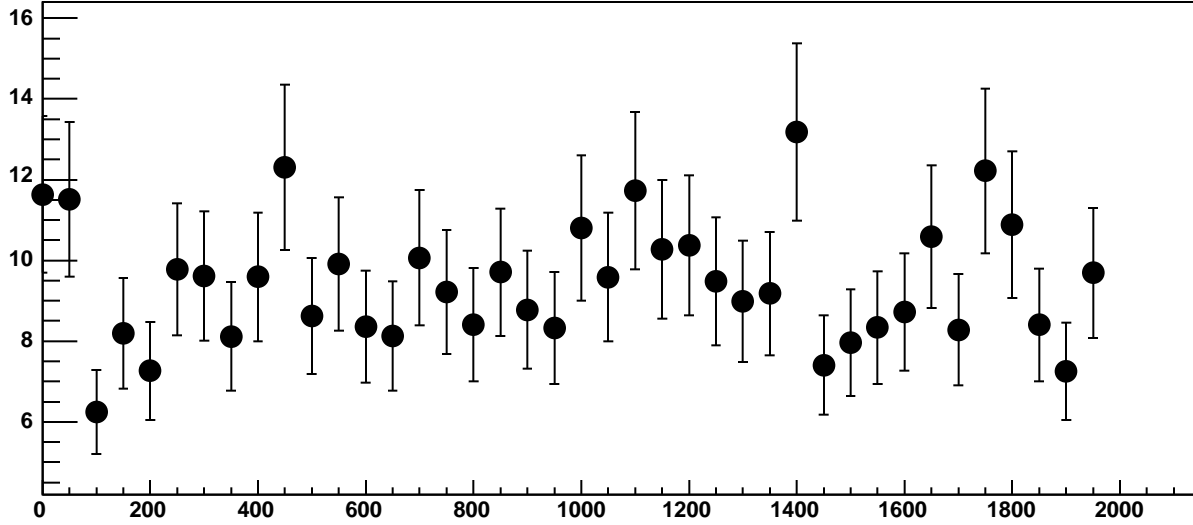


Chip 2, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC

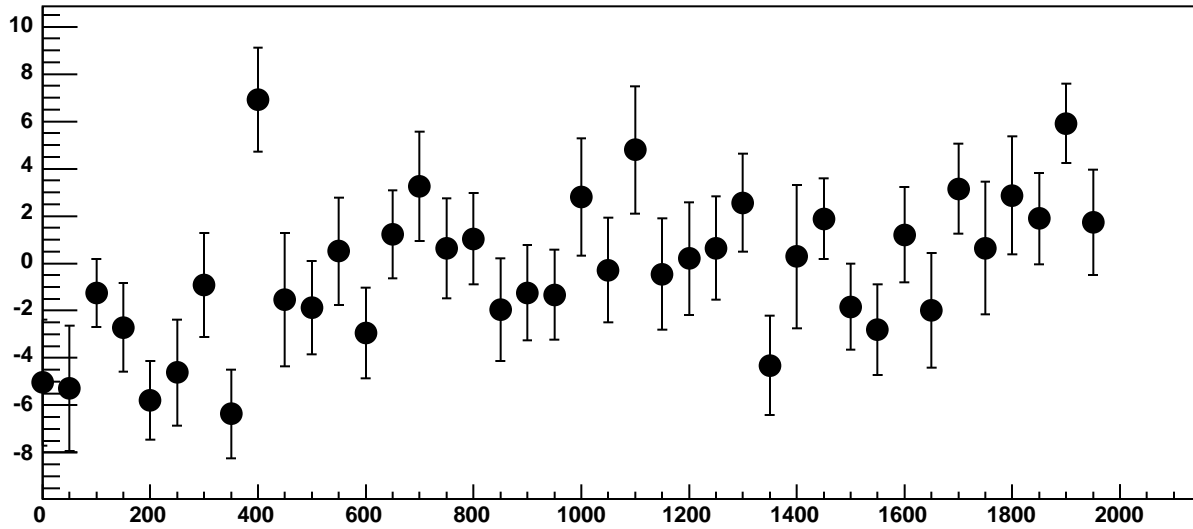
$\chi^2 / \text{ndf}$  12.31 / 11  
p0  $-1078 \pm 3.558$   
p1  $-0.006733 \pm 0.003248$



Chip 2, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC

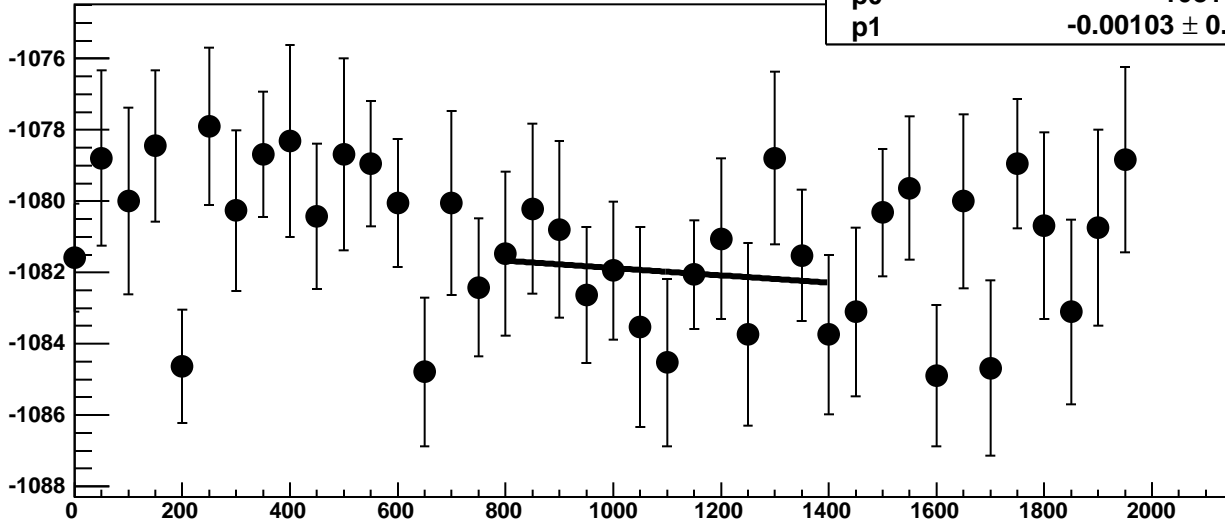


Chip 2, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC

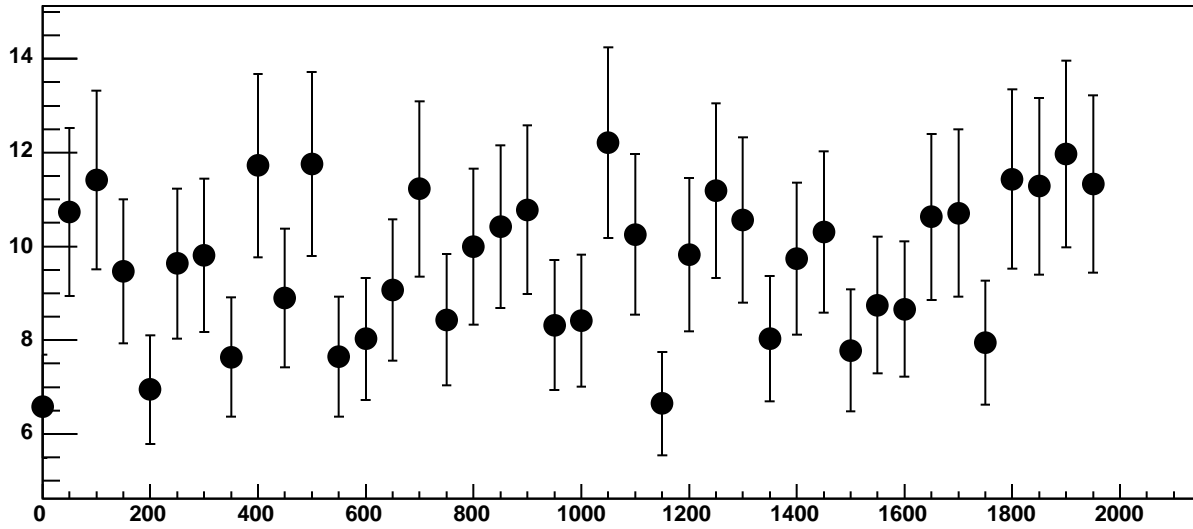


Chip 2, Channel 17, Enable 2!, Hold=30, ADC Mean vs DAC

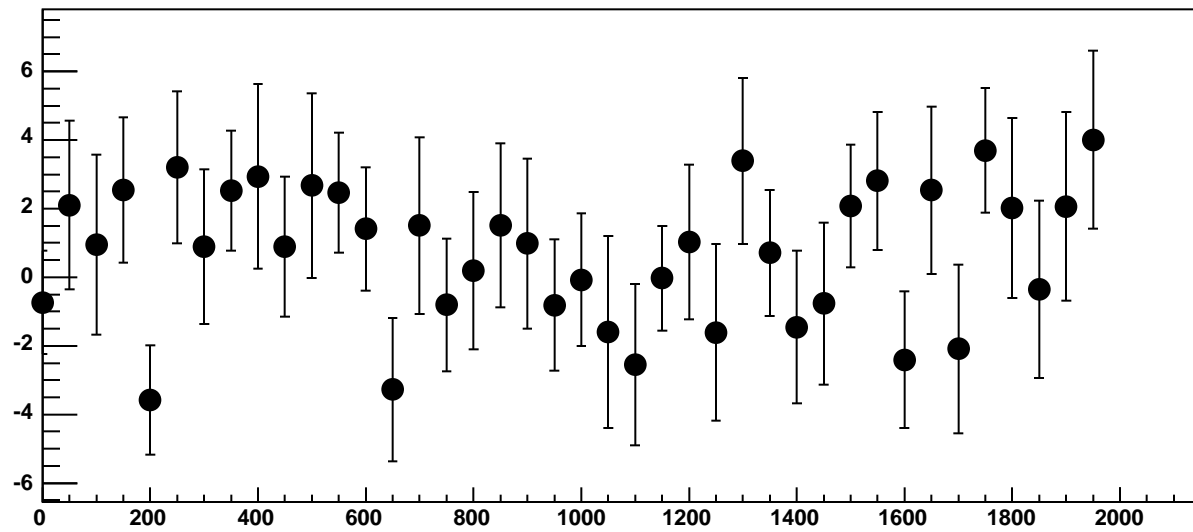
$\chi^2 / \text{ndf}$  5.377 / 11  
p0  $-1081 \pm 3.677$   
p1  $-0.00103 \pm 0.003274$



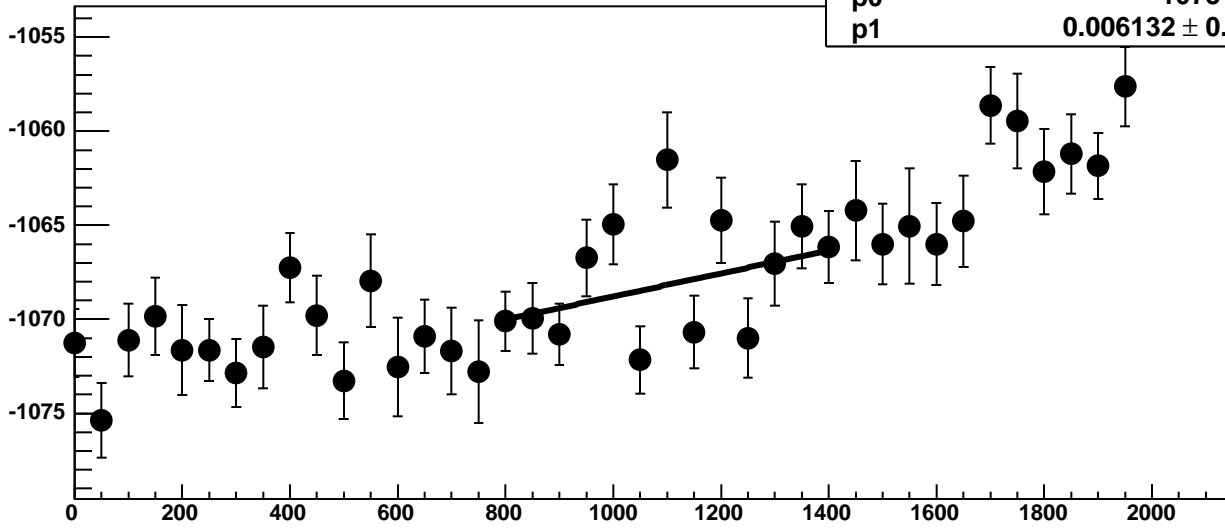
Chip 2, Channel 17, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 2, Channel 17, Enable 2!, Hold=30, ADC Residuals vs DAC

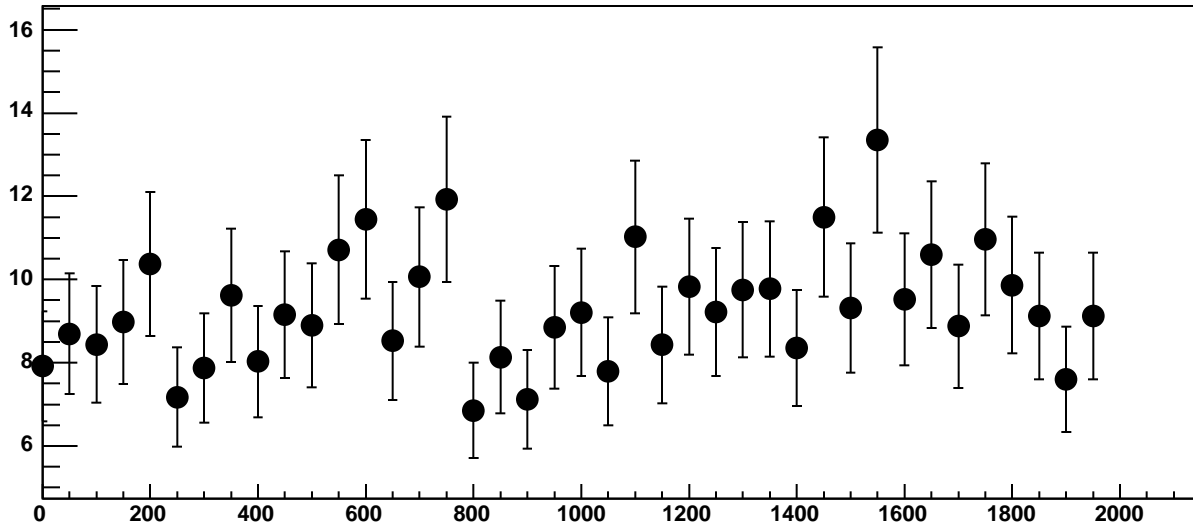


Chip 2, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC

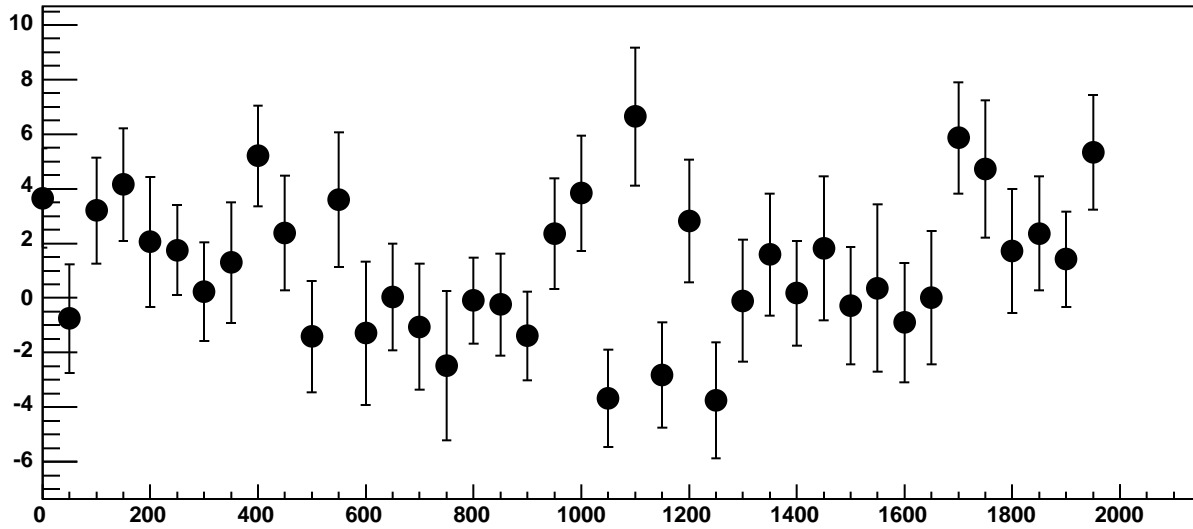


$\chi^2 / \text{ndf}$  23.87 / 11  
p0  $-1075 \pm 3.047$   
p1  $0.006132 \pm 0.002805$

Chip 2, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC

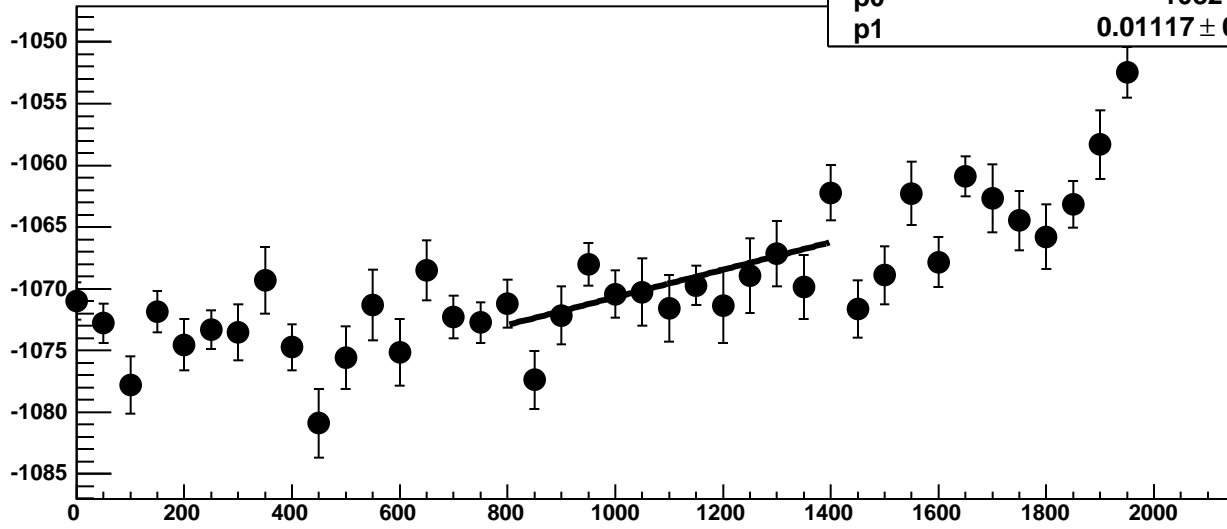


Chip 2, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC





Chip 2, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

15.32 / 11

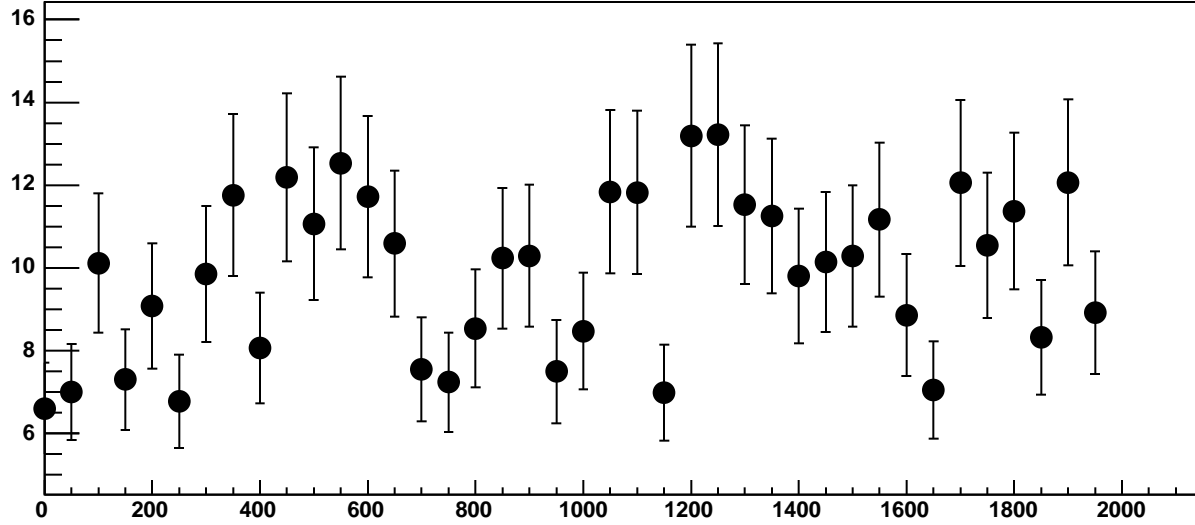
p0

$-1082 \pm 3.667$

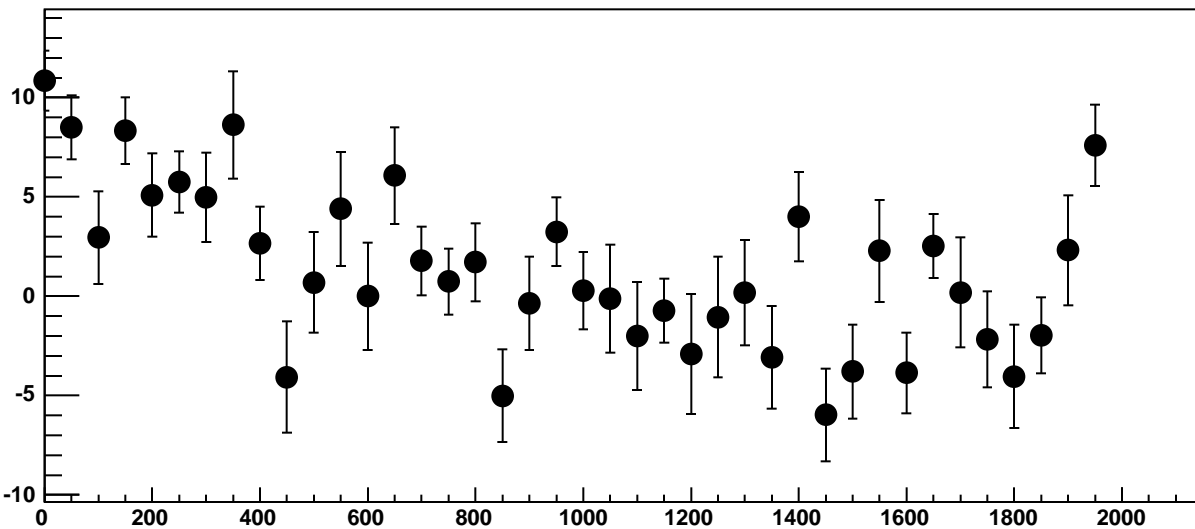
p1

$0.01117 \pm 0.00337$

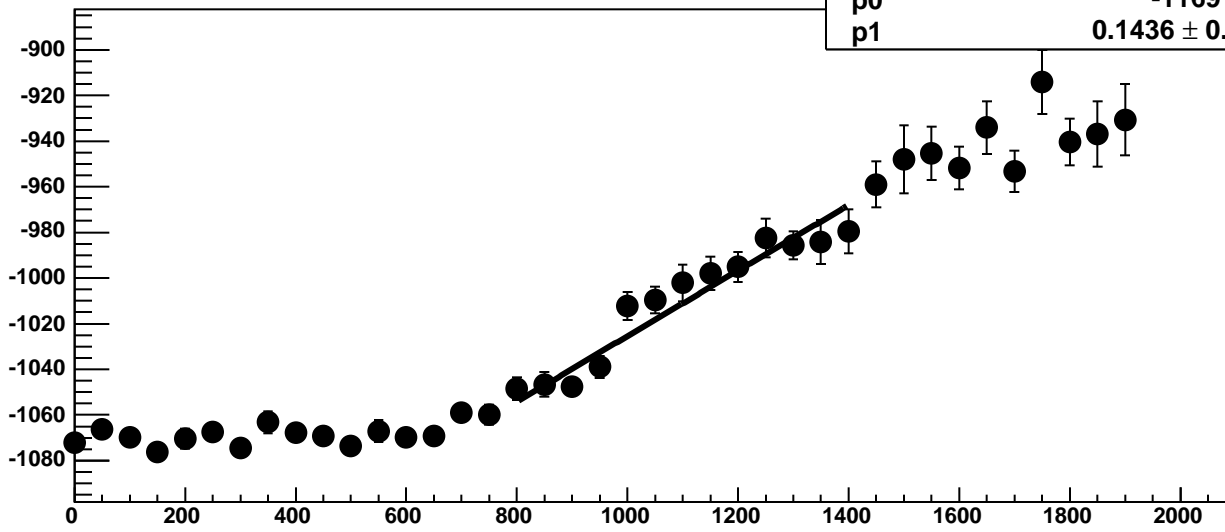
Chip 2, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC



Chip 2, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC

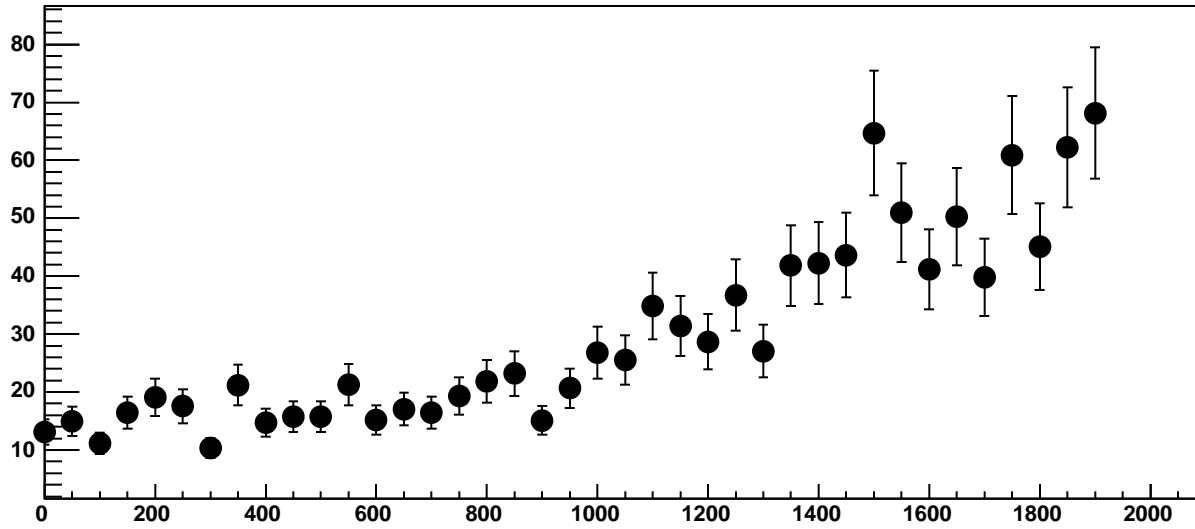


Chip 2, Channel 17, Enable 5, Hold=30, ADC Mean vs DAC

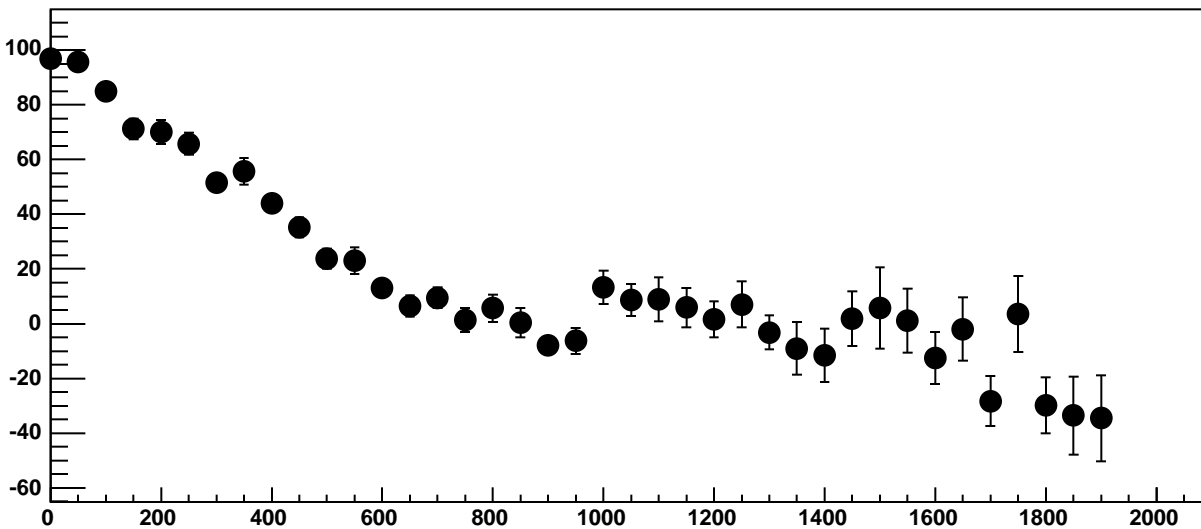


$\chi^2 / \text{ndf}$  20.31 / 11  
p0  $-1169 \pm 9.797$   
p1  $0.1436 \pm 0.009508$

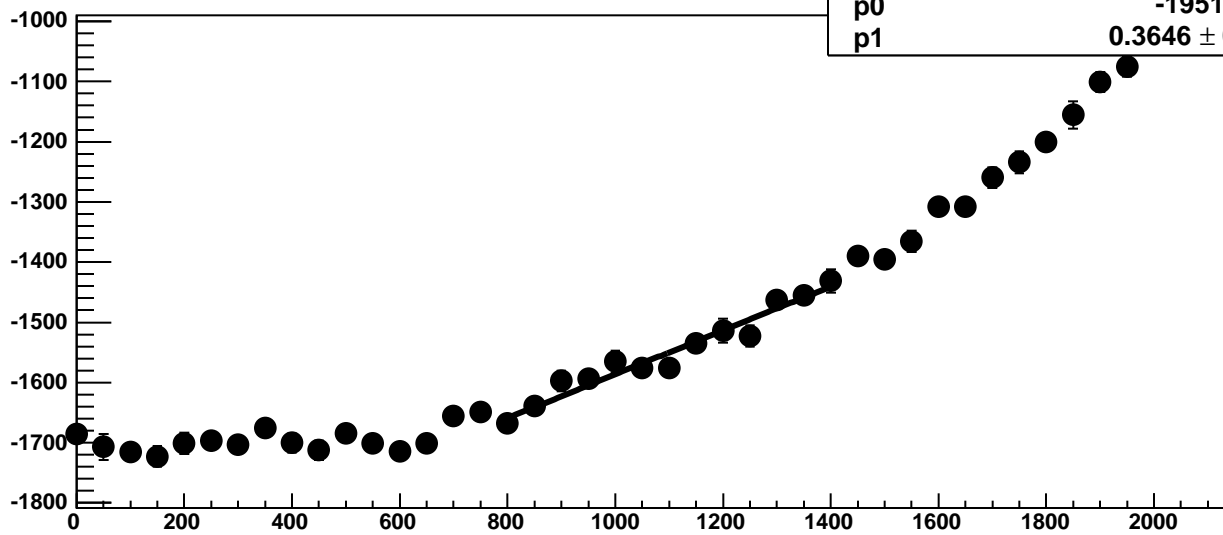
Chip 2, Channel 17, Enable 5, Hold=30, ADC Noise vs DAC



Chip 2, Channel 17, Enable 5, Hold=30, ADC Residuals vs DAC

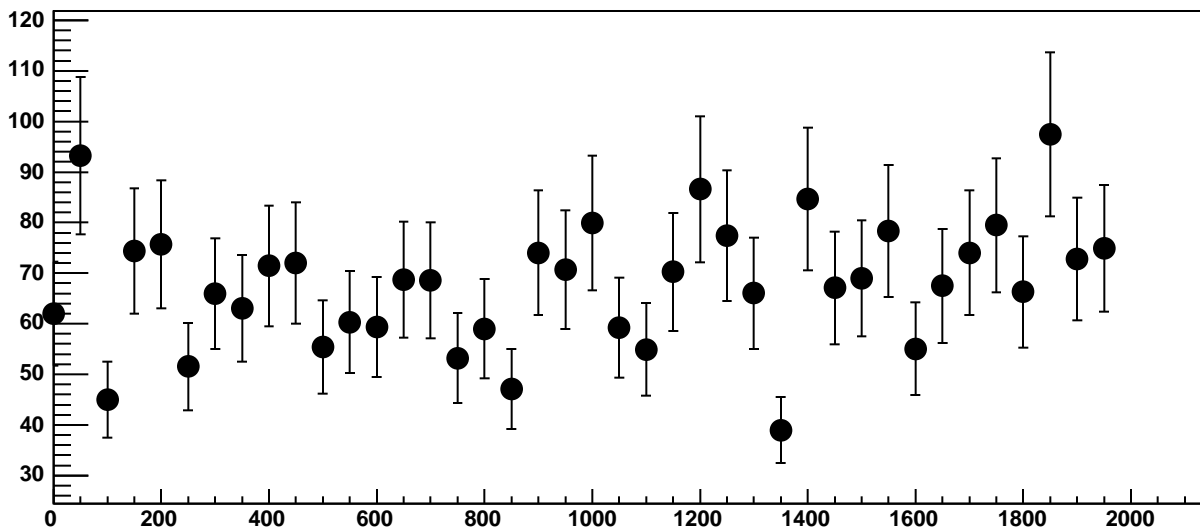


Chip 3, Channel 0, Enable 0, Hold=30, ADC Mean vs DAC

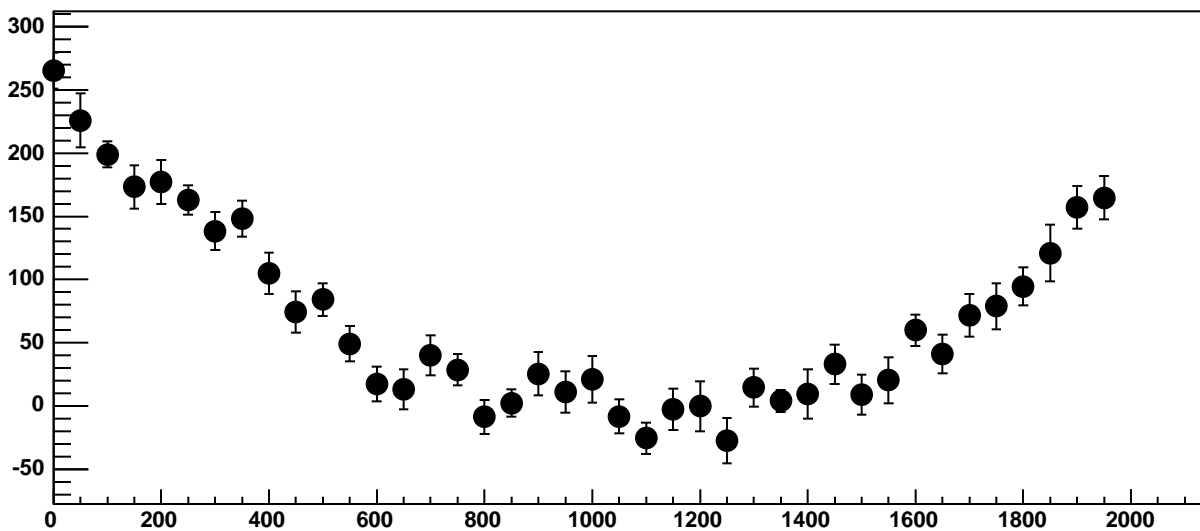


$\chi^2 / \text{ndf}$  12.72 / 11  
p0  $-1951 \pm 21.98$   
p1  $0.3646 \pm 0.01963$

Chip 3, Channel 0, Enable 0, Hold=30, ADC Noise vs DAC

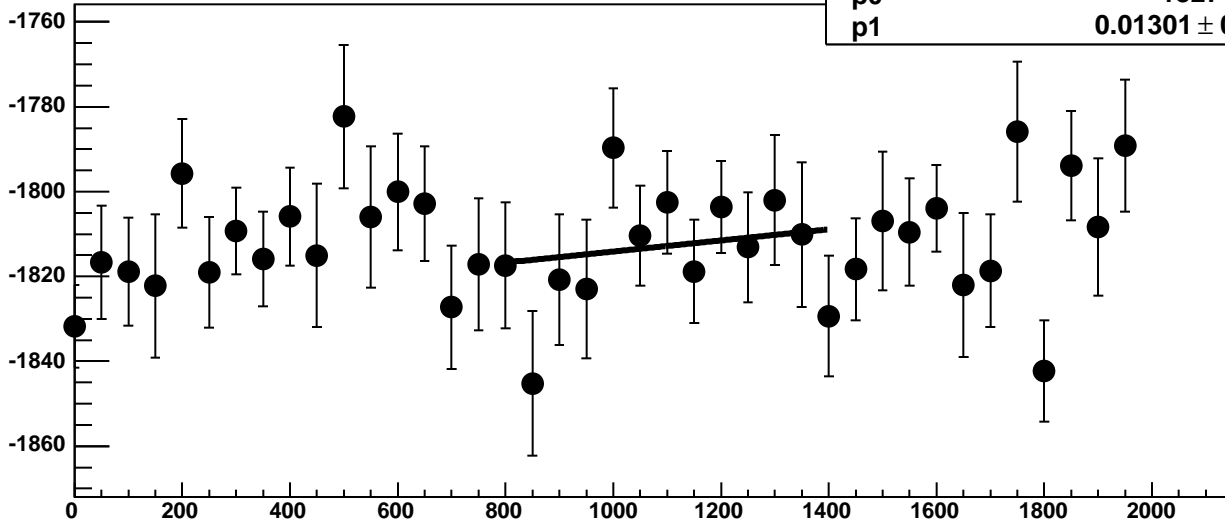


Chip 3, Channel 0, Enable 0, Hold=30, ADC Residuals vs DAC

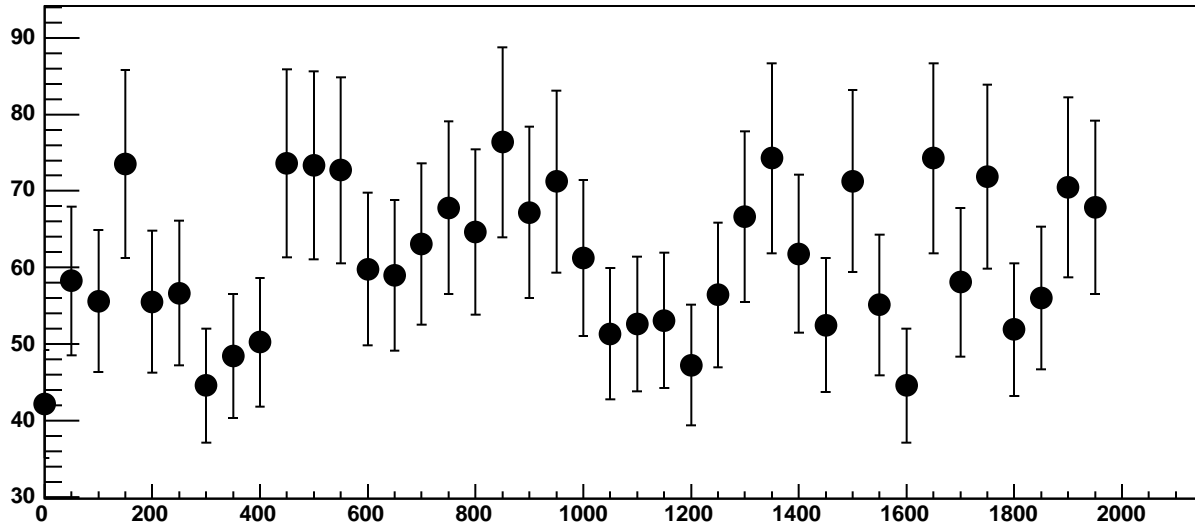


Chip 3, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC

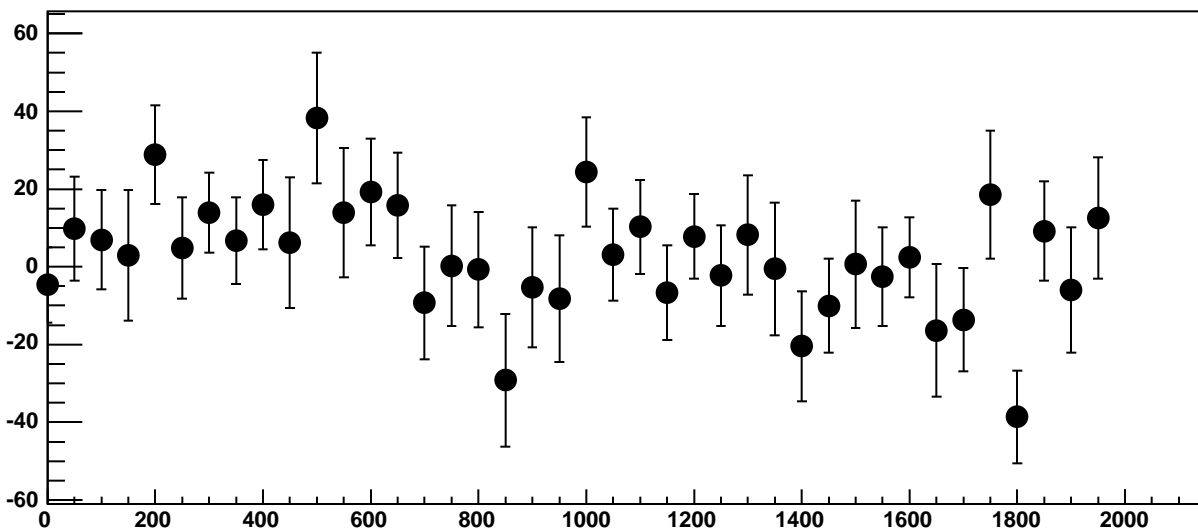
$\chi^2 / \text{ndf}$  10.31 / 11  
p0  $-1827 \pm 25.12$   
p1  $0.01301 \pm 0.02233$



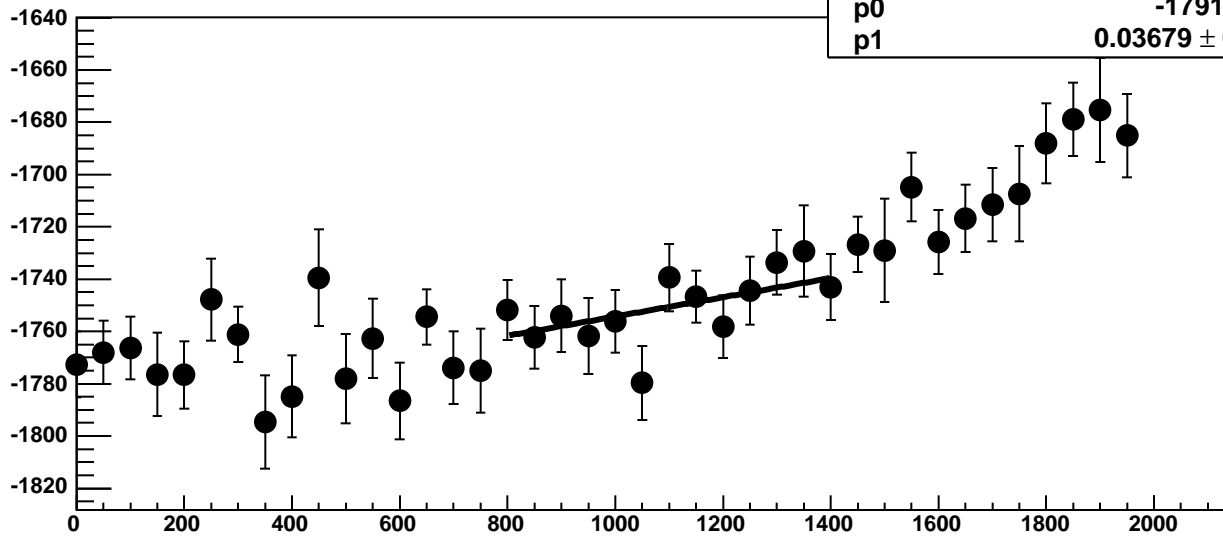
Chip 3, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC



Chip 3, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC



Chip 3, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

7.536 / 11

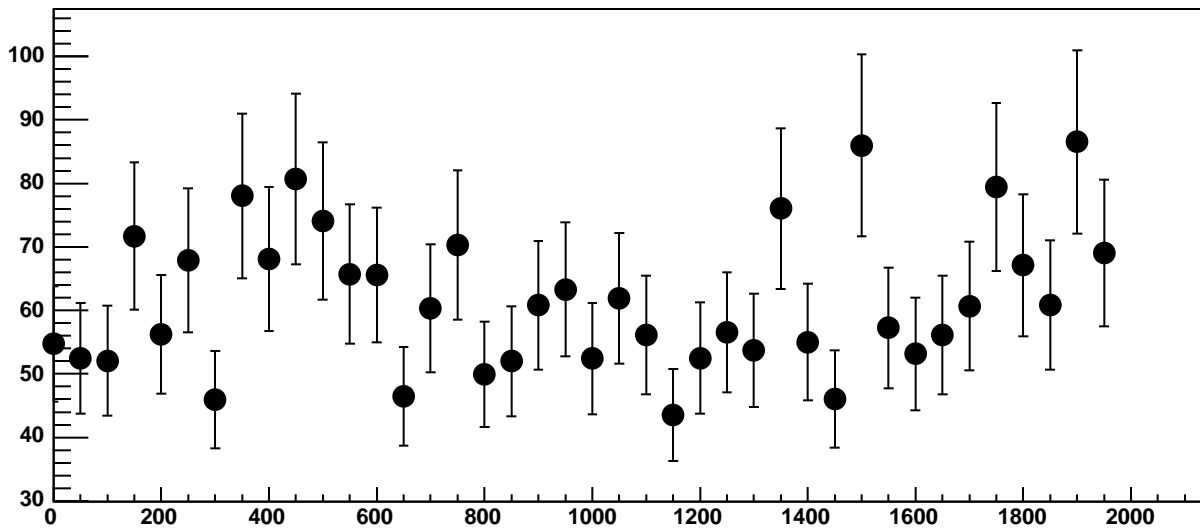
p0

$-1791 \pm 21.03$

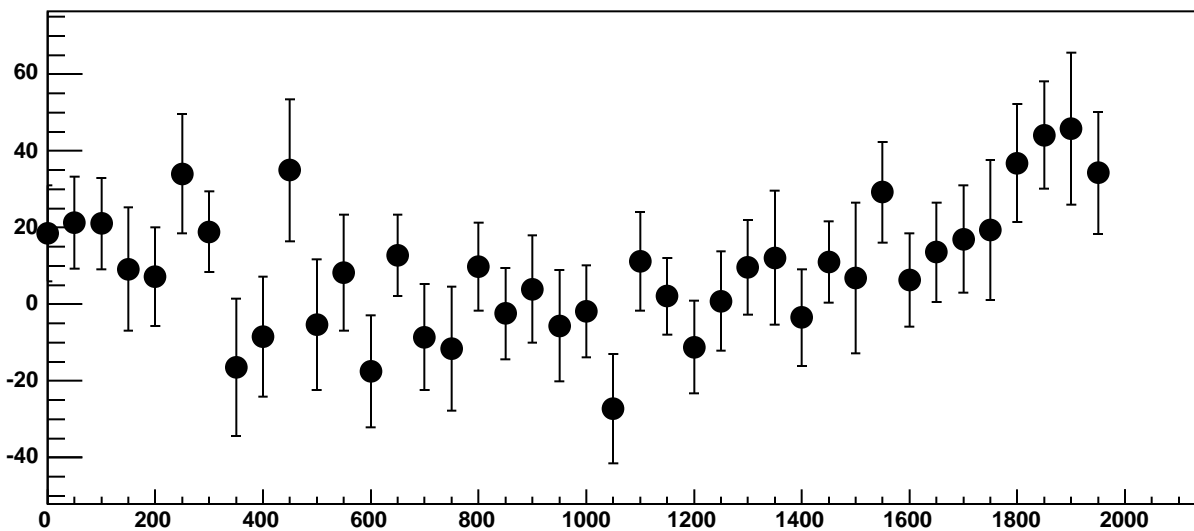
p1

$0.03679 \pm 0.01898$

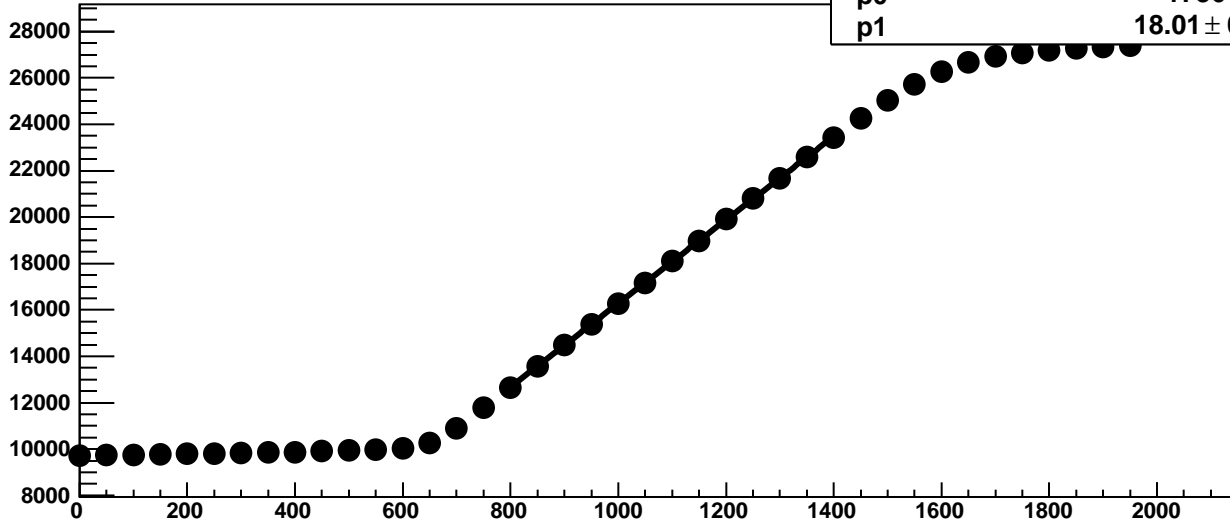
Chip 3, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC



Chip 3, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC

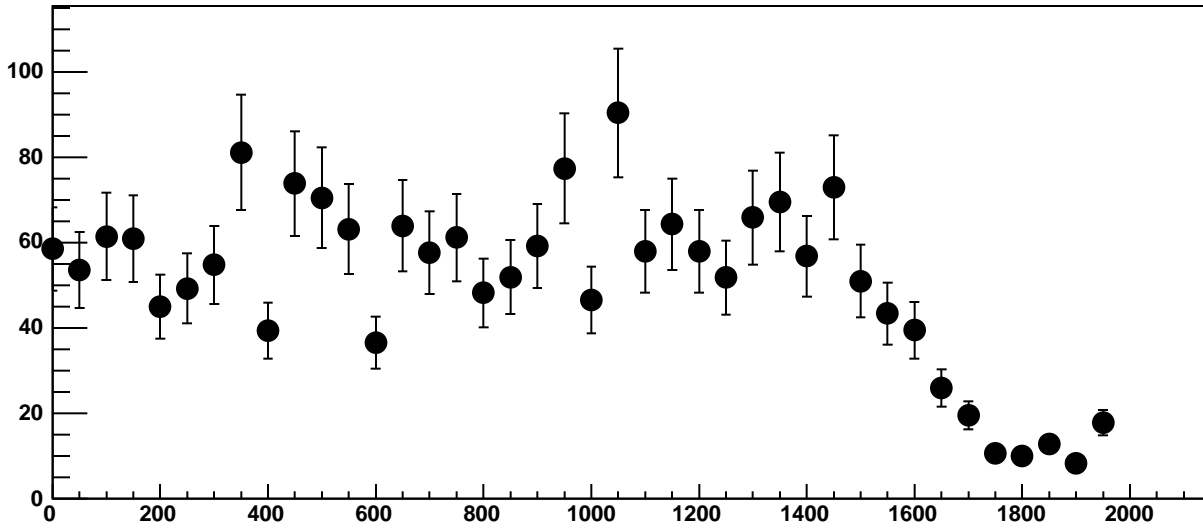


Chip 3, Channel 0, Enable 3!, Hold=30, ADC Mean vs DAC

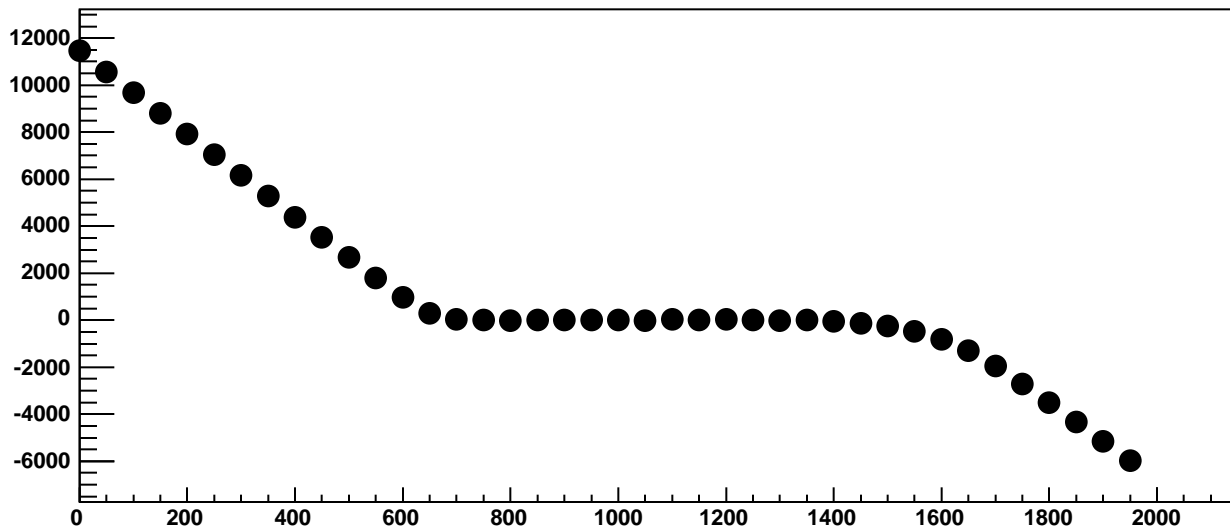


$\chi^2 / \text{ndf}$  37.96 / 11  
p0  $-1736 \pm 21.13$   
p1  $18.01 \pm 0.01921$

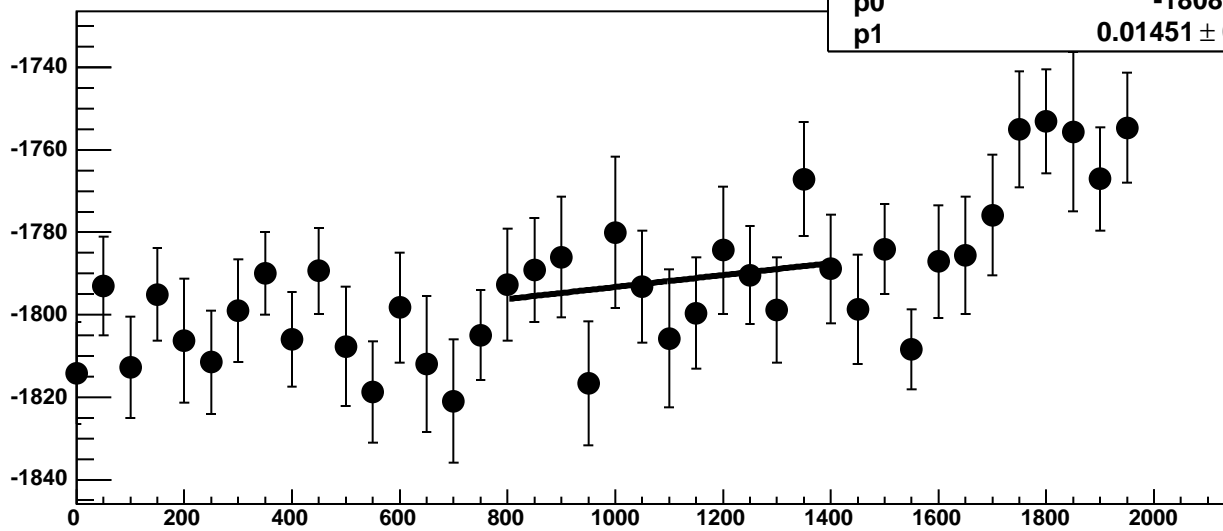
Chip 3, Channel 0, Enable 3!, Hold=30, ADC Noise vs DAC



Chip 3, Channel 0, Enable 3!, Hold=30, ADC Residuals vs DAC



Chip 3, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

7.653 / 11

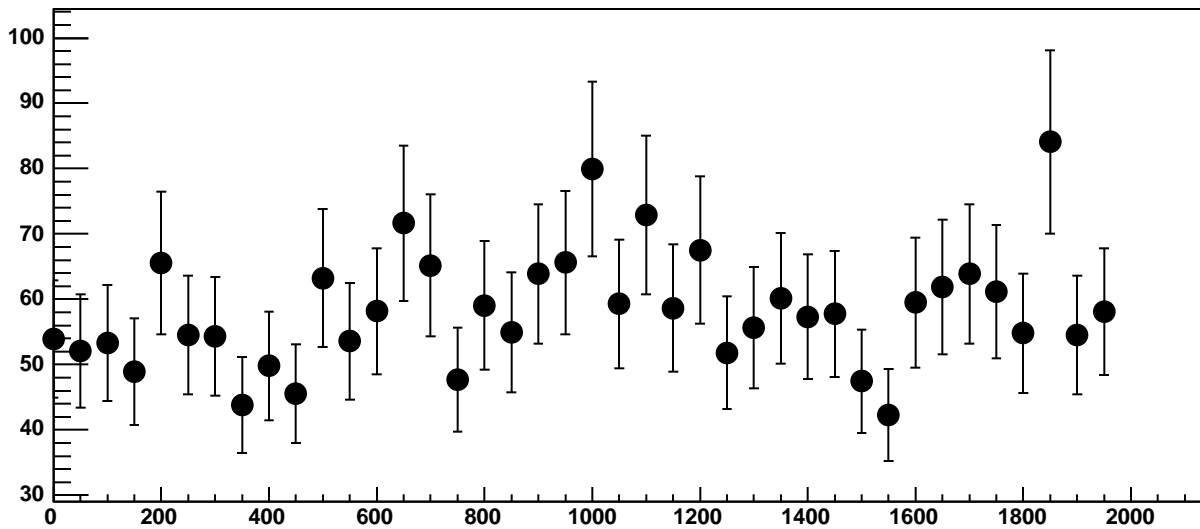
p0

$-1808 \pm 22.47$

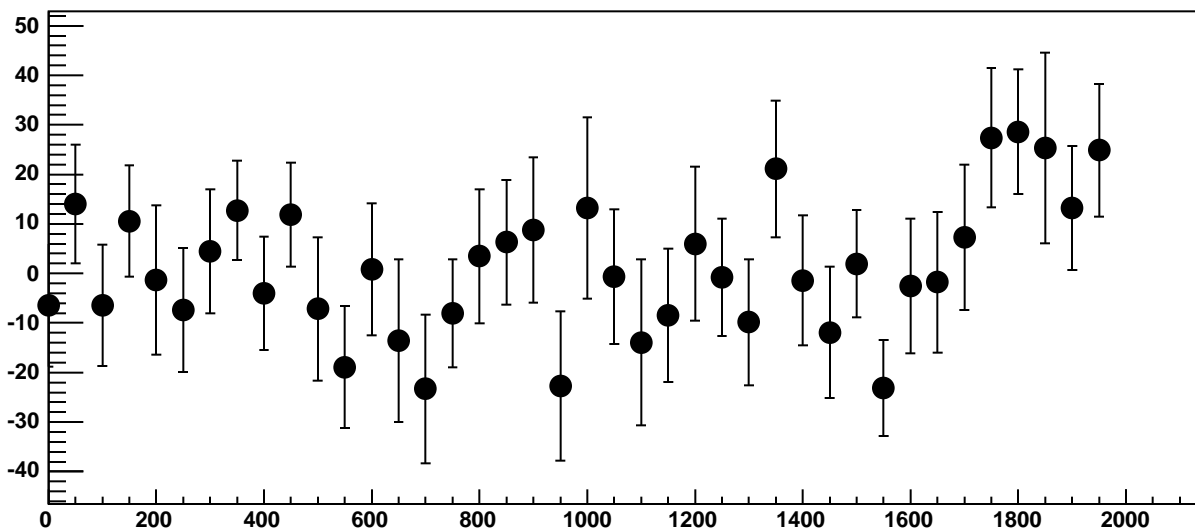
p1

$0.01451 \pm 0.01994$

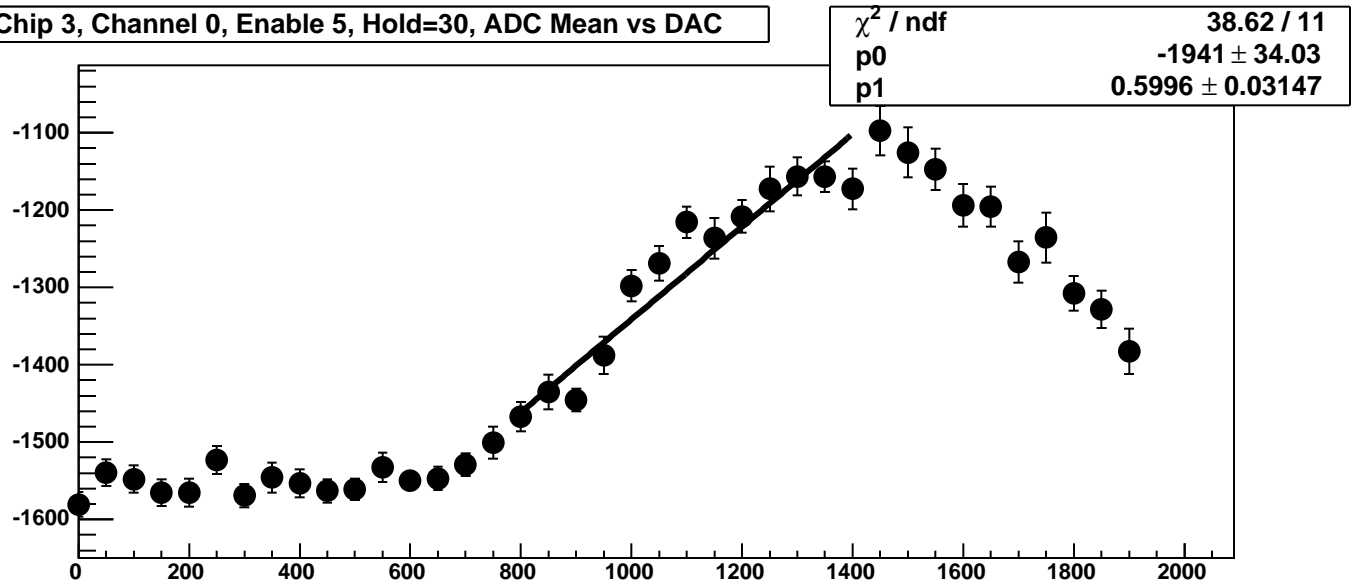
Chip 3, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC



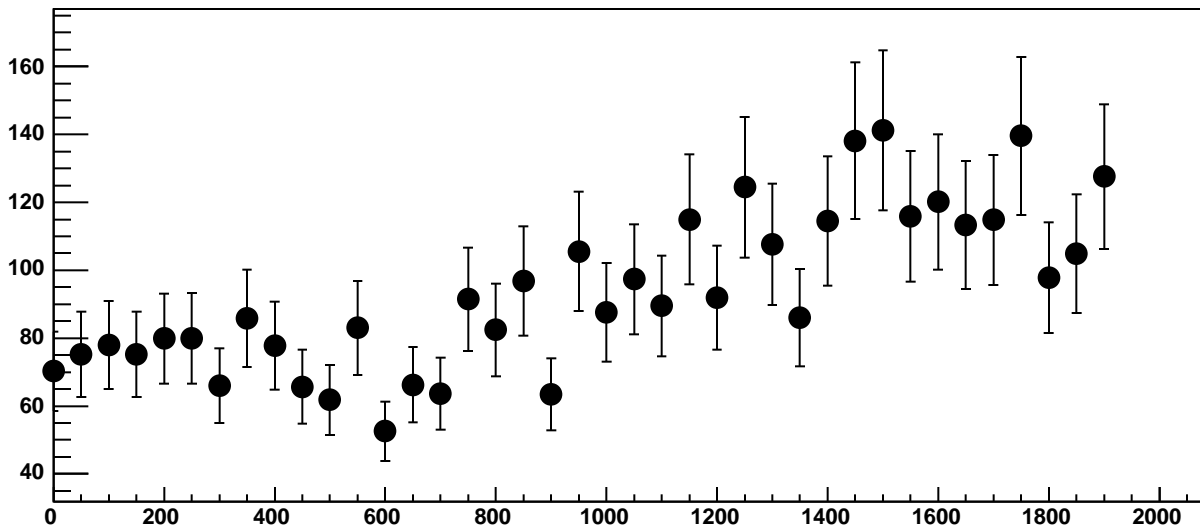
Chip 3, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC



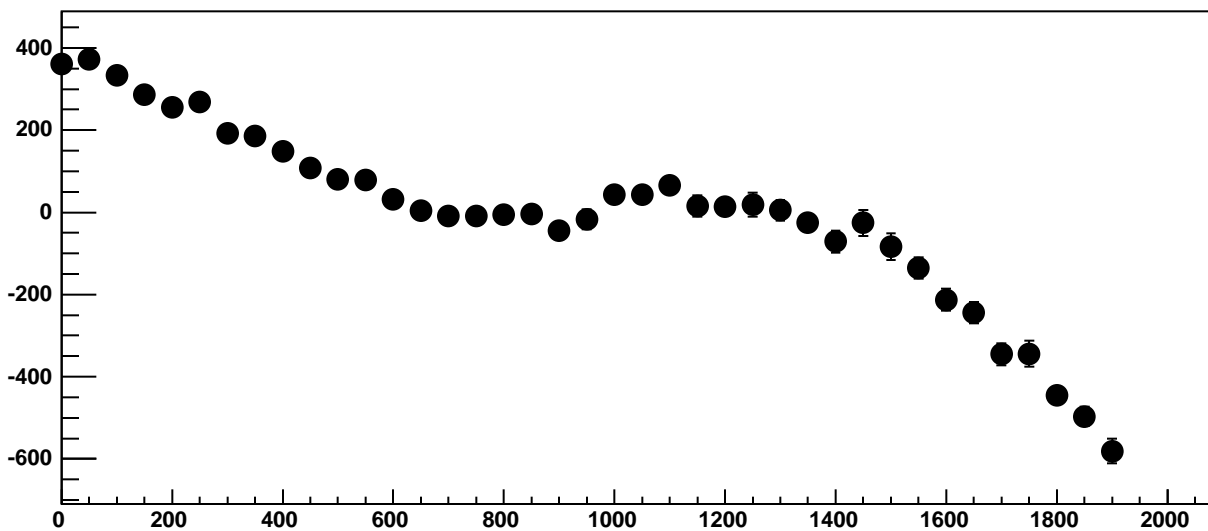
Chip 3, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC



Chip 3, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC

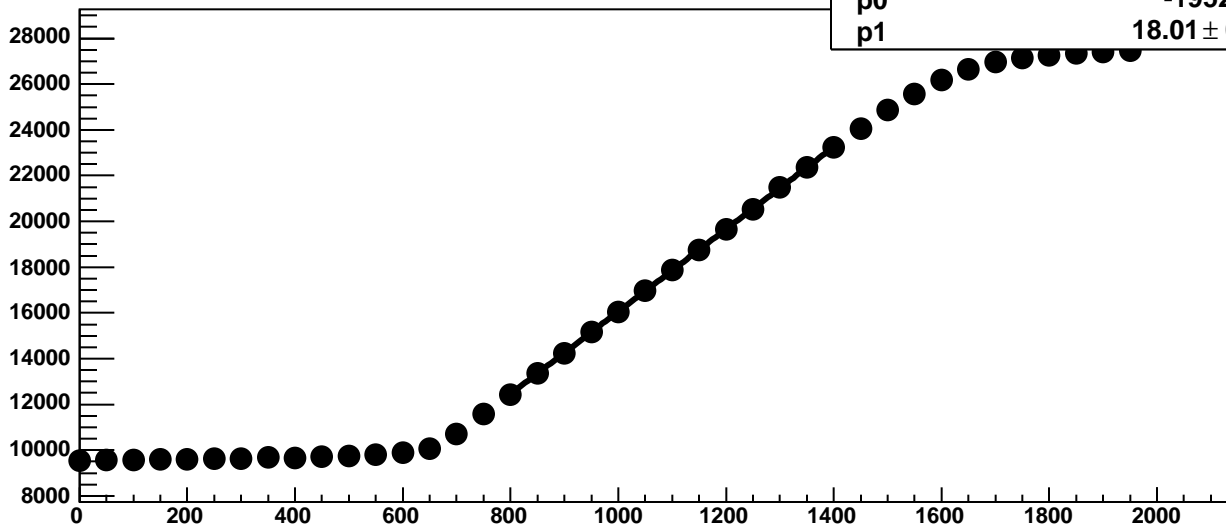


Chip 3, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC



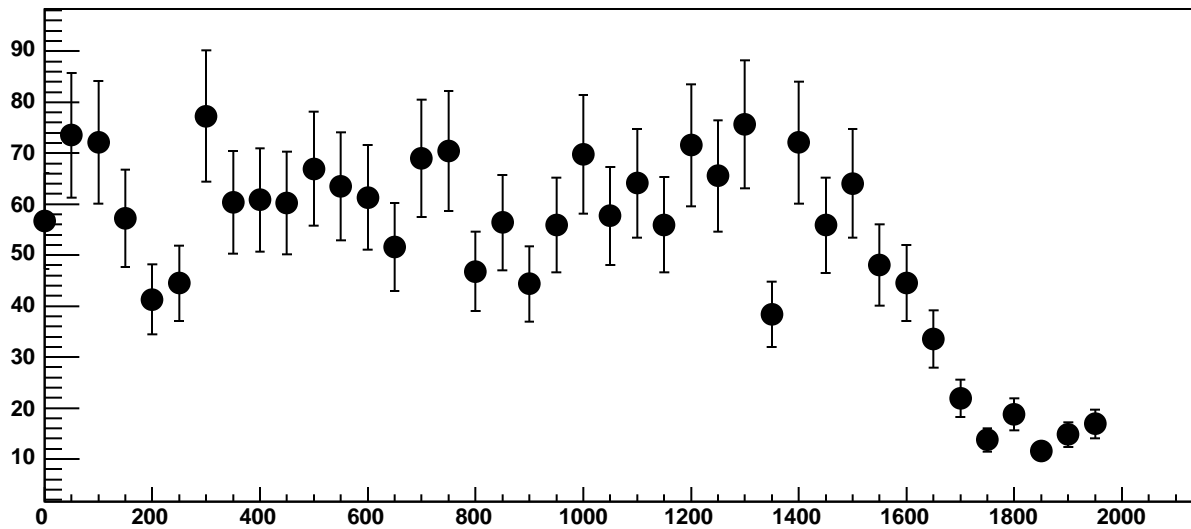


Chip 3, Channel 1, Enable 0!, Hold=30, ADC Mean vs DAC

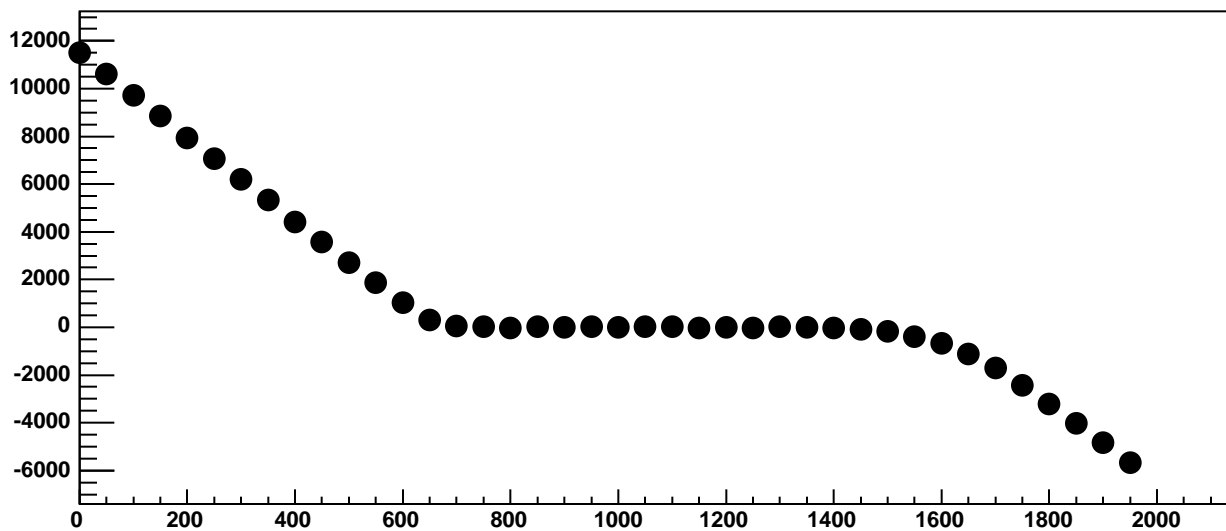


$\chi^2 / \text{ndf}$  31.98 / 11  
p0  $-1952 \pm 19.7$   
p1  $18.01 \pm 0.01788$

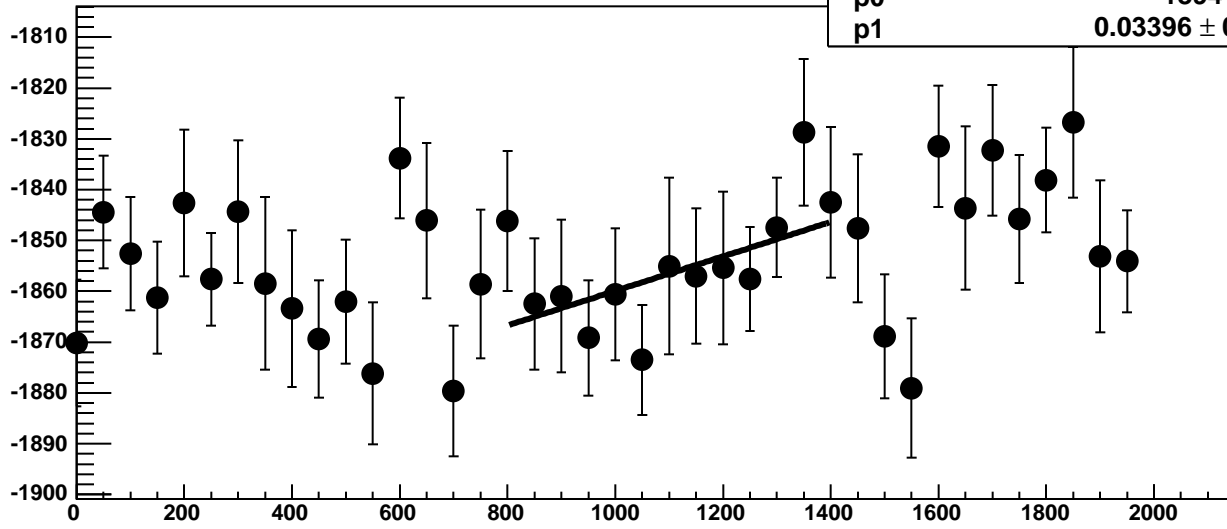
Chip 3, Channel 1, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 3, Channel 1, Enable 0!, Hold=30, ADC Residuals vs DAC

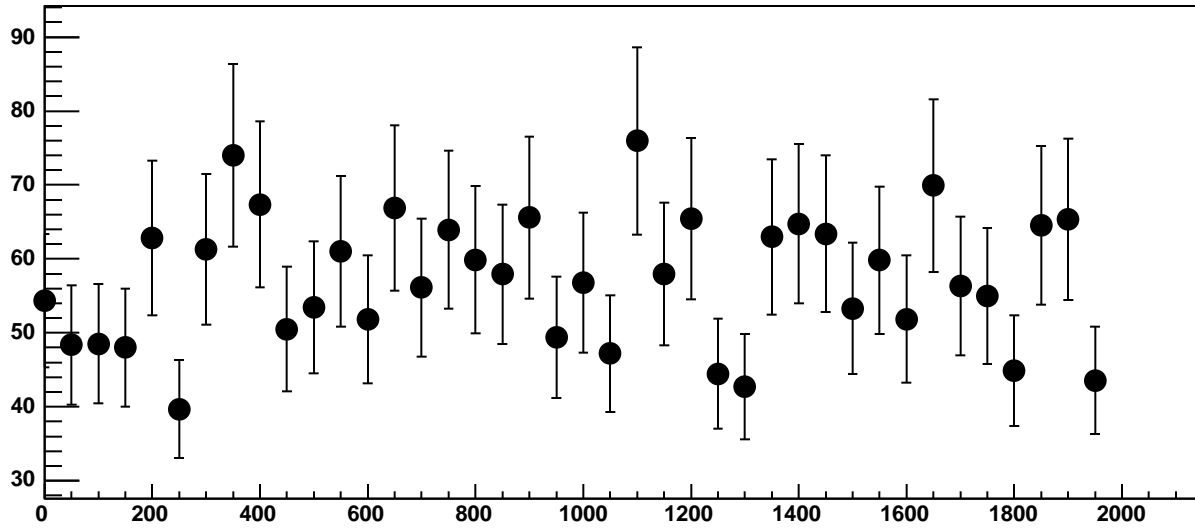


Chip 3, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC

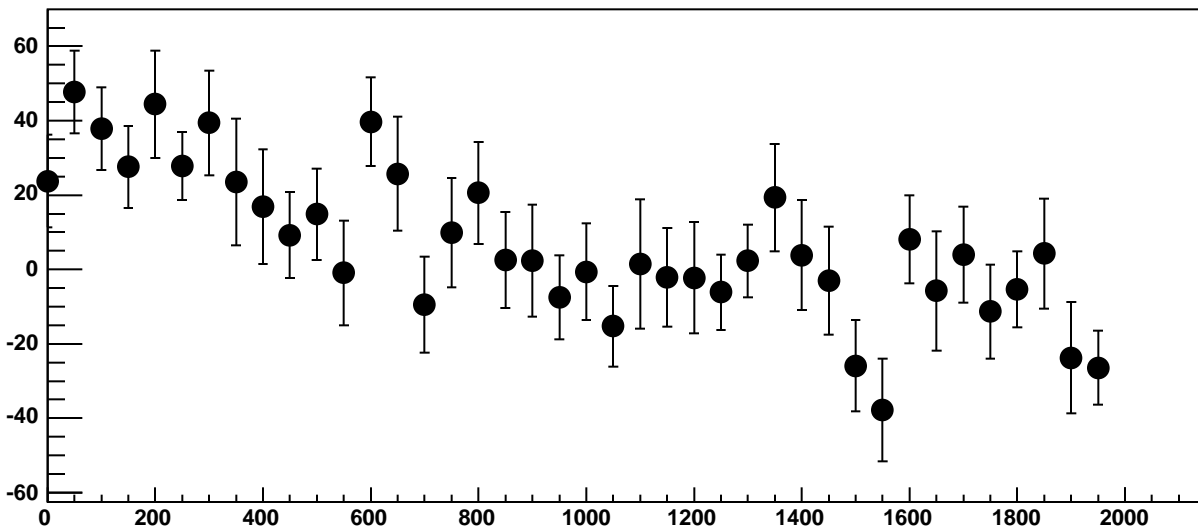


$\chi^2 / \text{ndf}$  7.08 / 11  
p0  $-1894 \pm 21.72$   
p1  $0.03396 \pm 0.01934$

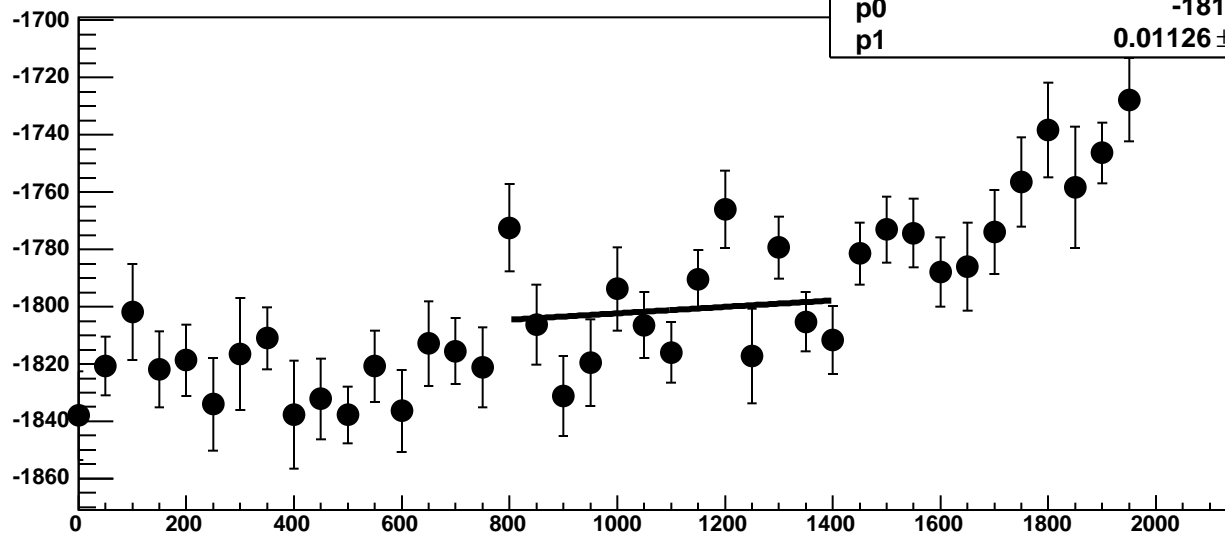
Chip 3, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC



Chip 3, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC



Chip 3, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

25.57 / 11

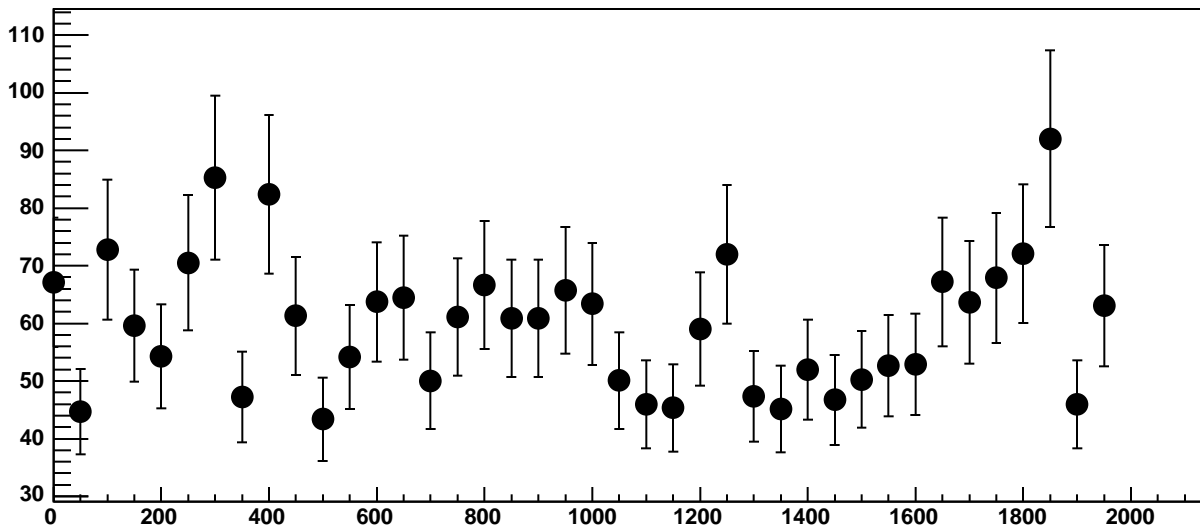
p0

$-1814 \pm 22$

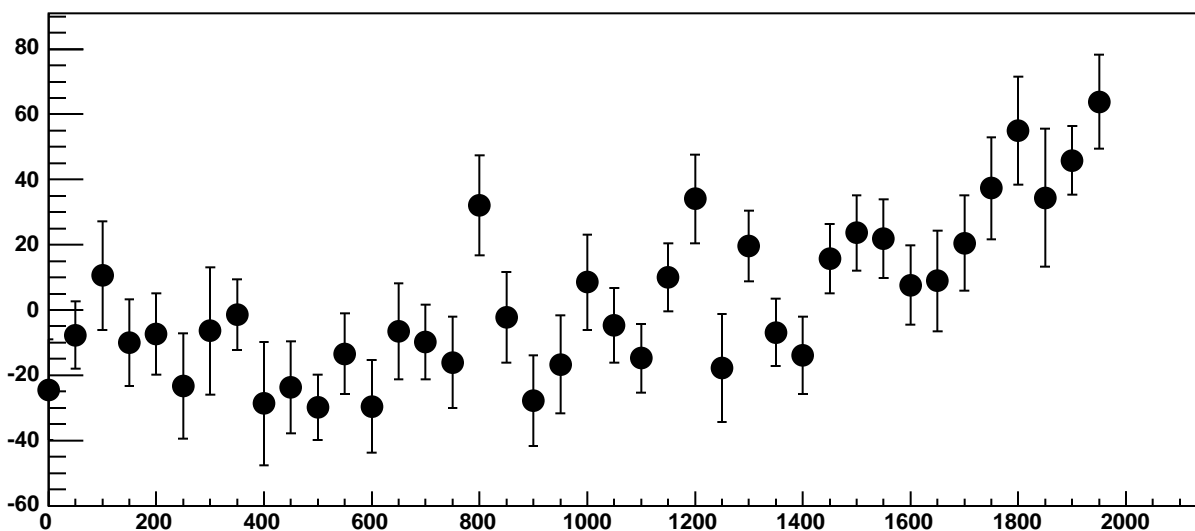
p1

$0.01126 \pm 0.0192$

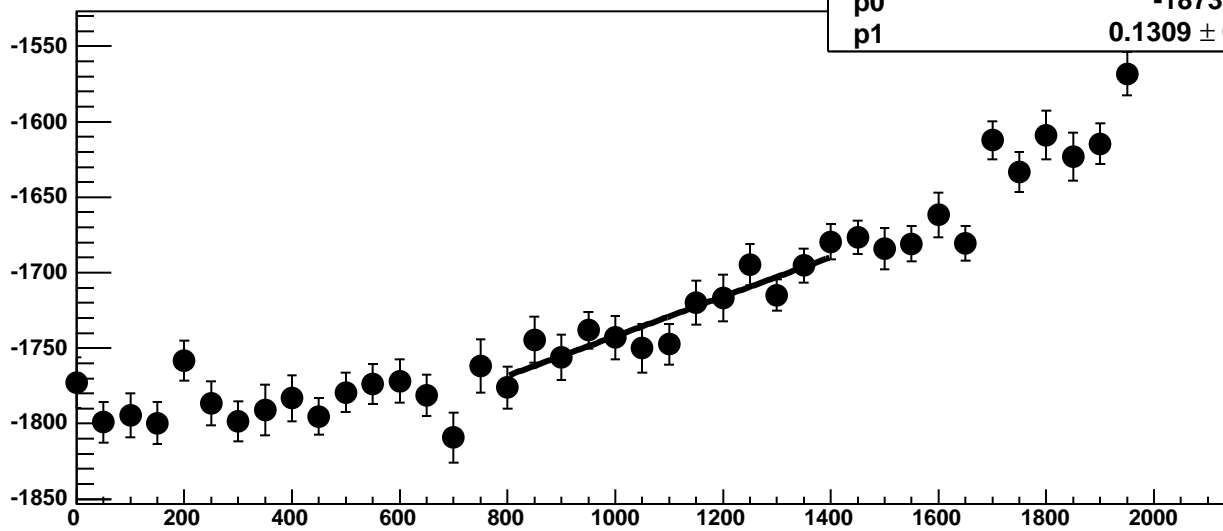
Chip 3, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC



Chip 3, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 3, Channel 1, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

8.263 / 11

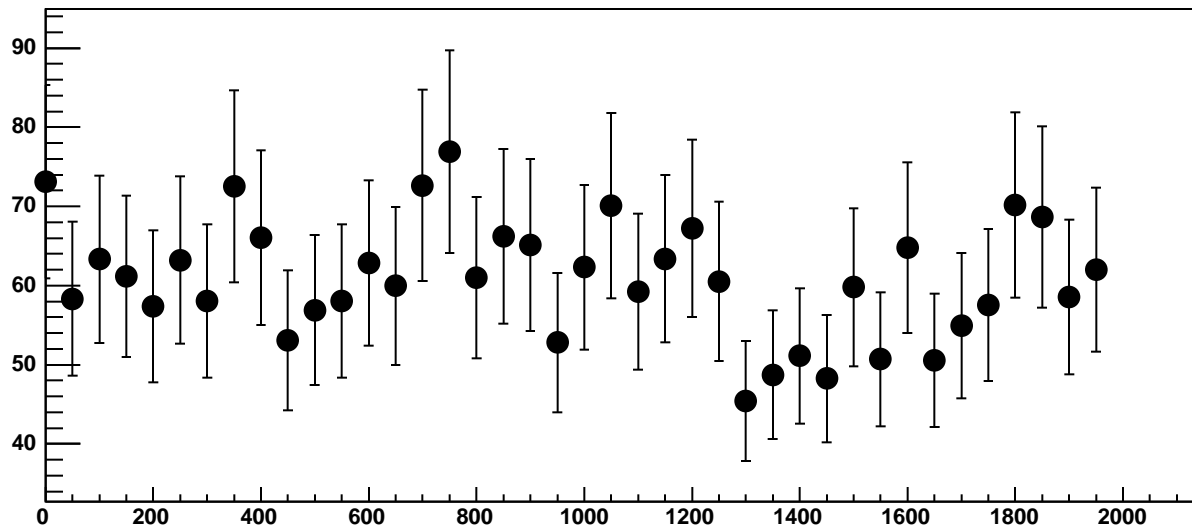
p0

$-1873 \pm 21.88$

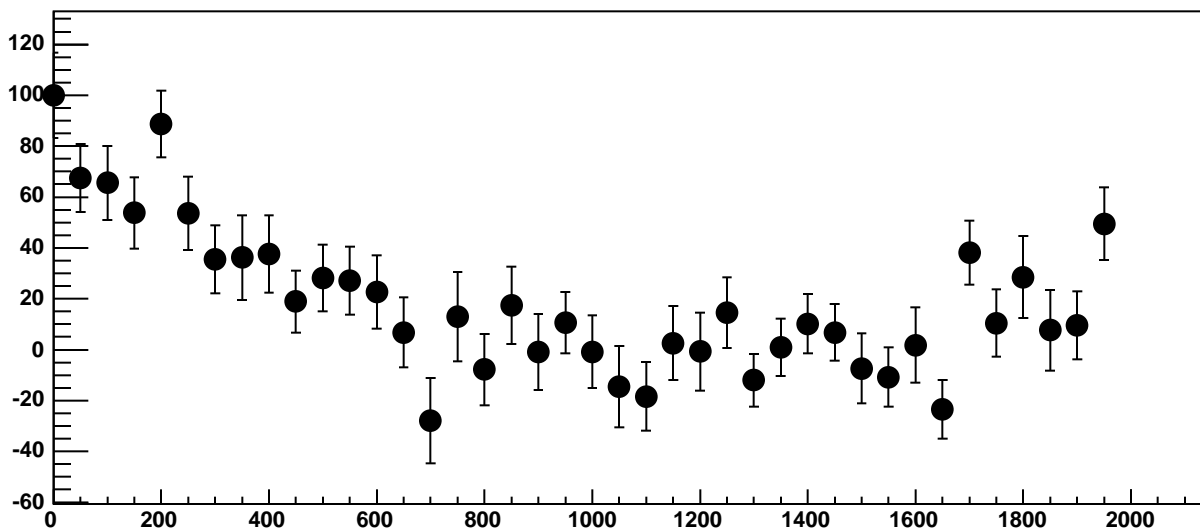
p1

$0.1309 \pm 0.01908$

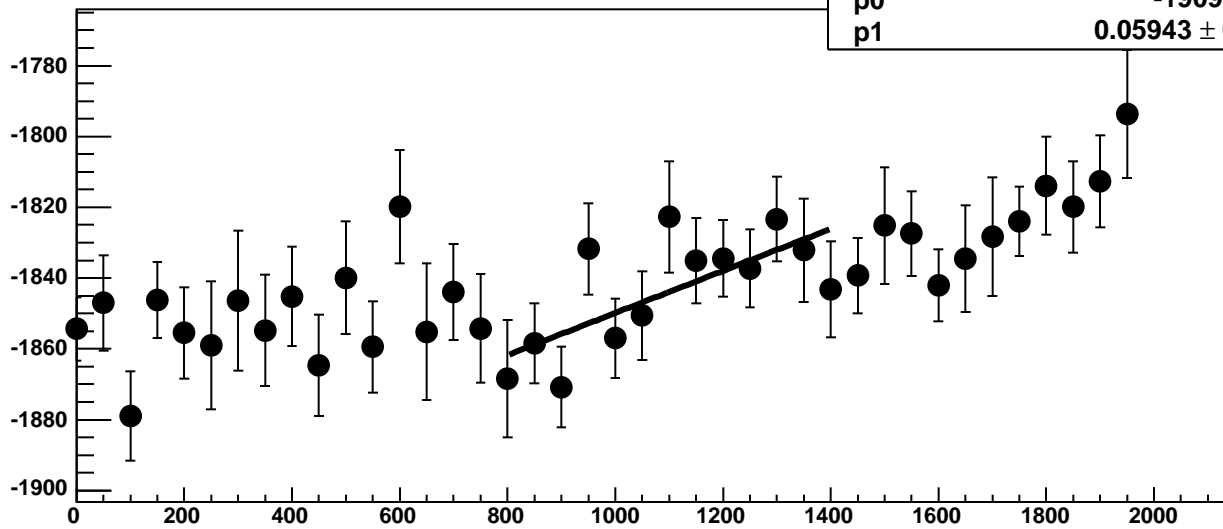
Chip 3, Channel 1, Enable 3, Hold=30, ADC Noise vs DAC



Chip 3, Channel 1, Enable 3, Hold=30, ADC Residuals vs DAC



Chip 3, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

9.409 / 11

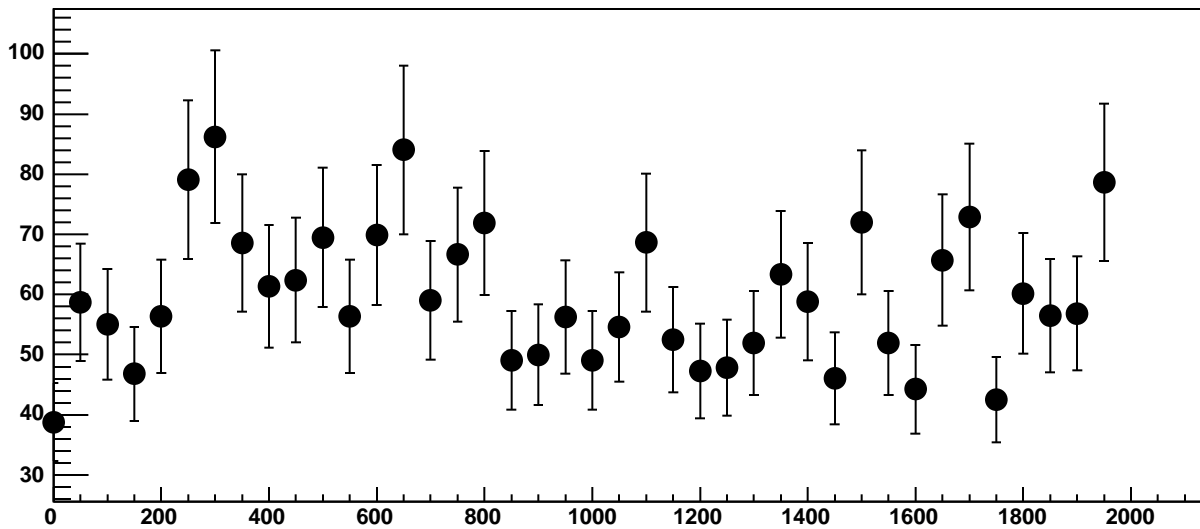
p0

$-1909 \pm 21.39$

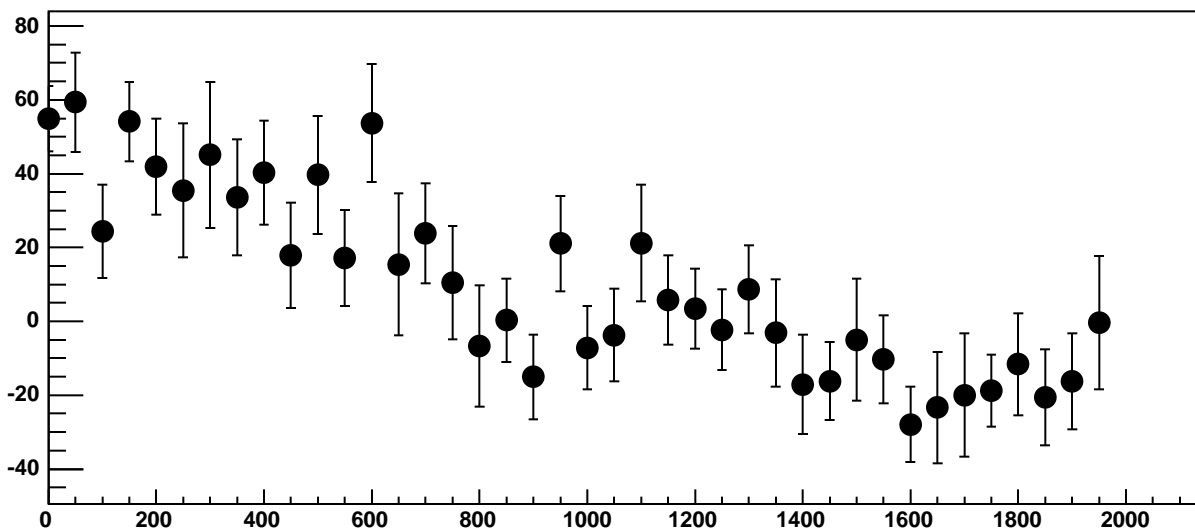
p1

$0.05943 \pm 0.01918$

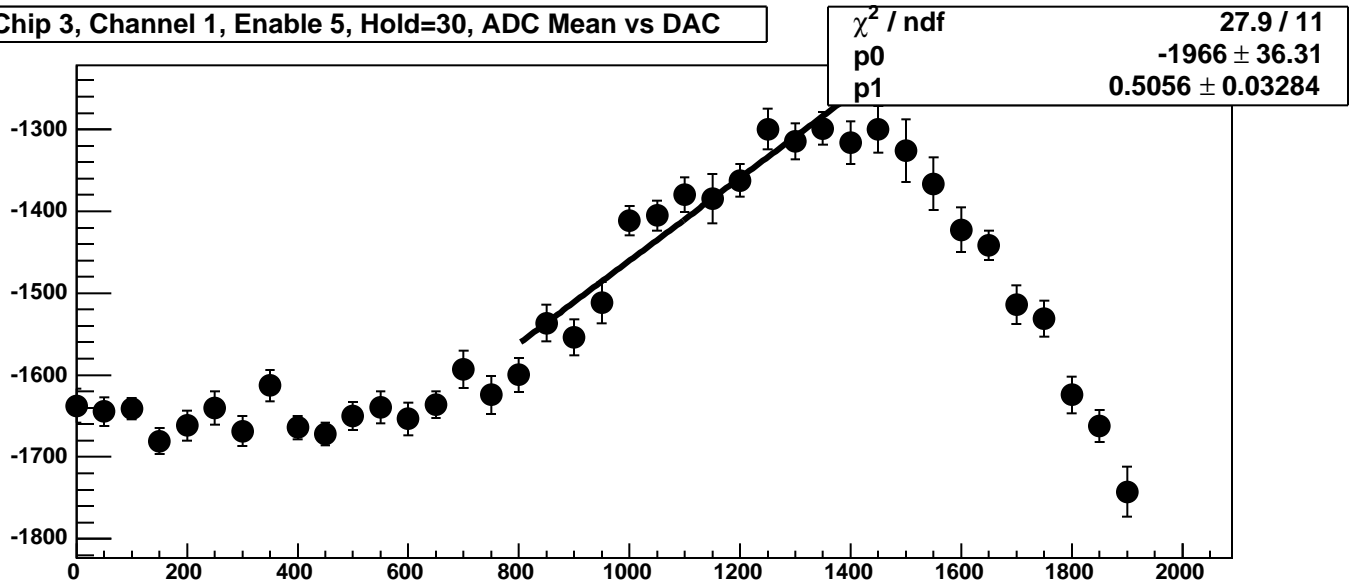
Chip 3, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC



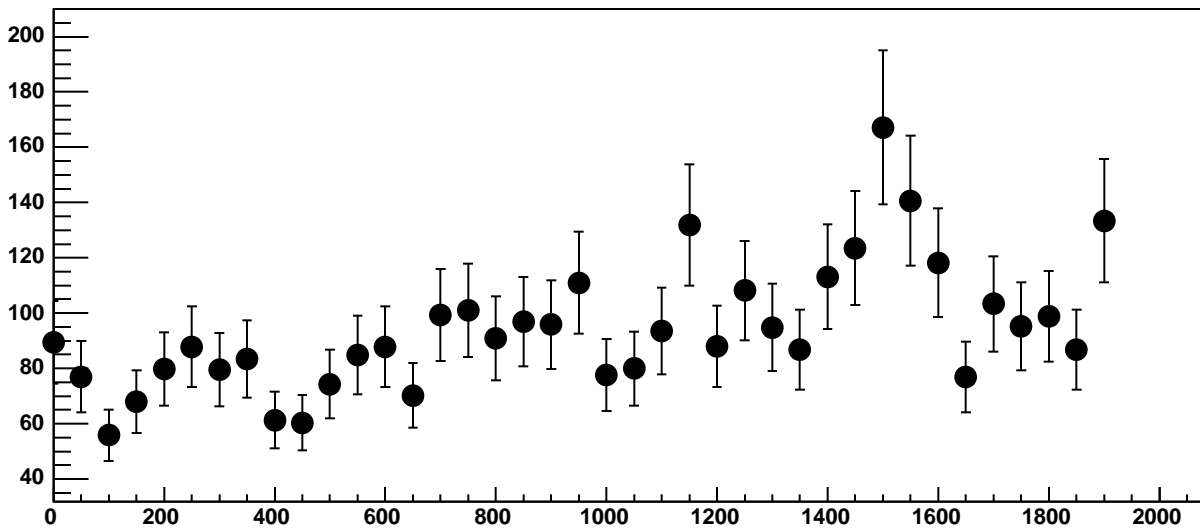
Chip 3, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC



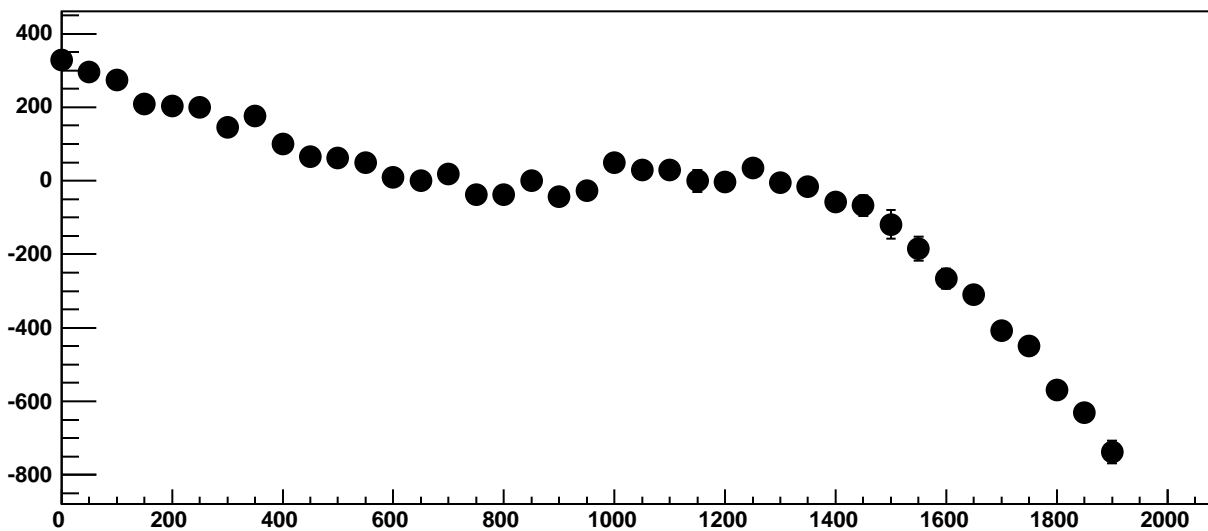
Chip 3, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC



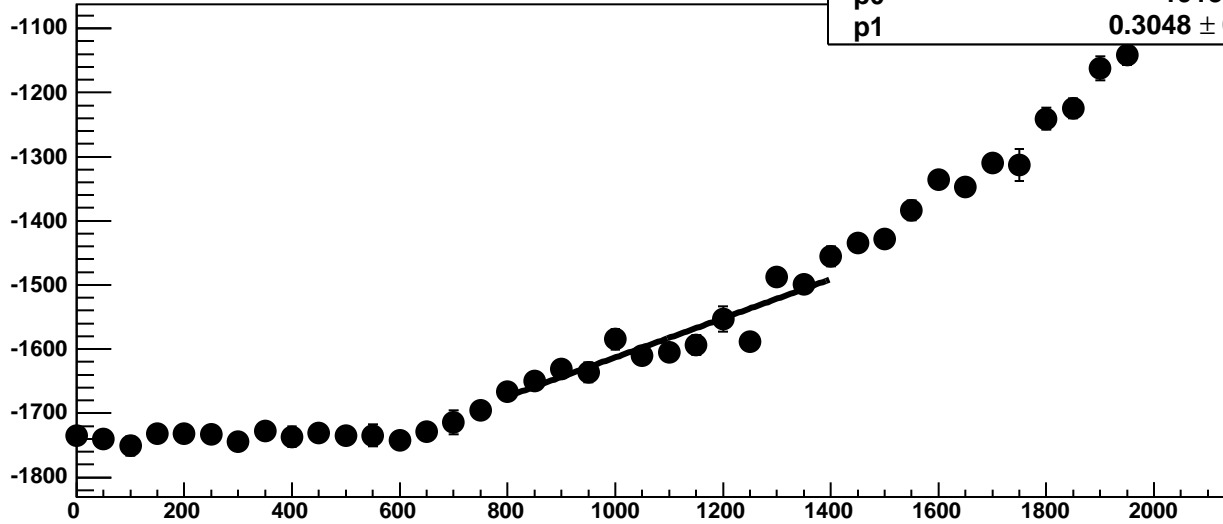
Chip 3, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC



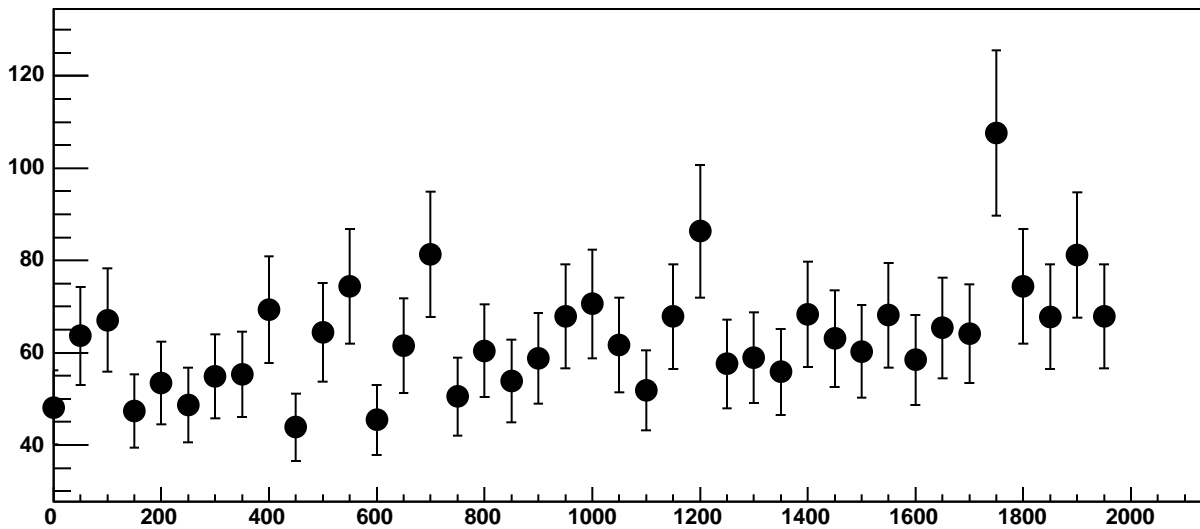
Chip 3, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC



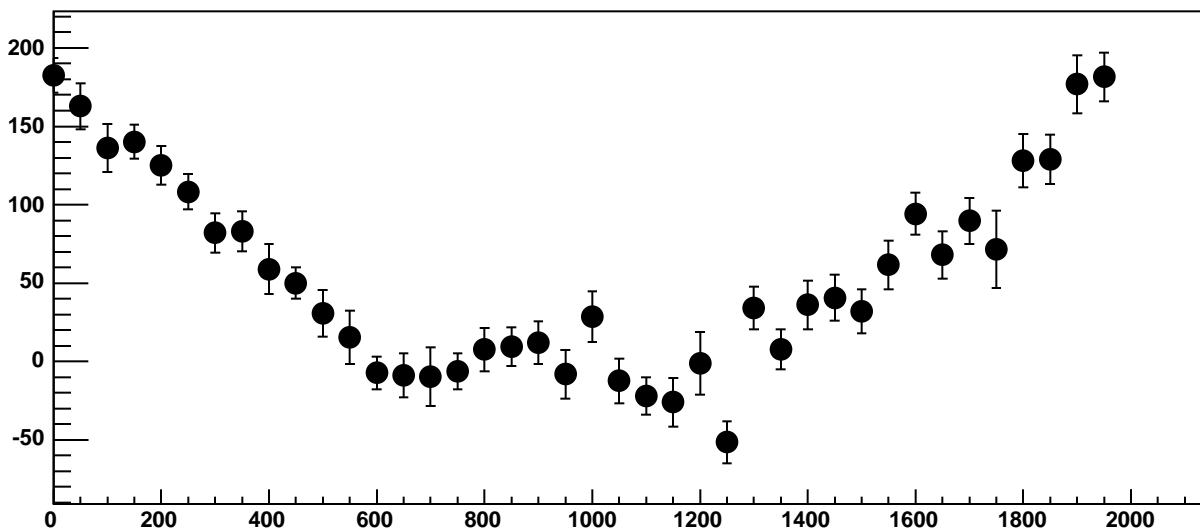
Chip 3, Channel 2, Enable 0, Hold=30, ADC Mean vs DAC



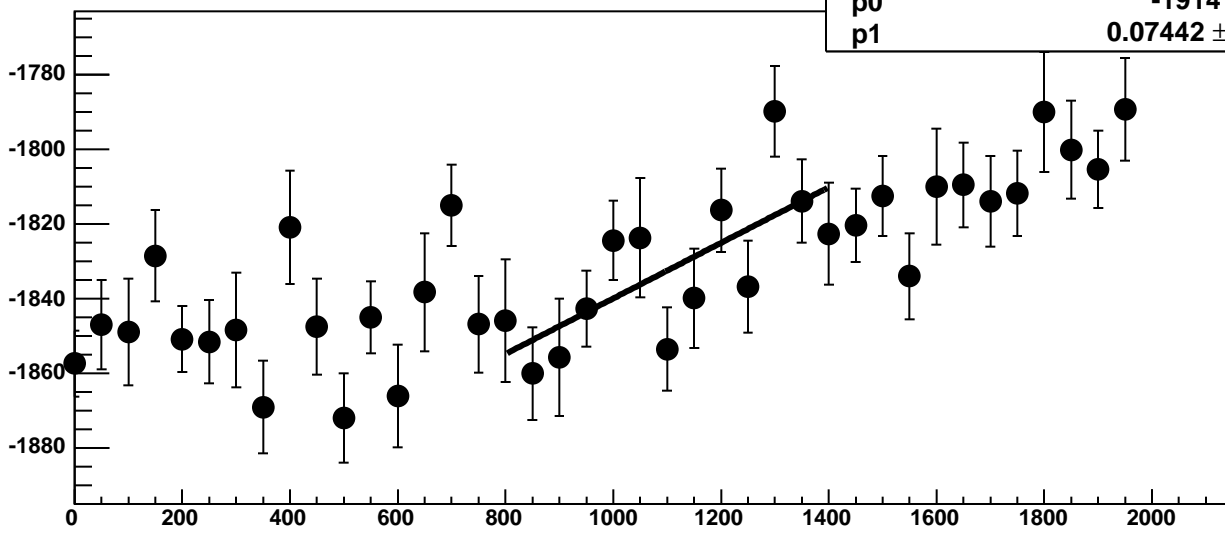
Chip 3, Channel 2, Enable 0, Hold=30, ADC Noise vs DAC



Chip 3, Channel 2, Enable 0, Hold=30, ADC Residuals vs DAC

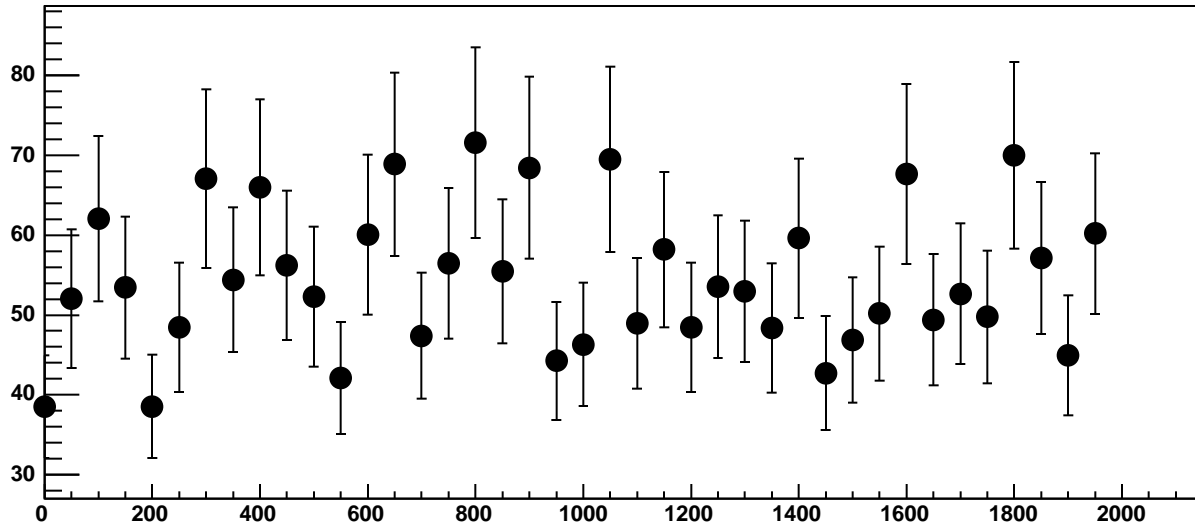


Chip 3, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC

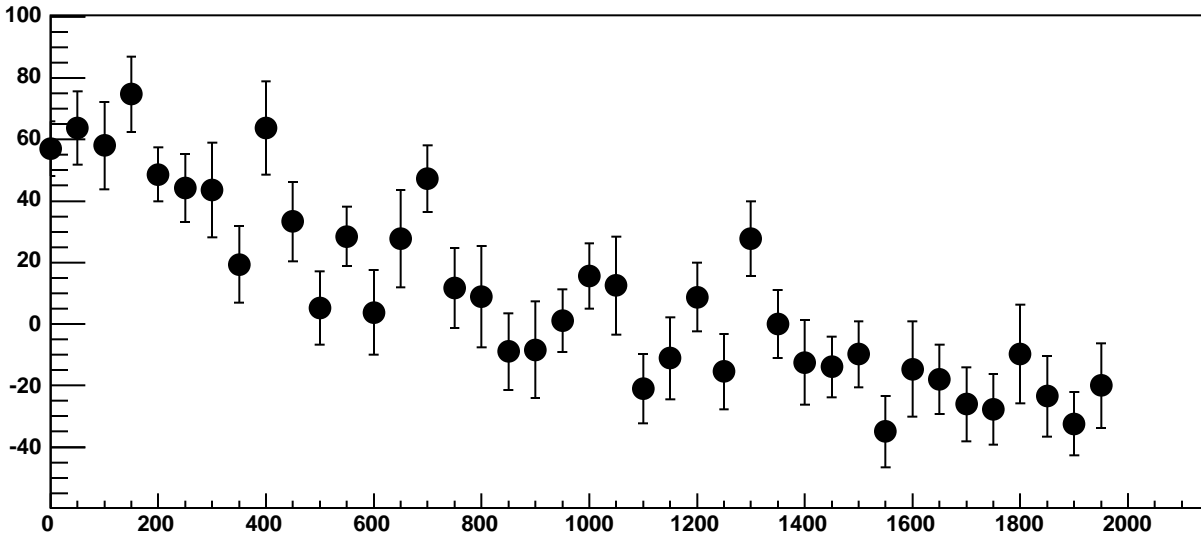


$\chi^2 / \text{ndf}$  16.32 / 11  
p0  $-1914 \pm 21.62$   
p1  $0.07442 \pm 0.0192$

Chip 3, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC

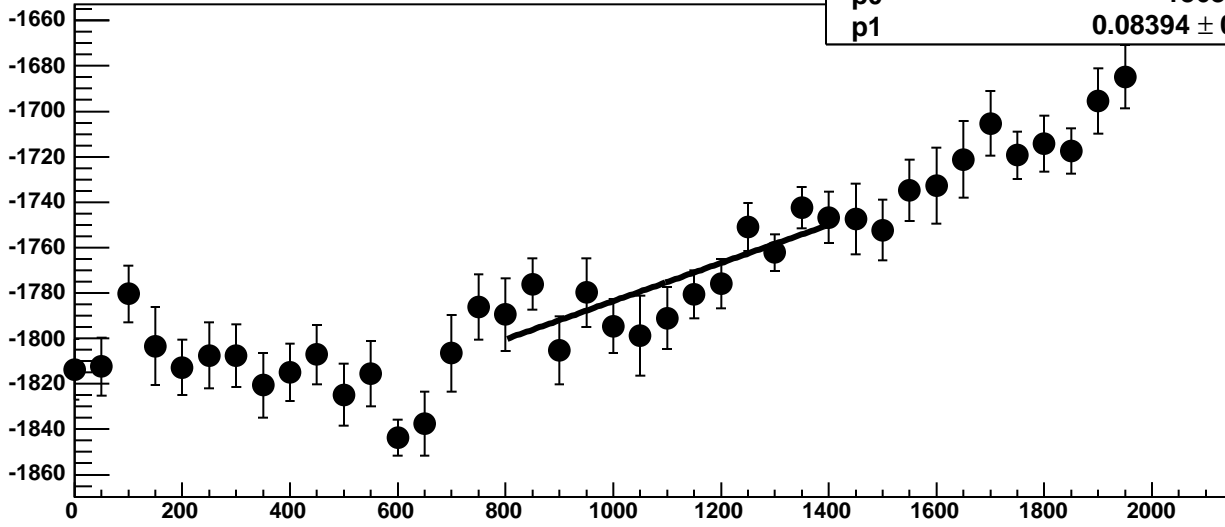


Chip 3, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC



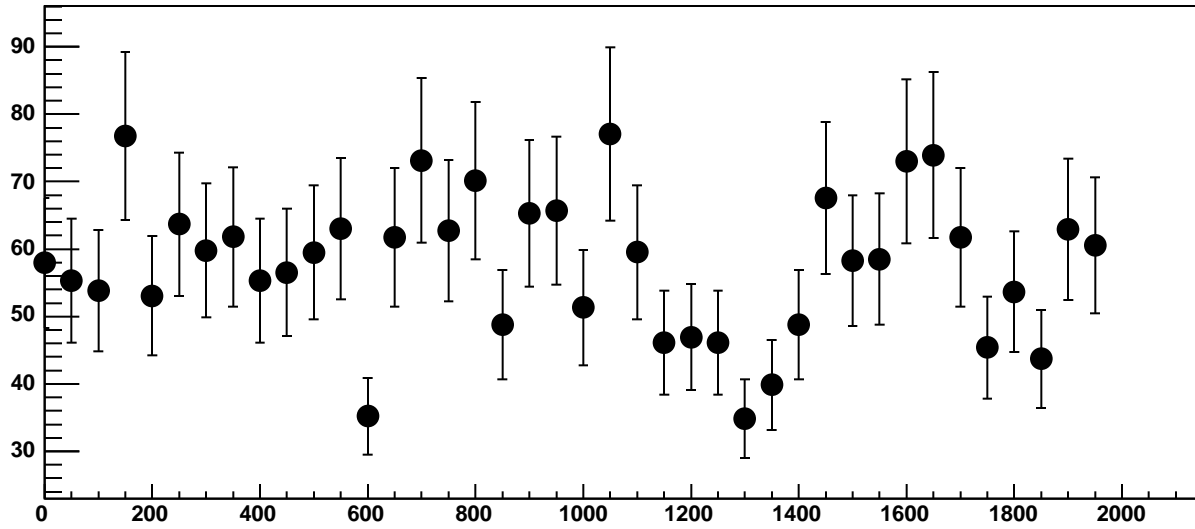


Chip 3, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC

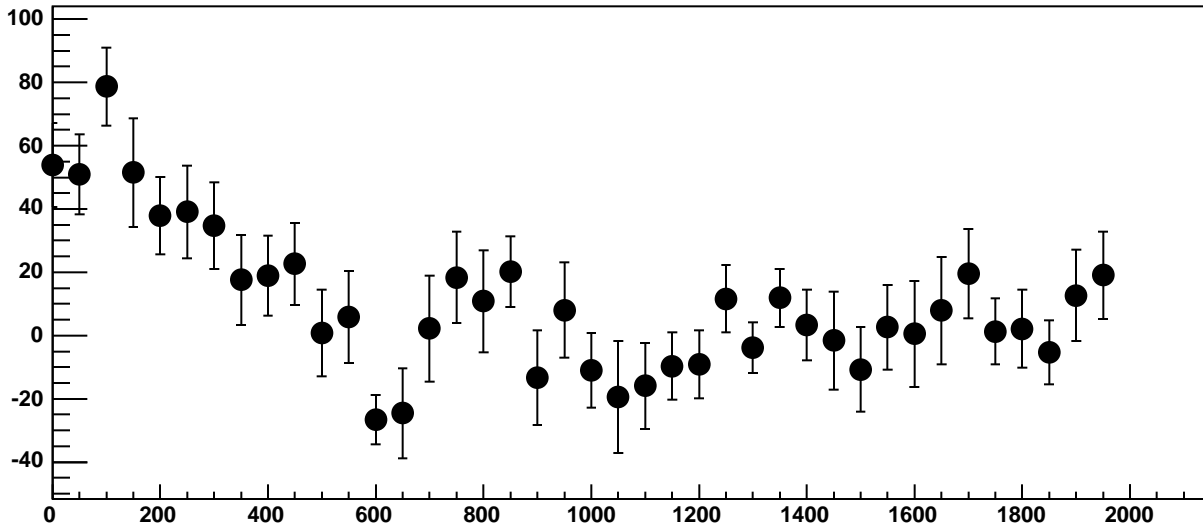


$\chi^2 / \text{ndf}$  12.95 / 11  
p0  $-1868 \pm 20.41$   
p1  $0.08394 \pm 0.01743$

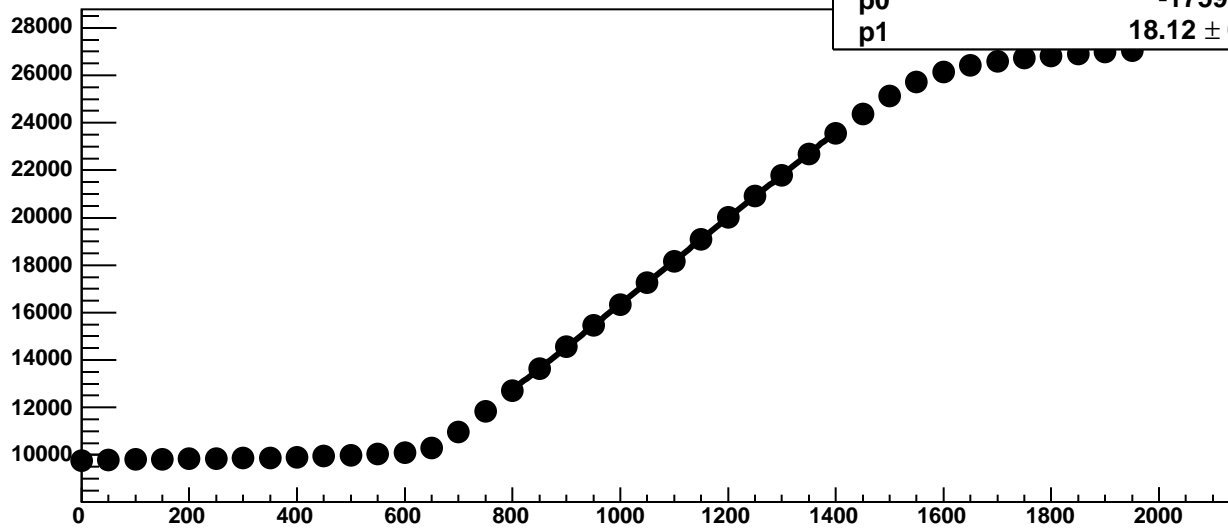
Chip 3, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC



Chip 3, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC

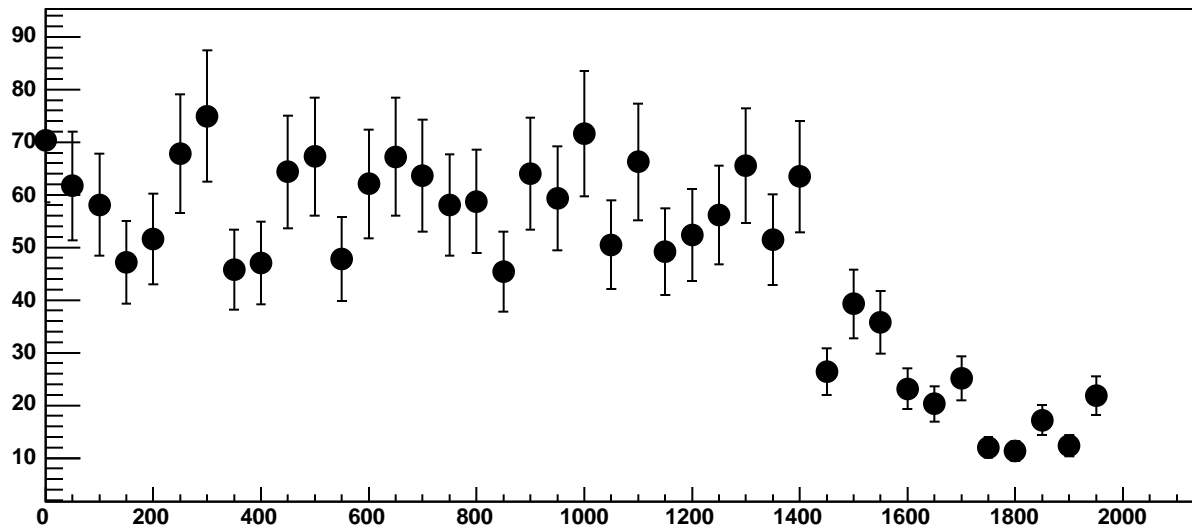


Chip 3, Channel 2, Enable 3!, Hold=30, ADC Mean vs DAC

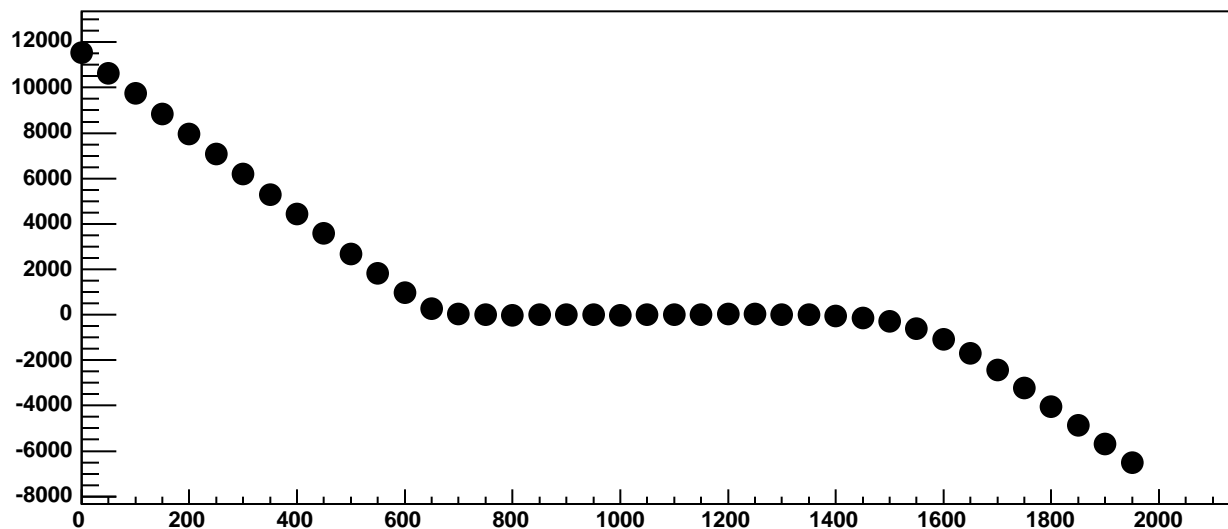


$\chi^2 / \text{ndf}$  32.58 / 11  
p0  $-1759 \pm 21.36$   
p1  $18.12 \pm 0.01922$

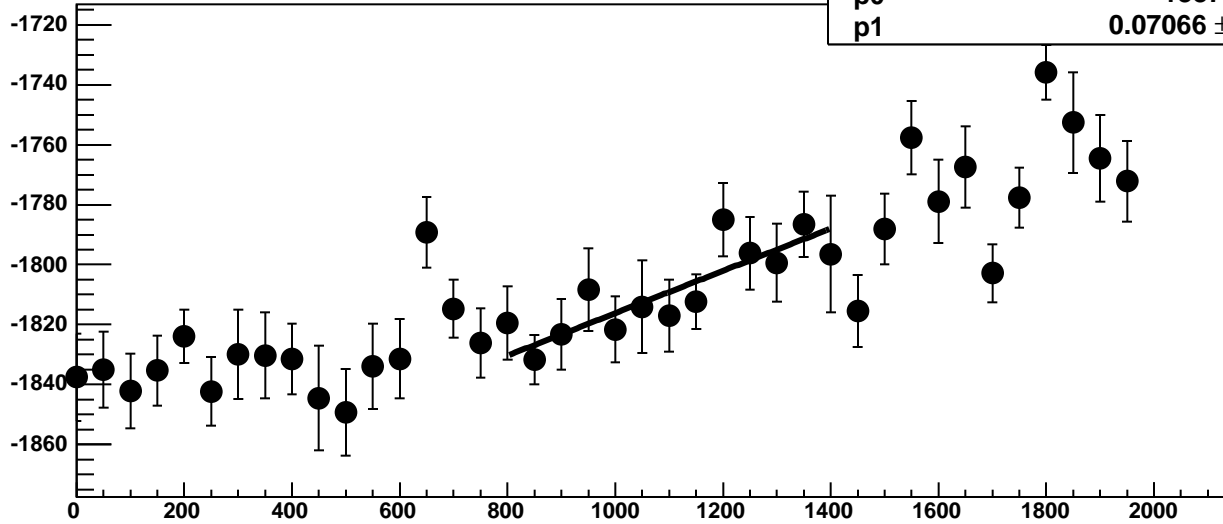
Chip 3, Channel 2, Enable 3!, Hold=30, ADC Noise vs DAC



Chip 3, Channel 2, Enable 3!, Hold=30, ADC Residuals vs DAC

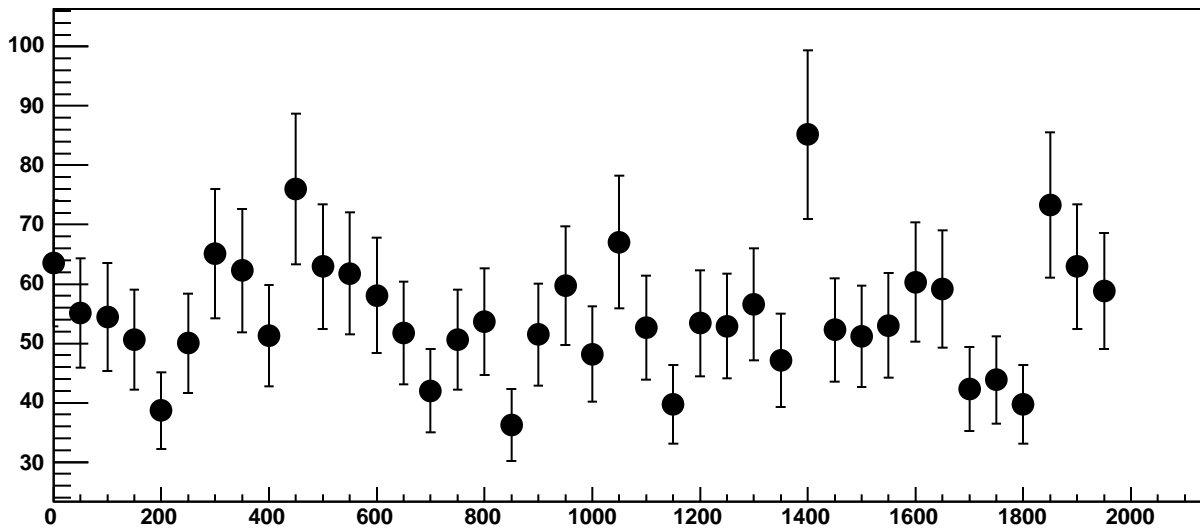


Chip 3, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC

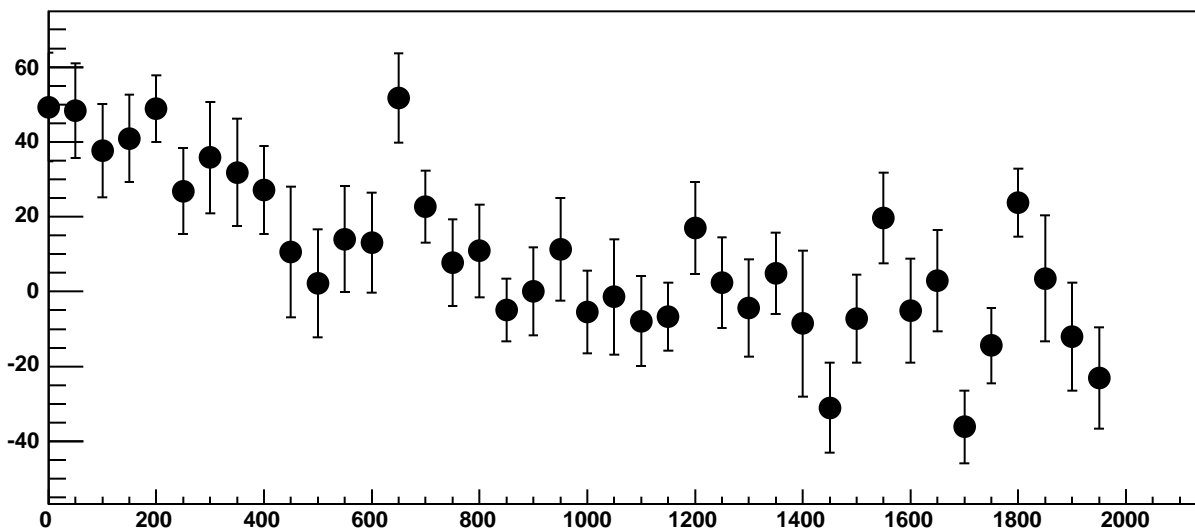


$\chi^2 / \text{ndf}$  5.495 / 11  
p0  $-1887 \pm 19.29$   
p1  $0.07066 \pm 0.0177$

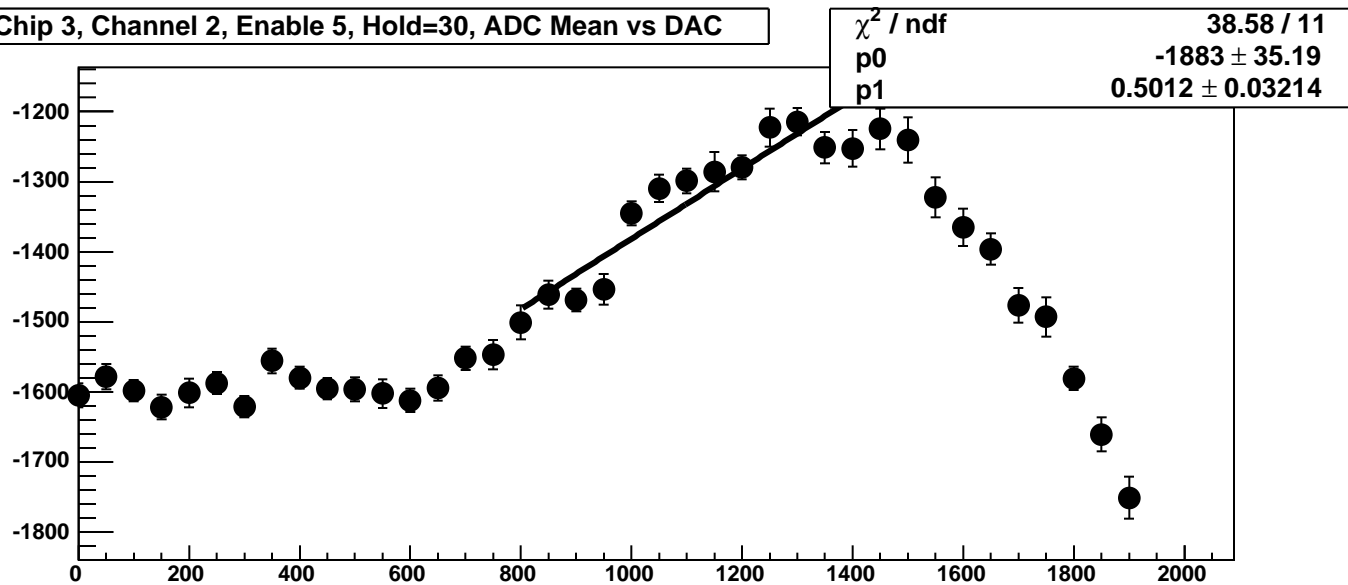
Chip 3, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC



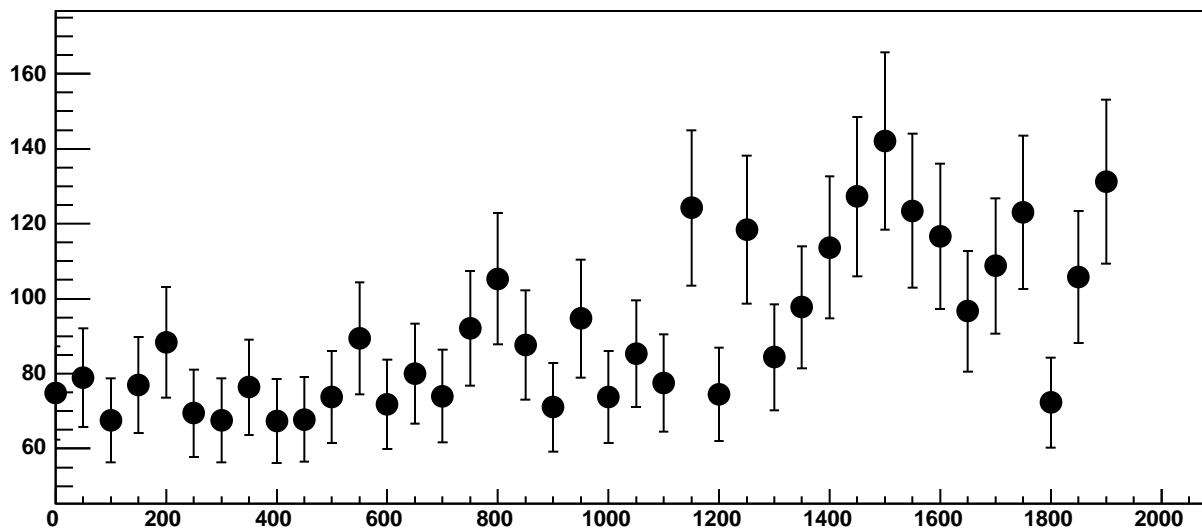
Chip 3, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC



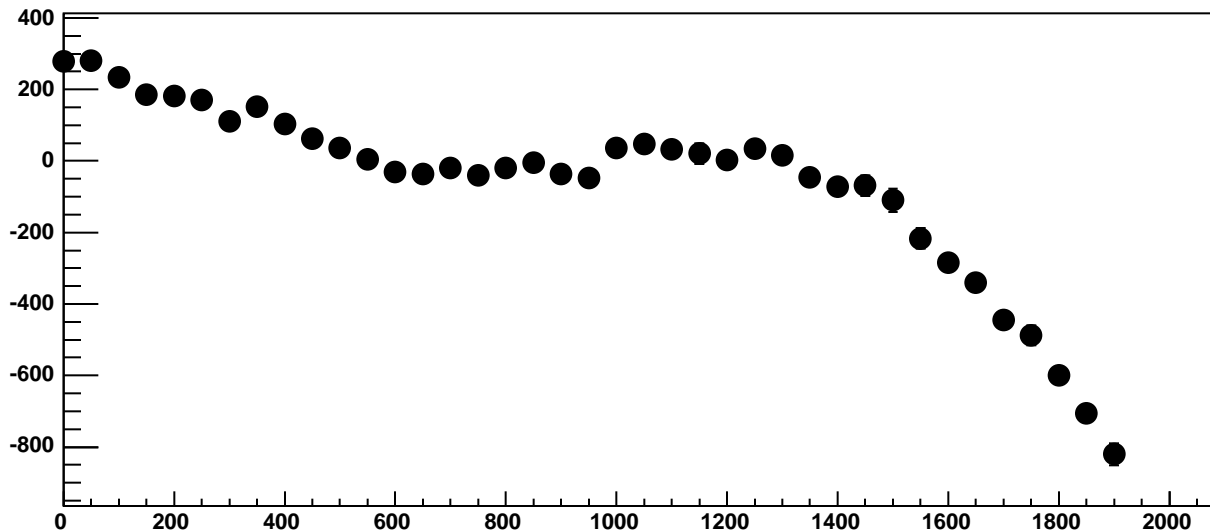
Chip 3, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC



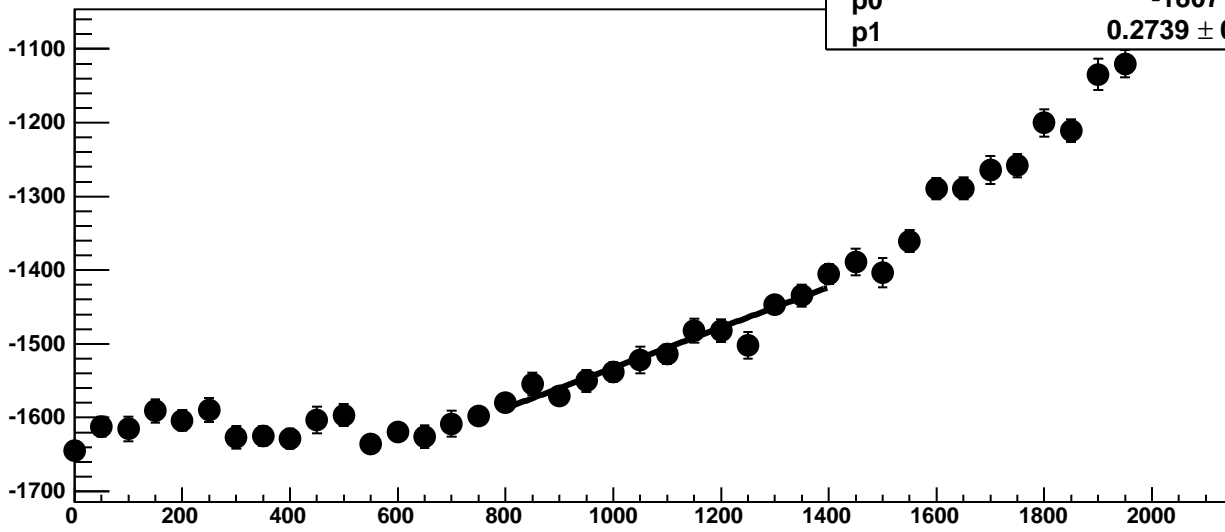
Chip 3, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC



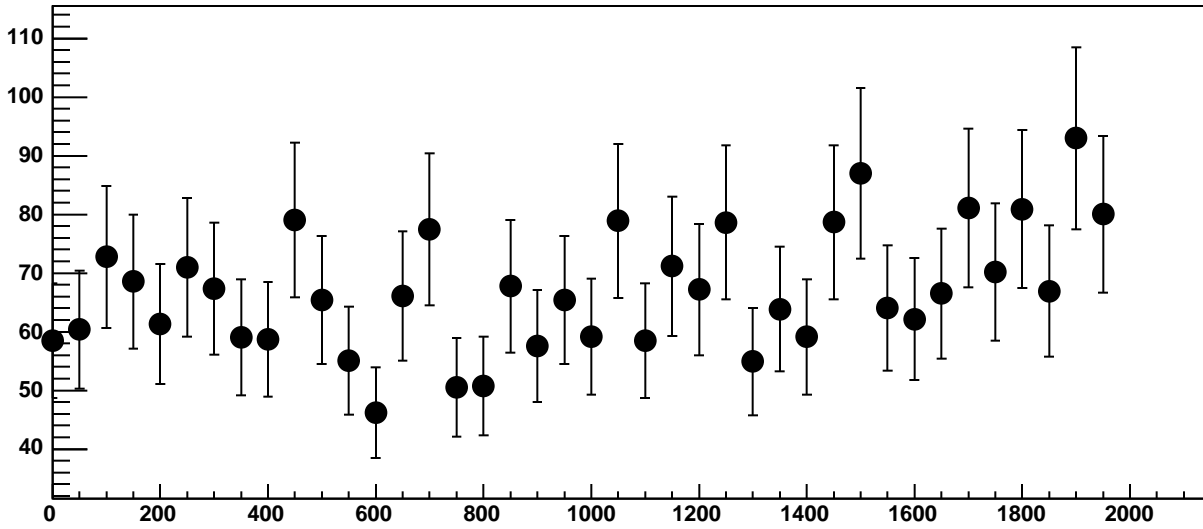
Chip 3, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC



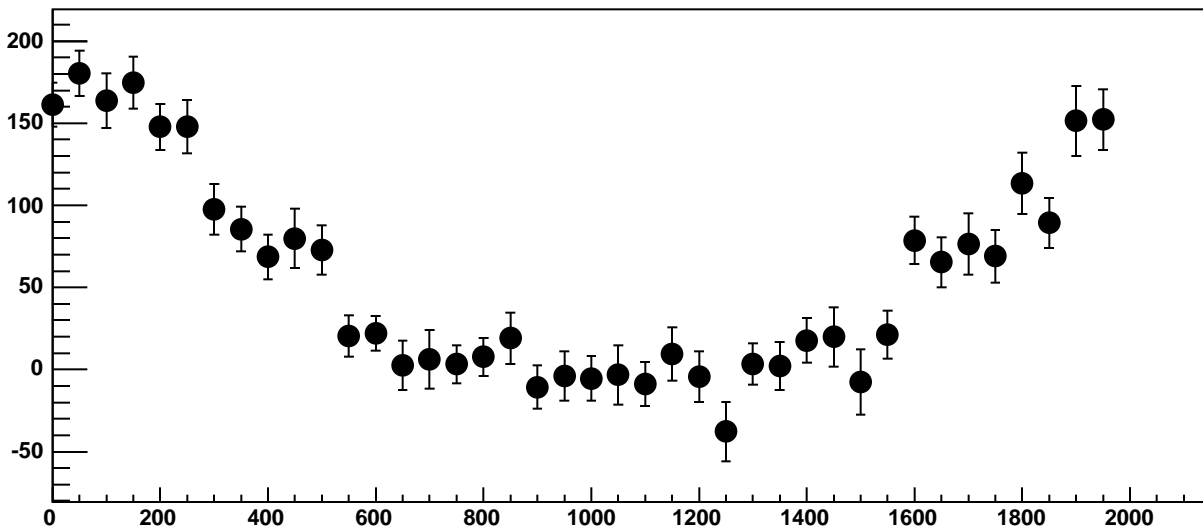
Chip 3, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC



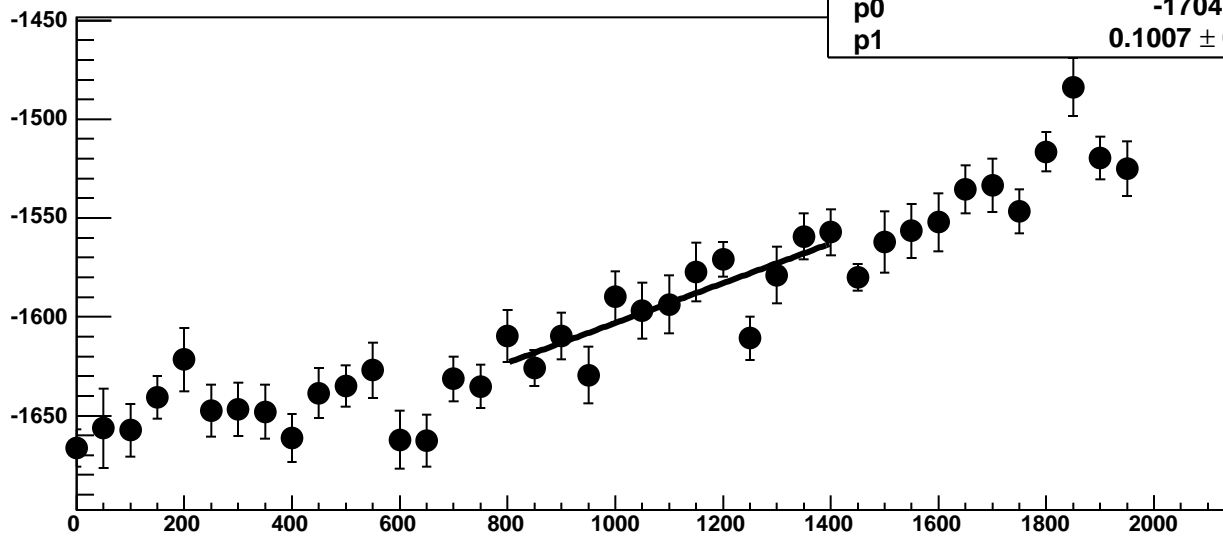
Chip 3, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC



Chip 3, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 3, Channel 3, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

17.53 / 11

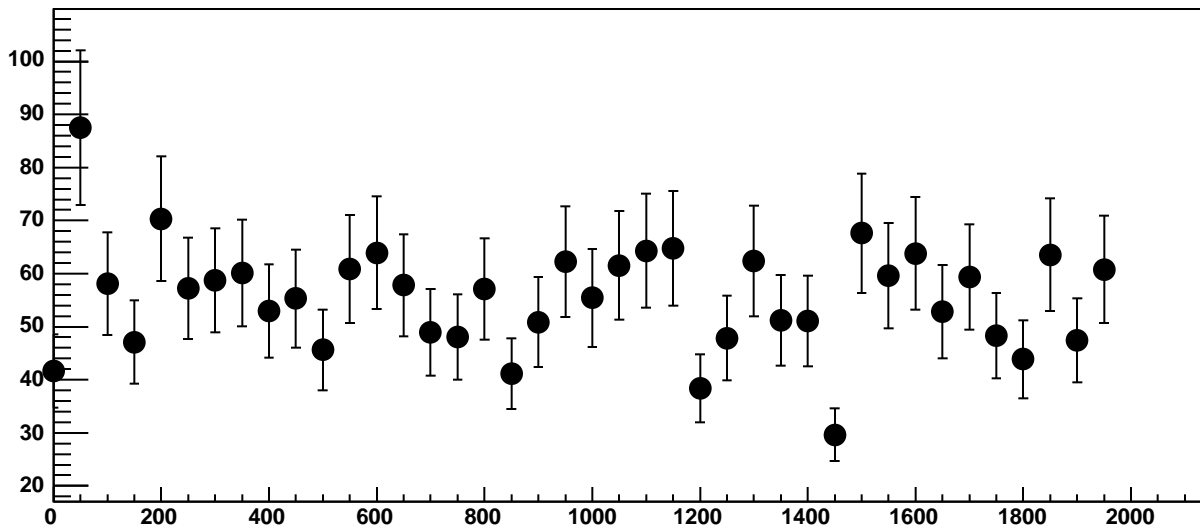
p0

$-1704 \pm 19.23$

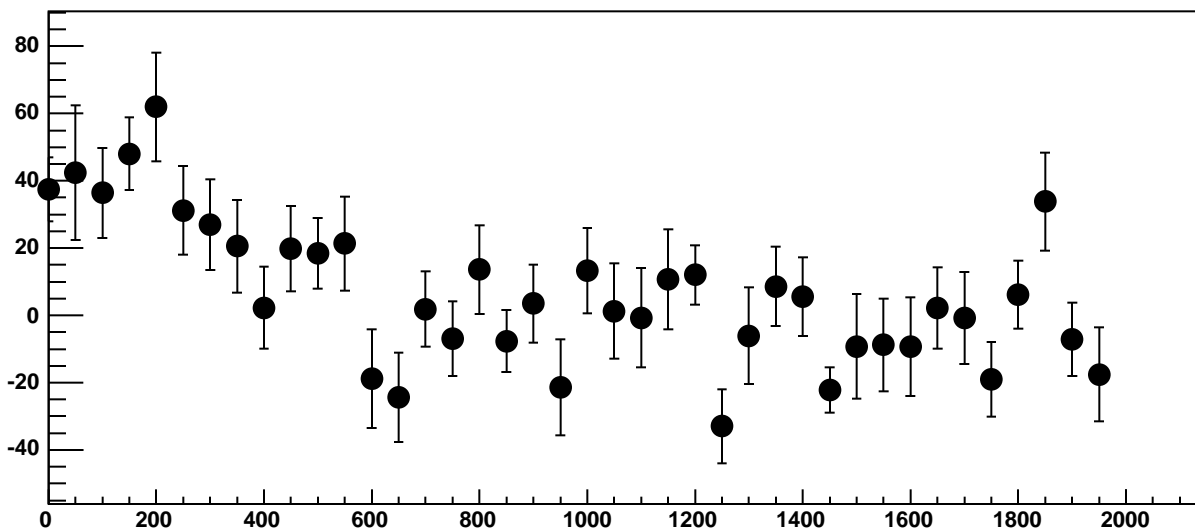
p1

$0.1007 \pm 0.01722$

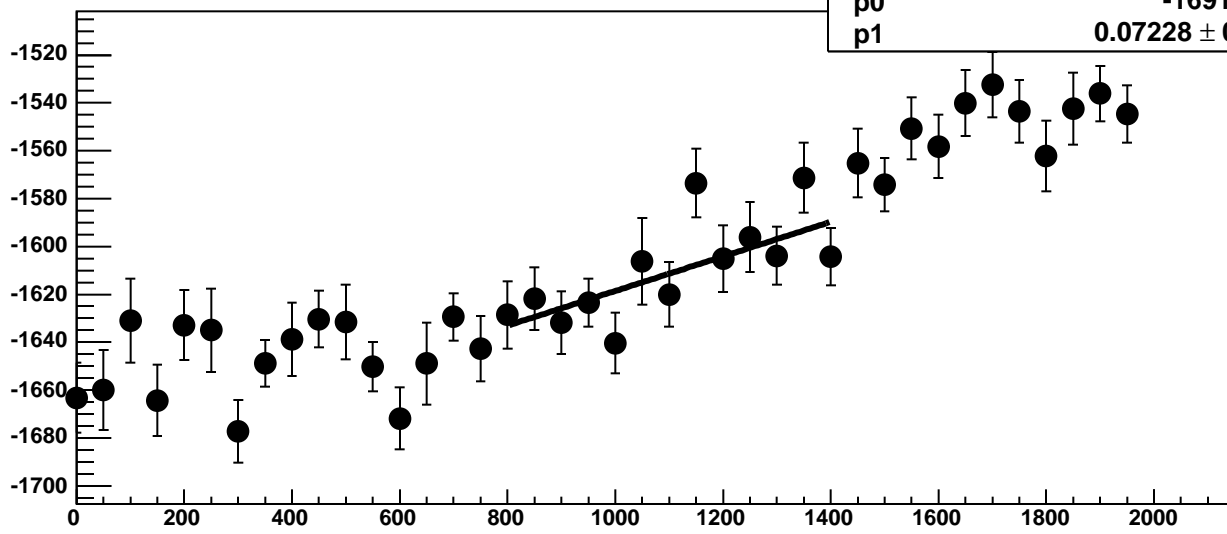
Chip 3, Channel 3, Enable 1, Hold=30, ADC Noise vs DAC



Chip 3, Channel 3, Enable 1, Hold=30, ADC Residuals vs DAC

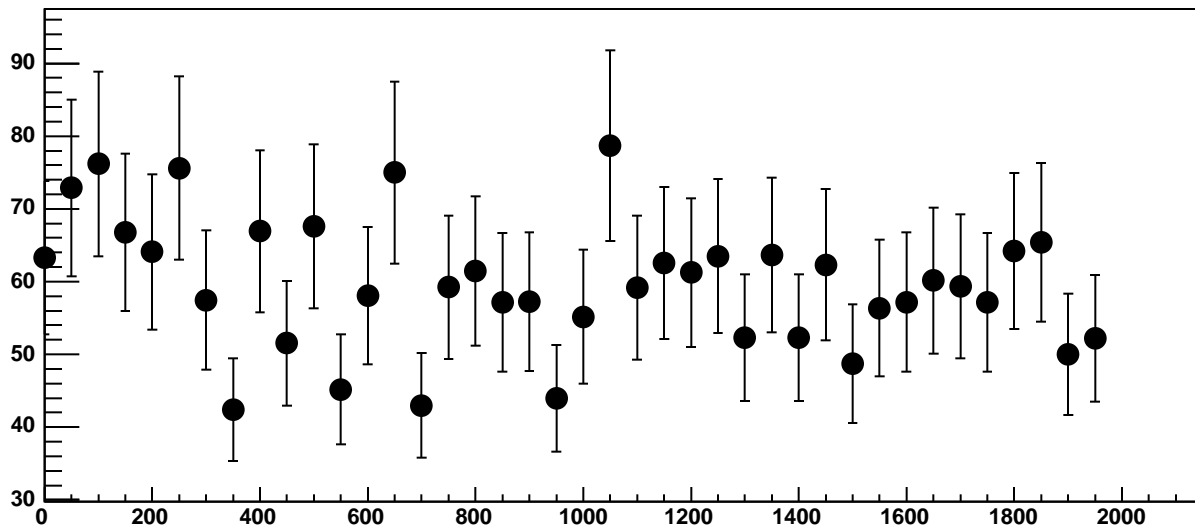


Chip 3, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC

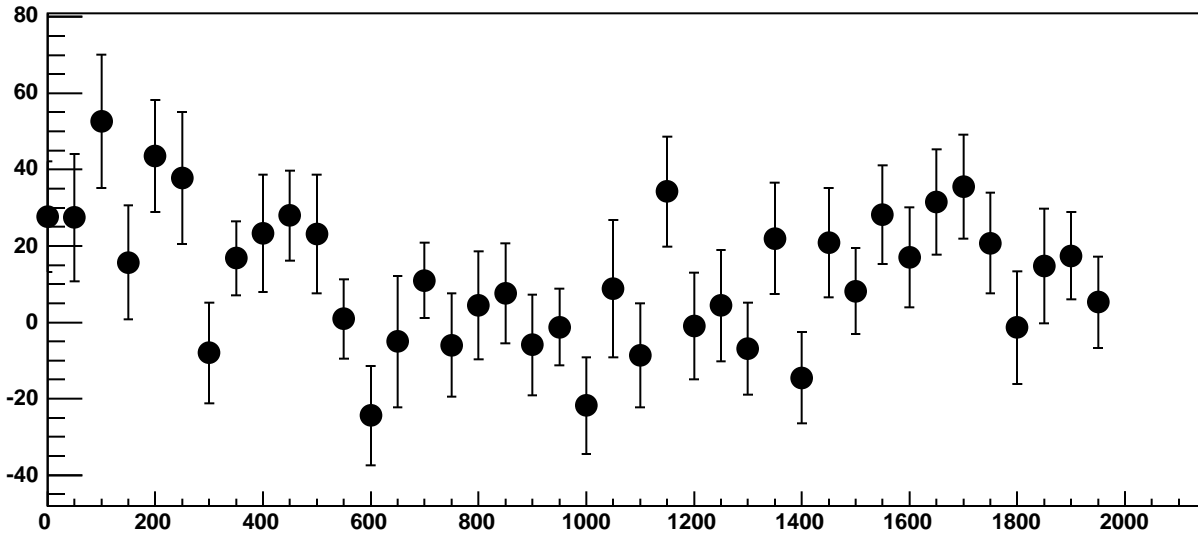


$\chi^2 / \text{ndf}$  14.13 / 11  
p0  $-1691 \pm 21.4$   
p1  $0.07228 \pm 0.01923$

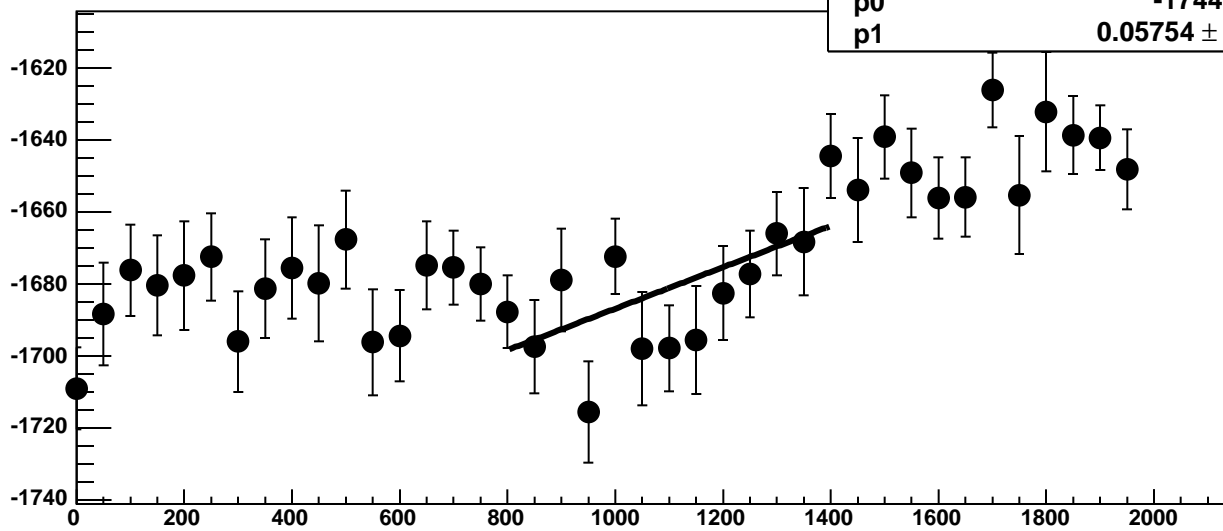
Chip 3, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC



Chip 3, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC

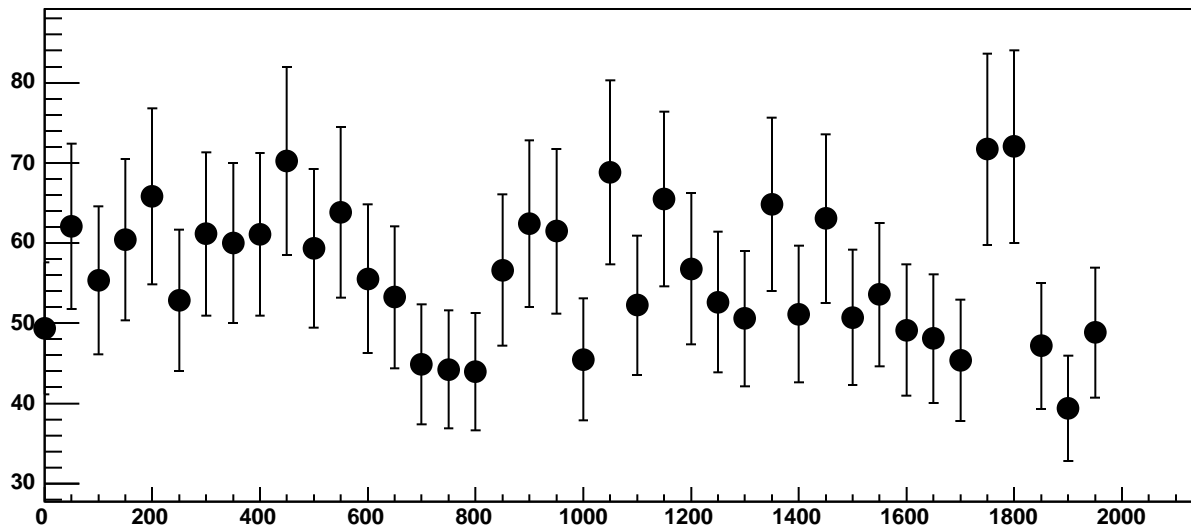


Chip 3, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC

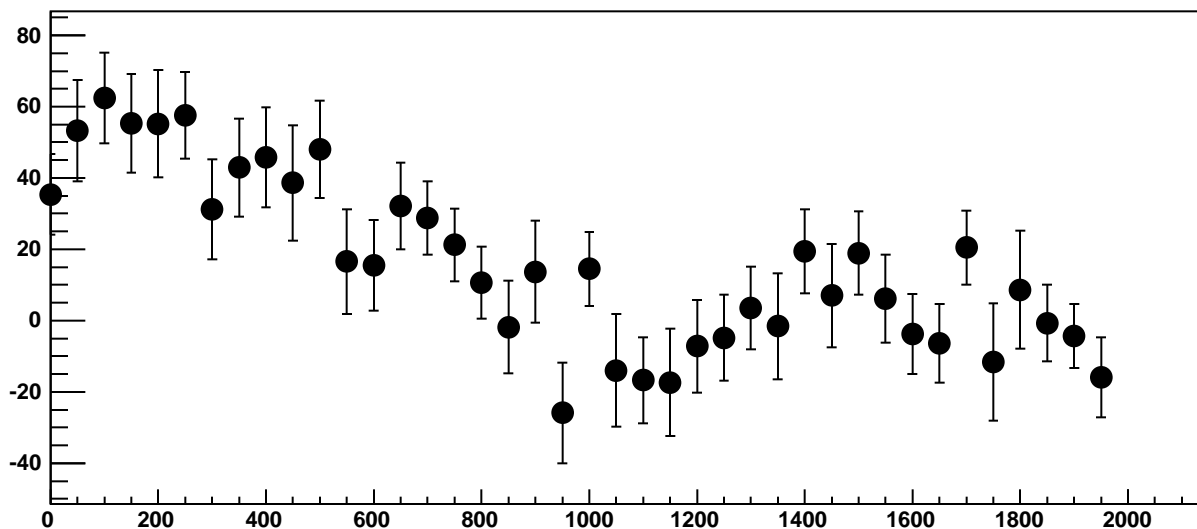


$\chi^2 / \text{ndf}$  14.72 / 11  
p0  $-1744 \pm 19.87$   
p1  $0.05754 \pm 0.01791$

Chip 3, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC

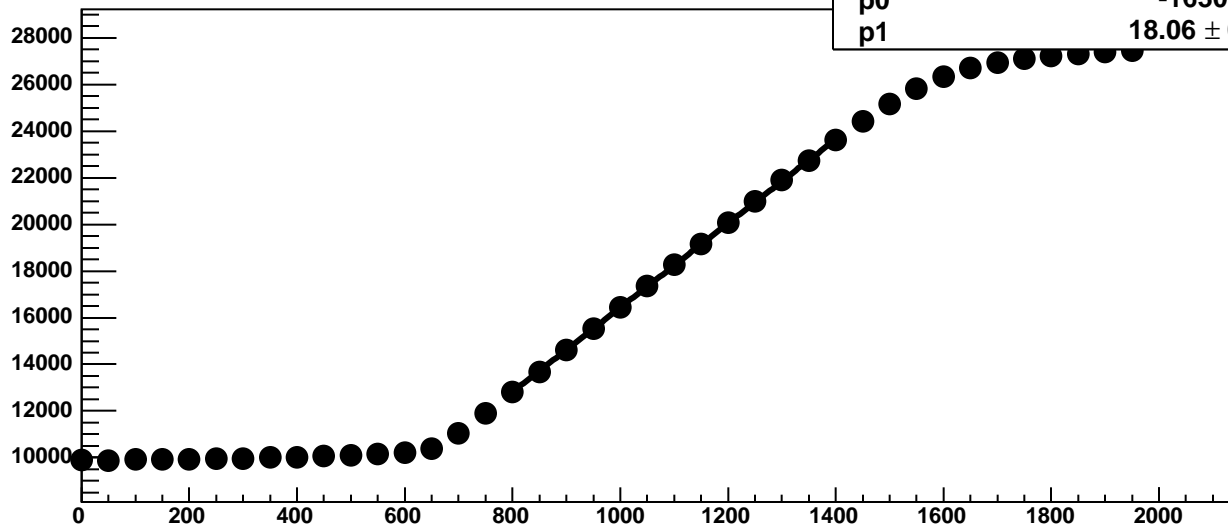


Chip 3, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC



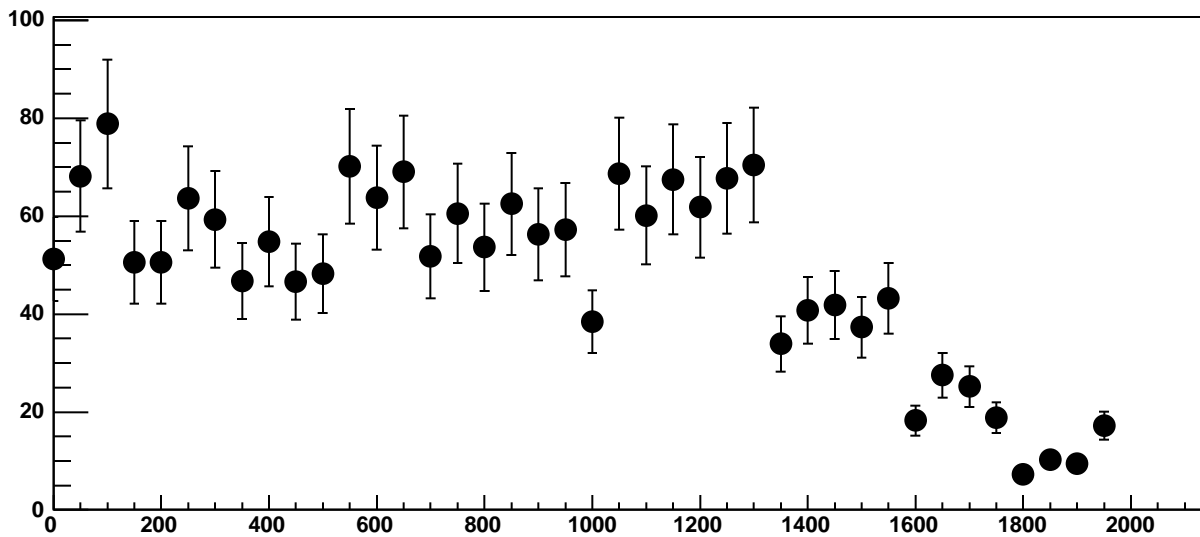


Chip 3, Channel 3, Enable 4!, Hold=30, ADC Mean vs DAC

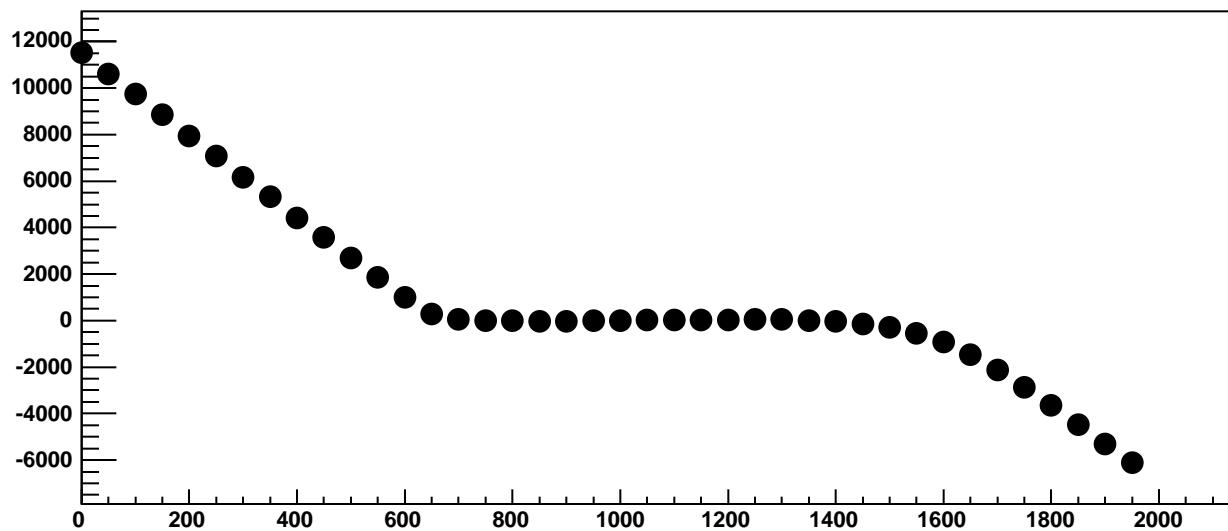


$\chi^2 / \text{ndf}$	58.14 / 11
p0	-1630 ± 18.96
p1	18.06 ± 0.01649

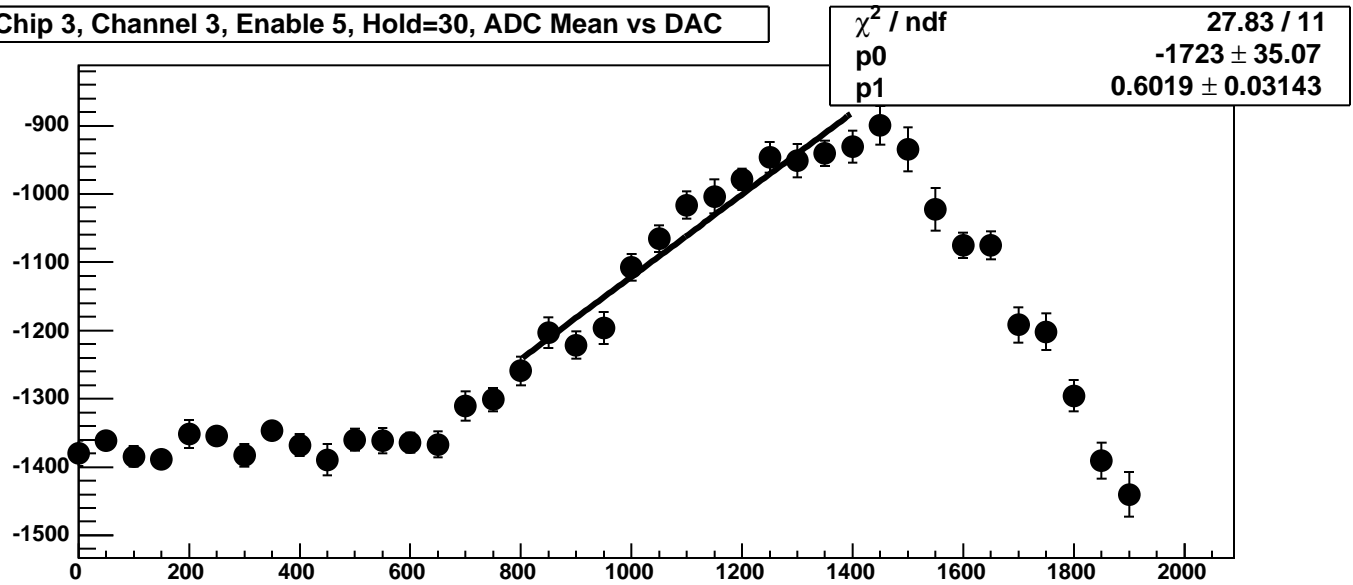
Chip 3, Channel 3, Enable 4!, Hold=30, ADC Noise vs DAC



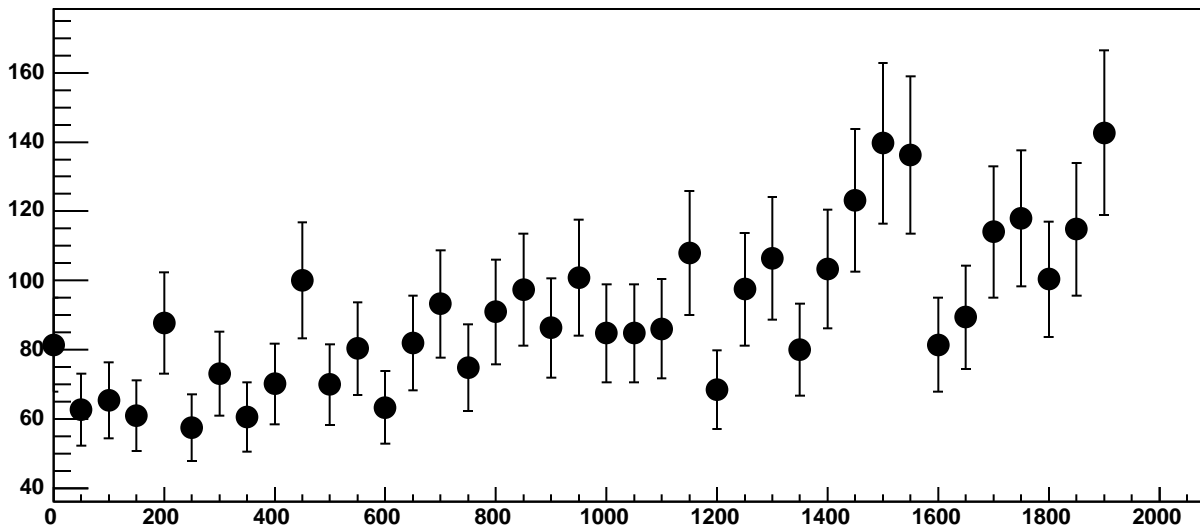
Chip 3, Channel 3, Enable 4!, Hold=30, ADC Residuals vs DAC



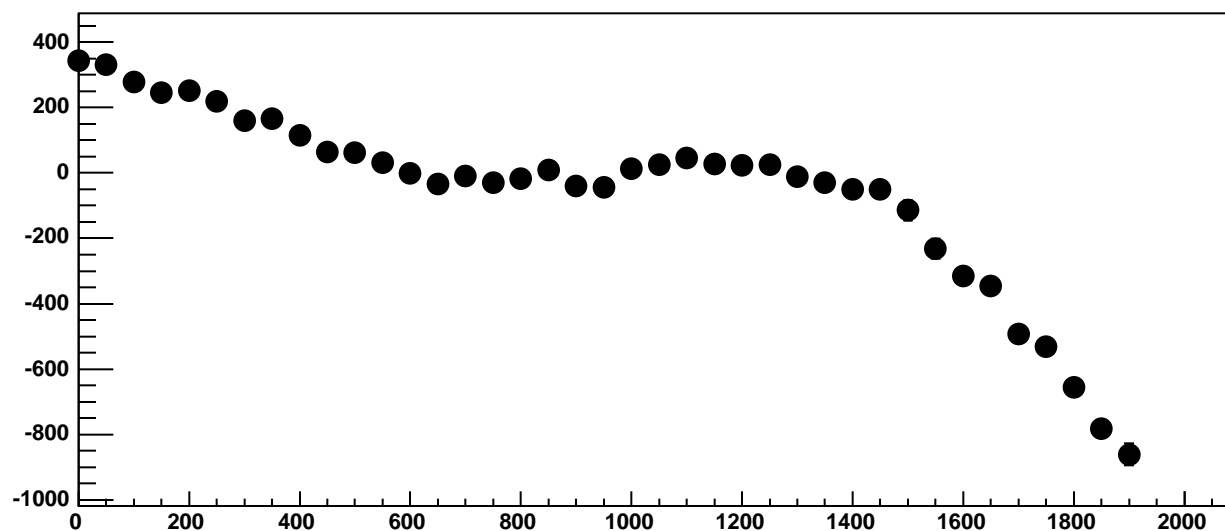
Chip 3, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC



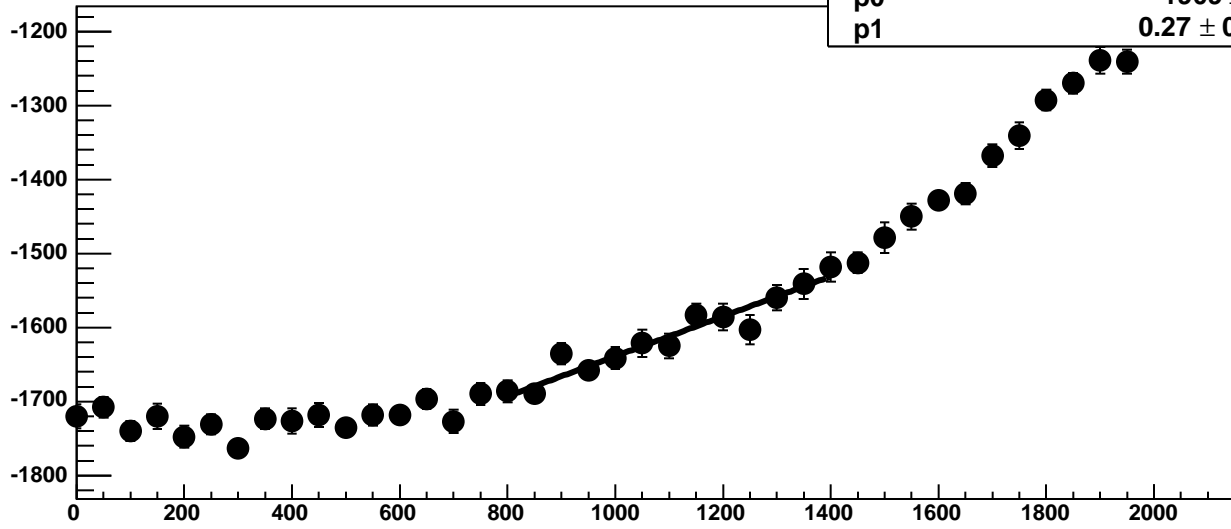
Chip 3, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC



Chip 3, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC

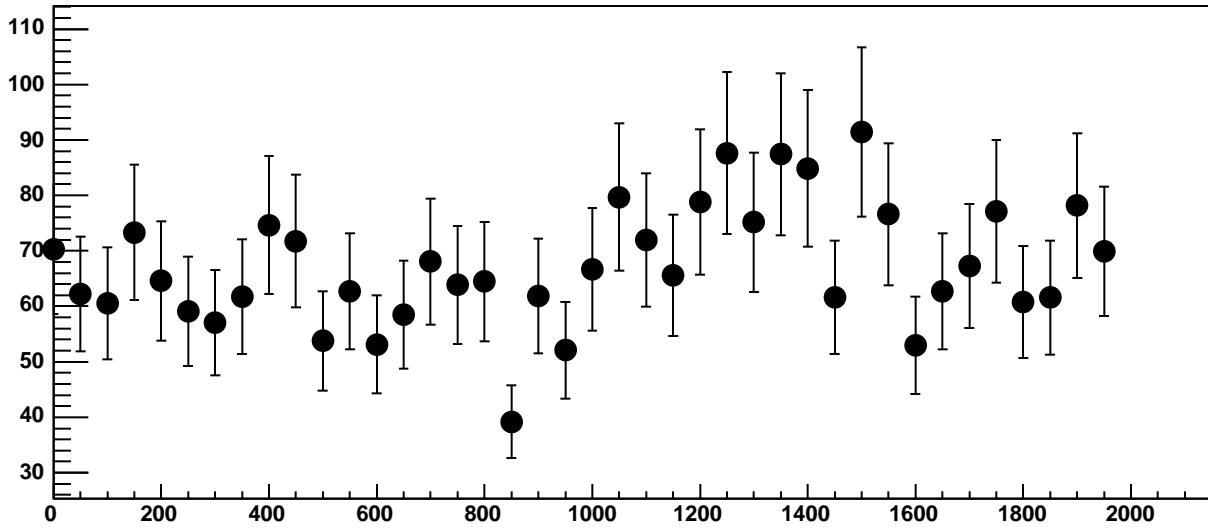


Chip 3, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC

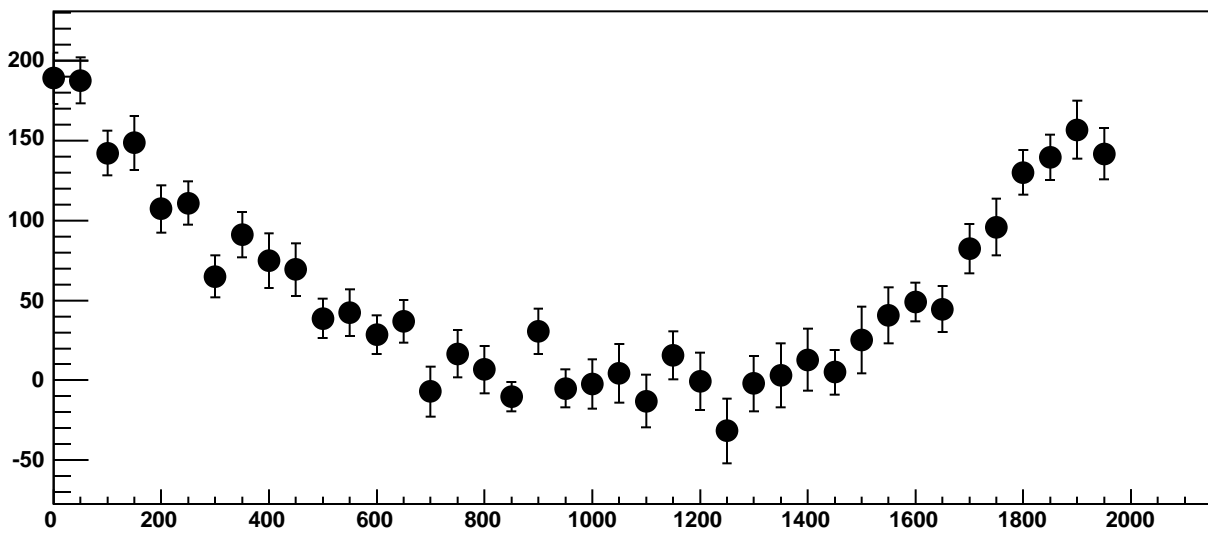


$\chi^2 / \text{ndf}$  11.09 / 11  
p0  $-1909 \pm 23.82$   
p1  $0.27 \pm 0.02278$

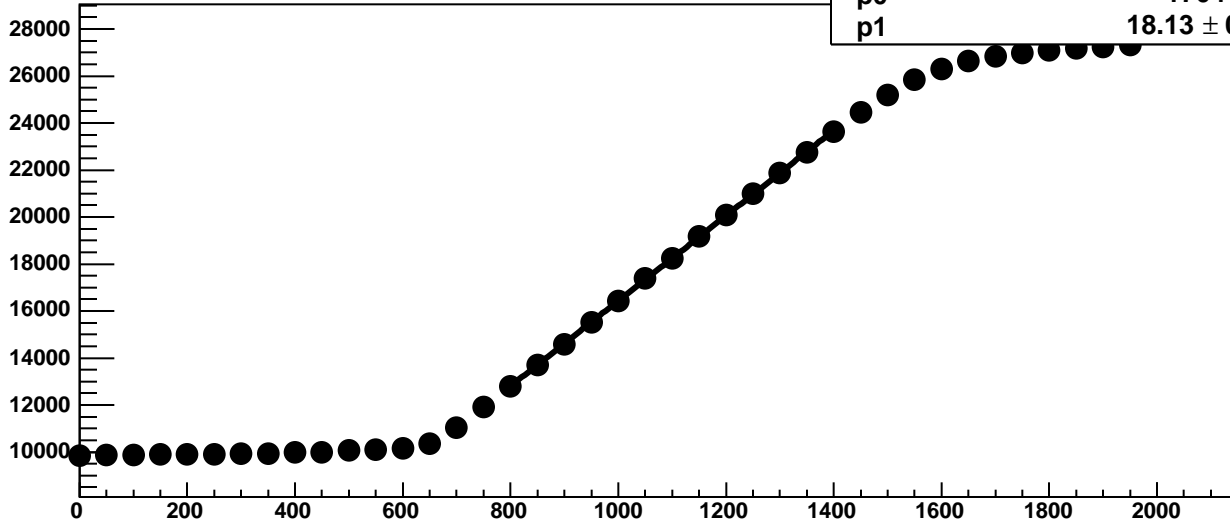
Chip 3, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC



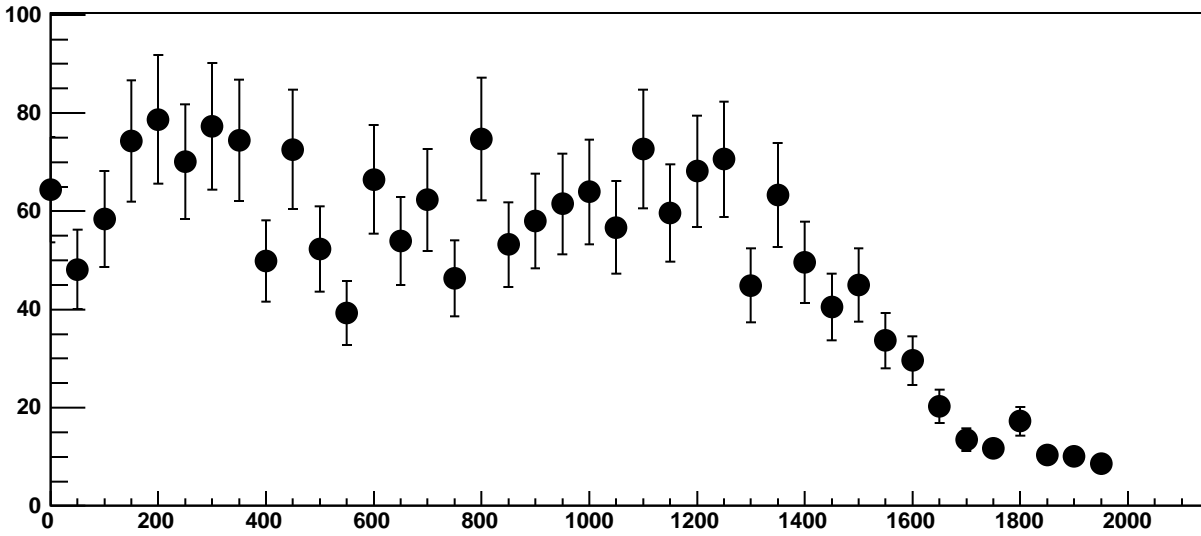
Chip 3, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC



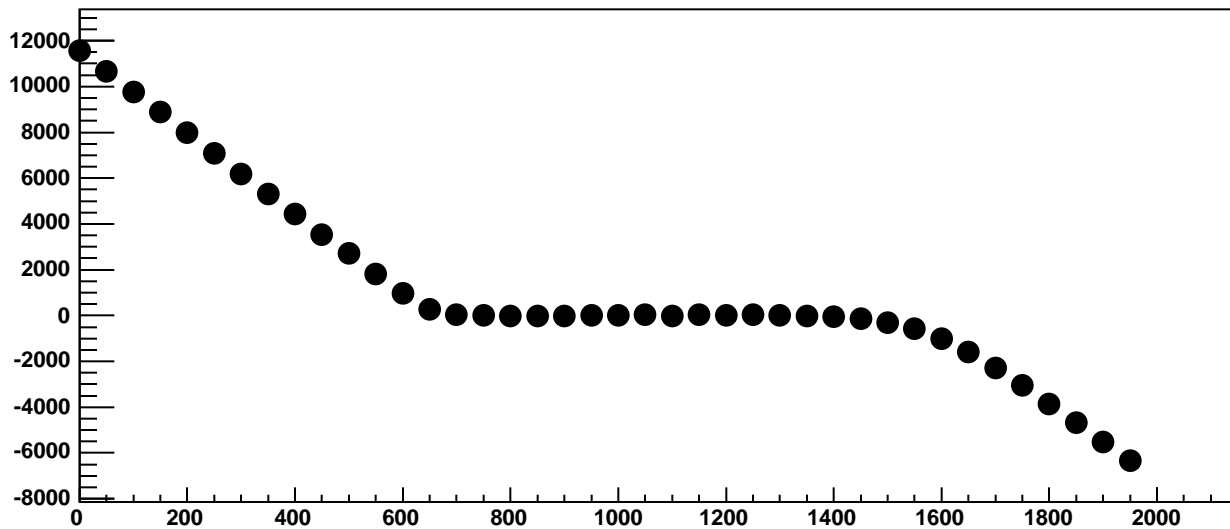
Chip 3, Channel 4, Enable 1!, Hold=30, ADC Mean vs DAC



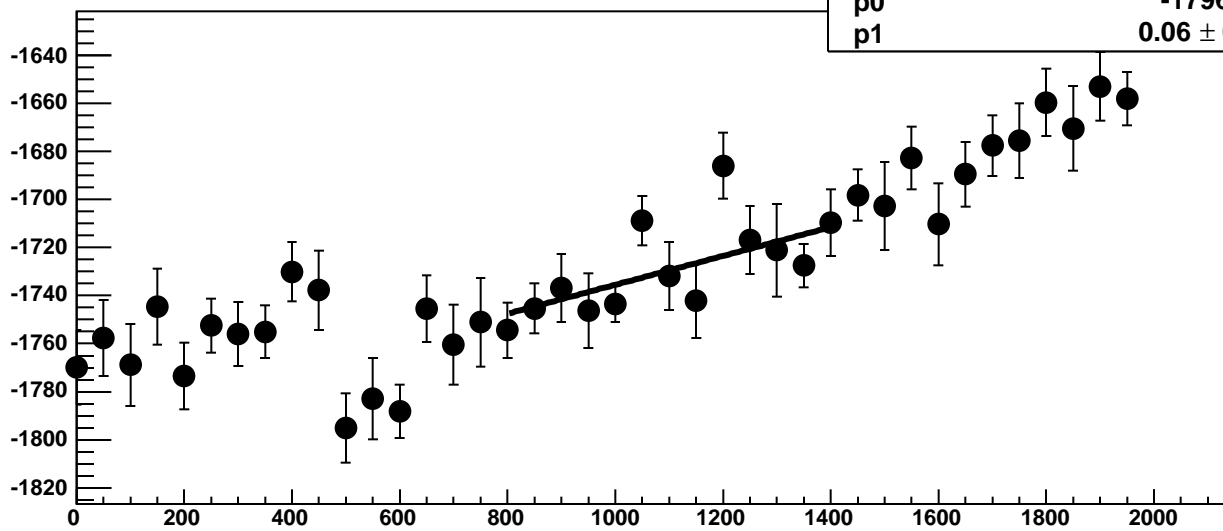
Chip 3, Channel 4, Enable 1!, Hold=30, ADC Noise vs DAC



Chip 3, Channel 4, Enable 1!, Hold=30, ADC Residuals vs DAC

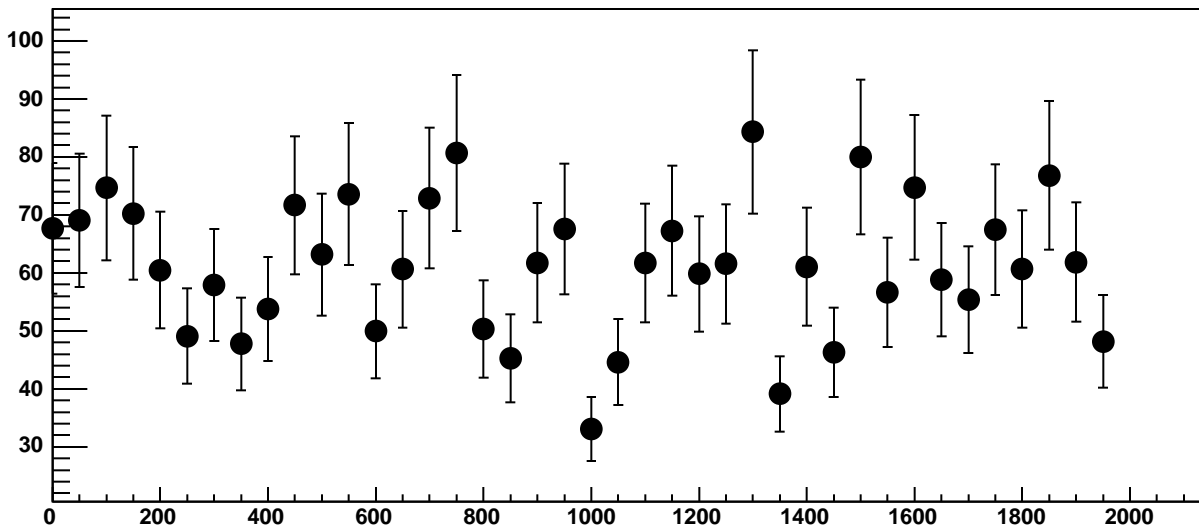


Chip 3, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC

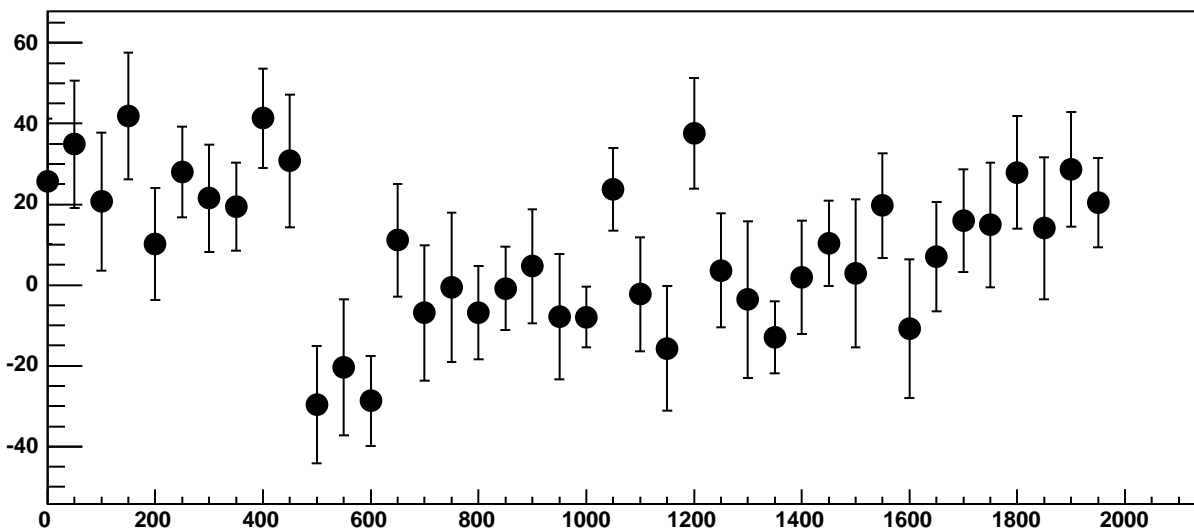


$\chi^2 / \text{ndf}$  17.96 / 11  
p0  $-1796 \pm 19.3$   
p1  $0.06 \pm 0.01759$

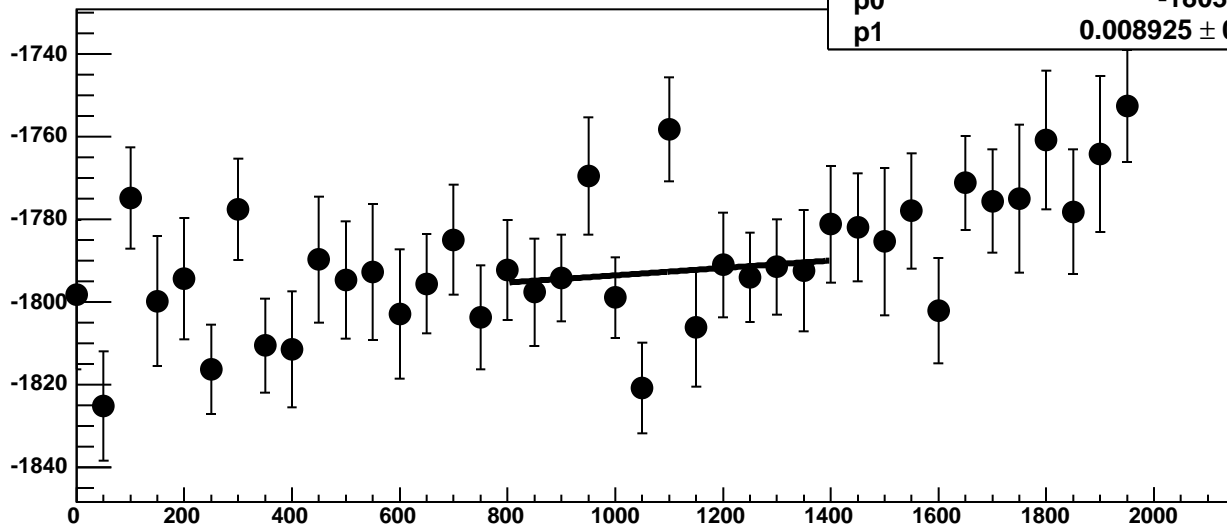
Chip 3, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC



Chip 3, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC

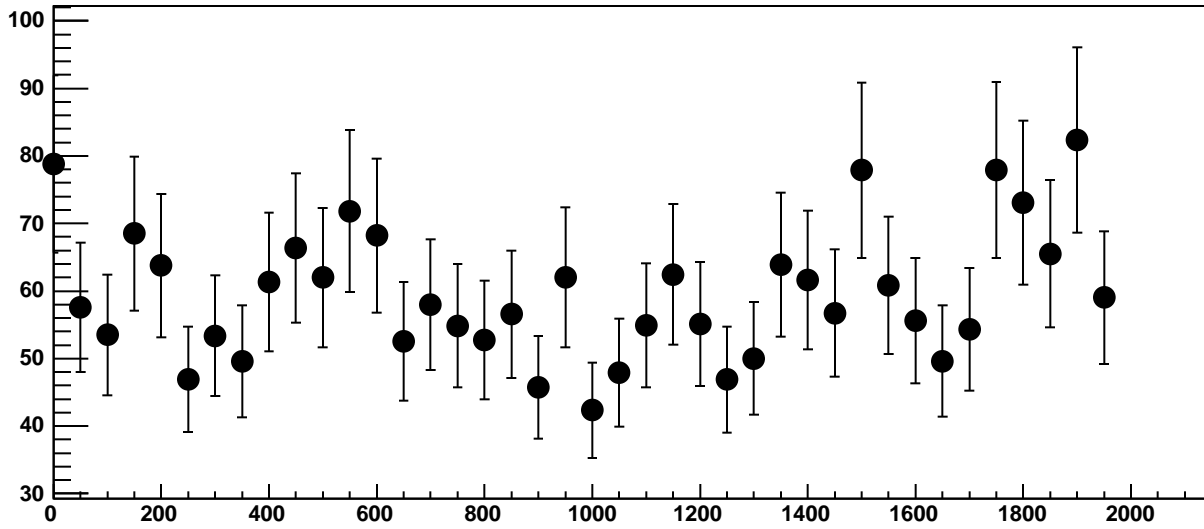


Chip 3, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC

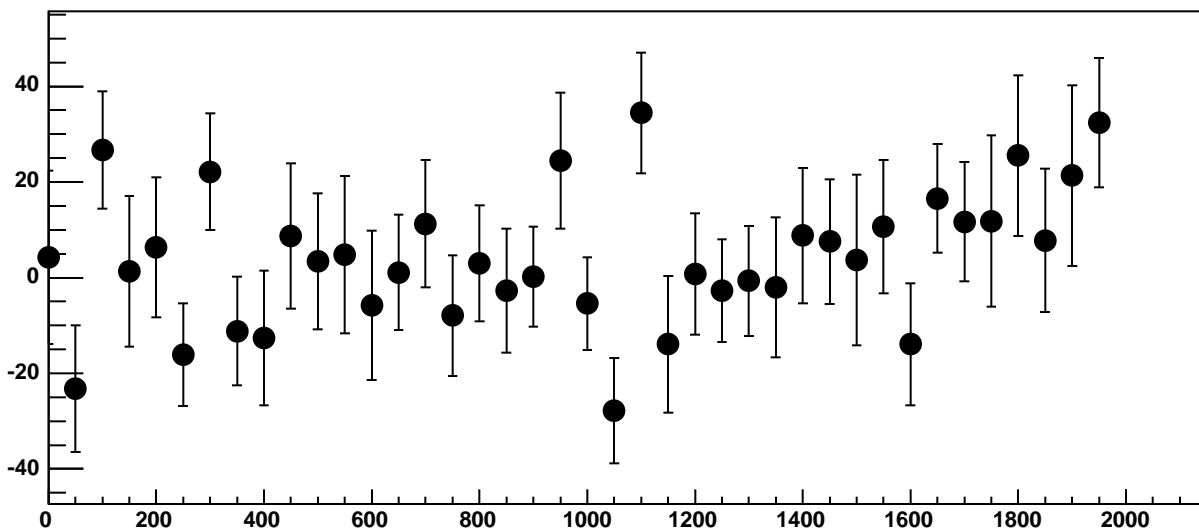


$\chi^2 / \text{ndf}$  18.65 / 11  
p0  $-1803 \pm 20.51$   
p1  $0.008925 \pm 0.01862$

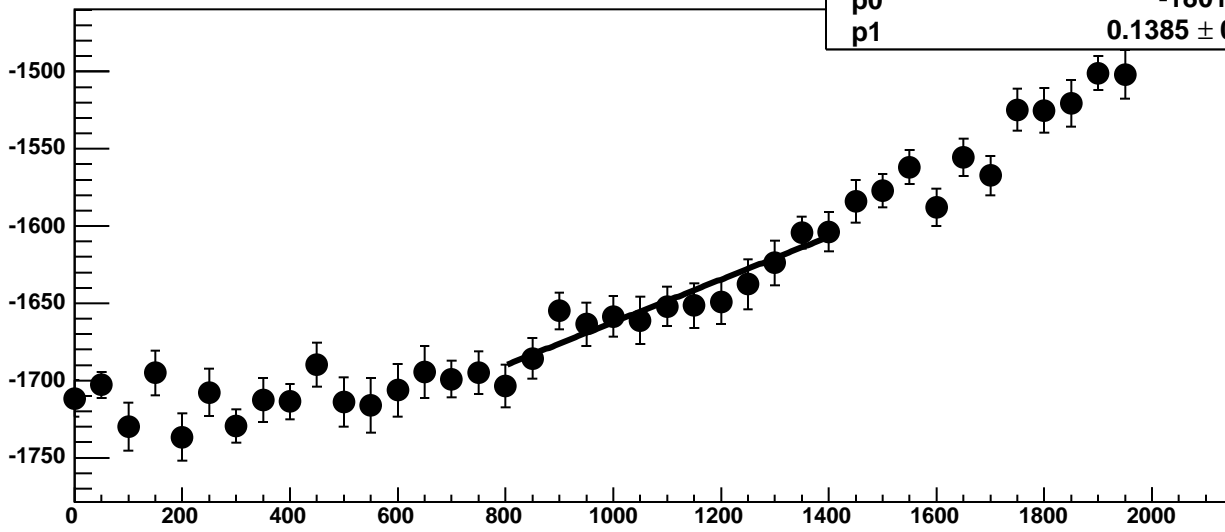
Chip 3, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC



Chip 3, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC

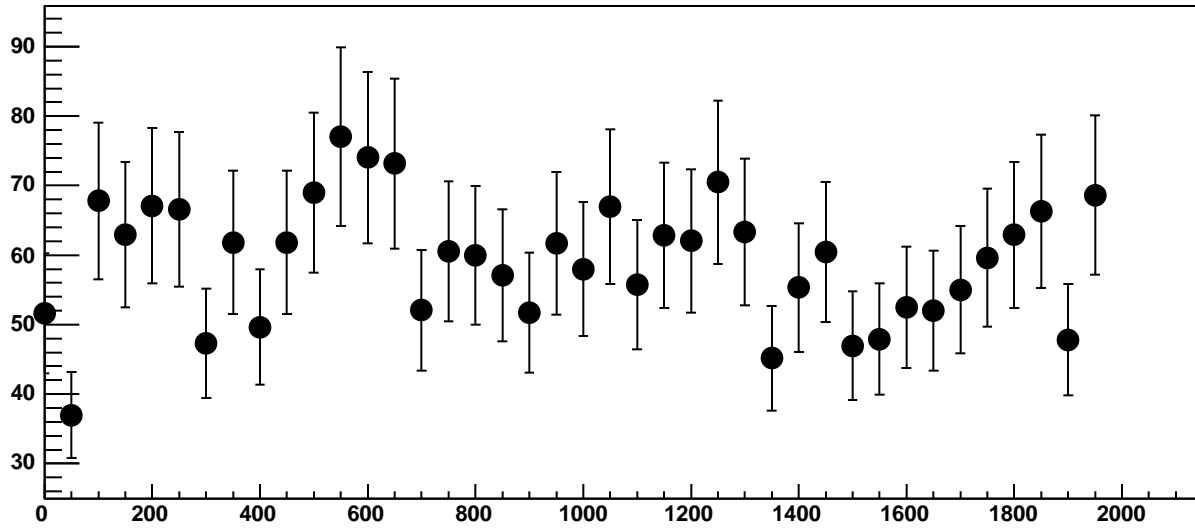


Chip 3, Channel 4, Enable 4, Hold=30, ADC Mean vs DAC

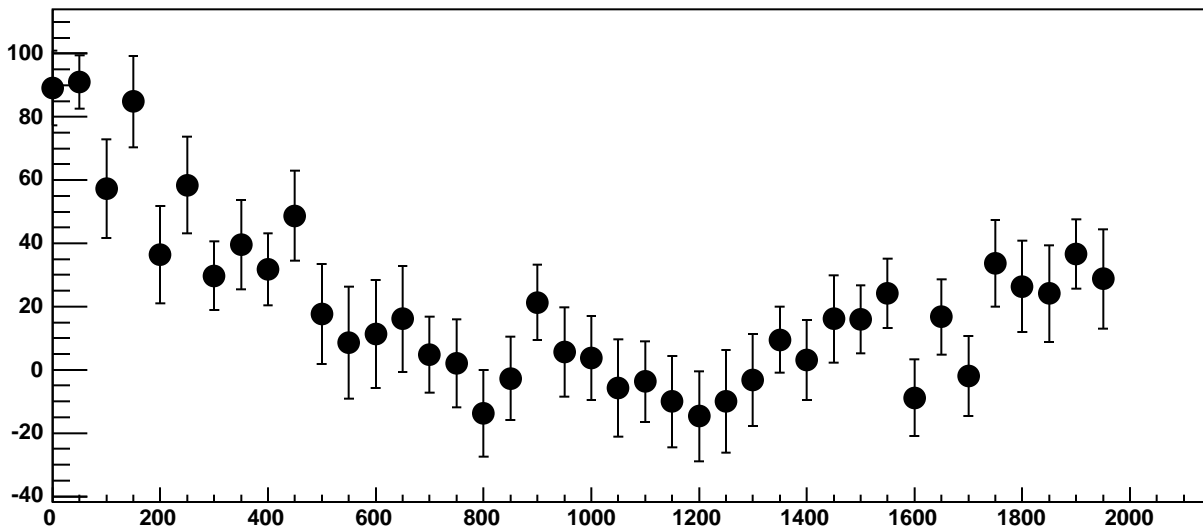


$\chi^2 / \text{ndf}$  7.587 / 11  
p0  $-1801 \pm 21.31$   
p1  $0.1385 \pm 0.01897$

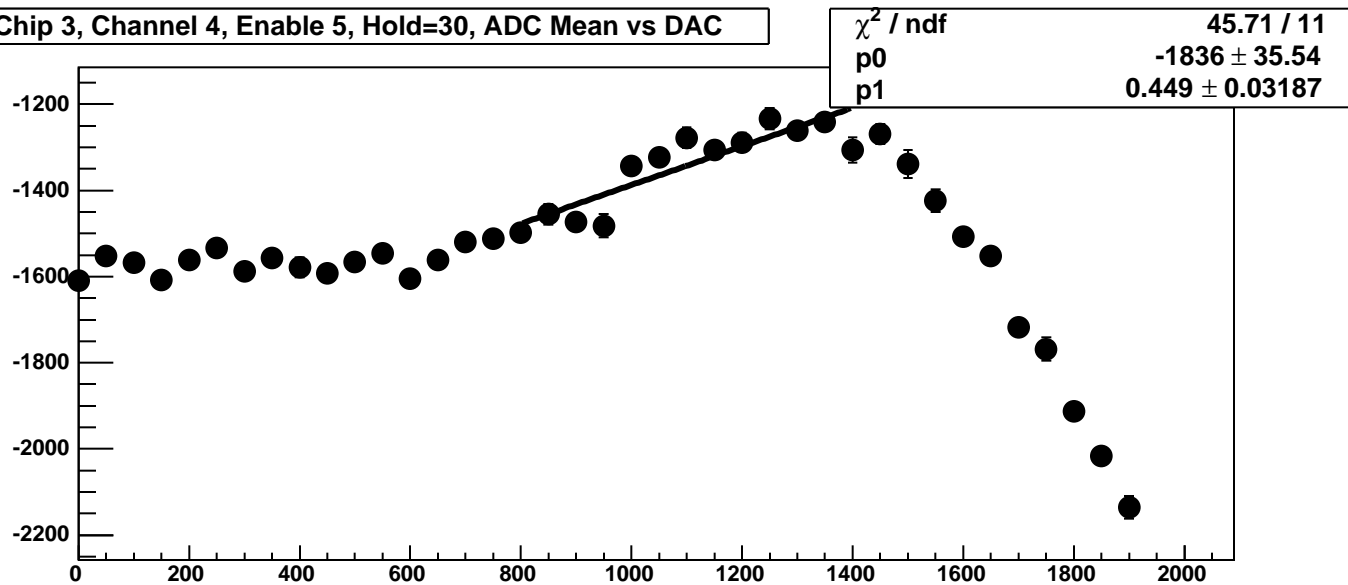
Chip 3, Channel 4, Enable 4, Hold=30, ADC Noise vs DAC



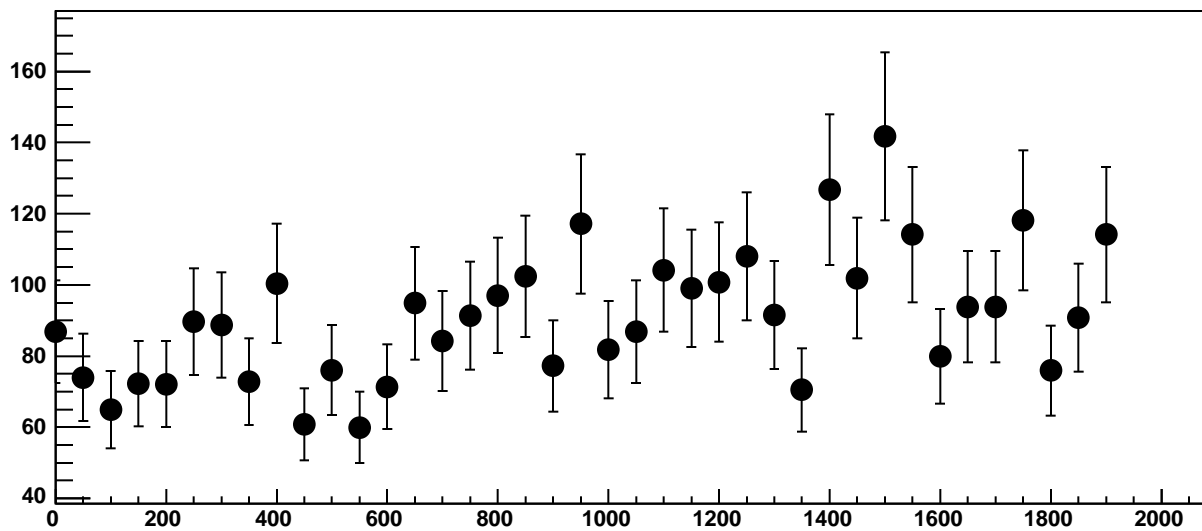
Chip 3, Channel 4, Enable 4, Hold=30, ADC Residuals vs DAC



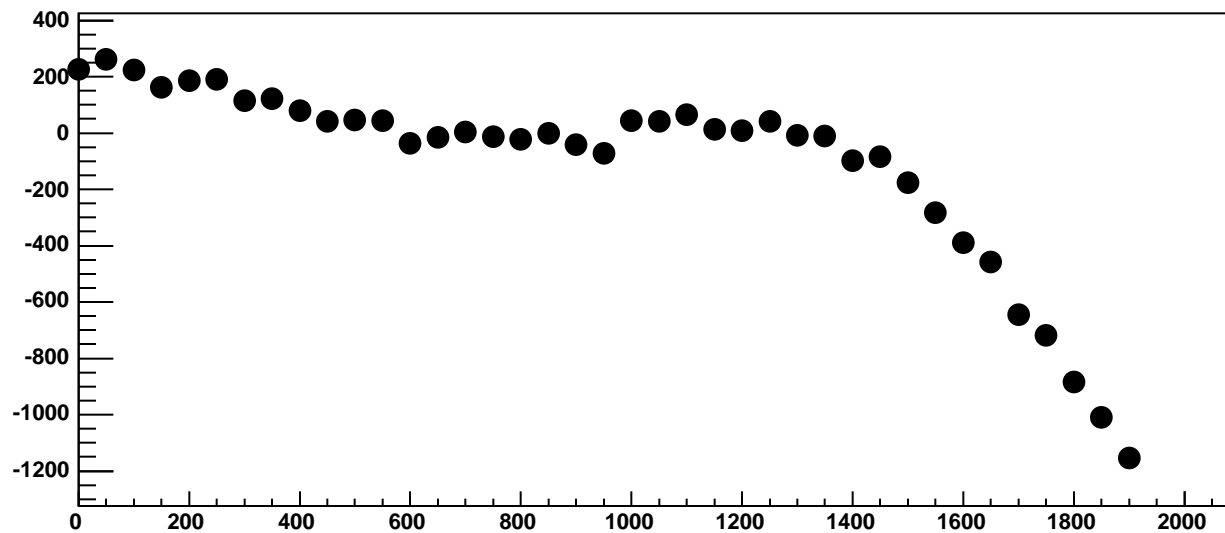
Chip 3, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC



Chip 3, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC

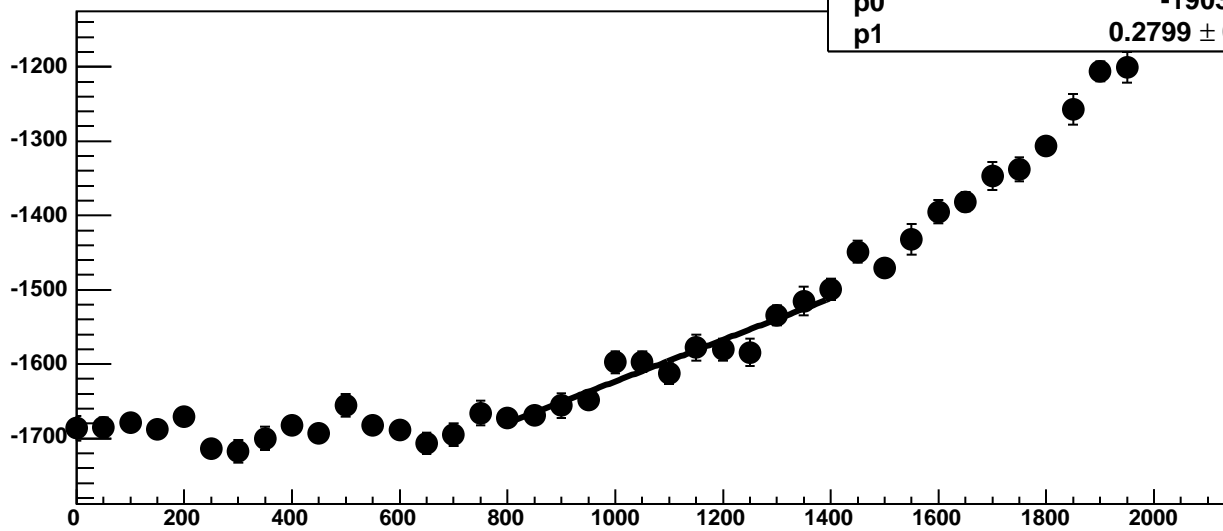


Chip 3, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC





Chip 3, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

11.36 / 11

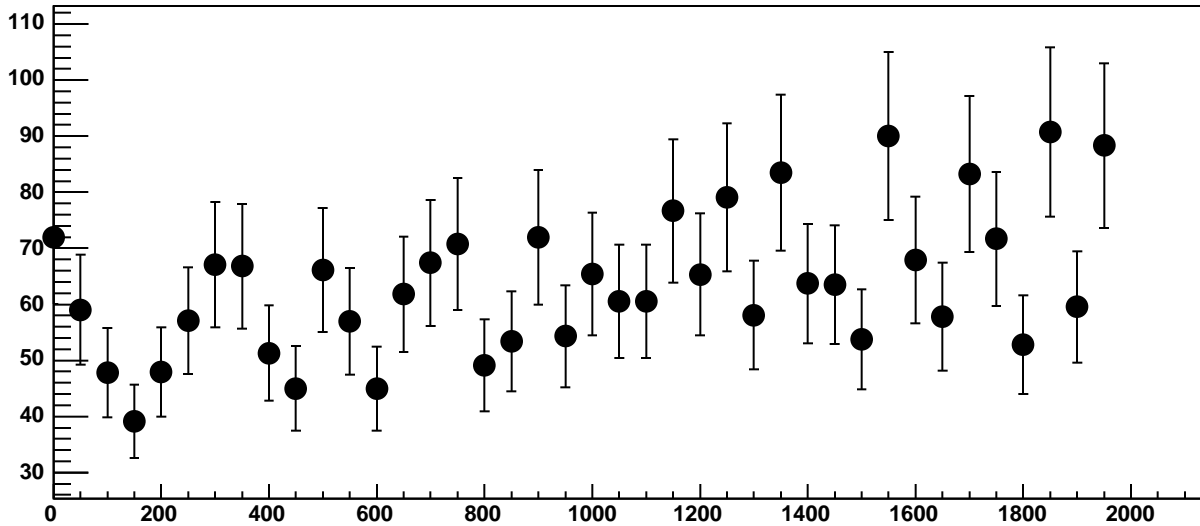
p0

$-1903 \pm 22.4$

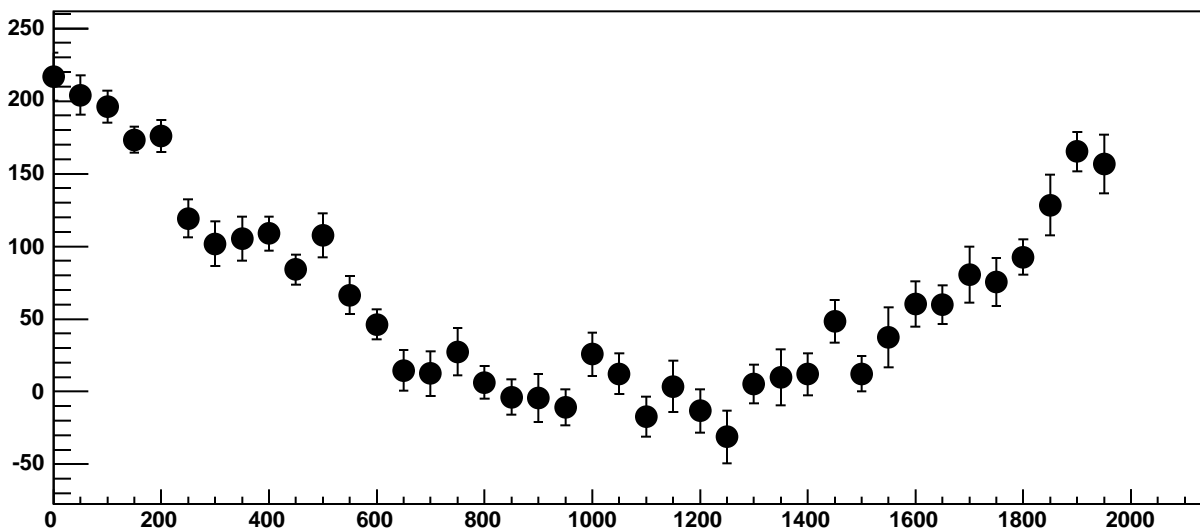
p1

$0.2799 \pm 0.02068$

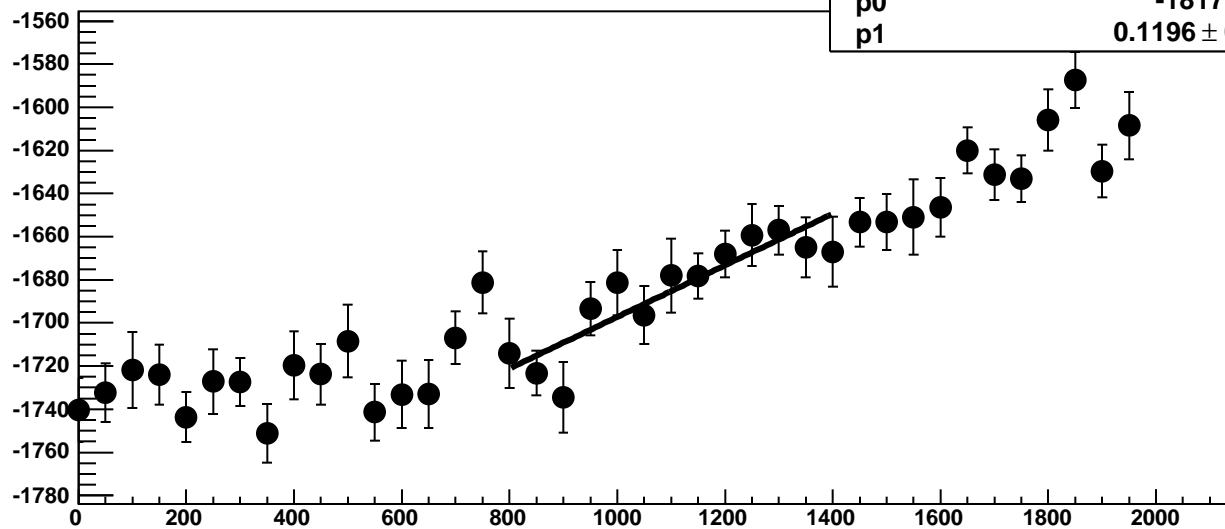
Chip 3, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC



Chip 3, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 3, Channel 5, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

7.544 / 11

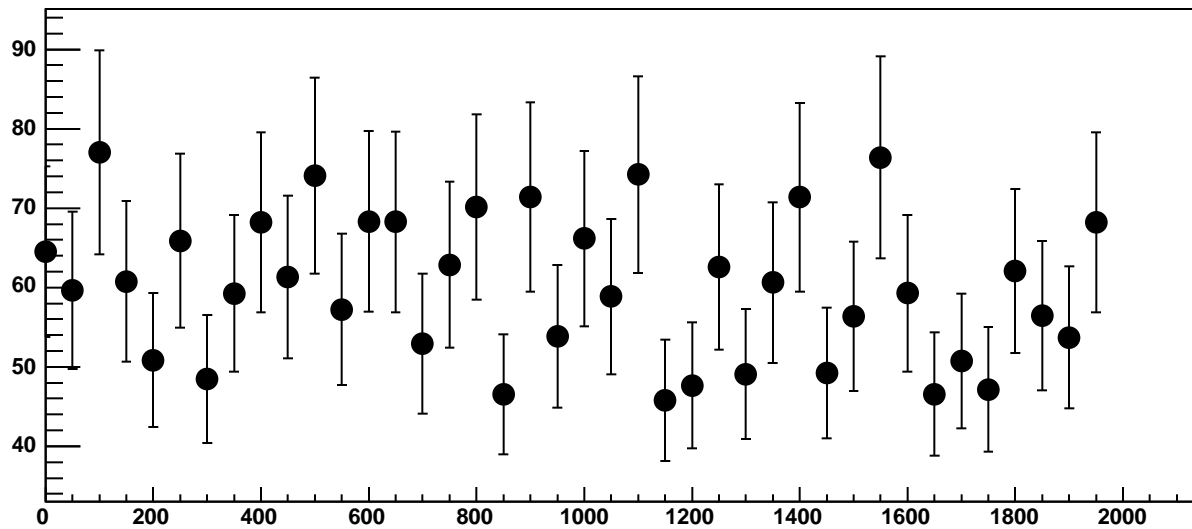
p0

$-1817 \pm 22.47$

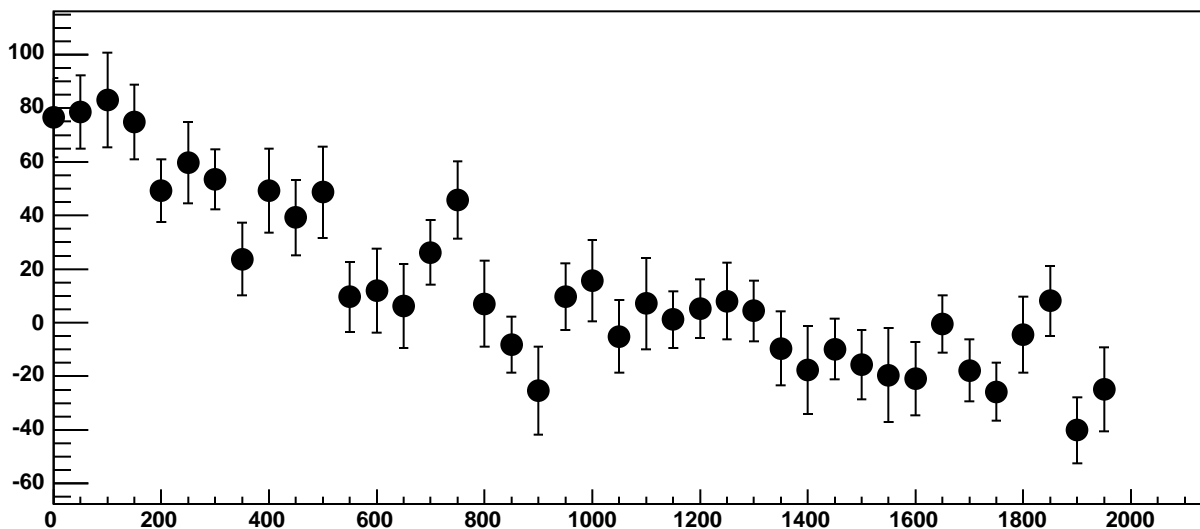
p1

$0.1196 \pm 0.02013$

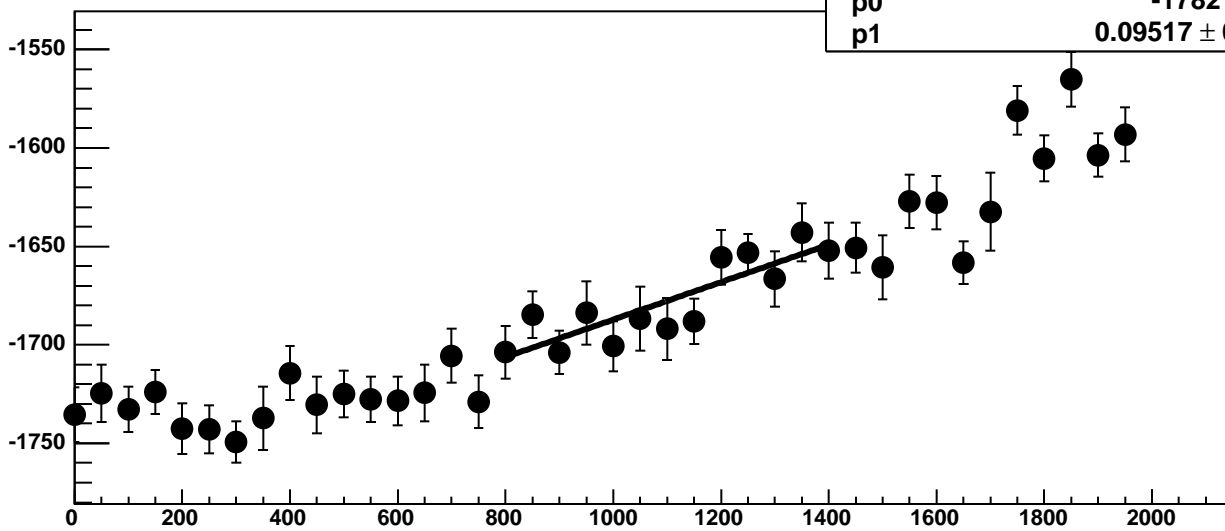
Chip 3, Channel 5, Enable 1, Hold=30, ADC Noise vs DAC



Chip 3, Channel 5, Enable 1, Hold=30, ADC Residuals vs DAC

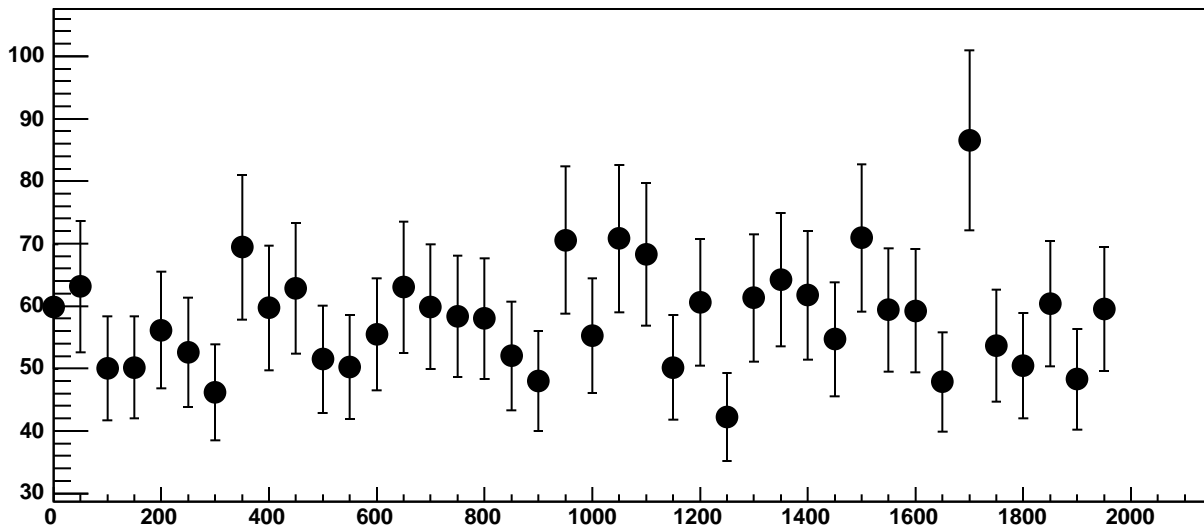


Chip 3, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC

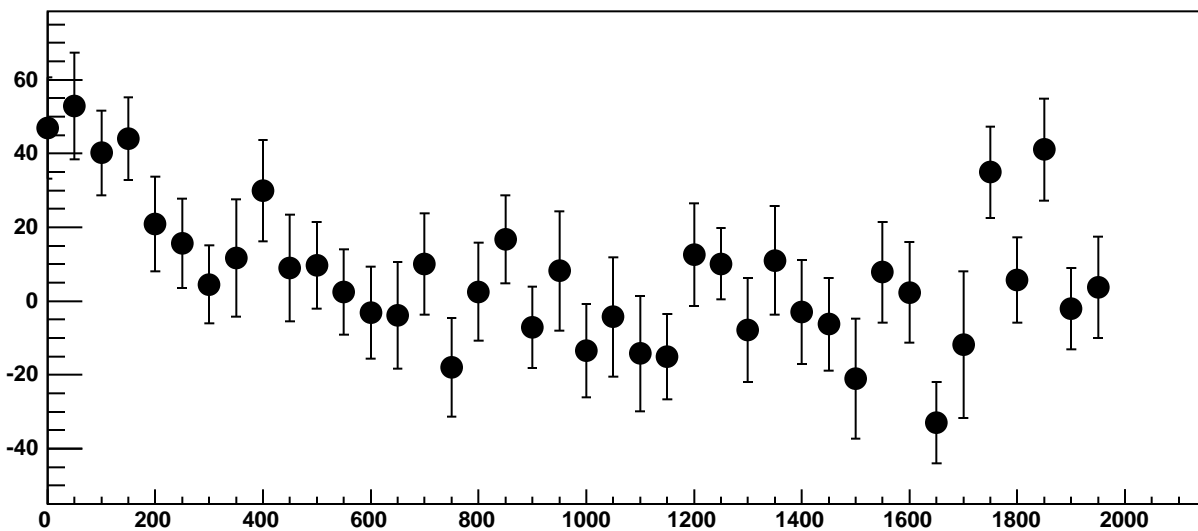


$\chi^2 / \text{ndf}$  9.234 / 11  
p0  $-1782 \pm 21.46$   
p1  $0.09517 \pm 0.01931$

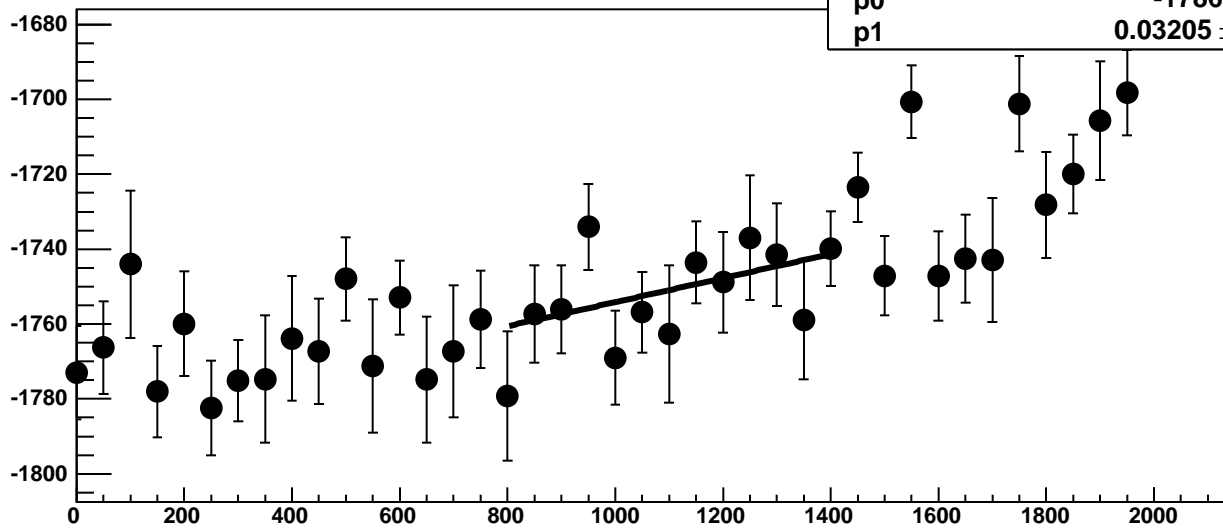
Chip 3, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC



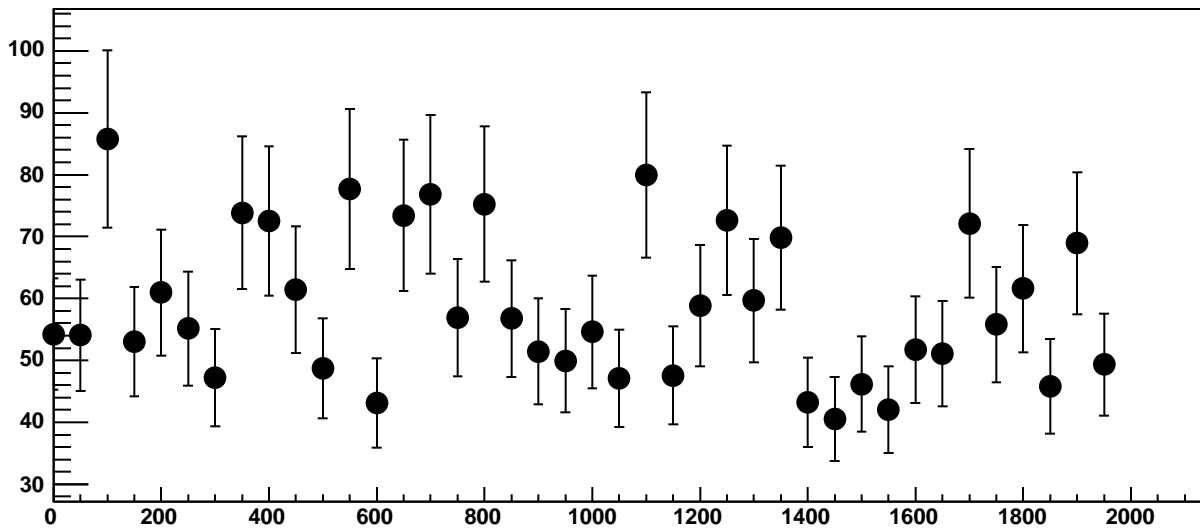
Chip 3, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC



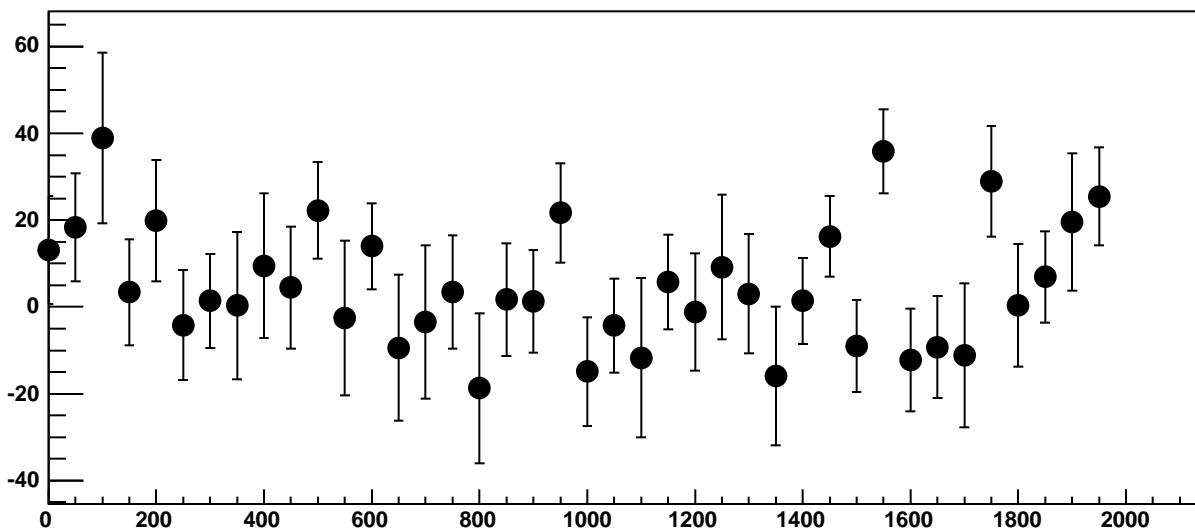
Chip 3, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC



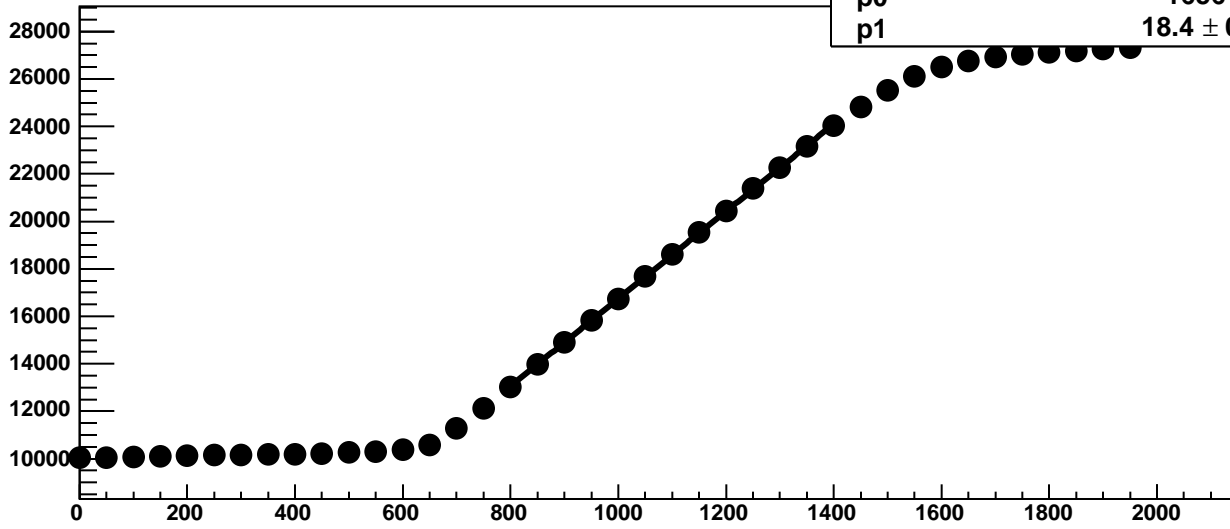
Chip 3, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC



Chip 3, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC

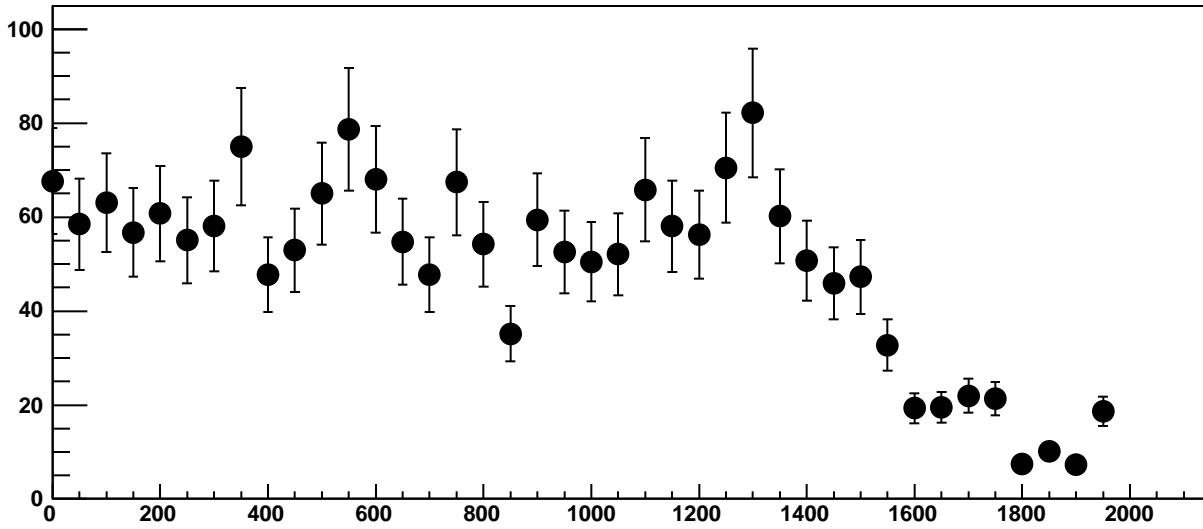


Chip 3, Channel 5, Enable 4!, Hold=30, ADC Mean vs DAC

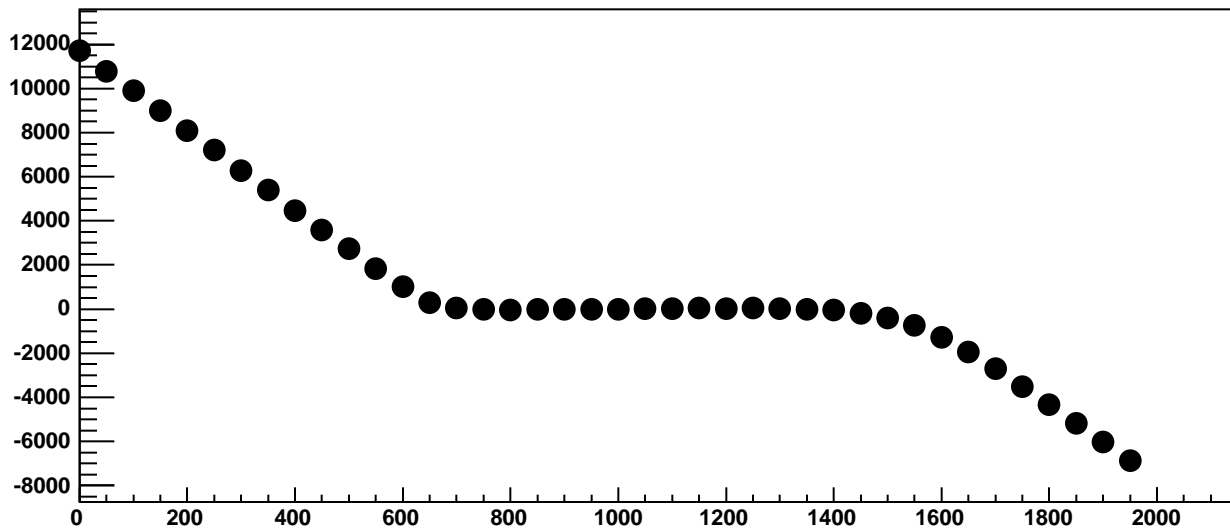


$\chi^2 / \text{ndf}$	67.97 / 11
p0	-1656 ± 19.25
p1	18.4 ± 0.01788

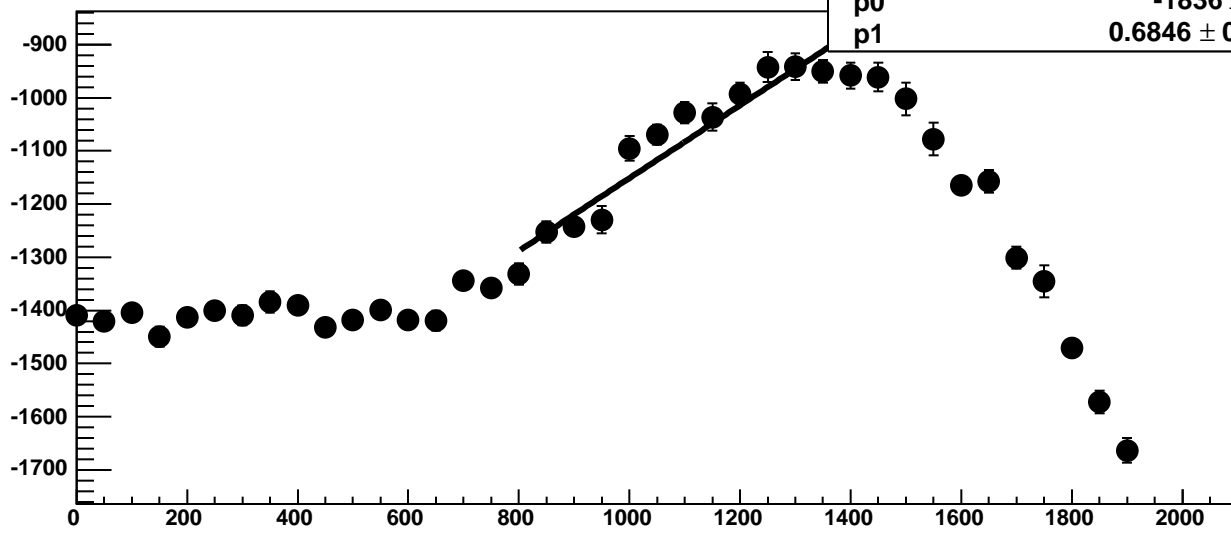
Chip 3, Channel 5, Enable 4!, Hold=30, ADC Noise vs DAC



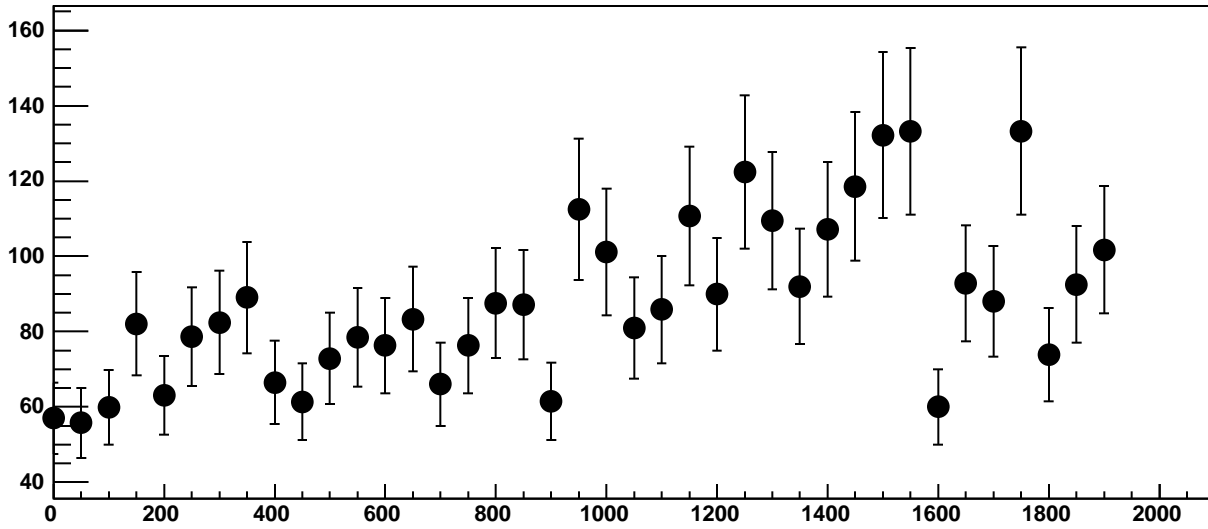
Chip 3, Channel 5, Enable 4!, Hold=30, ADC Residuals vs DAC



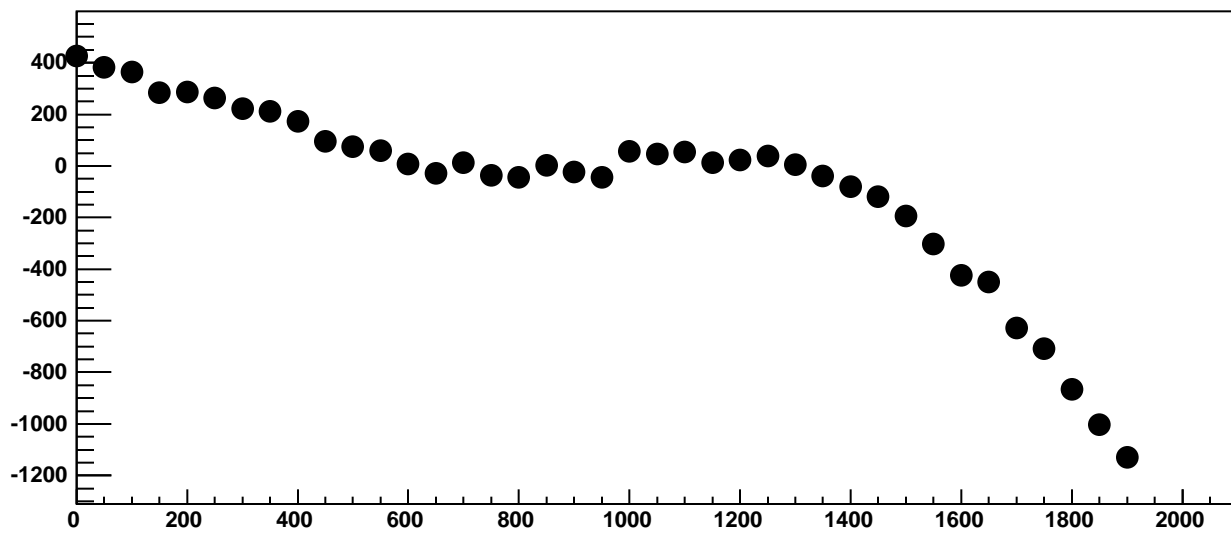
Chip 3, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC



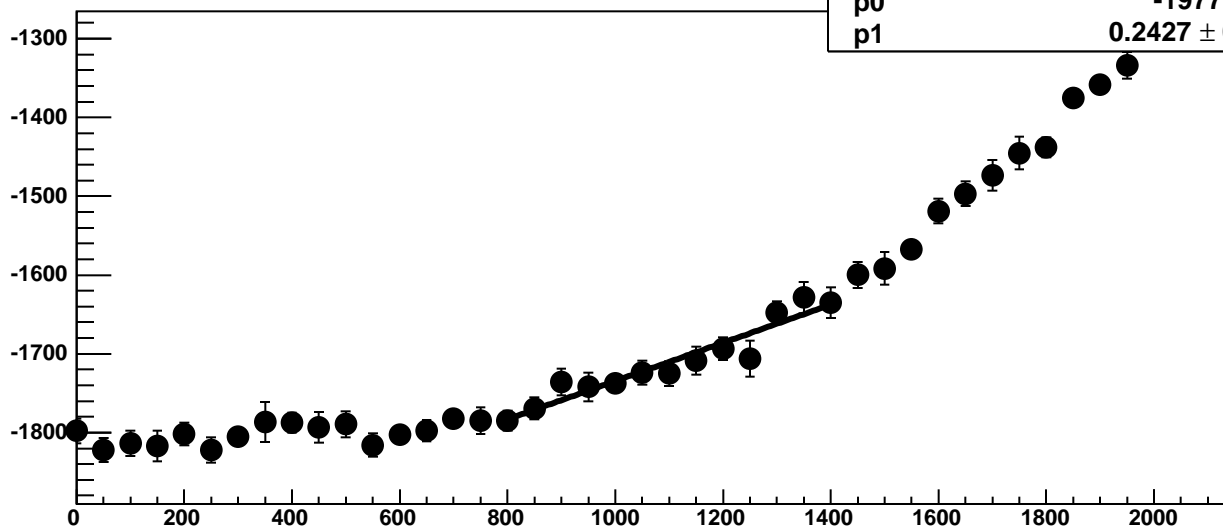
Chip 3, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC



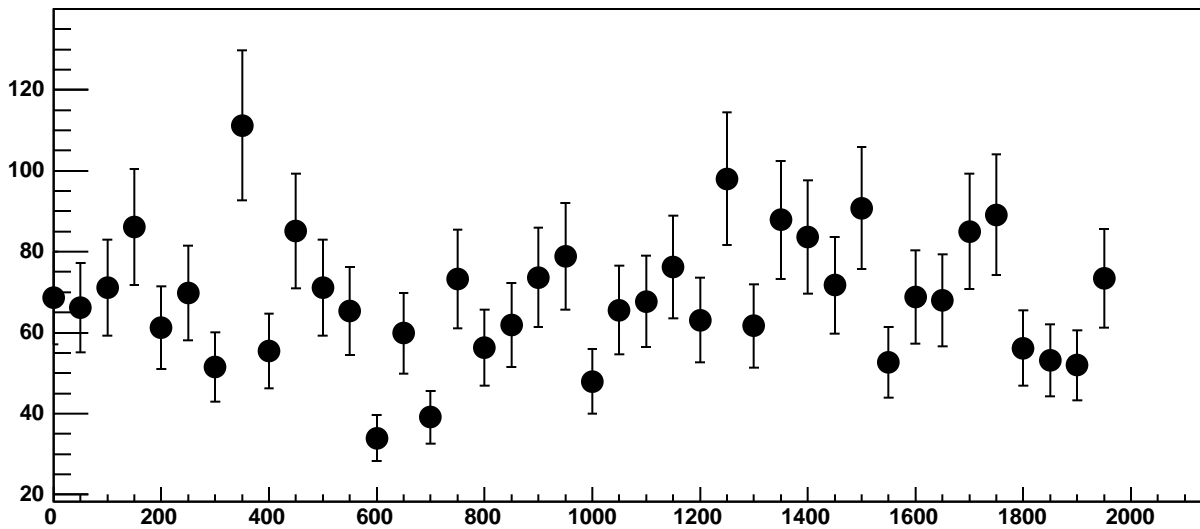
Chip 3, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC



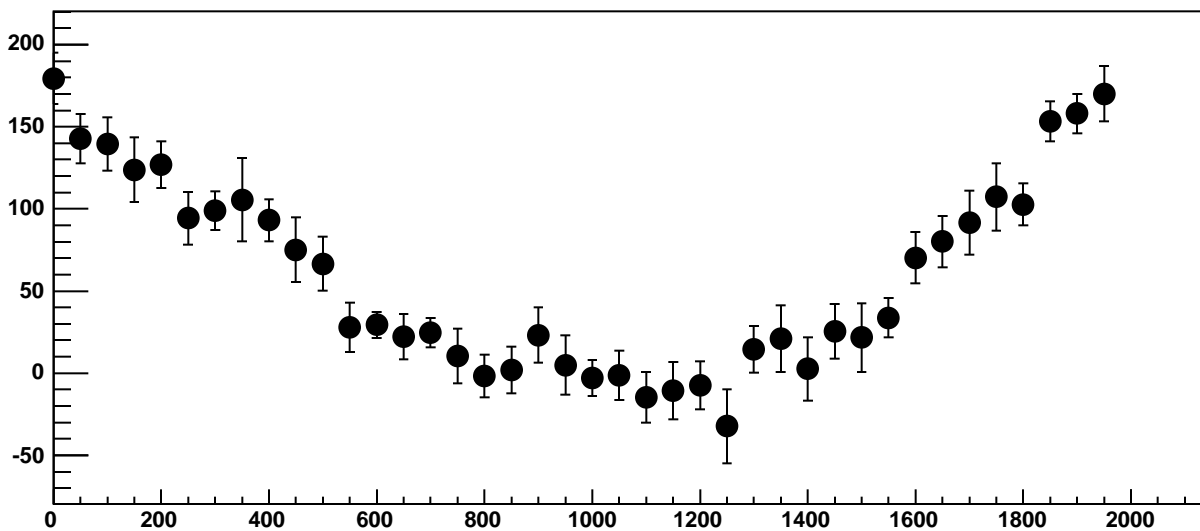
Chip 3, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC



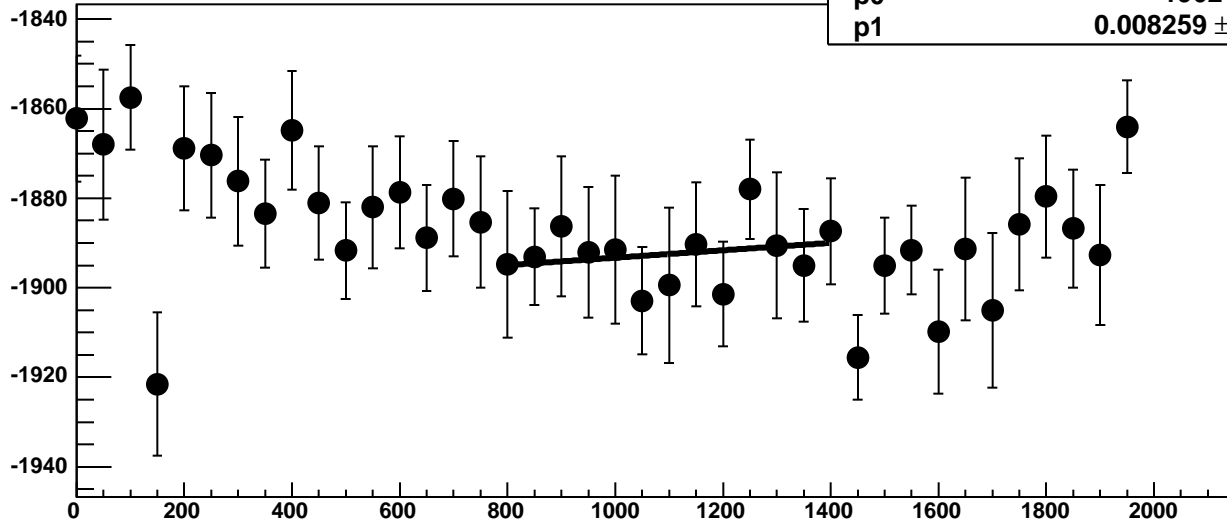
Chip 3, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC



Chip 3, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC

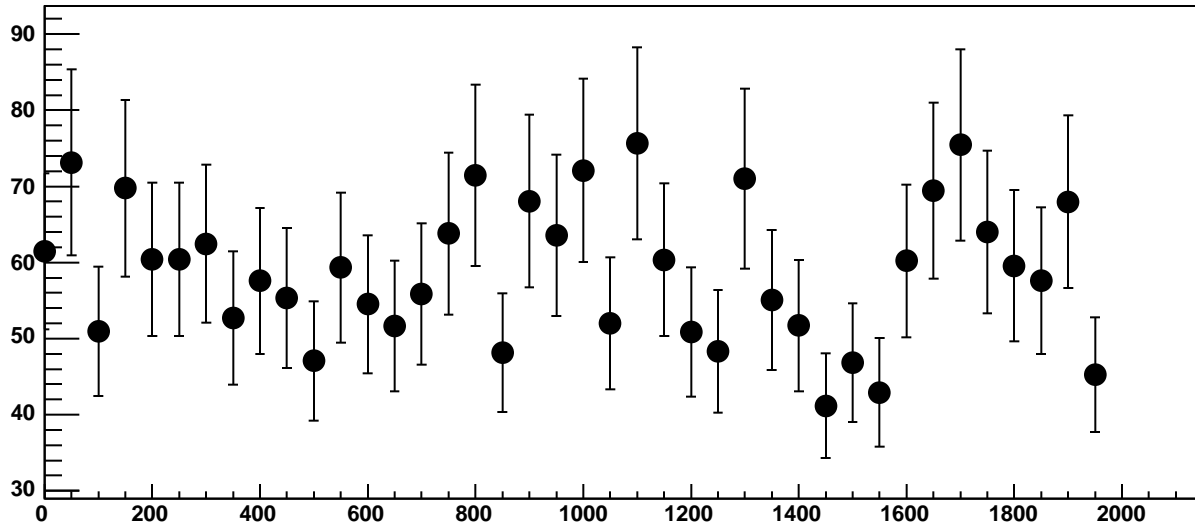


Chip 3, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC

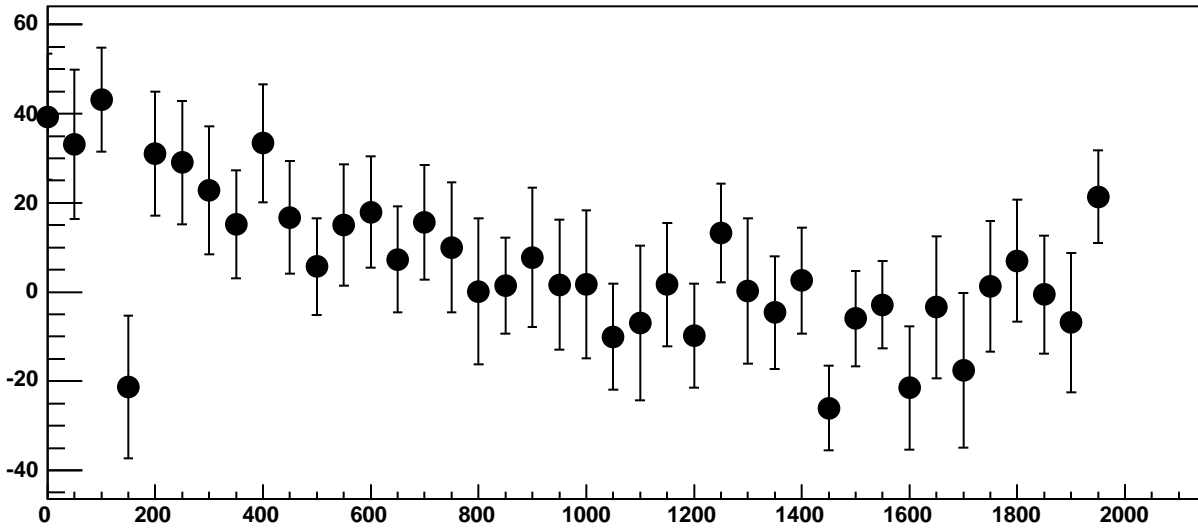


$\chi^2 / \text{ndf}$  3.479 / 11  
p0  $-1902 \pm 22.07$   
p1  $0.008259 \pm 0.0195$

Chip 3, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC

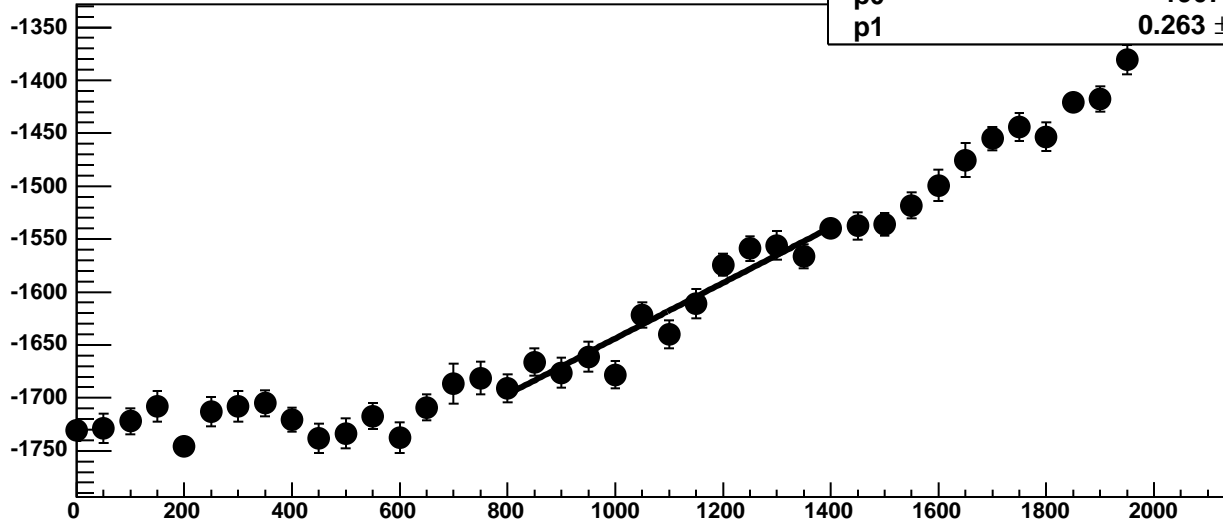


Chip 3, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC



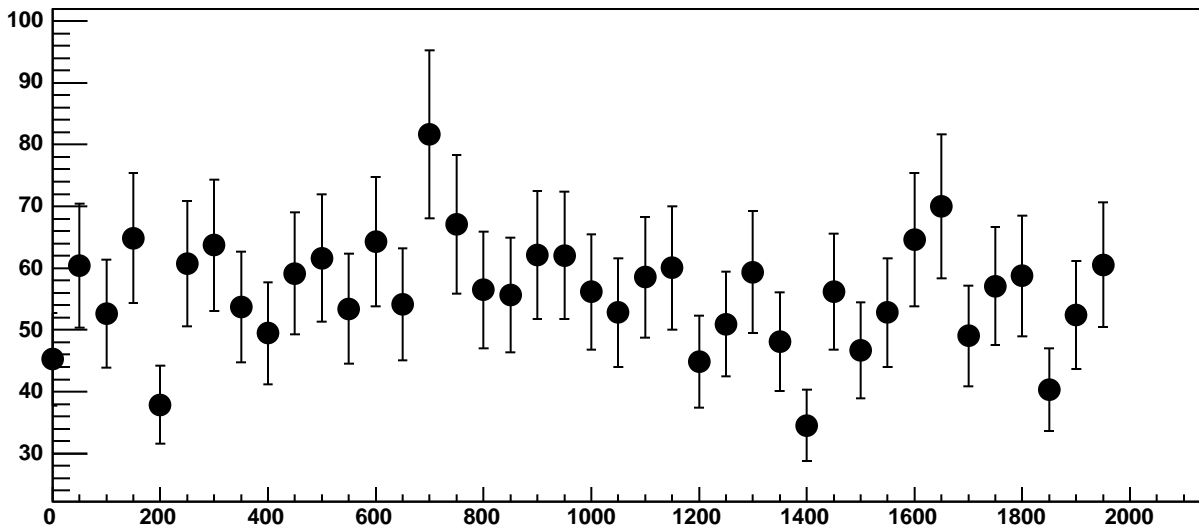


Chip 3, Channel 6, Enable 2, Hold=30, ADC Mean vs DAC

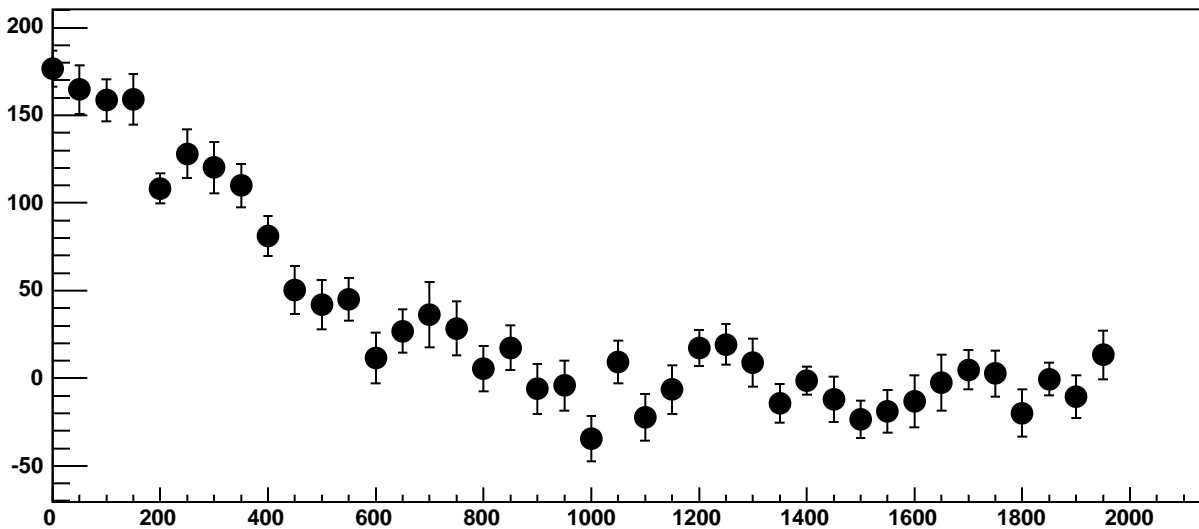


$\chi^2 / \text{ndf}$  20.63 / 11  
p0  $-1907 \pm 19.53$   
p1  $0.263 \pm 0.0168$

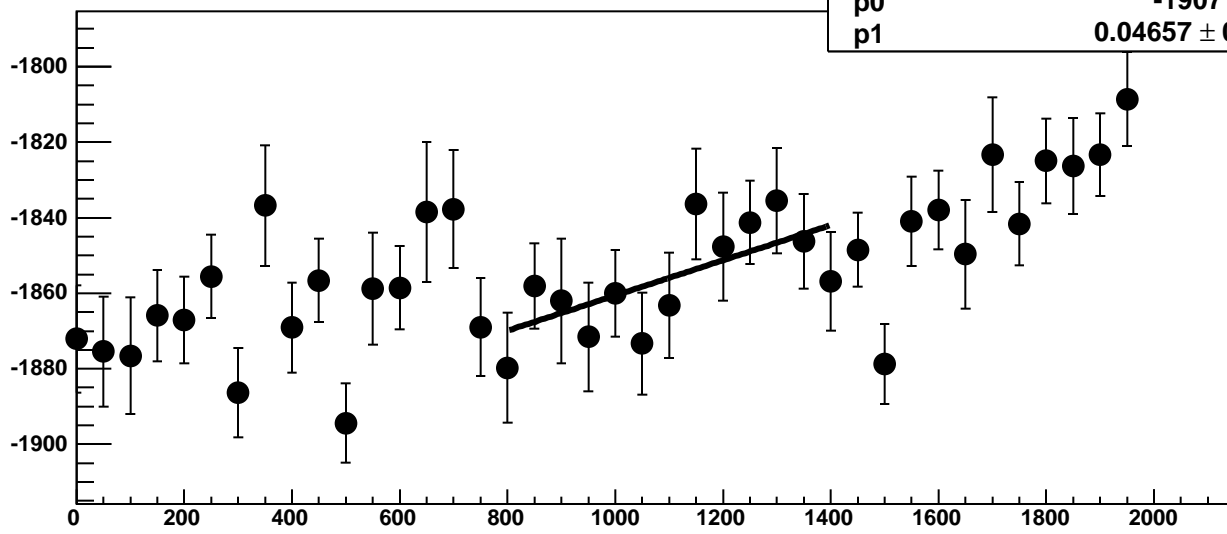
Chip 3, Channel 6, Enable 2, Hold=30, ADC Noise vs DAC



Chip 3, Channel 6, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 3, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

6.981 / 11

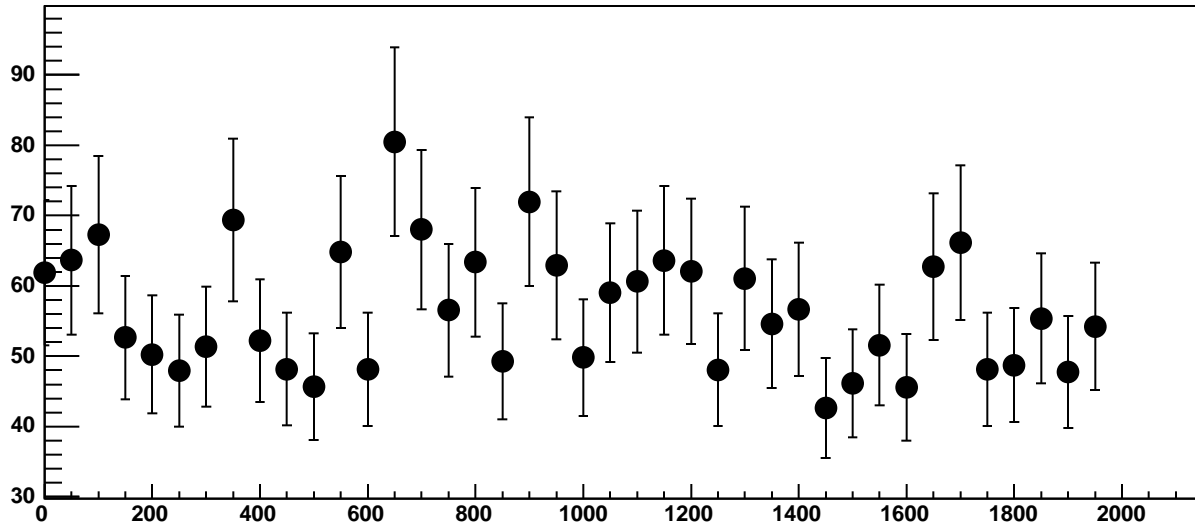
p0

$-1907 \pm 21.86$

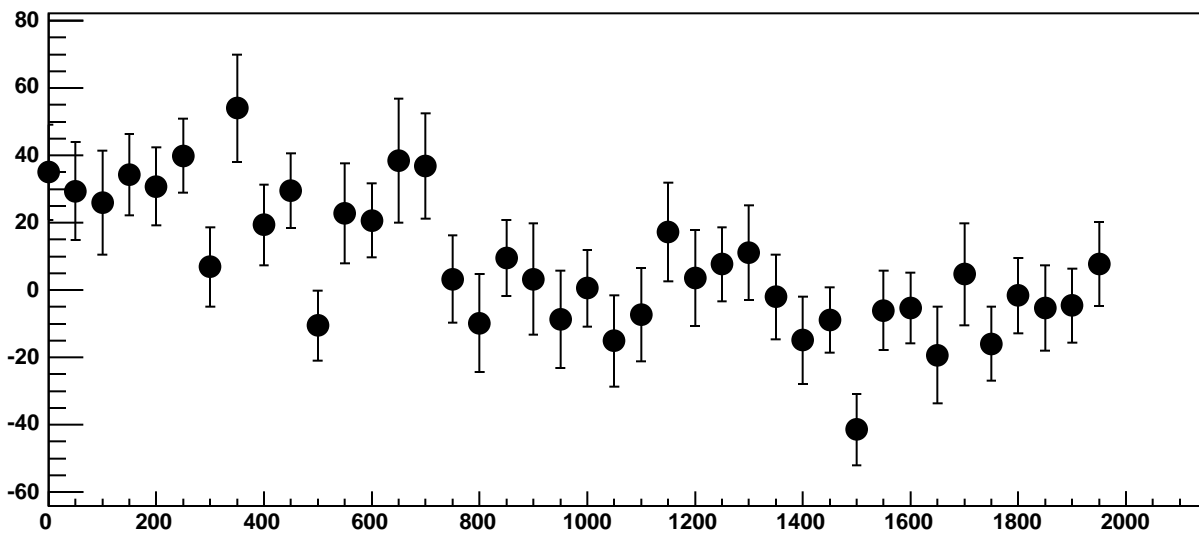
p1

$0.04657 \pm 0.01948$

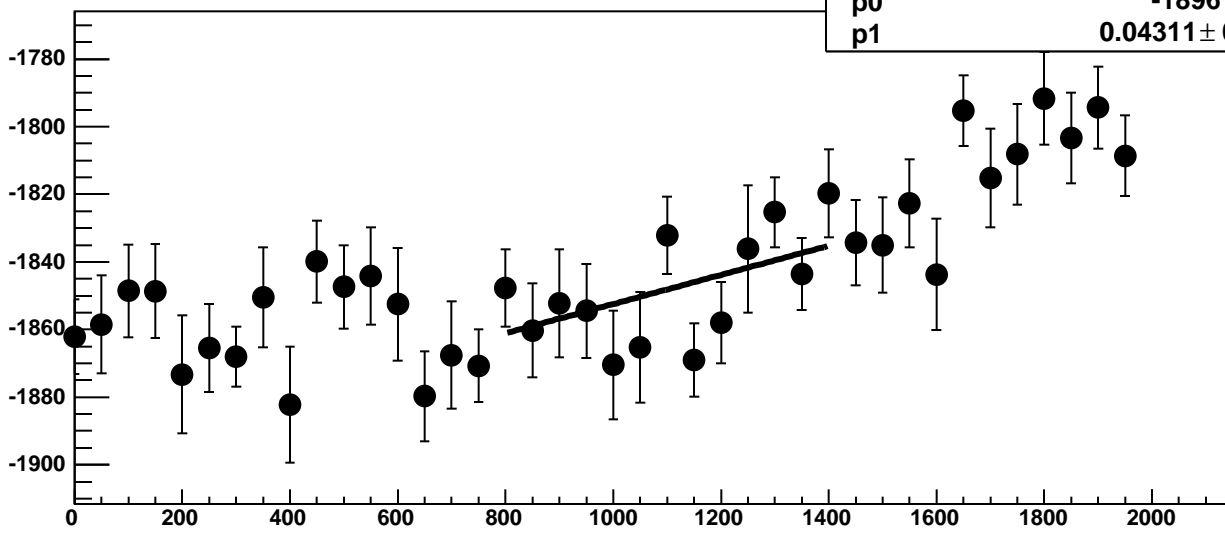
Chip 3, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC



Chip 3, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC

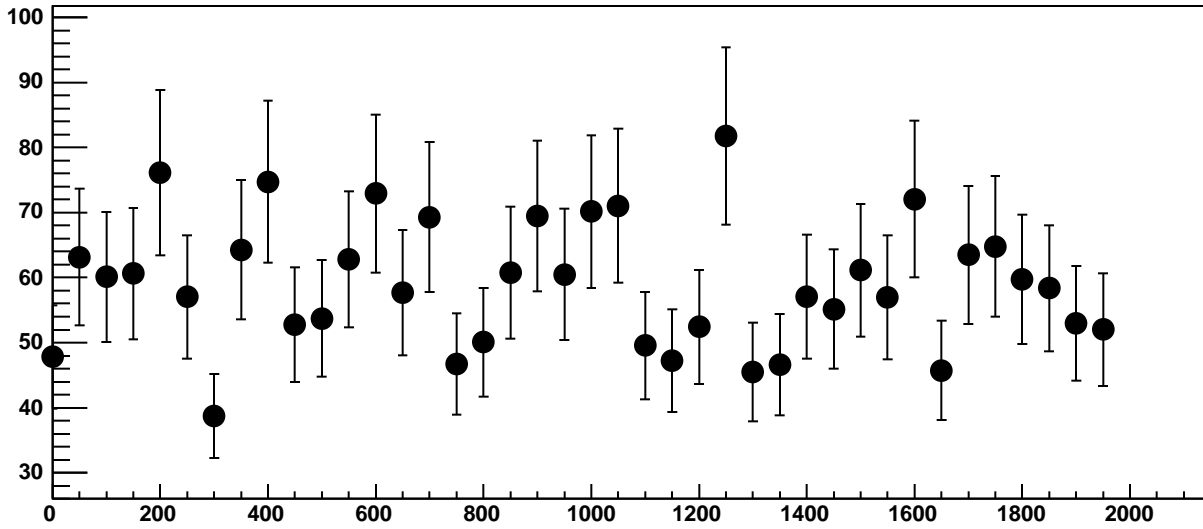


Chip 3, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC

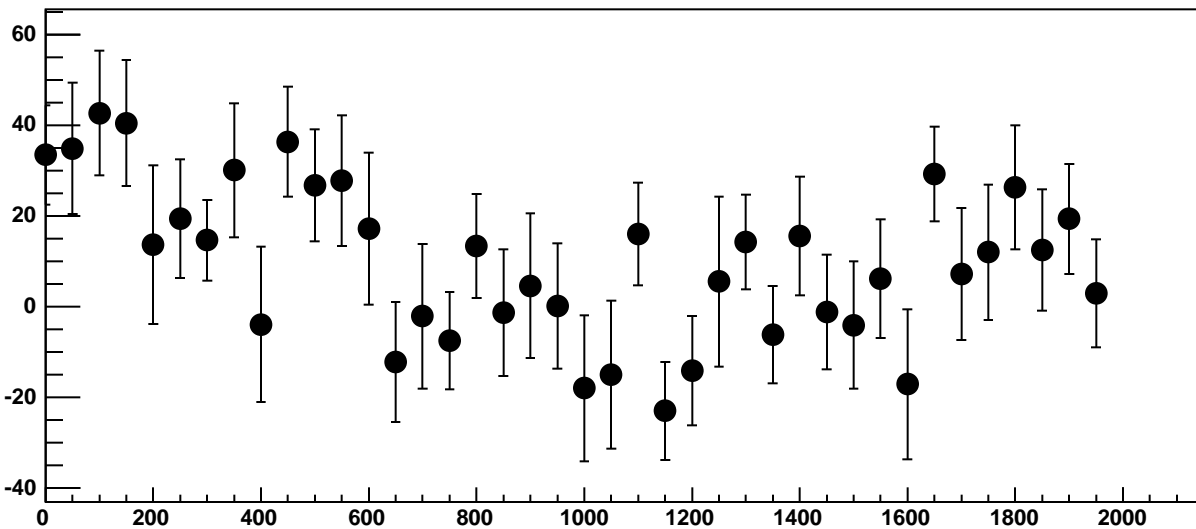


$\chi^2 / \text{ndf}$  15.09 / 11  
p0  $-1896 \pm 21.16$   
p1  $0.04311 \pm 0.01861$

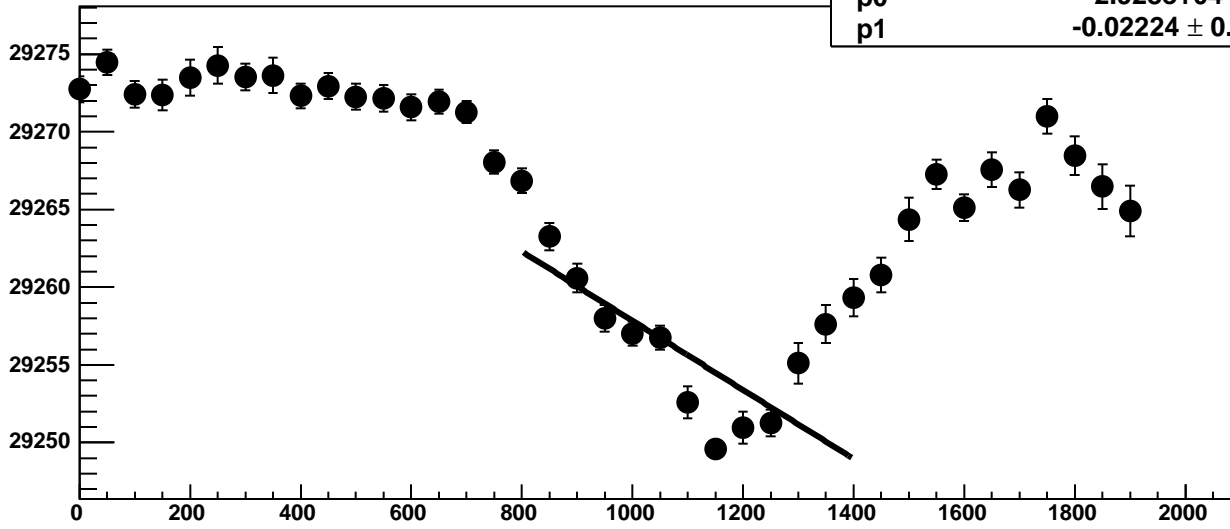
Chip 3, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC



Chip 3, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC

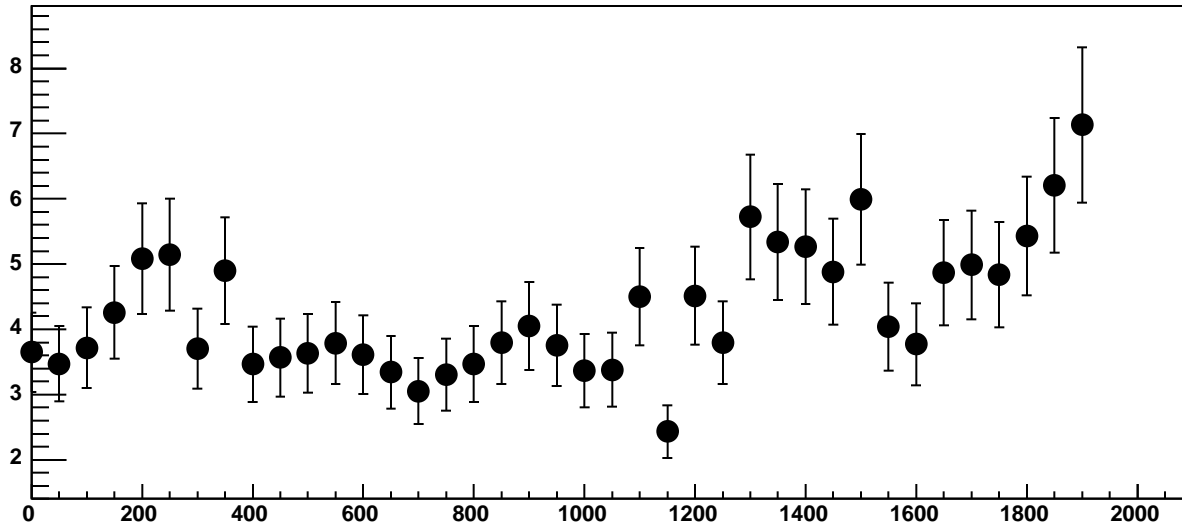


Chip 3, Channel 6, Enable 5!, Hold=30, ADC Mean vs DAC

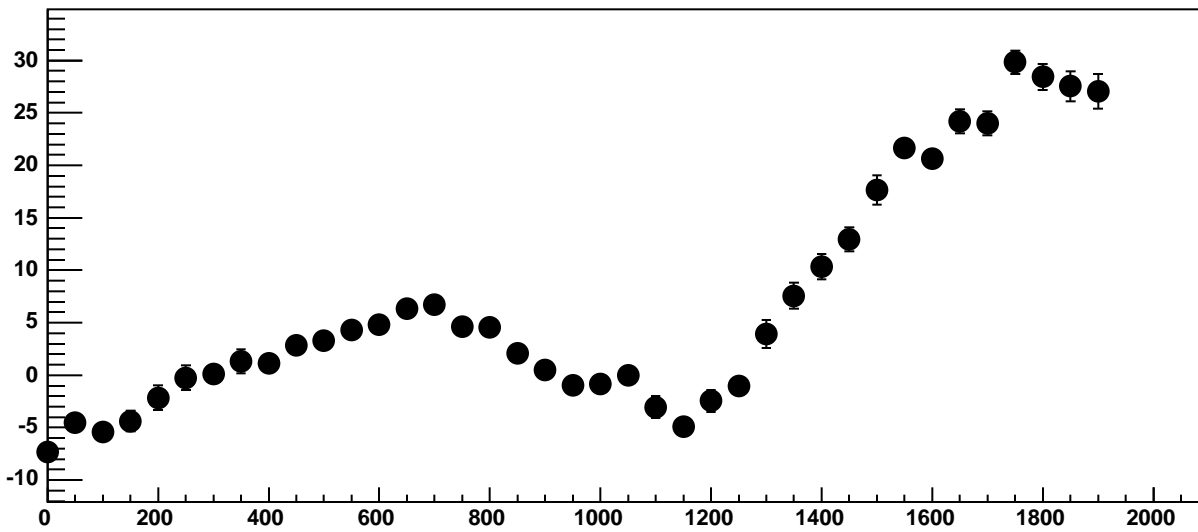


$\chi^2 / \text{ndf}$  255.4 / 11  
p0  $2.928\text{e}+04 \pm 1.564$   
p1  $-0.02224 \pm 0.001447$

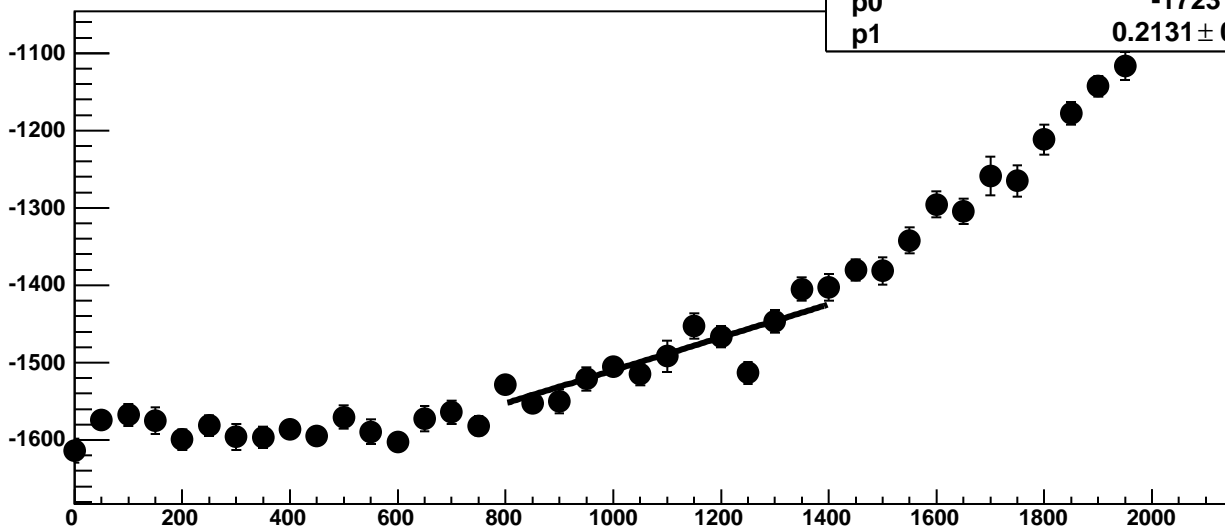
Chip 3, Channel 6, Enable 5!, Hold=30, ADC Noise vs DAC



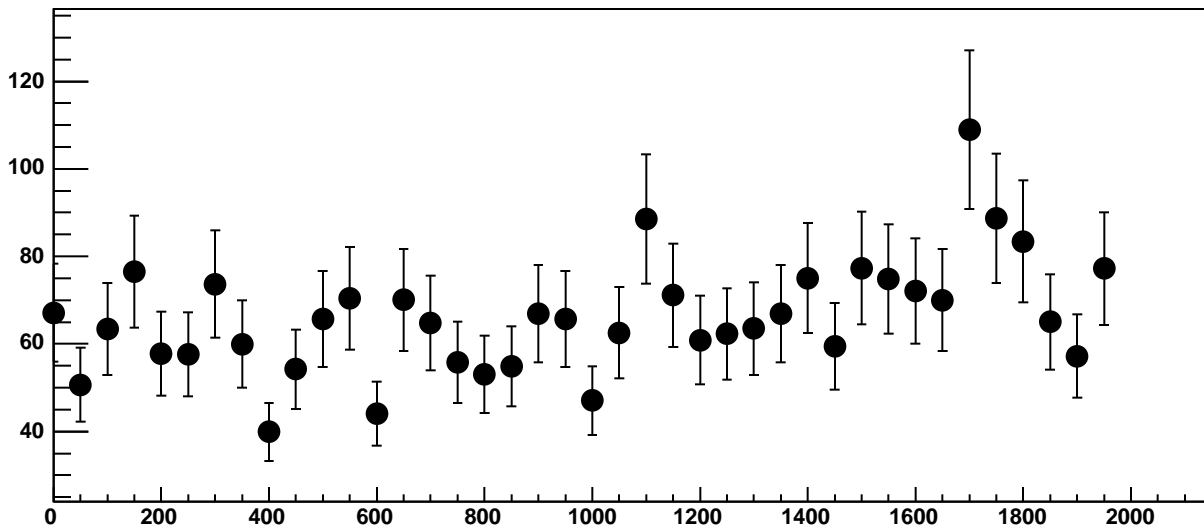
Chip 3, Channel 6, Enable 5!, Hold=30, ADC Residuals vs DAC



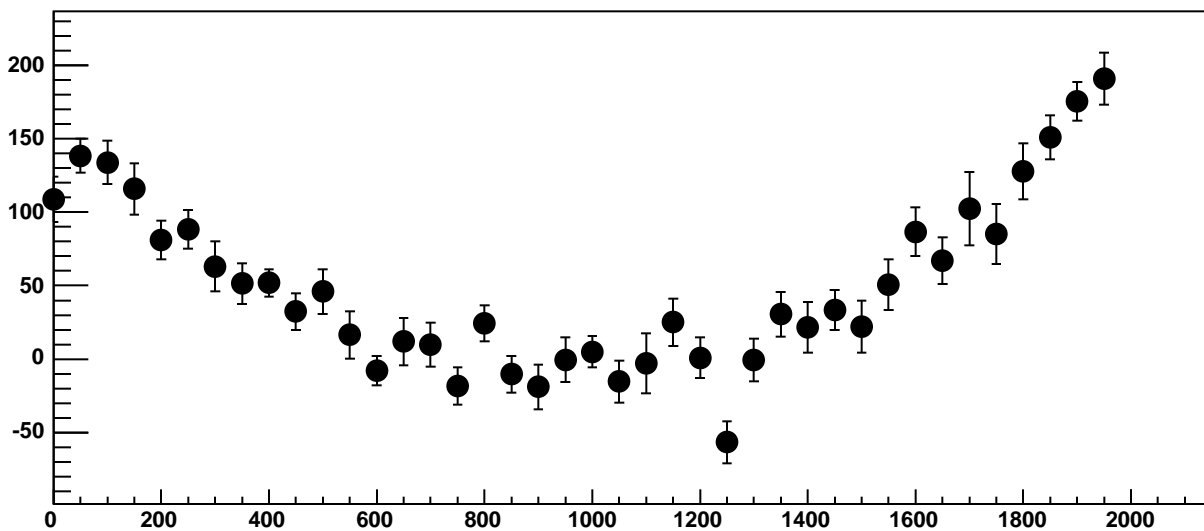
Chip 3, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC



Chip 3, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC

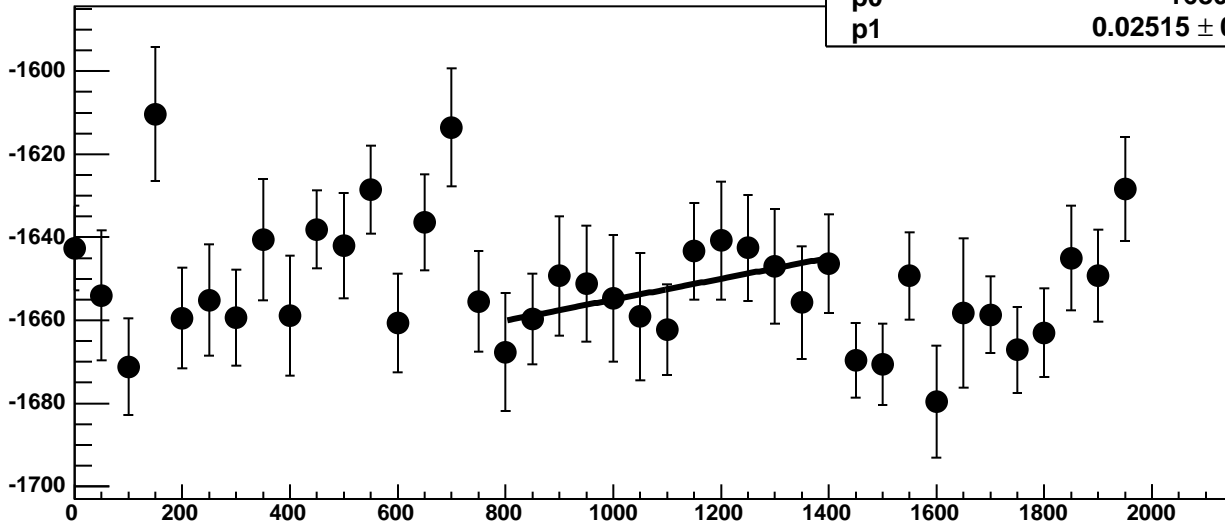


Chip 3, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC

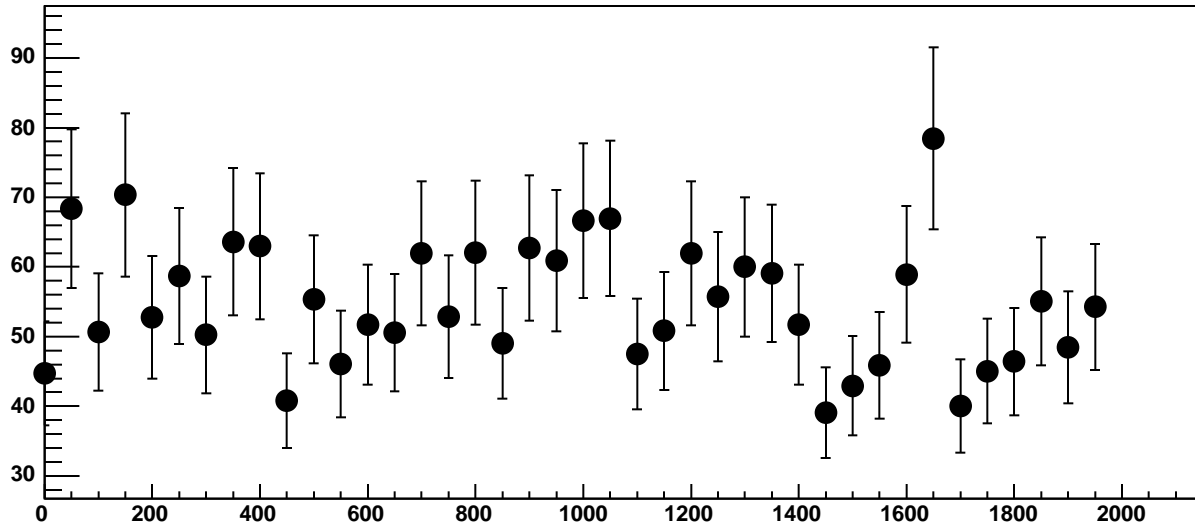


Chip 3, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC

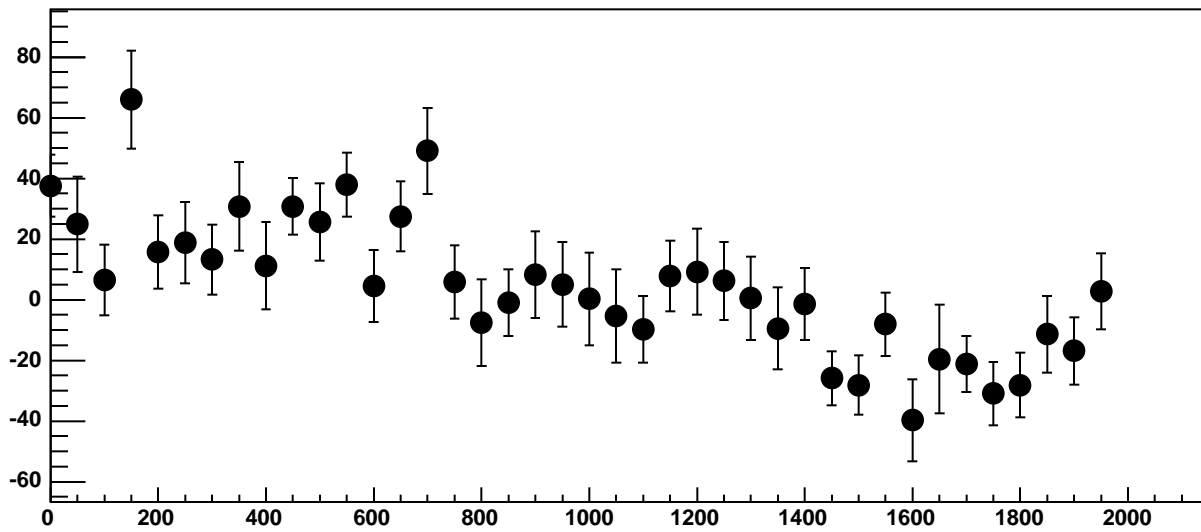
$\chi^2 / \text{ndf}$  3.301 / 11  
p0  $-1680 \pm 21.5$   
p1  $0.02515 \pm 0.01917$



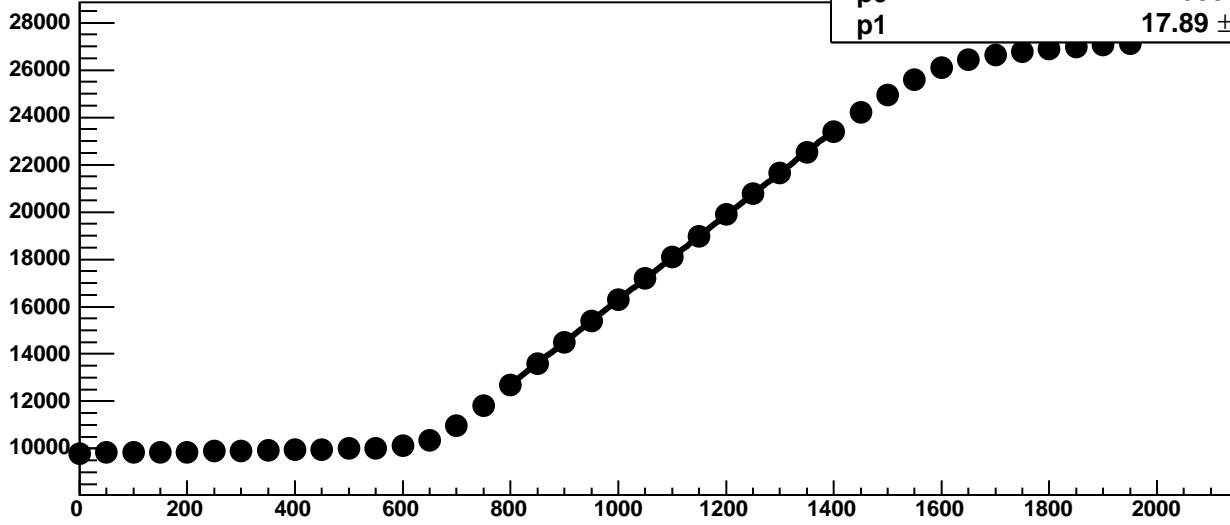
Chip 3, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC



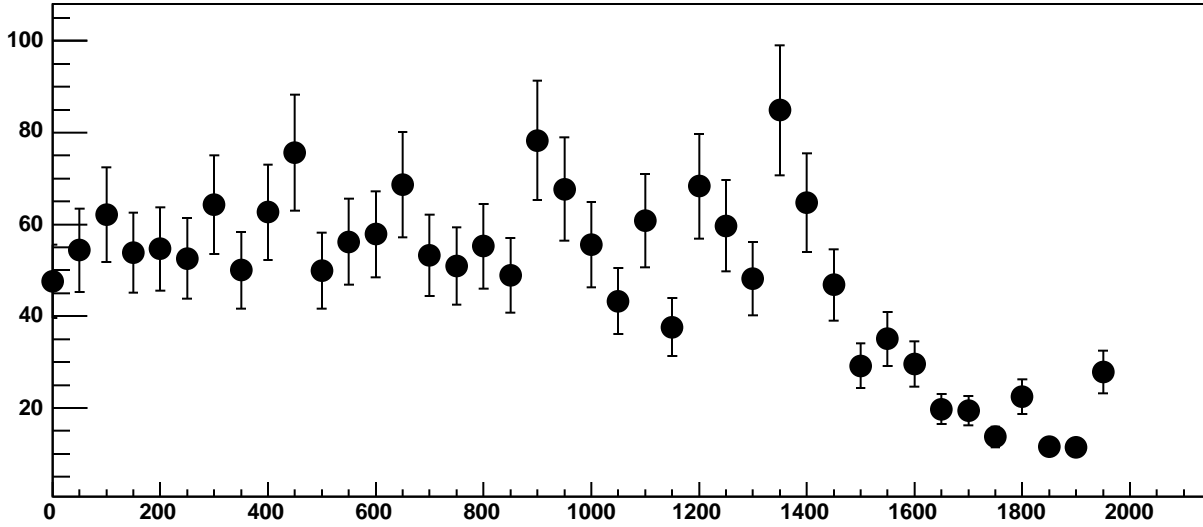
Chip 3, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC



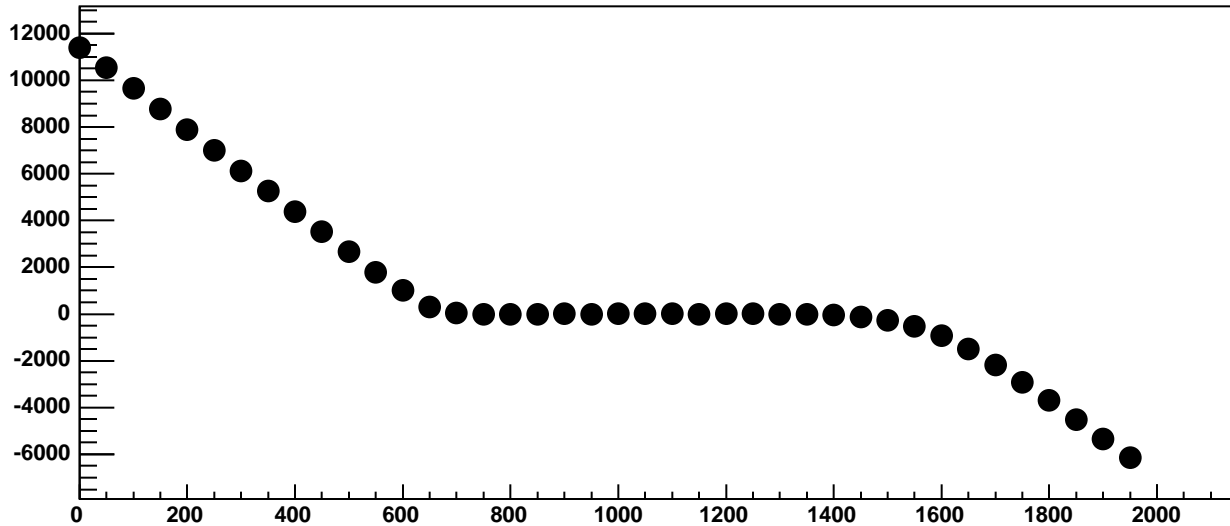
Chip 3, Channel 7, Enable 2!, Hold=30, ADC Mean vs DAC



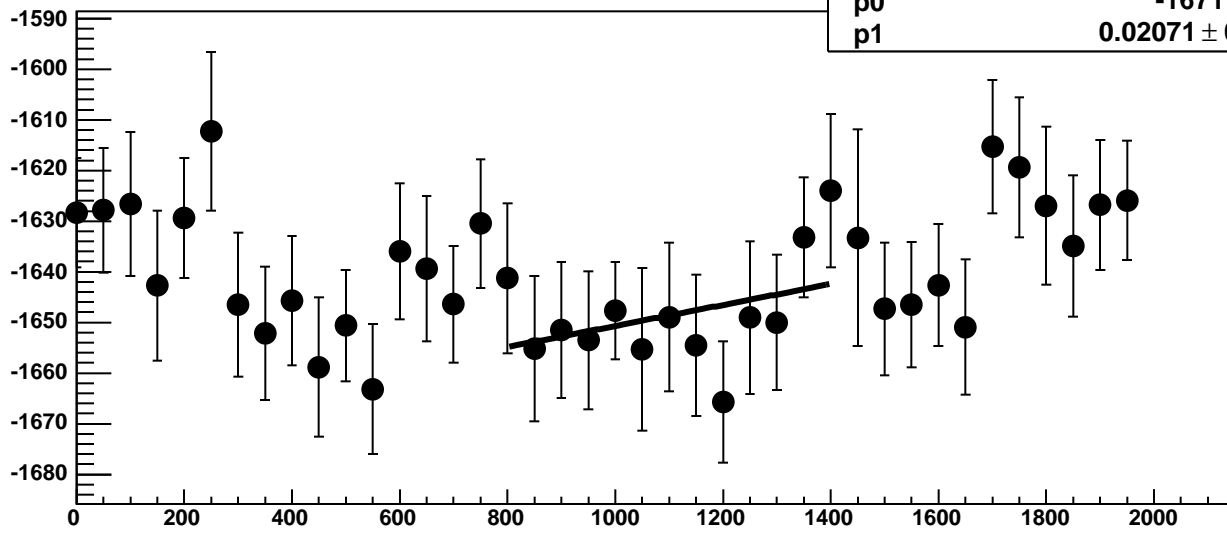
Chip 3, Channel 7, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 3, Channel 7, Enable 2!, Hold=30, ADC Residuals vs DAC



Chip 3, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

6.367 / 11

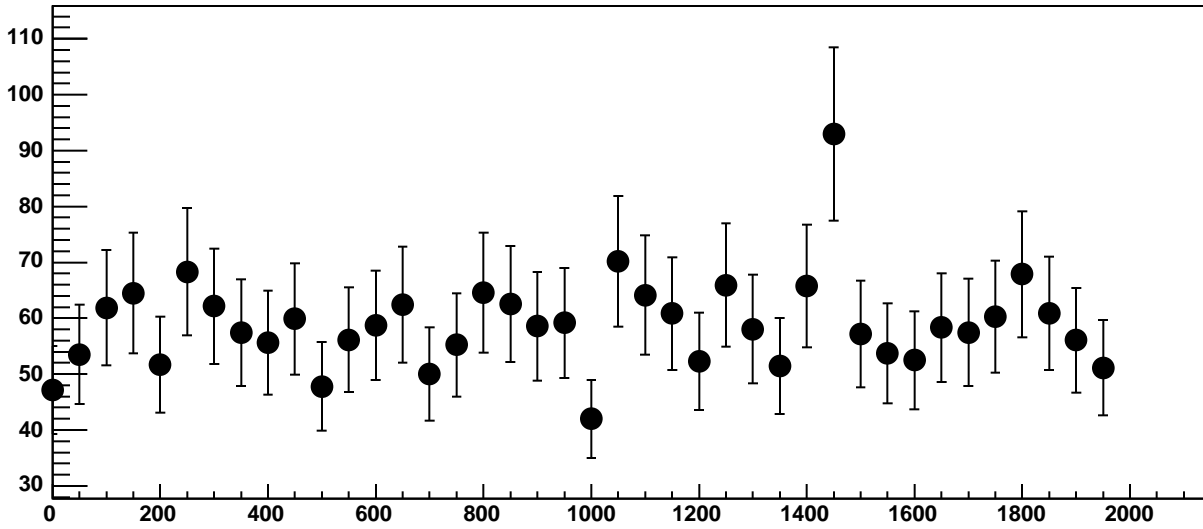
p0

$-1671 \pm 22.67$

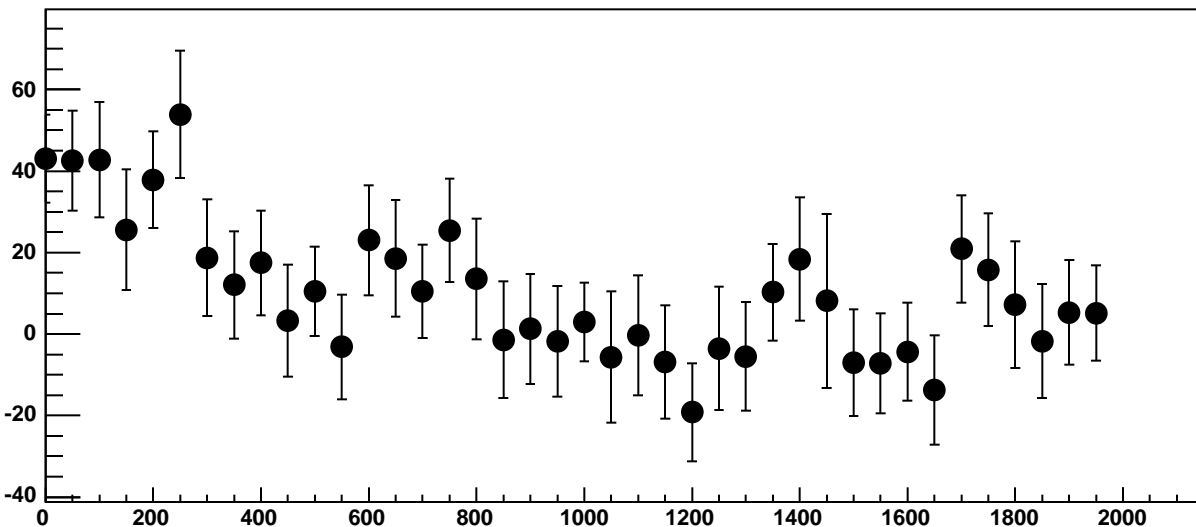
p1

$0.02071 \pm 0.02031$

Chip 3, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC

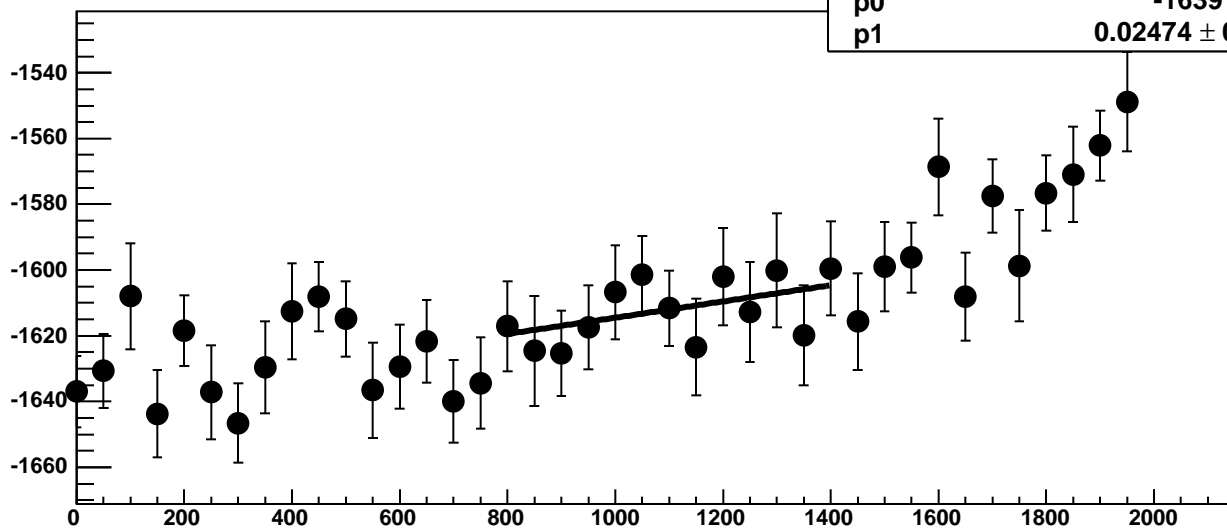


Chip 3, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC





Chip 3, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

4.12 / 11

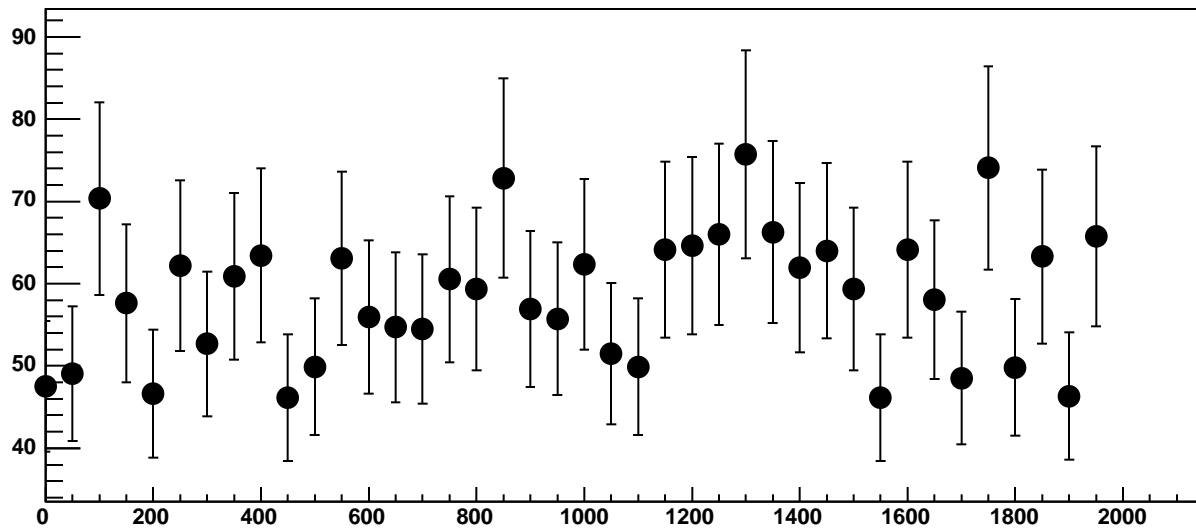
p0

$-1639 \pm 23.82$

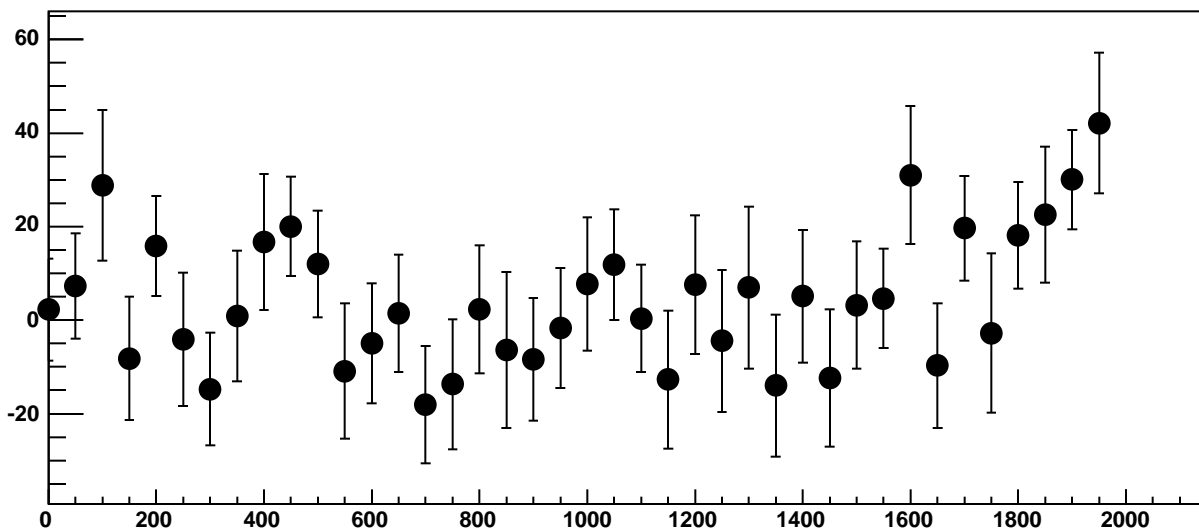
p1

$0.02474 \pm 0.02162$

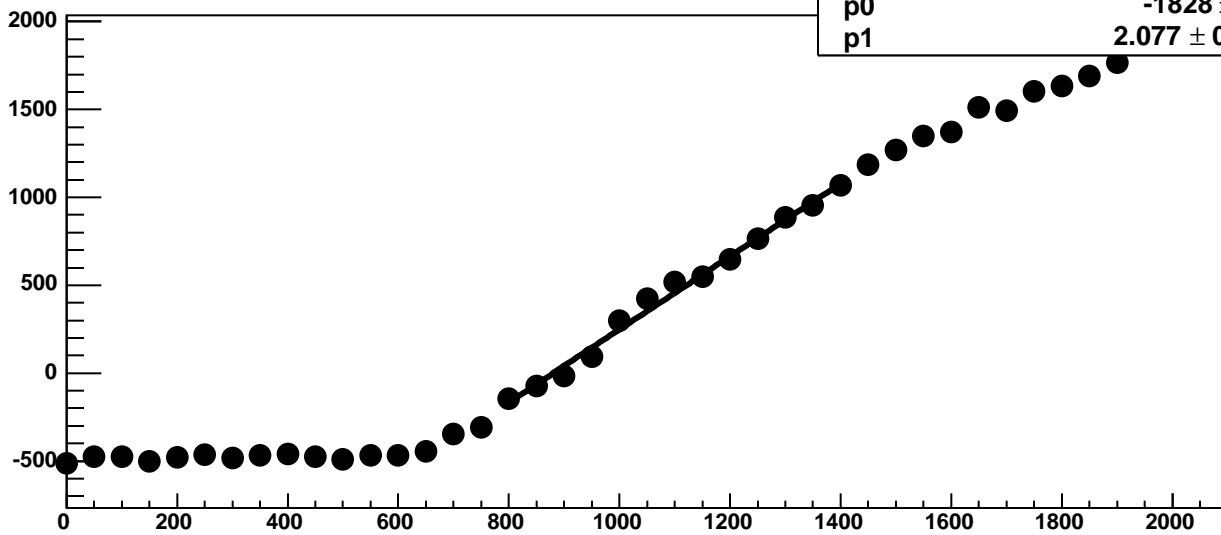
Chip 3, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC



Chip 3, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC

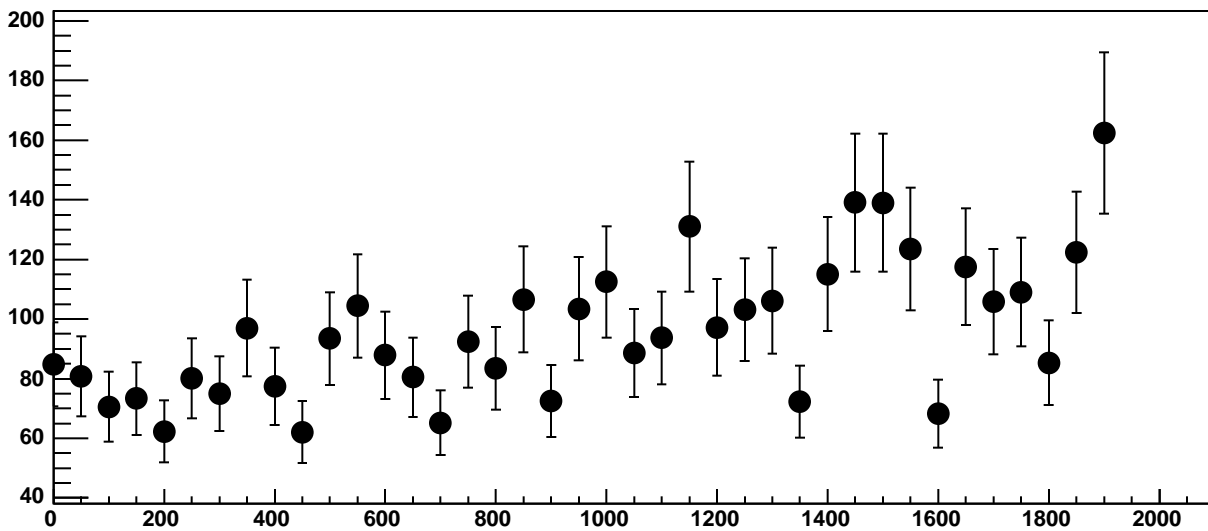


Chip 3, Channel 7, Enable 5, Hold=30, ADC Mean vs DAC

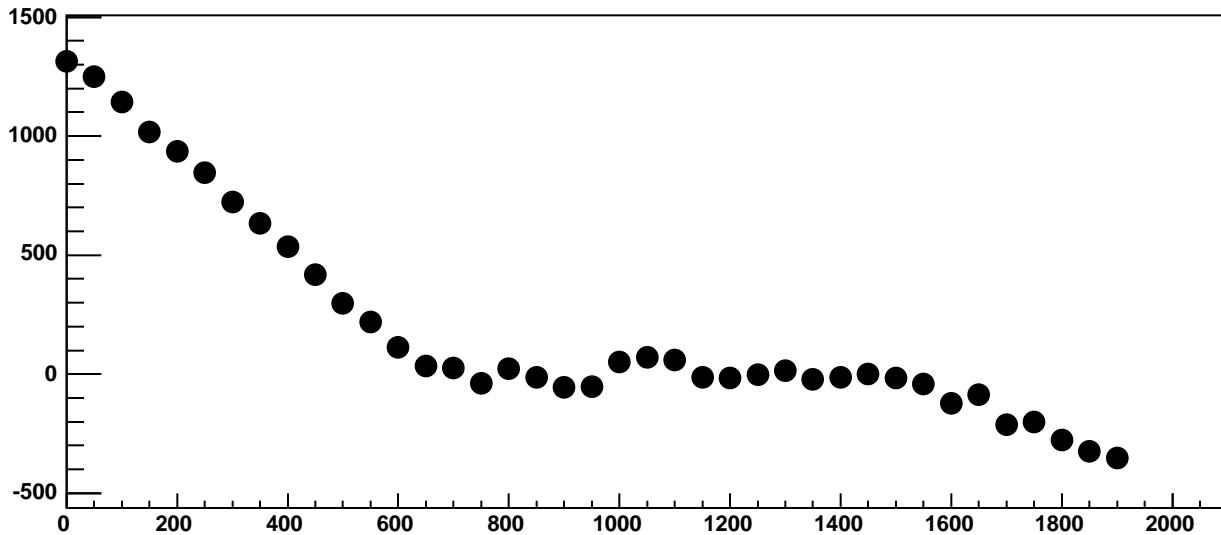


$\chi^2 / \text{ndf}$  44.5 / 11  
p0  $-1828 \pm 34.32$   
p1  $2.077 \pm 0.03102$

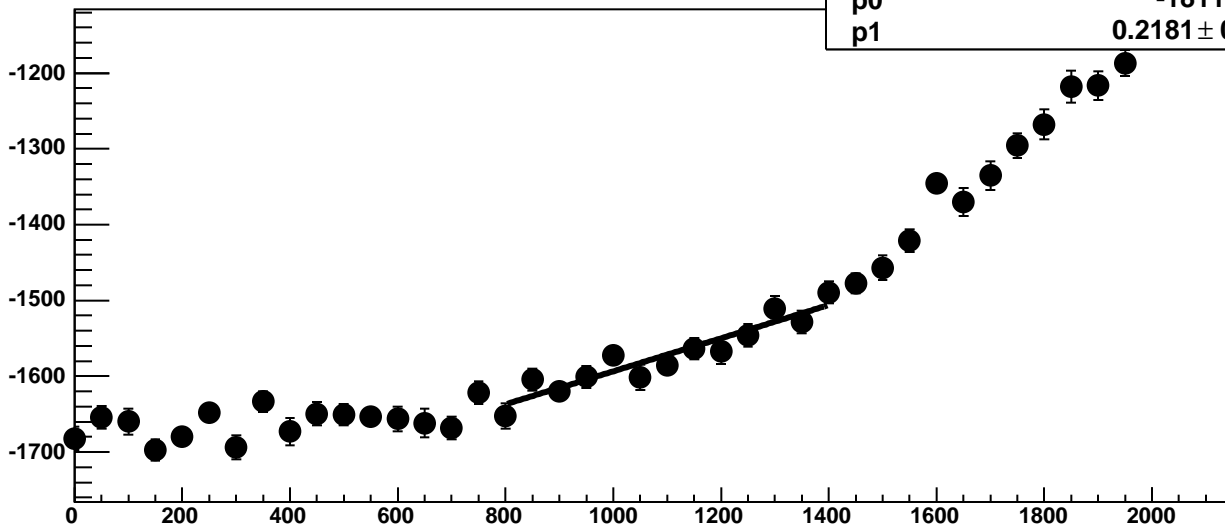
Chip 3, Channel 7, Enable 5, Hold=30, ADC Noise vs DAC



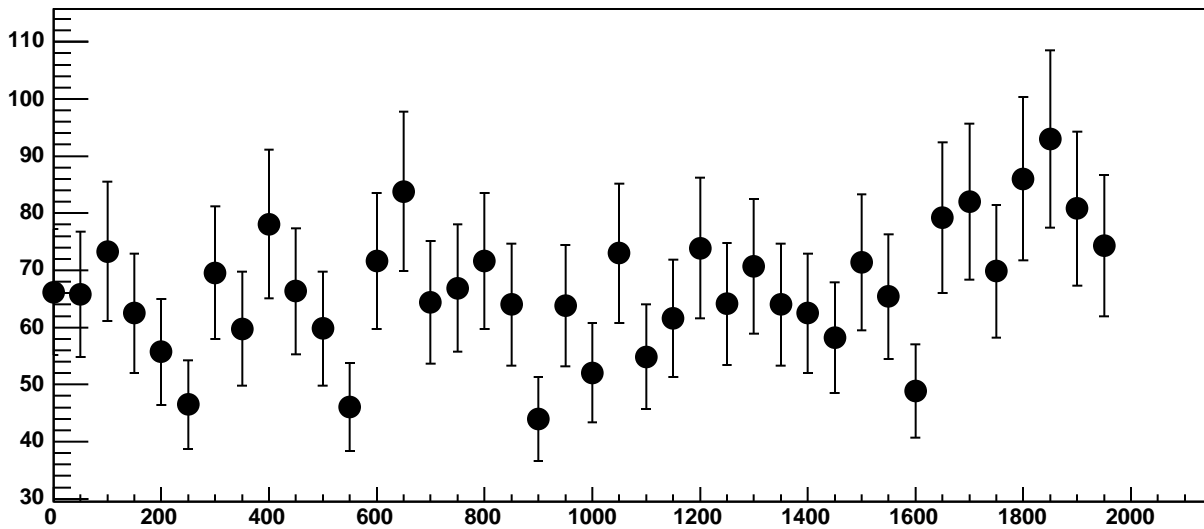
Chip 3, Channel 7, Enable 5, Hold=30, ADC Residuals vs DAC



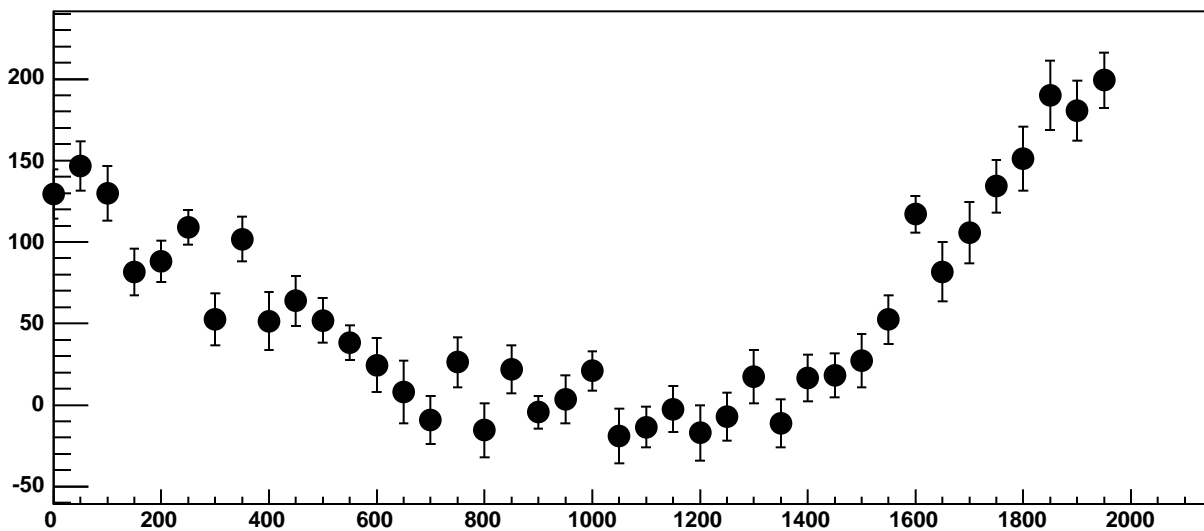
Chip 3, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC



Chip 3, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC



Chip 3, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 3, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

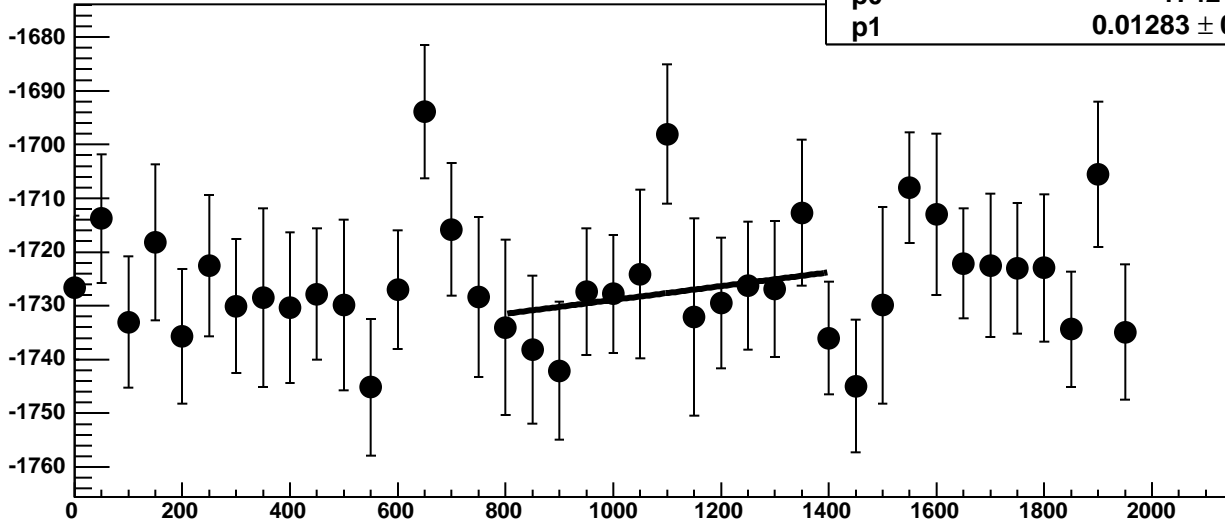
8.77 / 11

p0

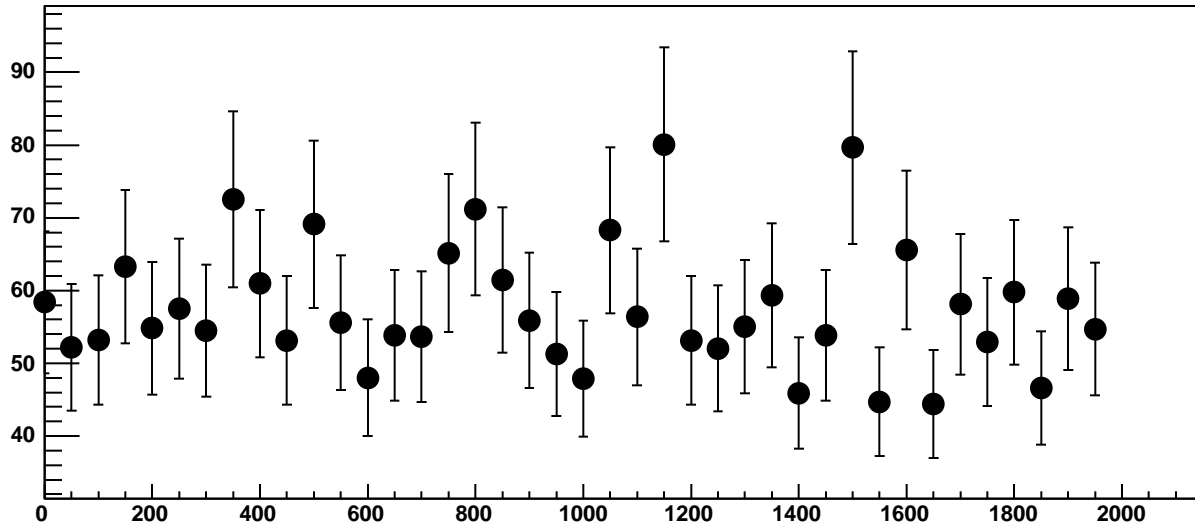
$-1742 \pm 21.52$

p1

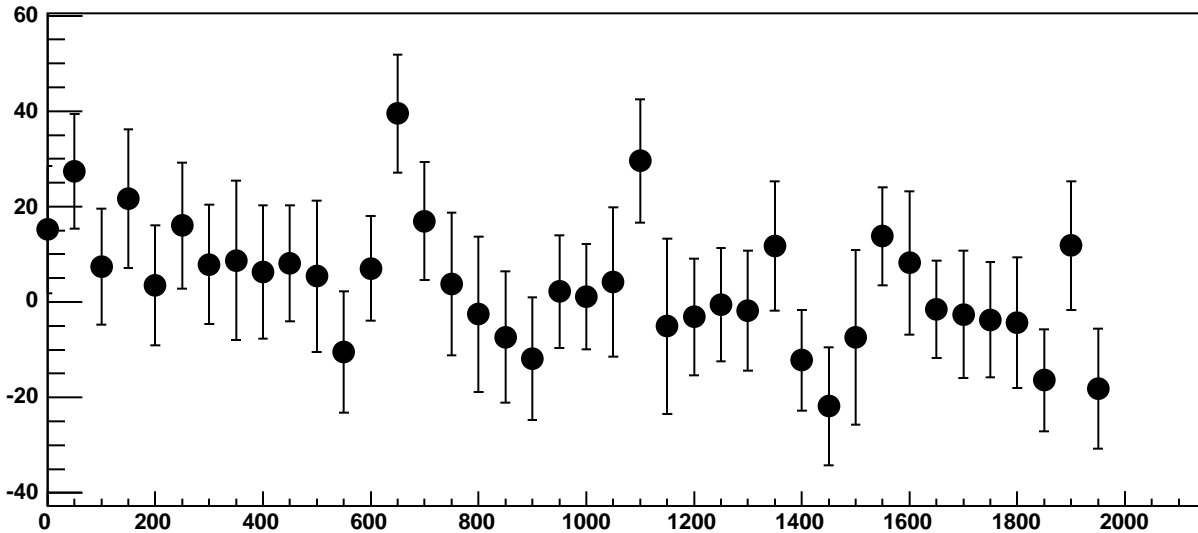
$0.01283 \pm 0.01898$



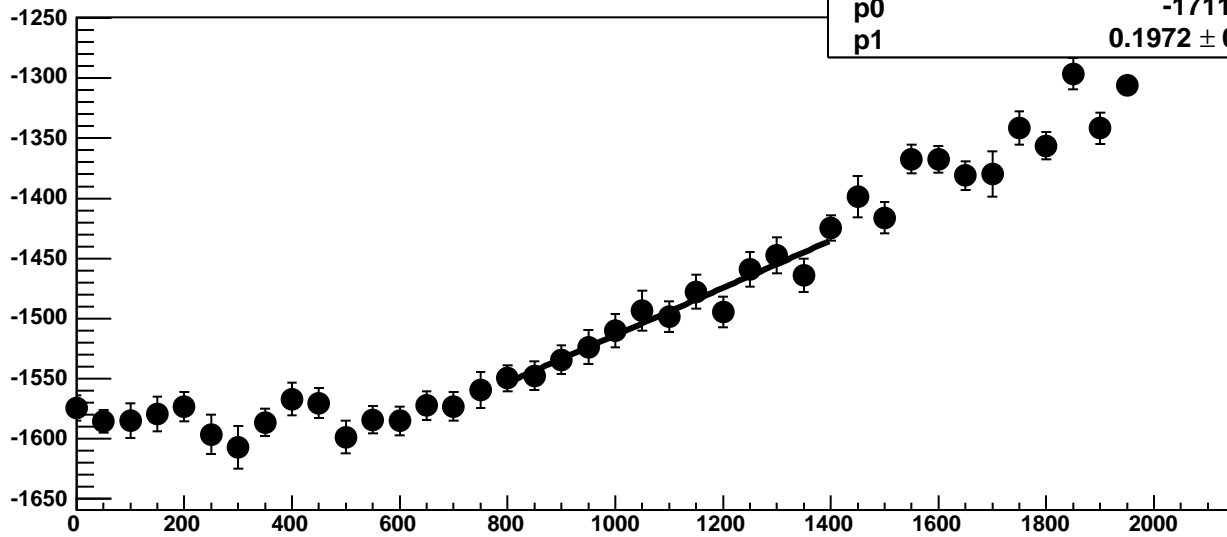
Chip 3, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC



Chip 3, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC

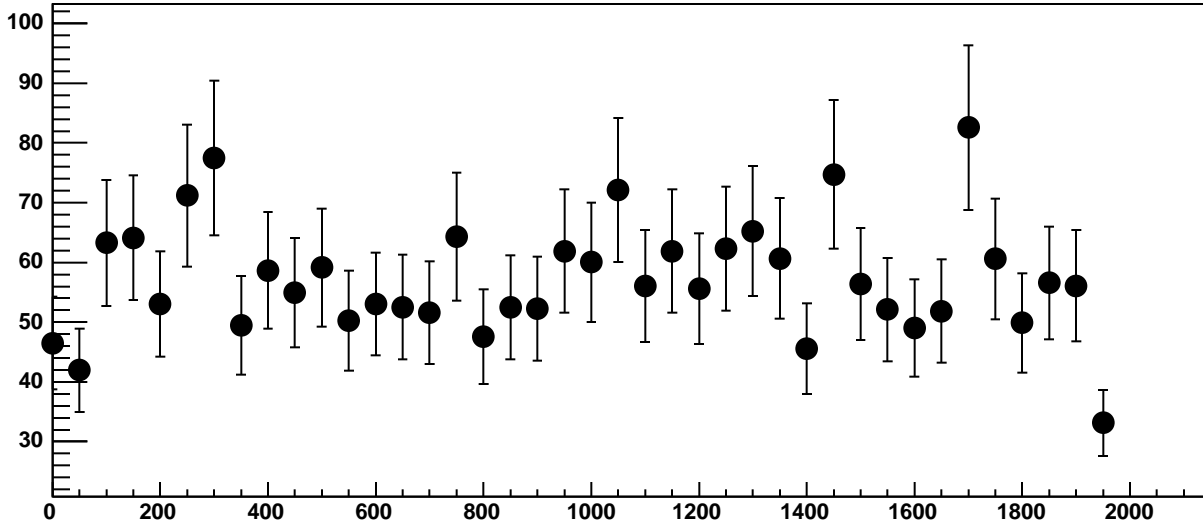


Chip 3, Channel 8, Enable 2, Hold=30, ADC Mean vs DAC

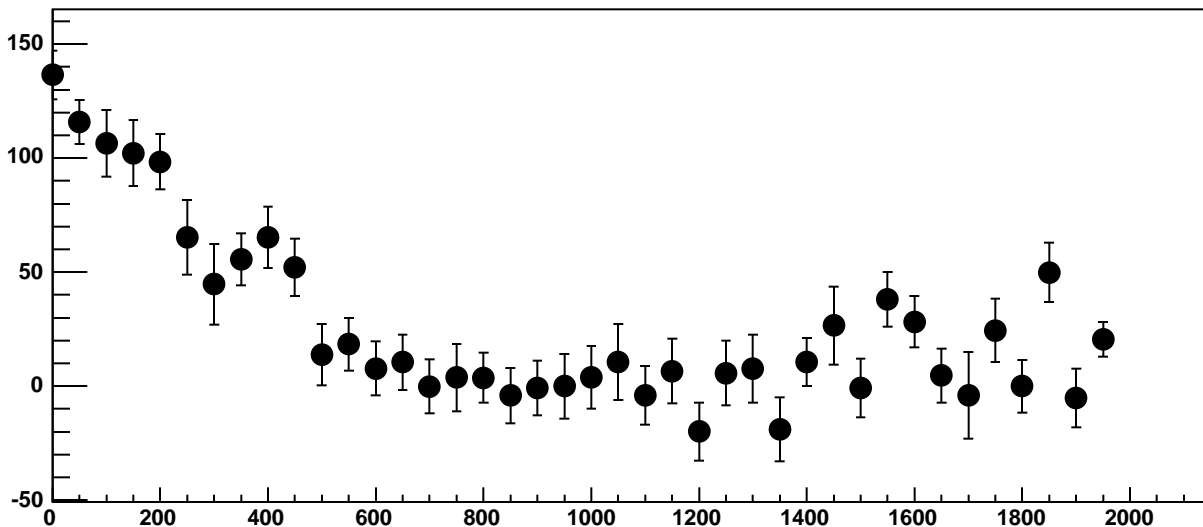


$\chi^2 / \text{ndf}$  6.783 / 11  
p0 -1711 ± 19.81  
p1 0.1972 ± 0.01783

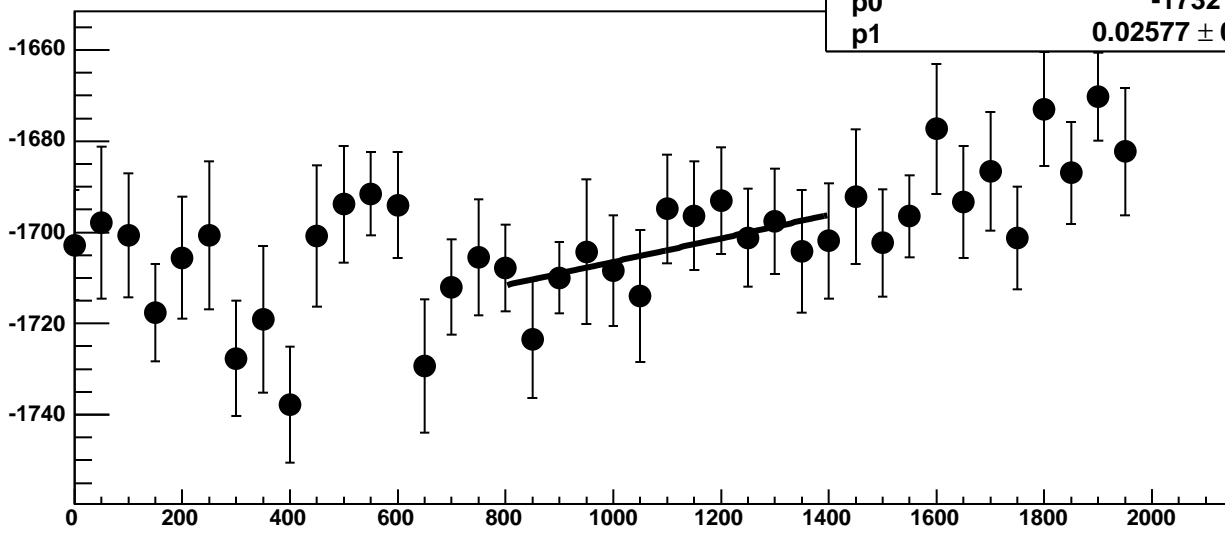
Chip 3, Channel 8, Enable 2, Hold=30, ADC Noise vs DAC



Chip 3, Channel 8, Enable 2, Hold=30, ADC Residuals vs DAC

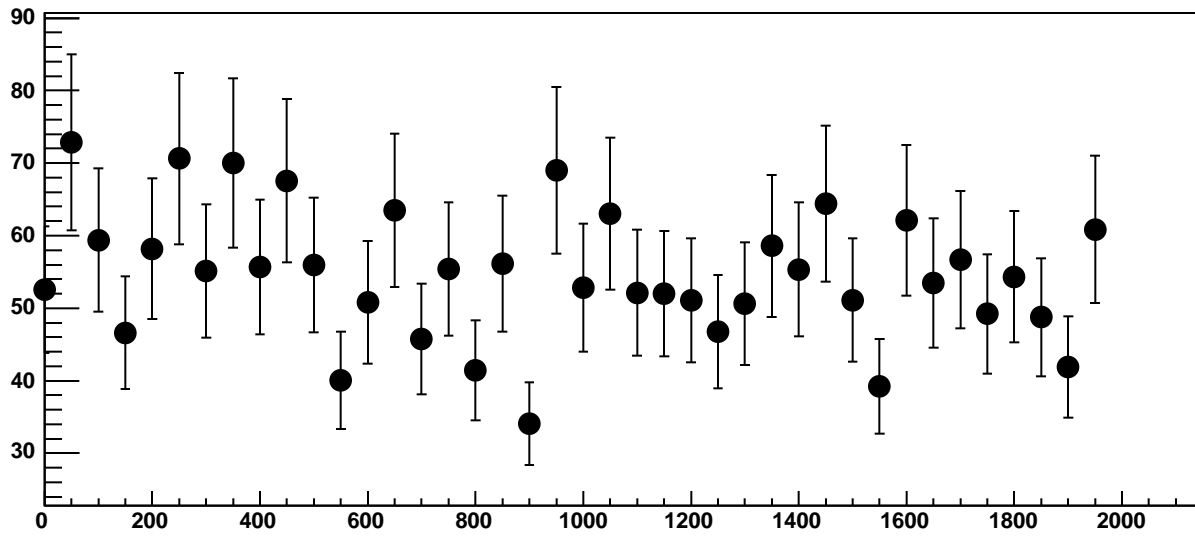


Chip 3, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC

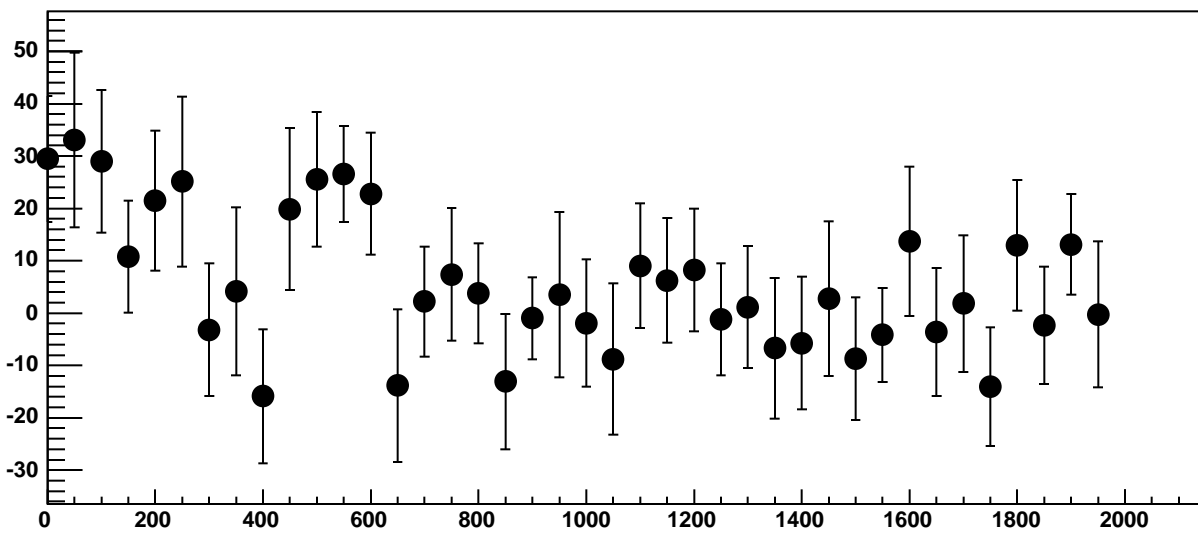


$\chi^2 / \text{ndf}$  3.469 / 11  
p0  $-1732 \pm 18.14$   
p1  $0.02577 \pm 0.01662$

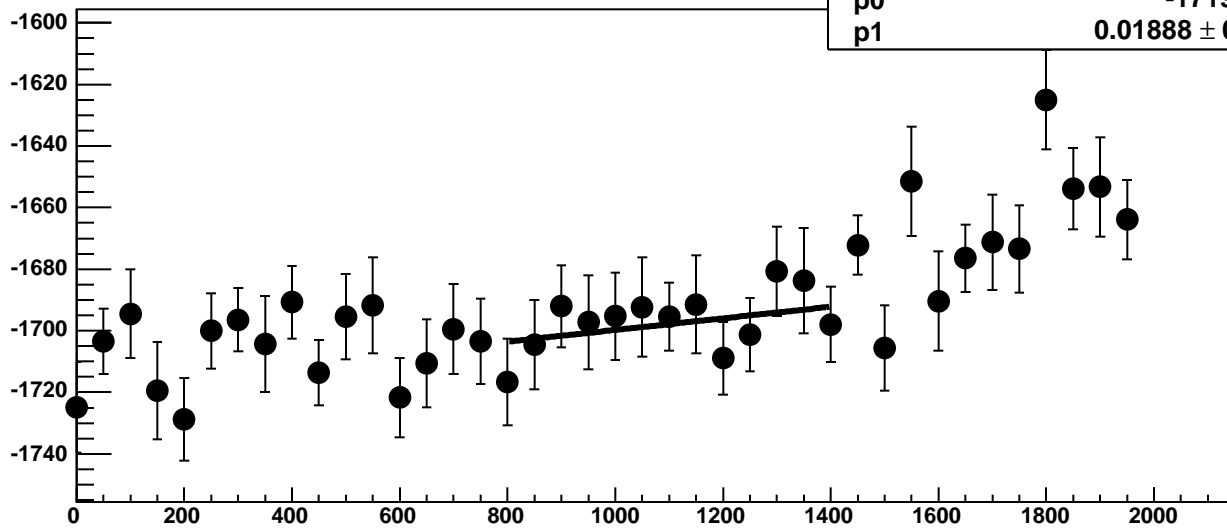
Chip 3, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC



Chip 3, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC

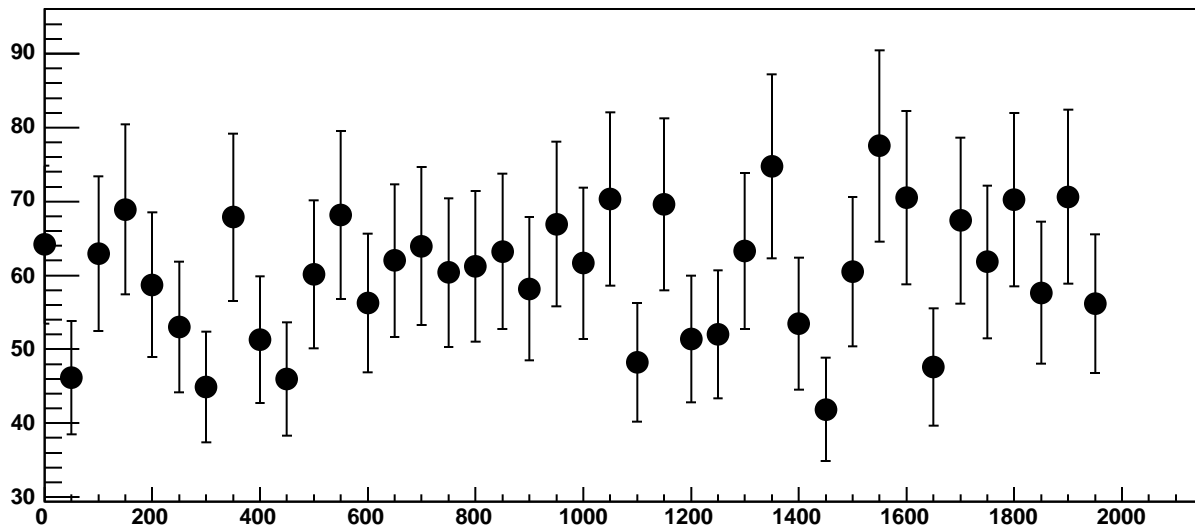


Chip 3, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC

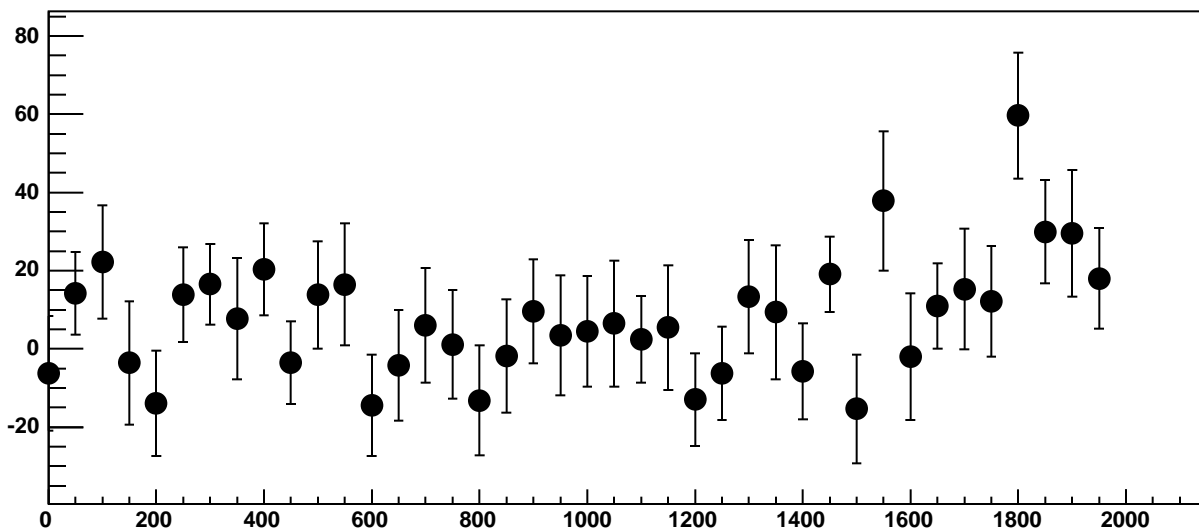


$\chi^2 / \text{ndf}$  4.735 / 11  
p0  $-1719 \pm 23.1$   
p1  $0.01888 \pm 0.02056$

Chip 3, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC



Chip 3, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC



Chip 3, Channel 8, Enable 5!, Hold=30, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

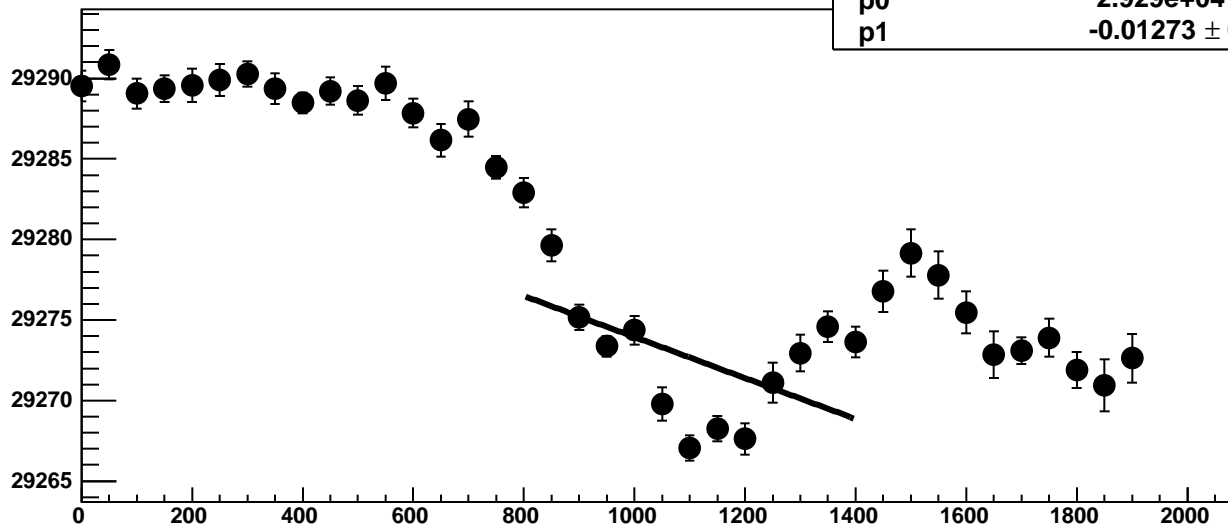
228.6 / 11

p0

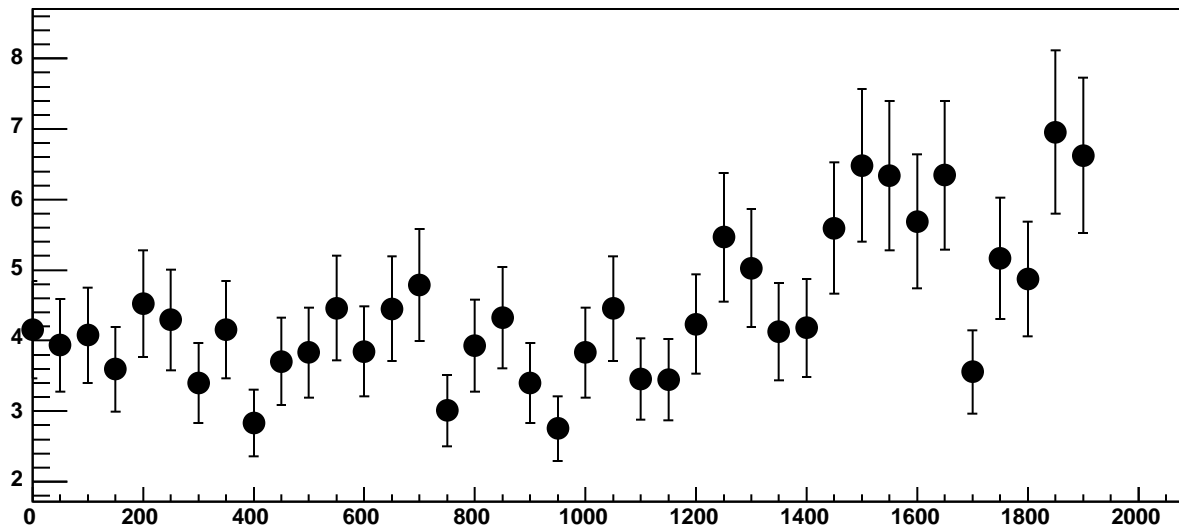
$2.929\text{e}+04 \pm 1.499$

p1

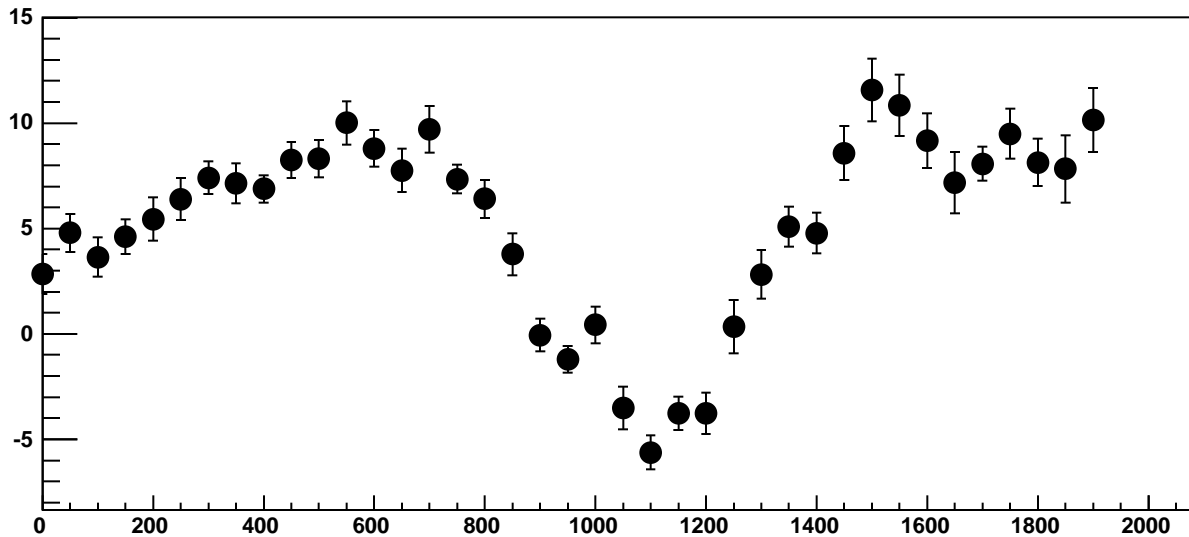
$-0.01273 \pm 0.00138$



Chip 3, Channel 8, Enable 5!, Hold=30, ADC Noise vs DAC

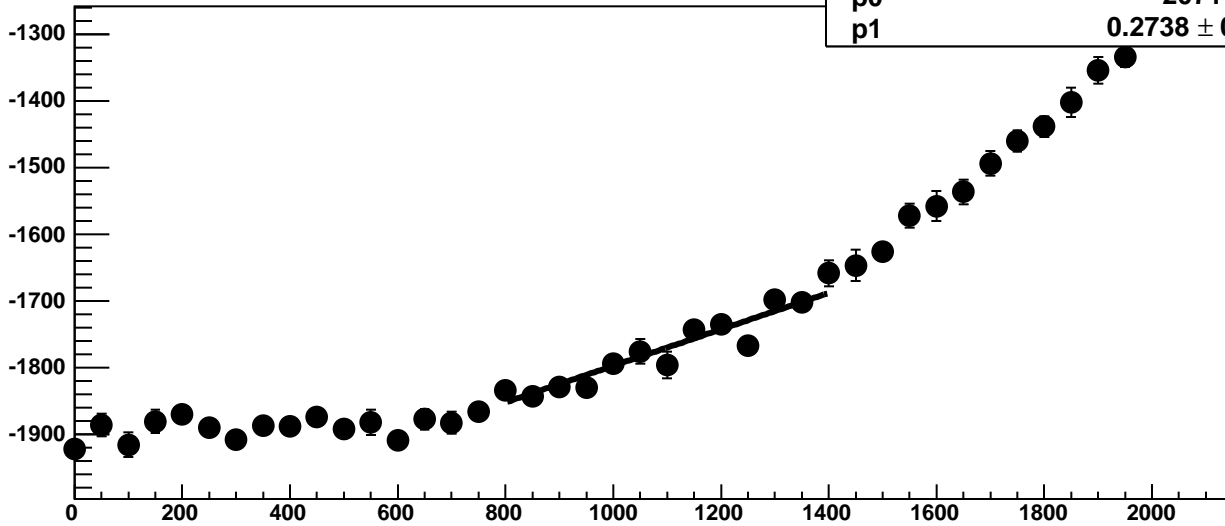


Chip 3, Channel 8, Enable 5!, Hold=30, ADC Residuals vs DAC

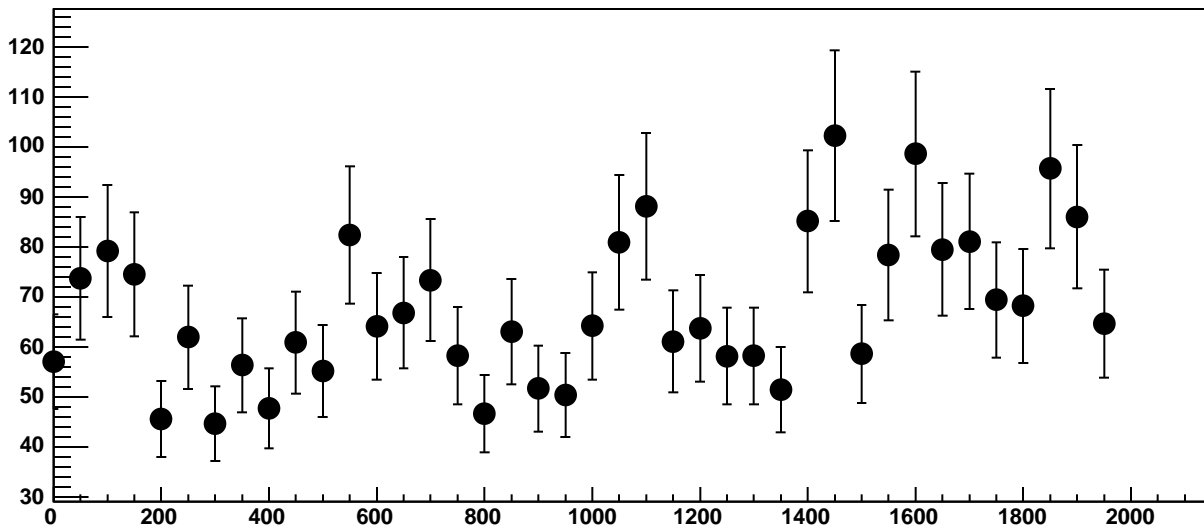




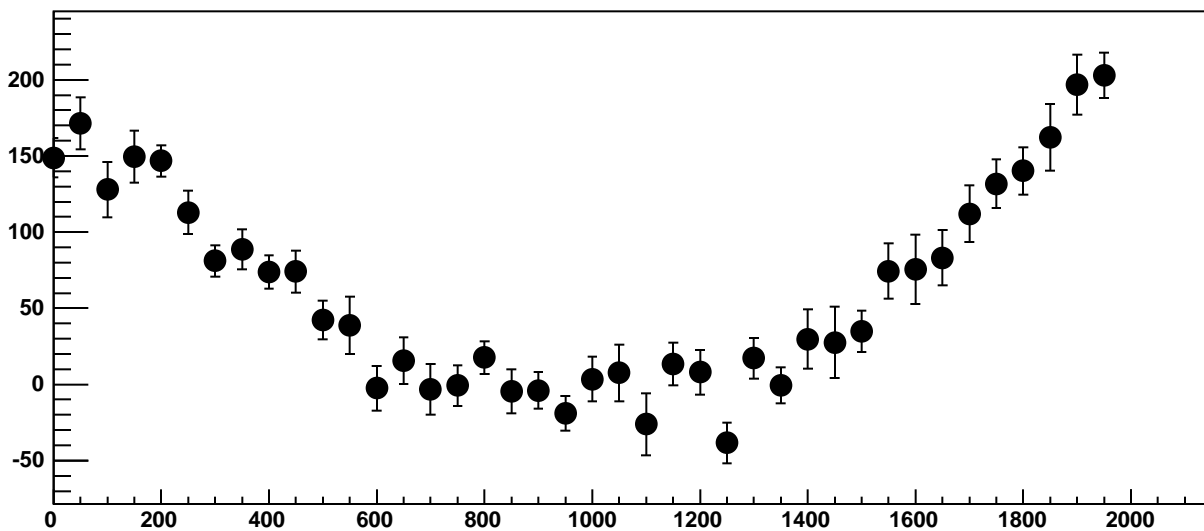
Chip 3, Channel 9, Enable 0, Hold=30, ADC Mean vs DAC



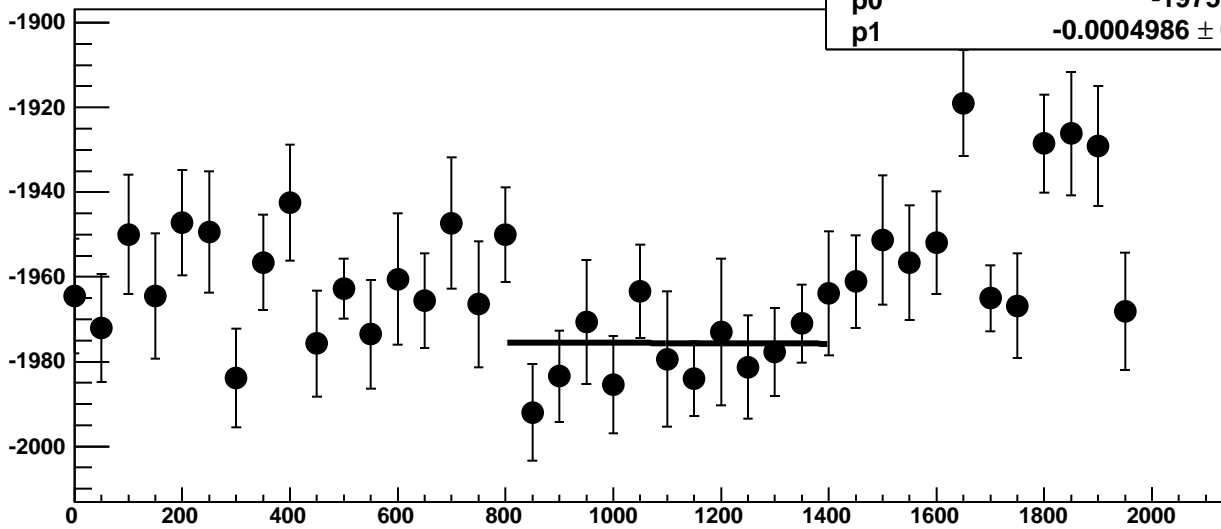
Chip 3, Channel 9, Enable 0, Hold=30, ADC Noise vs DAC



Chip 3, Channel 9, Enable 0, Hold=30, ADC Residuals vs DAC

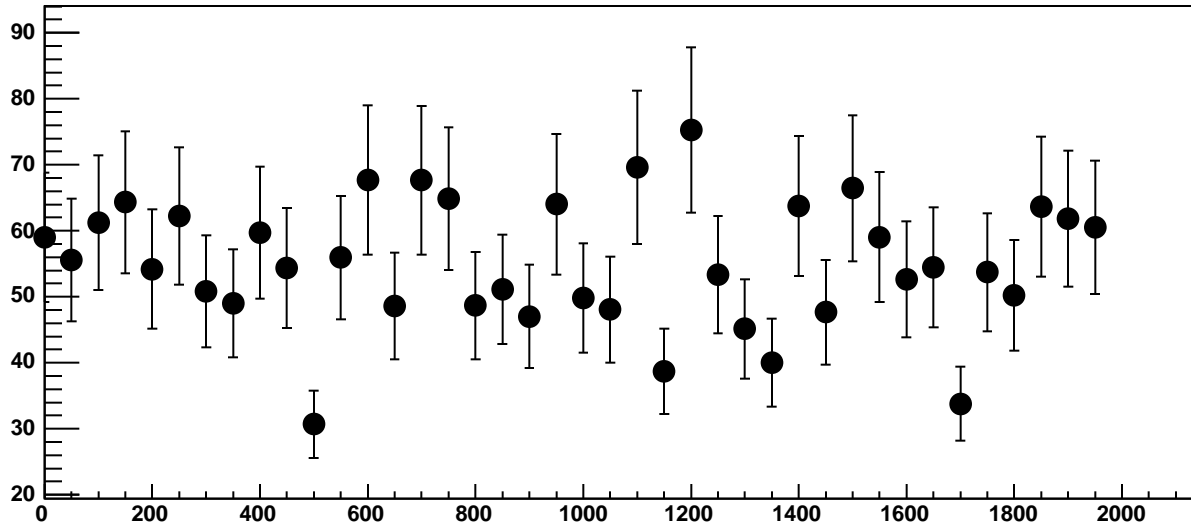


Chip 3, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC

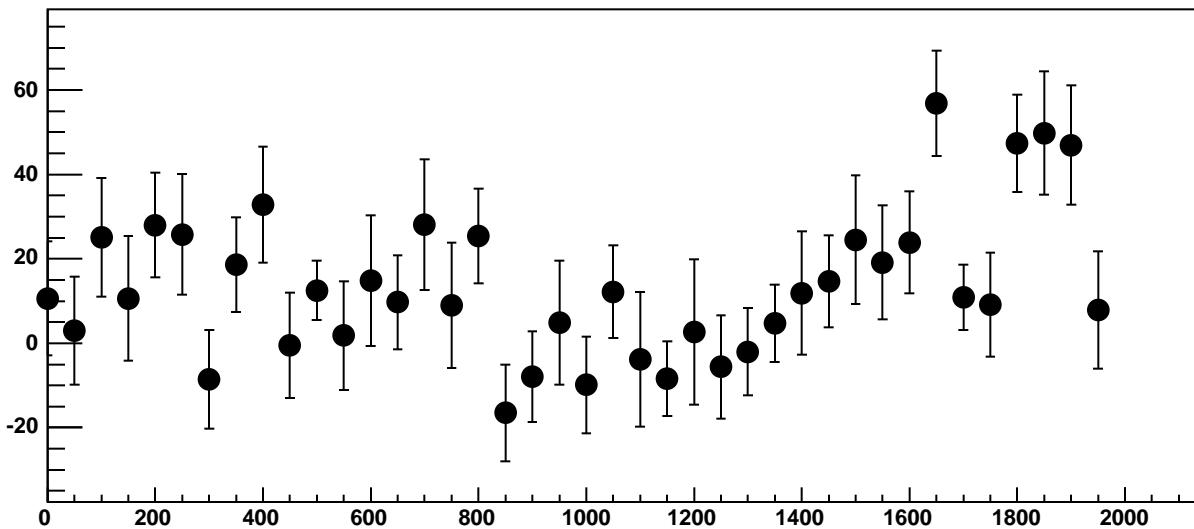


$\chi^2 / \text{ndf}$  12 / 11  
p0  $-1975 \pm 18.93$   
p1  $-0.0004986 \pm 0.01692$

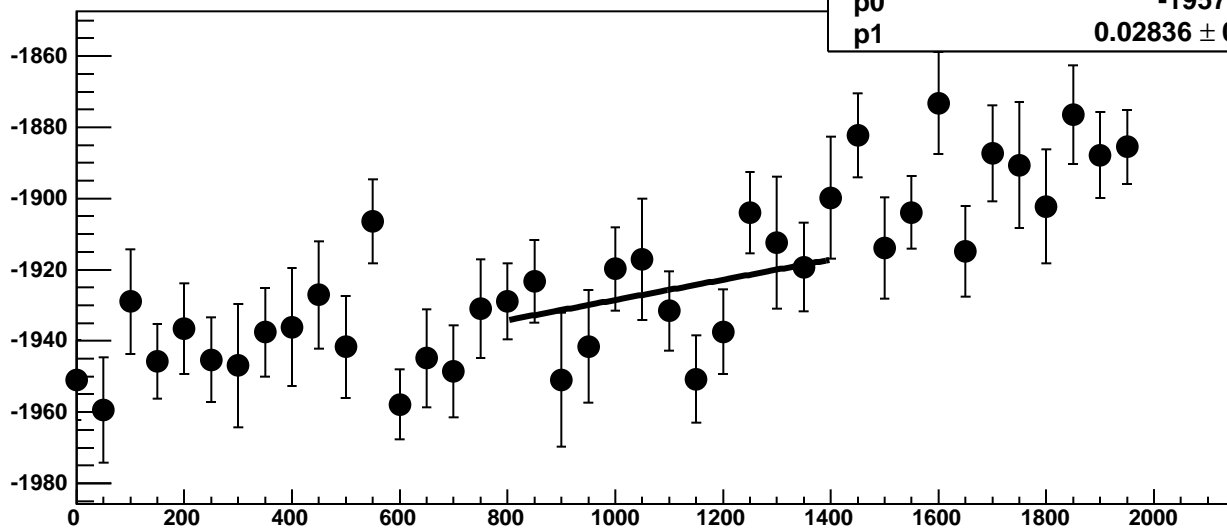
Chip 3, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC



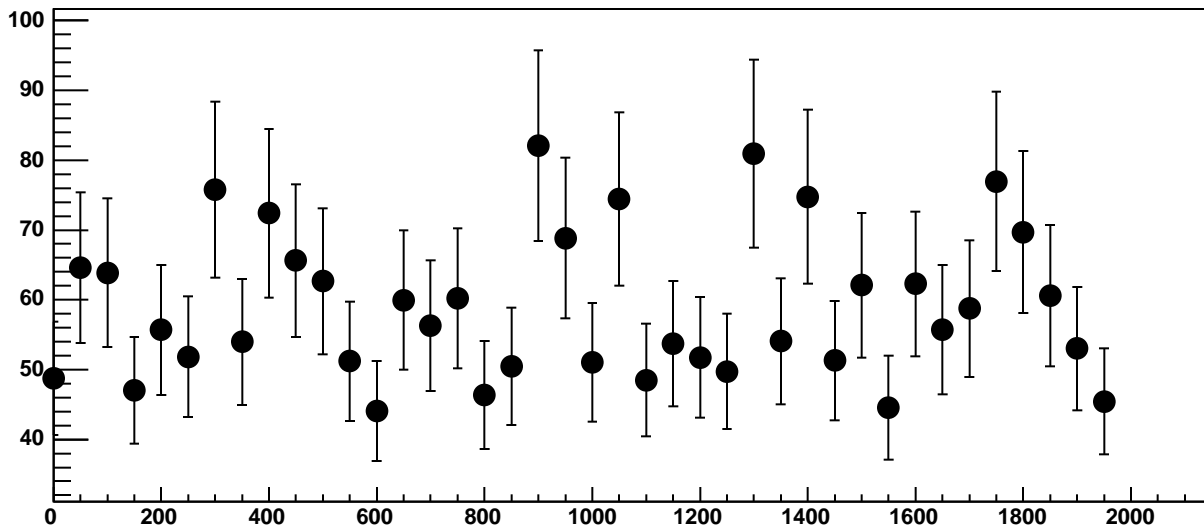
Chip 3, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC



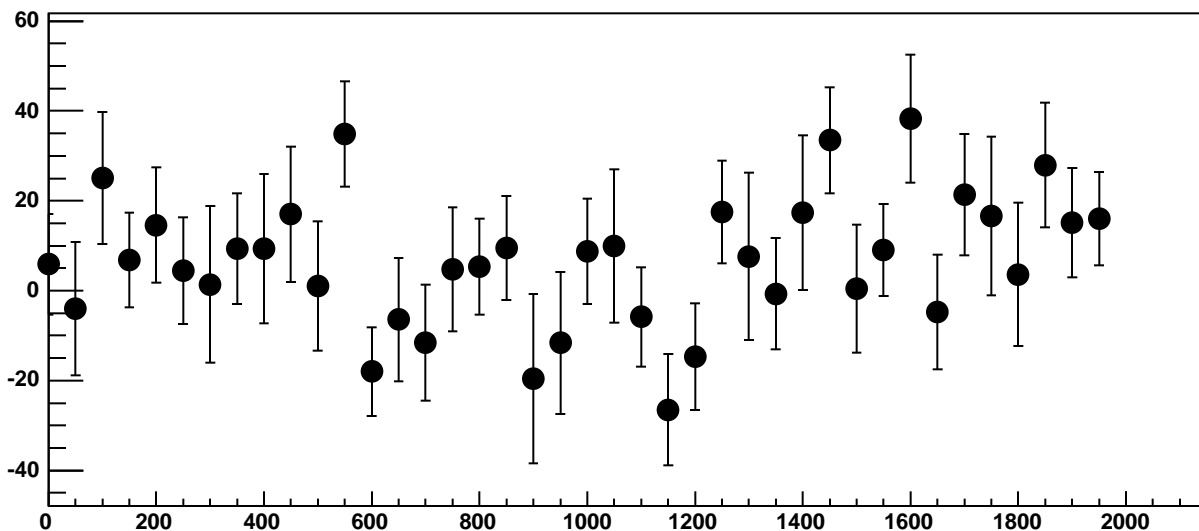
Chip 3, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC



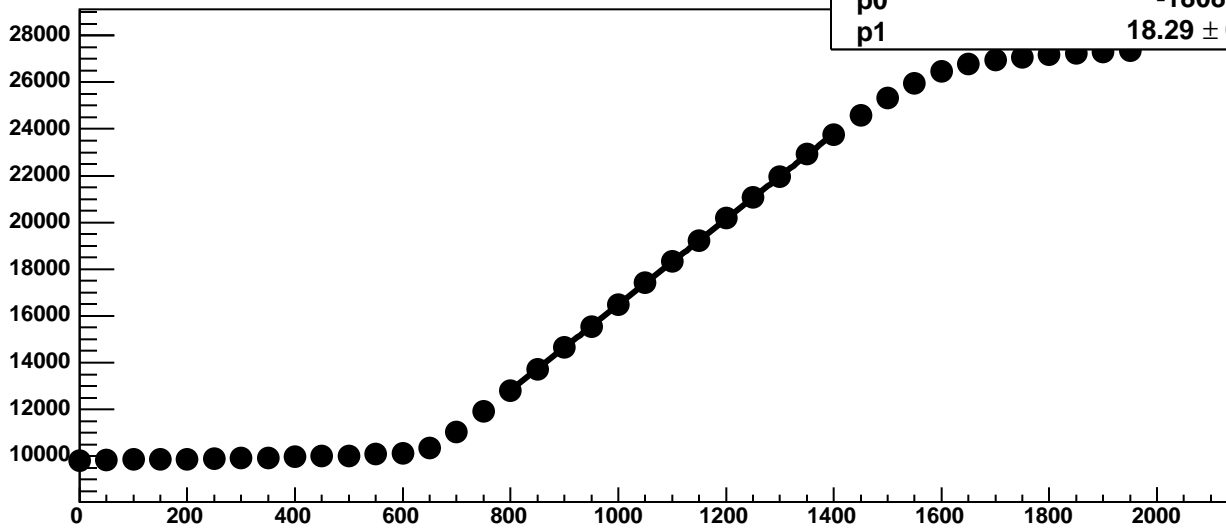
Chip 3, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC



Chip 3, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC

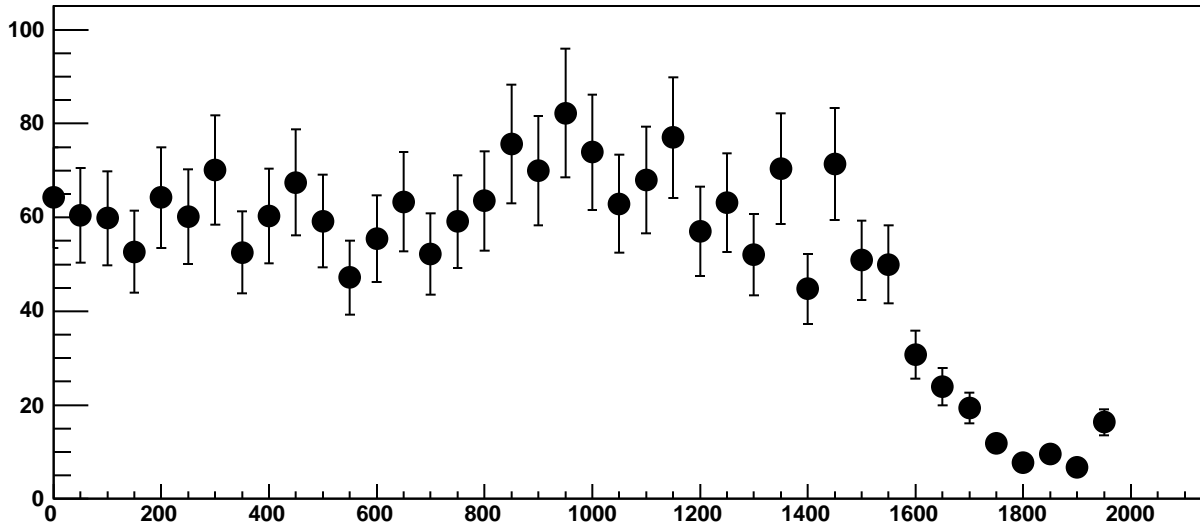


Chip 3, Channel 9, Enable 3!, Hold=30, ADC Mean vs DAC

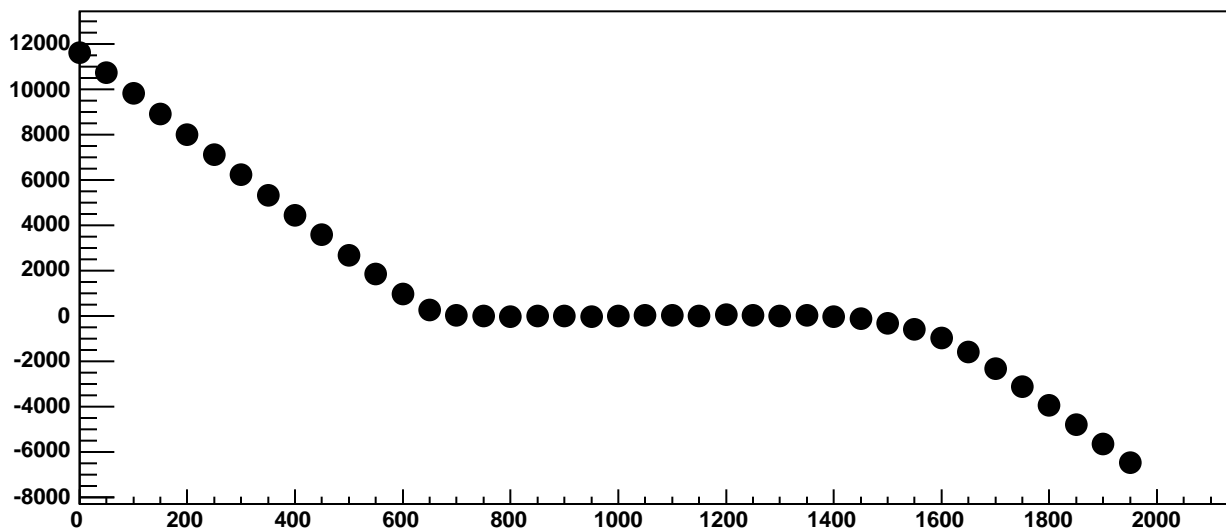


$\chi^2 / \text{ndf}$  51.07 / 11  
p0  $-1808 \pm 24.03$   
p1  $18.29 \pm 0.02072$

Chip 3, Channel 9, Enable 3!, Hold=30, ADC Noise vs DAC



Chip 3, Channel 9, Enable 3!, Hold=30, ADC Residuals vs DAC



Chip 3, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

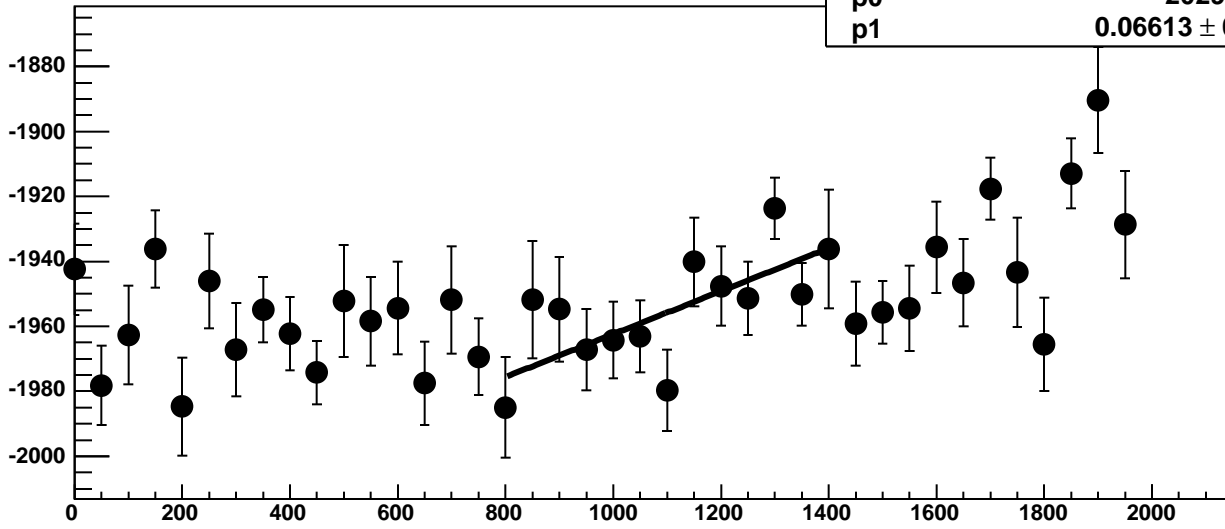
12.63 / 11

p0

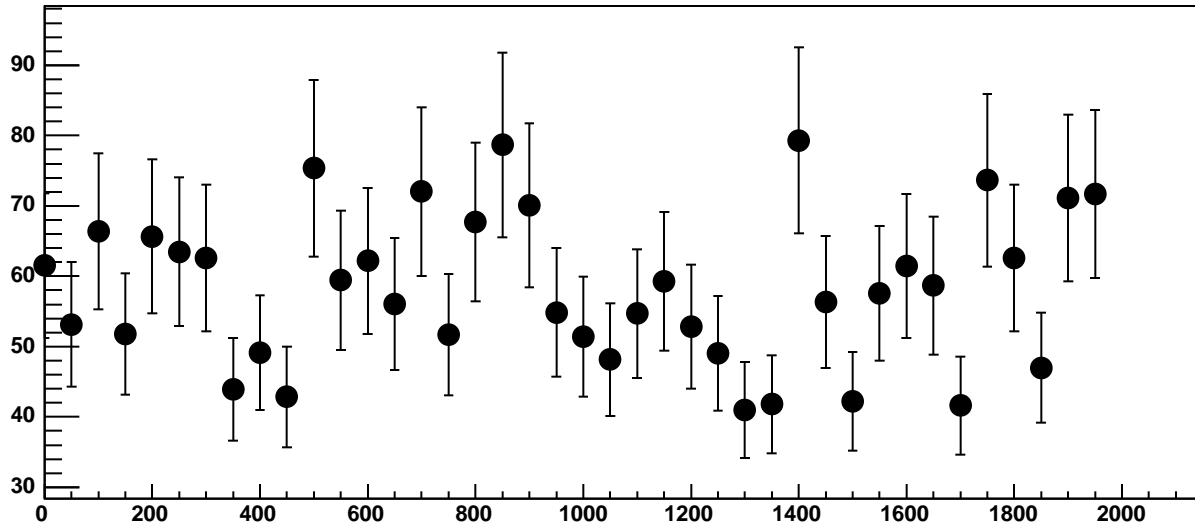
$-2029 \pm 23.01$

p1

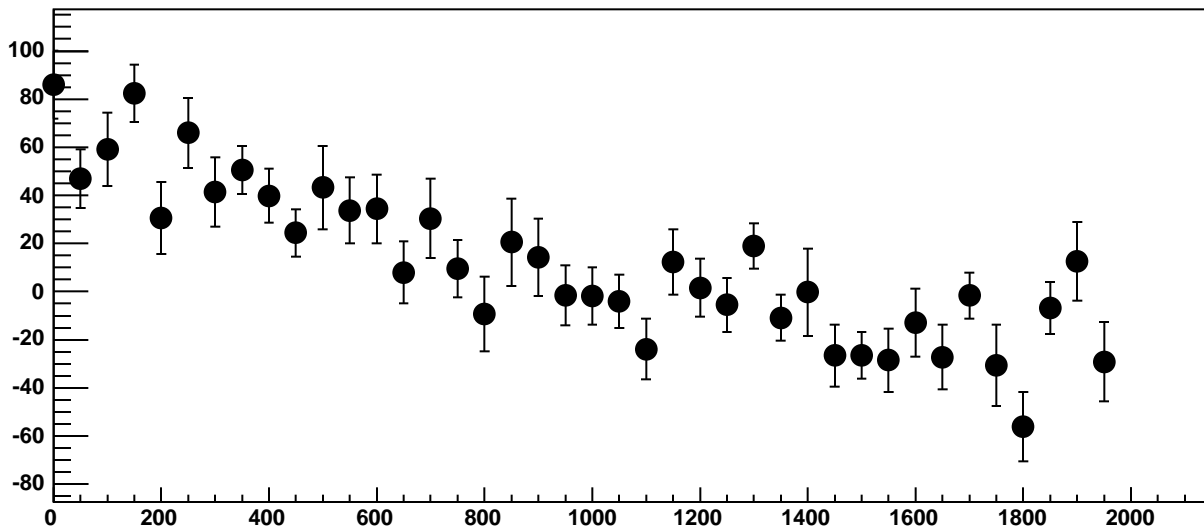
$0.06613 \pm 0.02001$



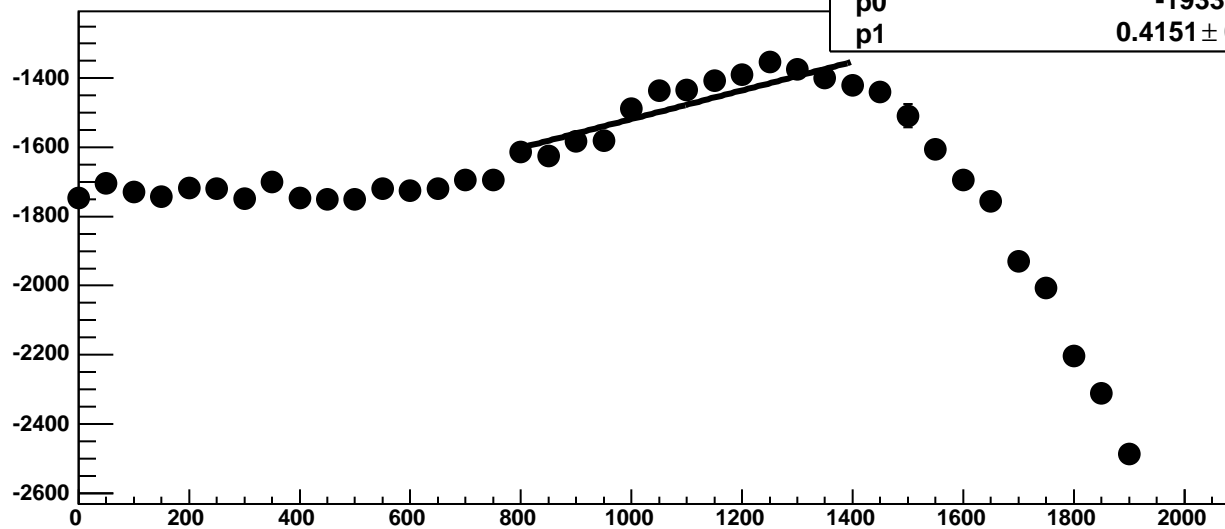
Chip 3, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC



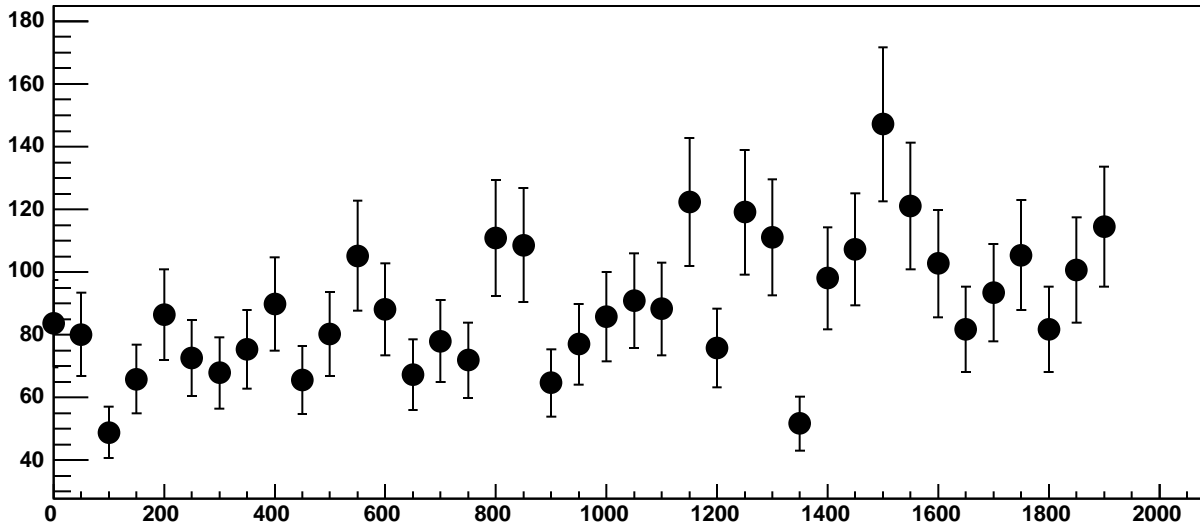
Chip 3, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC



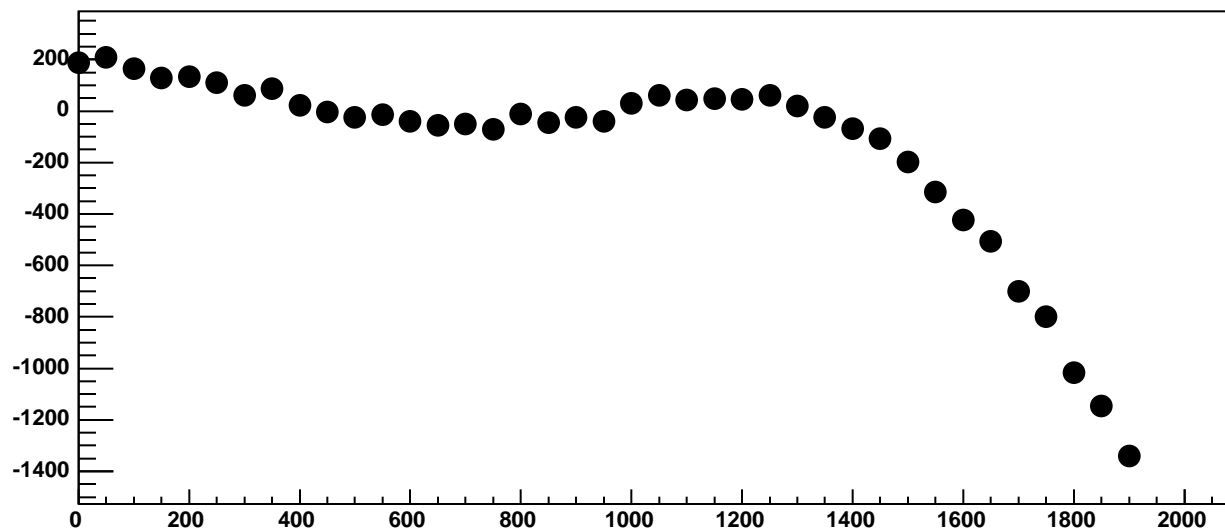
Chip 3, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC



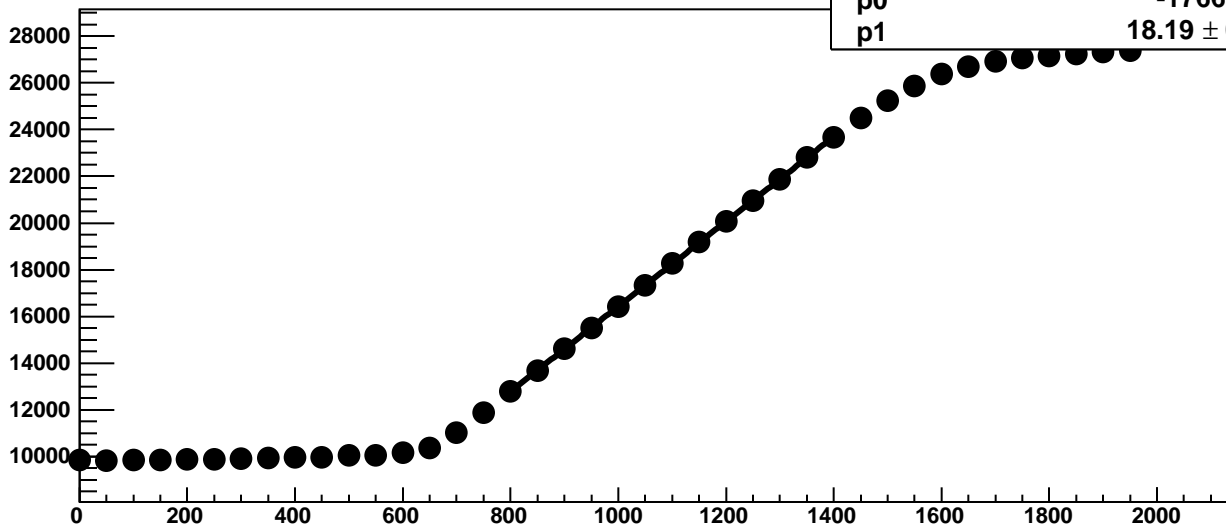
Chip 3, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC



Chip 3, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC

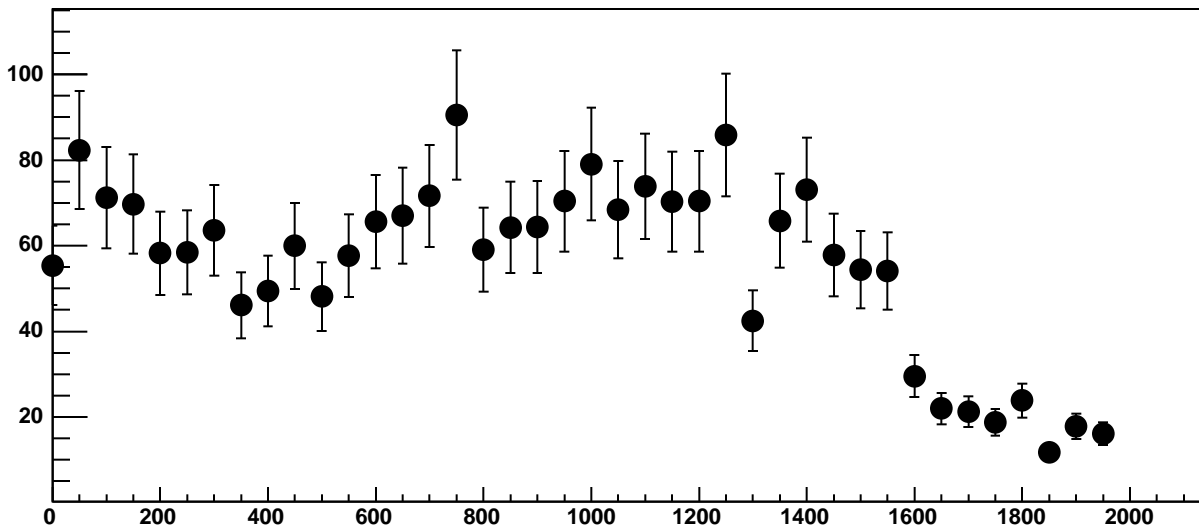


Chip 3, Channel 10, Enable 0!, Hold=30, ADC Mean vs DAC

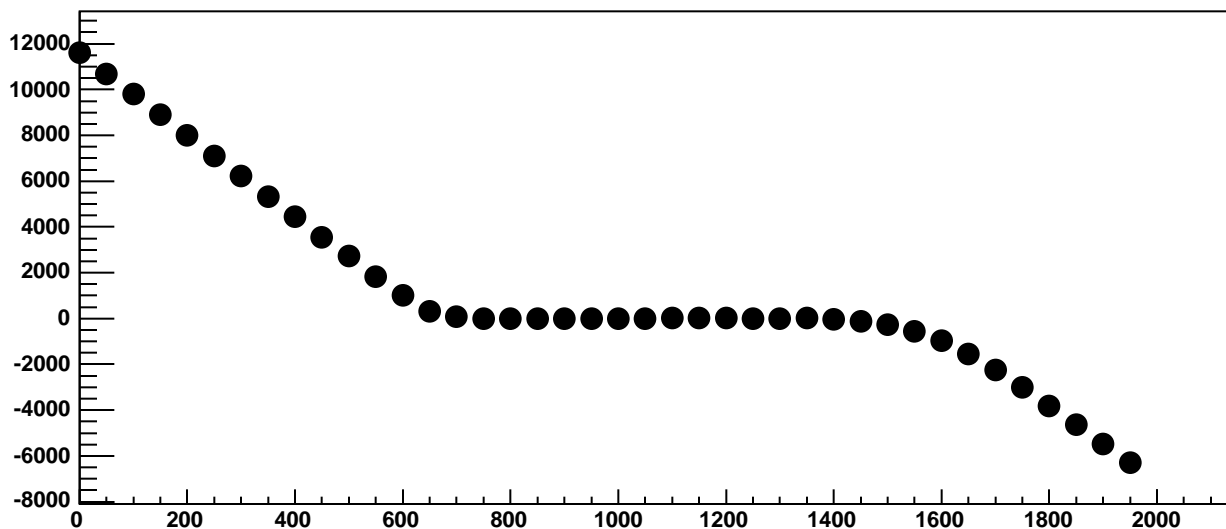


$\chi^2 / \text{ndf}$  12.83 / 11  
p0  $-1766 \pm 23.95$   
p1  $18.19 \pm 0.02129$

Chip 3, Channel 10, Enable 0!, Hold=30, ADC Noise vs DAC

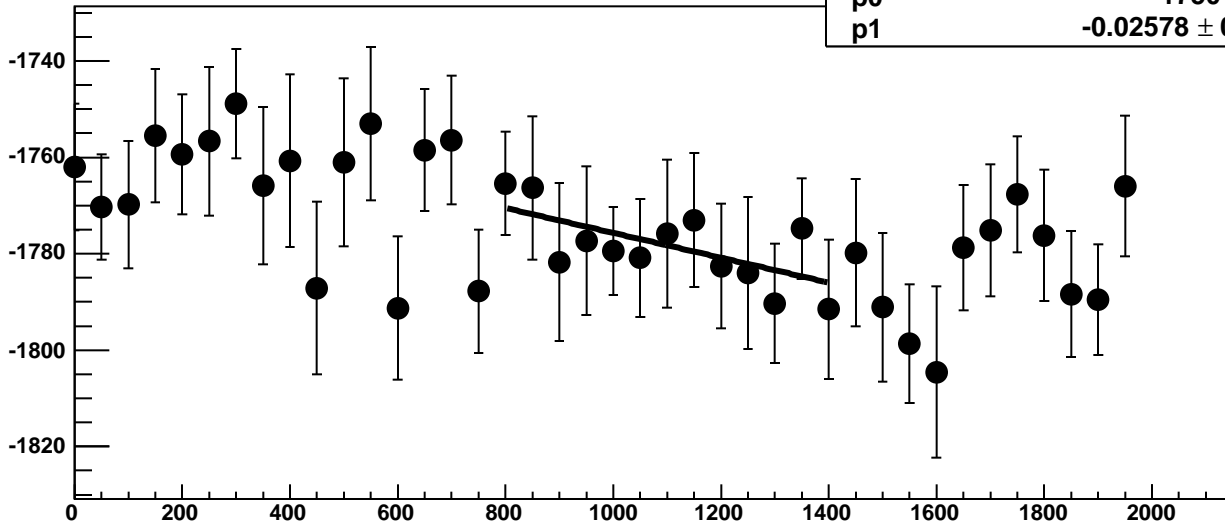


Chip 3, Channel 10, Enable 0!, Hold=30, ADC Residuals vs DAC

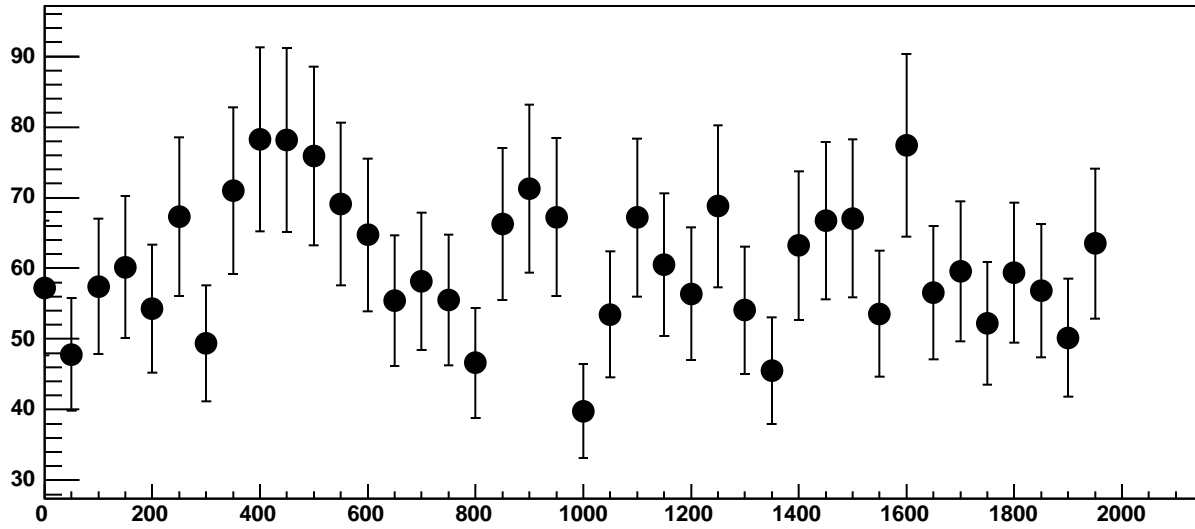


Chip 3, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC

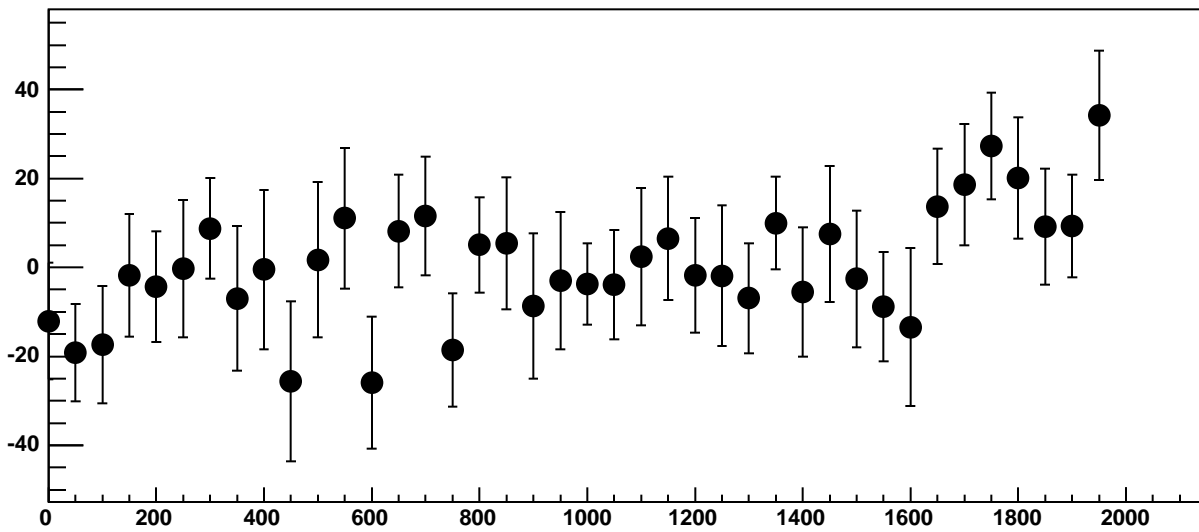
$\chi^2 / \text{ndf}$  2.591 / 11  
p0  $-1750 \pm 20.77$   
p1  $-0.02578 \pm 0.01865$



Chip 3, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC

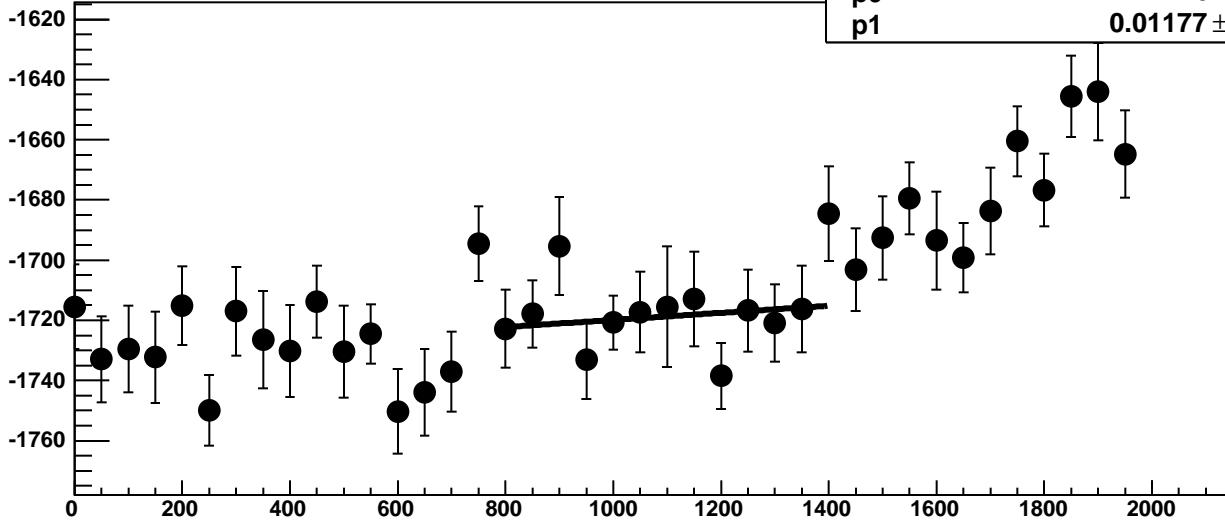


Chip 3, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC

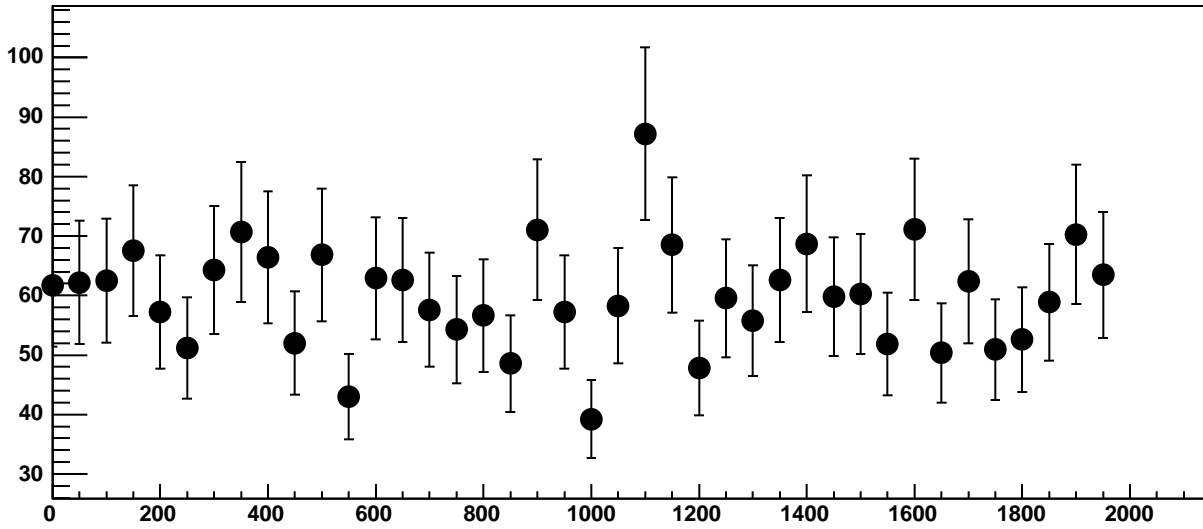




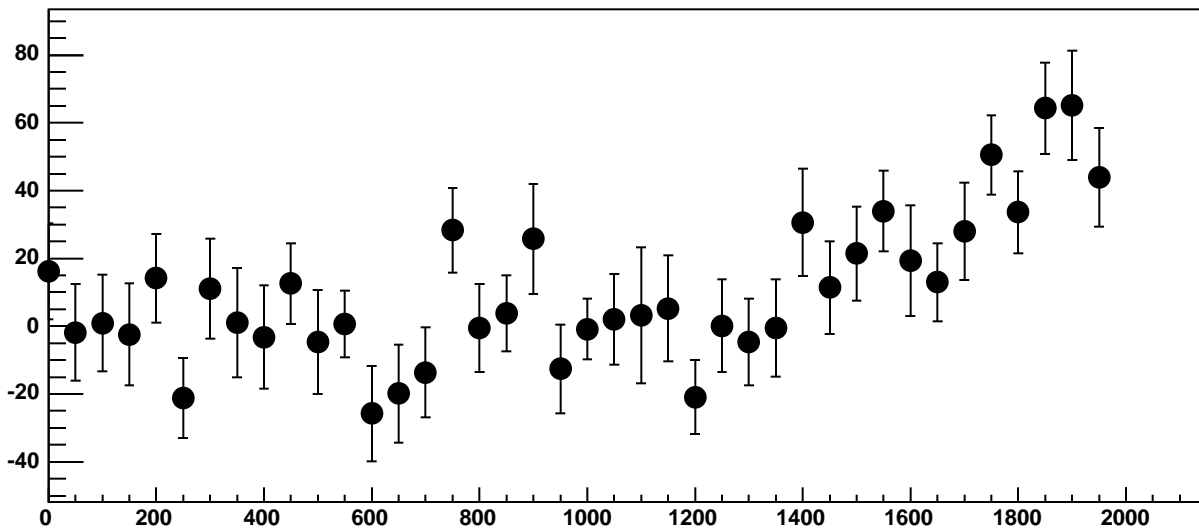
Chip 3, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC



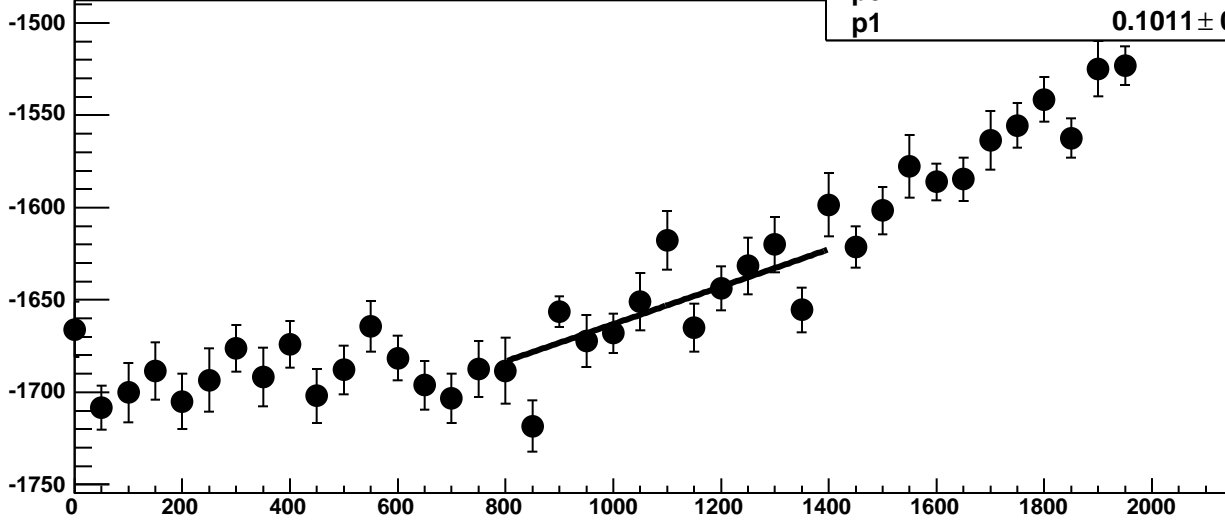
Chip 3, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC



Chip 3, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC

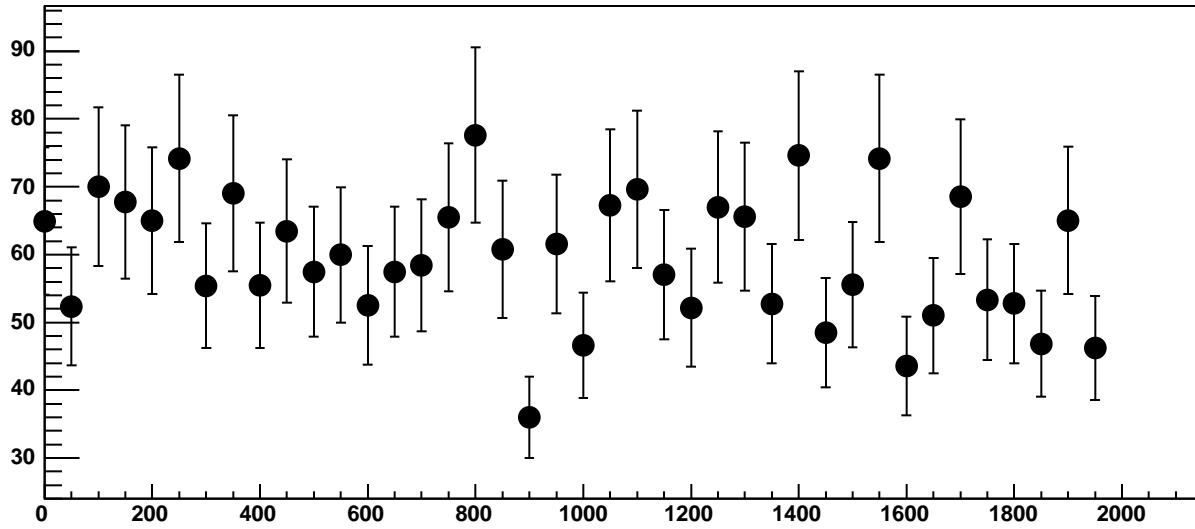


Chip 3, Channel 10, Enable 3, Hold=30, ADC Mean vs DAC

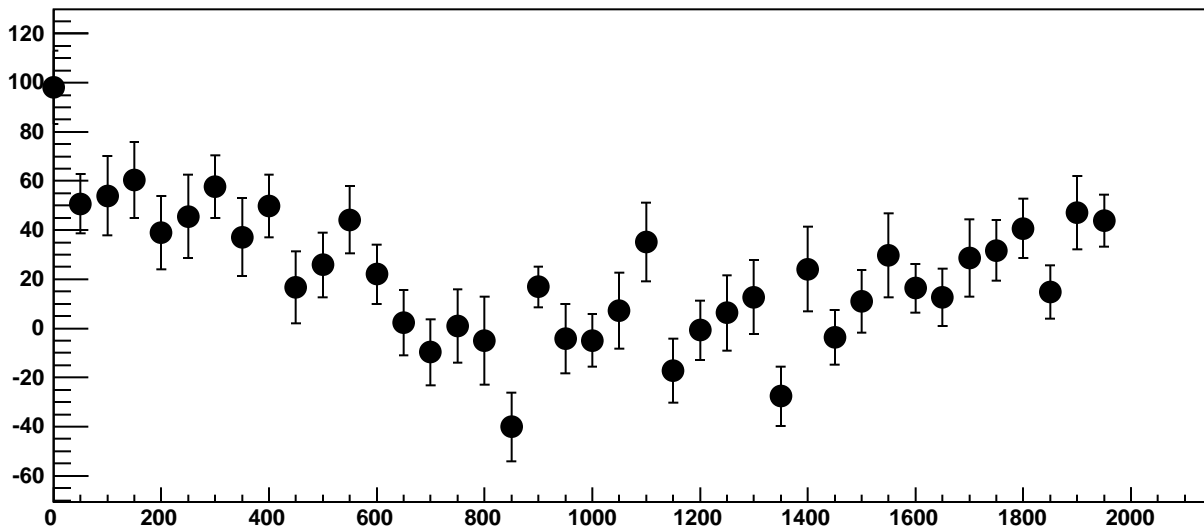


$\chi^2 / \text{ndf}$  27.68 / 11  
p0  $-1764 \pm 22.05$   
p1  $0.1011 \pm 0.02019$

Chip 3, Channel 10, Enable 3, Hold=30, ADC Noise vs DAC

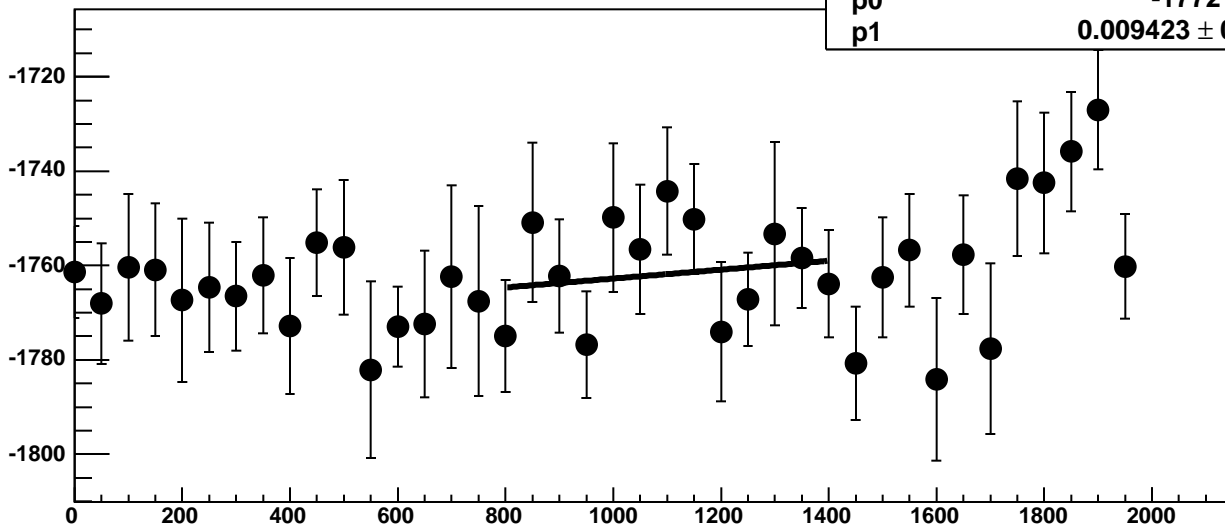


Chip 3, Channel 10, Enable 3, Hold=30, ADC Residuals vs DAC

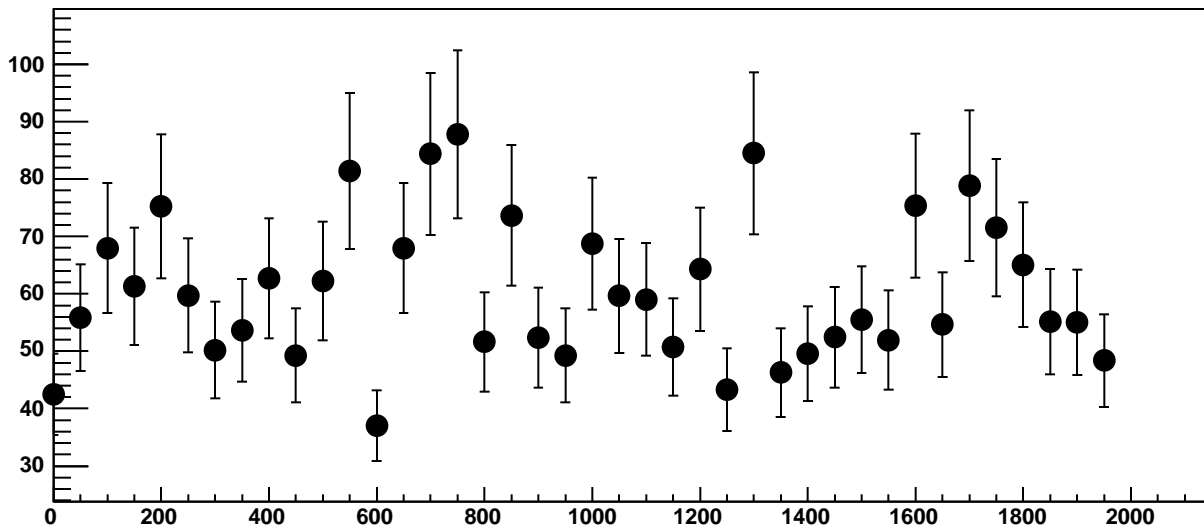


Chip 3, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC

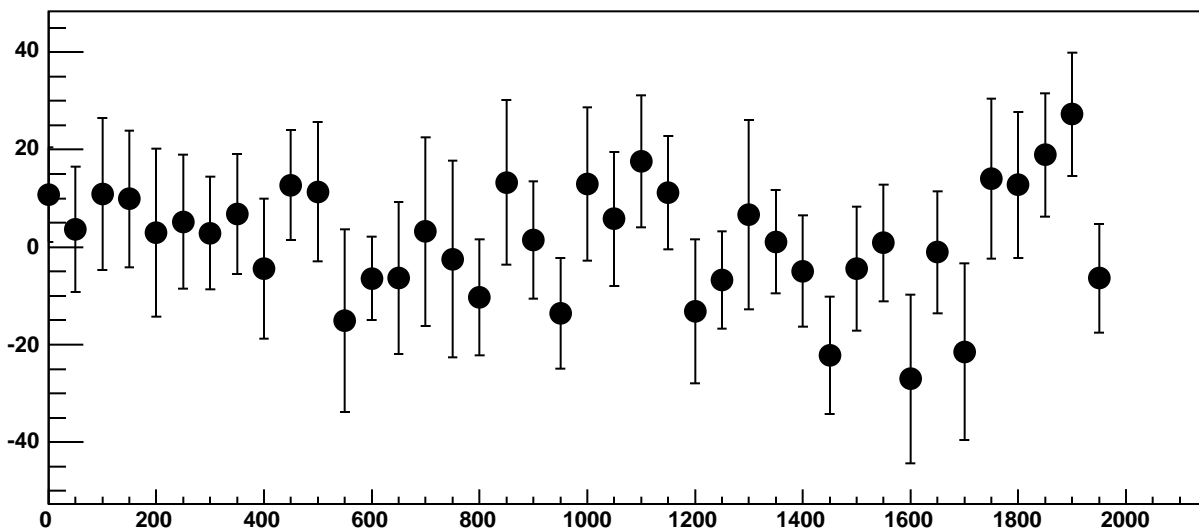
$\chi^2 / \text{ndf}$  7.865 / 11  
p0  $-1772 \pm 20.65$   
p1  $0.009423 \pm 0.01826$



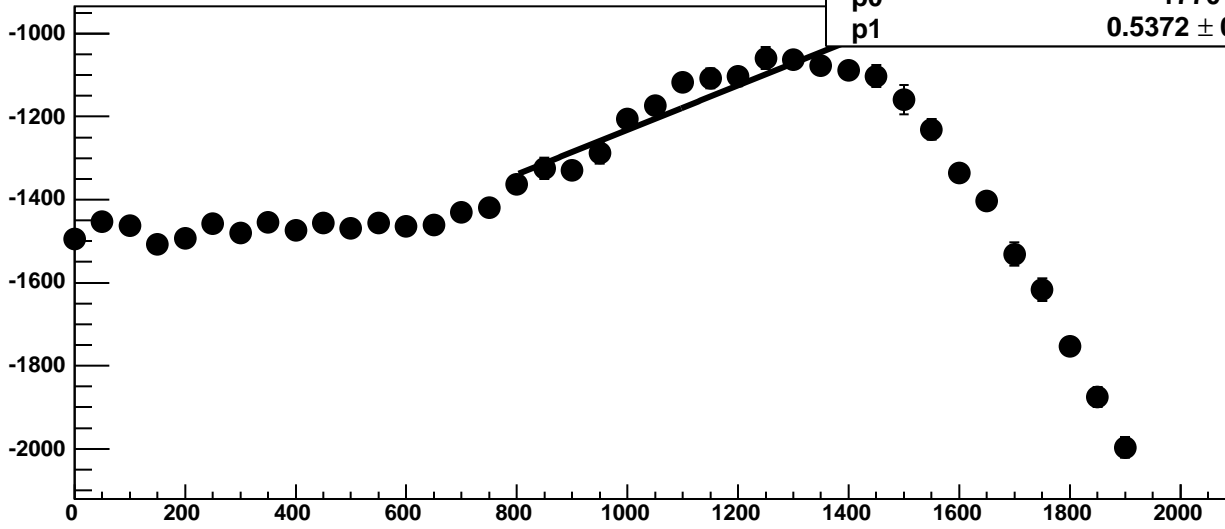
Chip 3, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC



Chip 3, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC



Chip 3, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

37.62 / 11

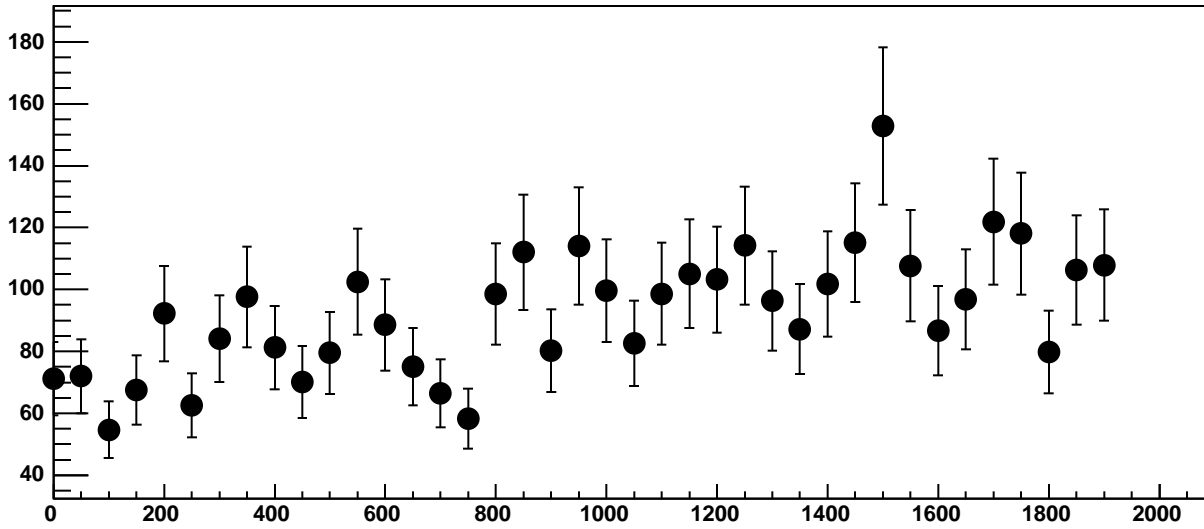
p0

$-1770 \pm 37.02$

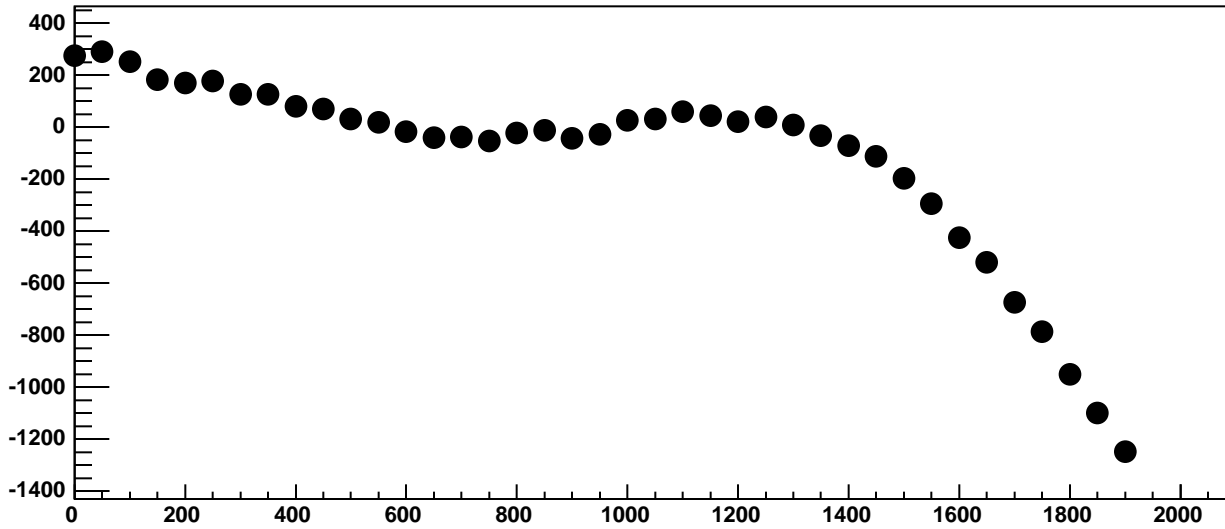
p1

$0.5372 \pm 0.03322$

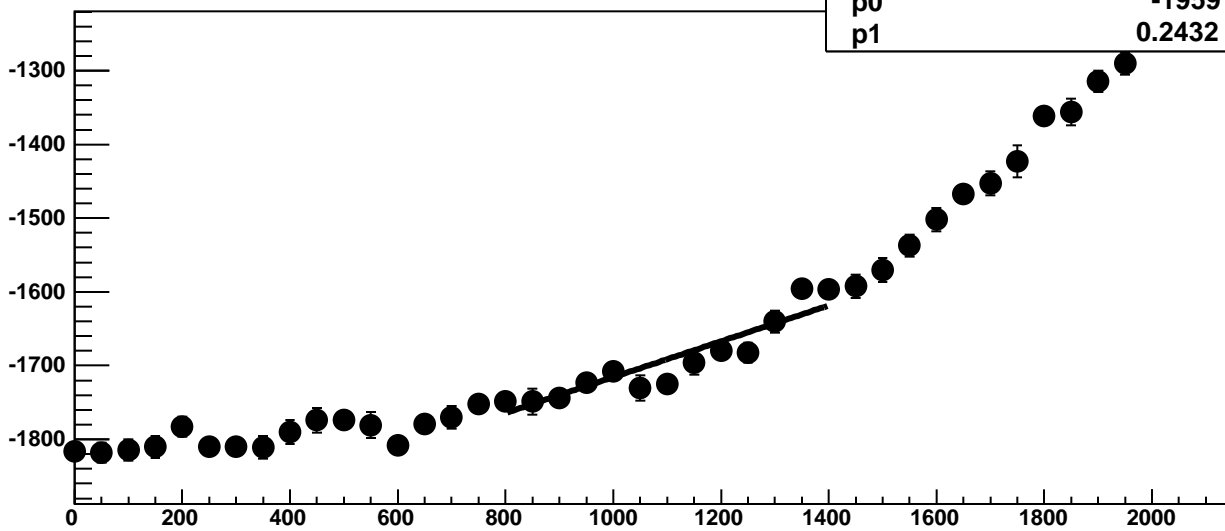
Chip 3, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC



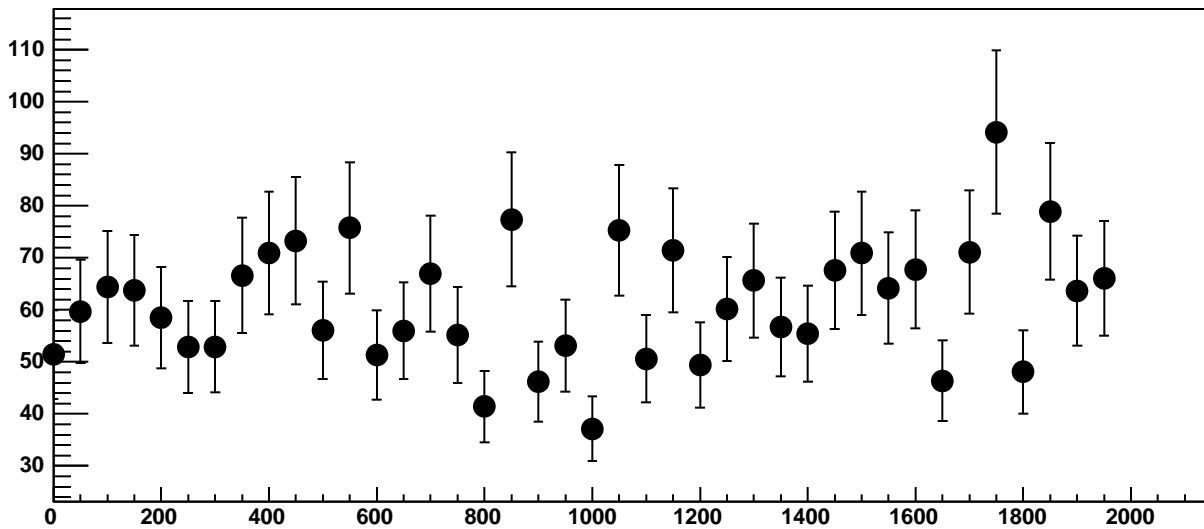
Chip 3, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC



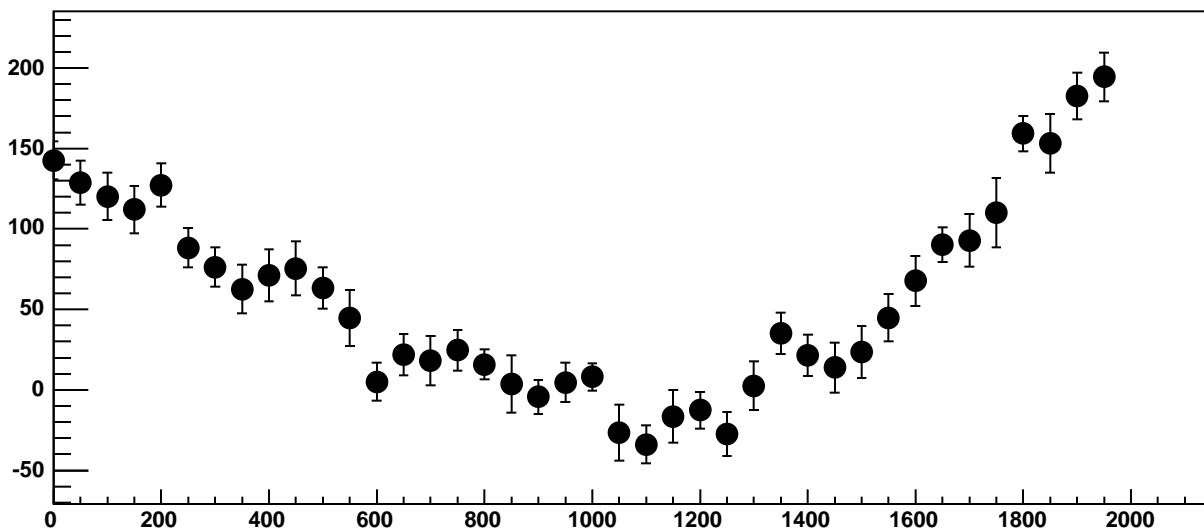
Chip 3, Channel 11, Enable 0, Hold=30, ADC Mean vs DAC



Chip 3, Channel 11, Enable 0, Hold=30, ADC Noise vs DAC



Chip 3, Channel 11, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 3, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

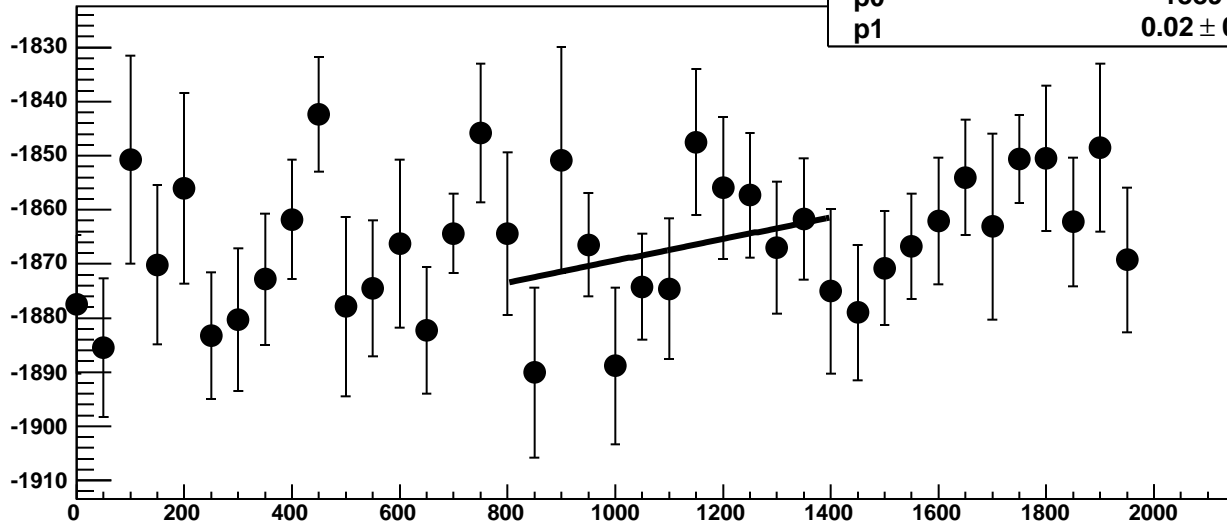
8.985 / 11

p0

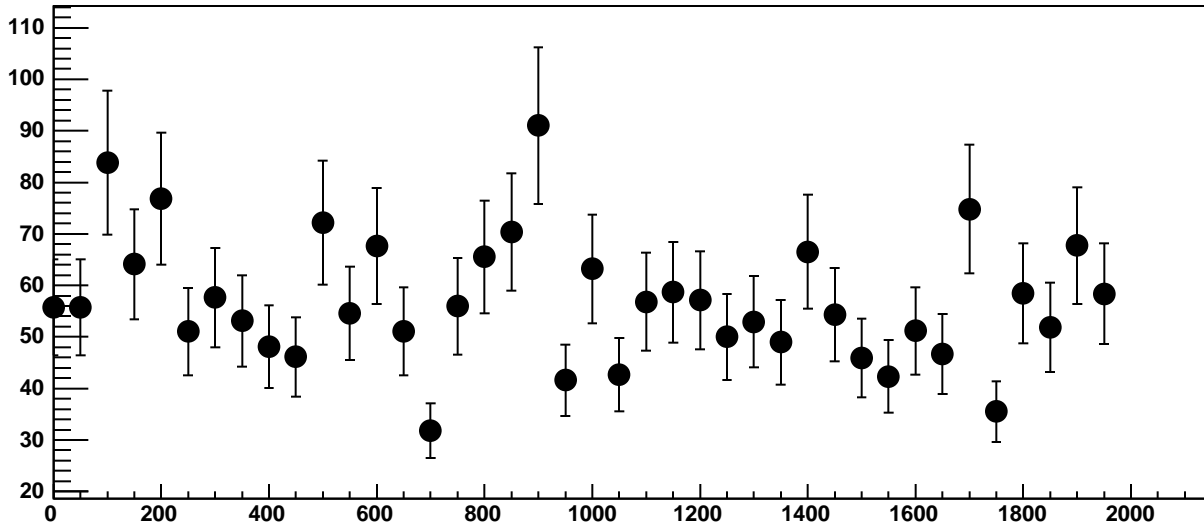
$-1889 \pm 22.84$

p1

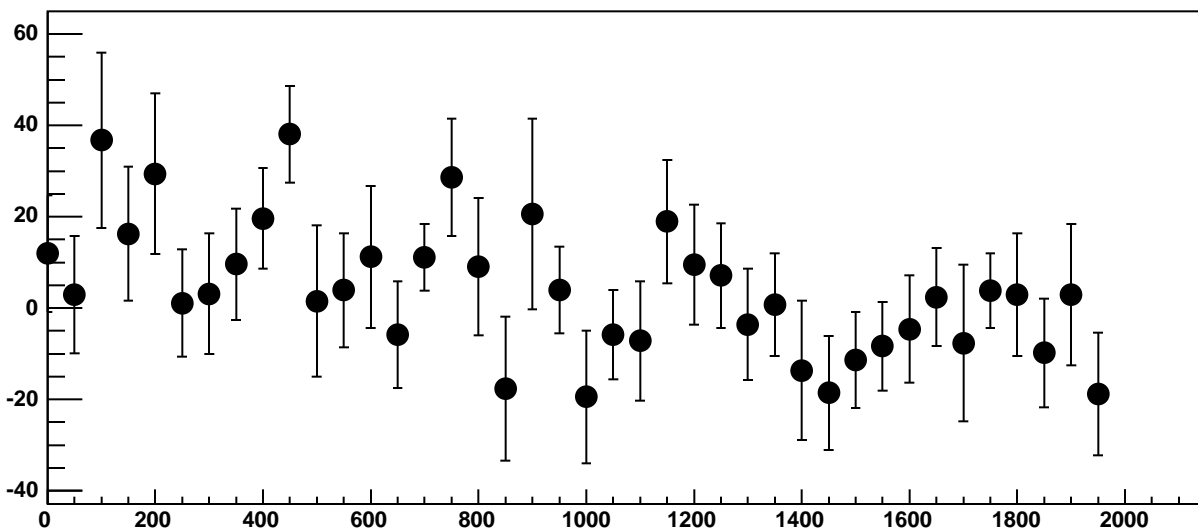
$0.02 \pm 0.02024$



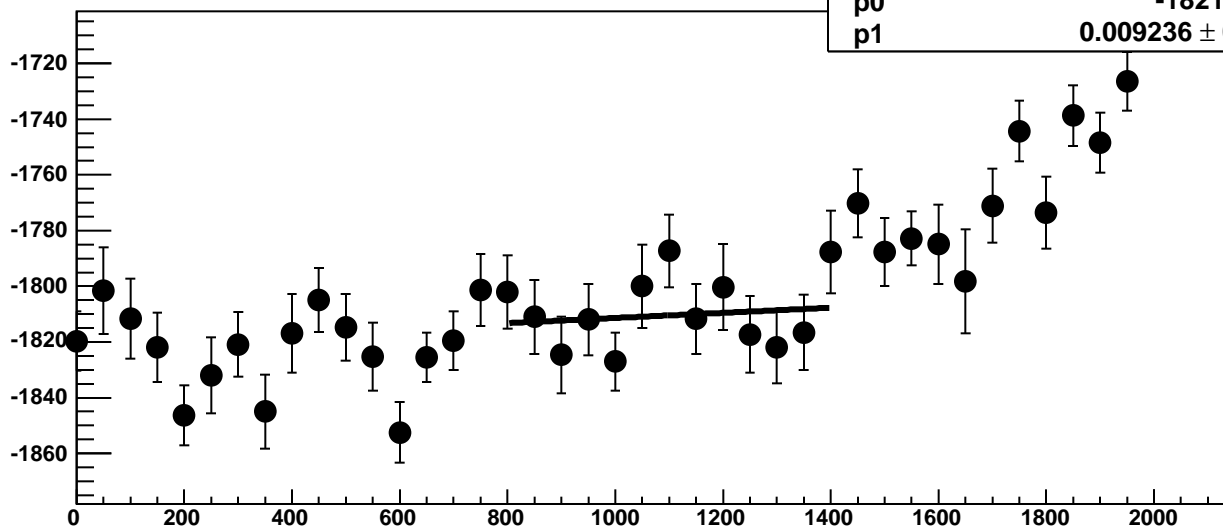
Chip 3, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC



Chip 3, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC

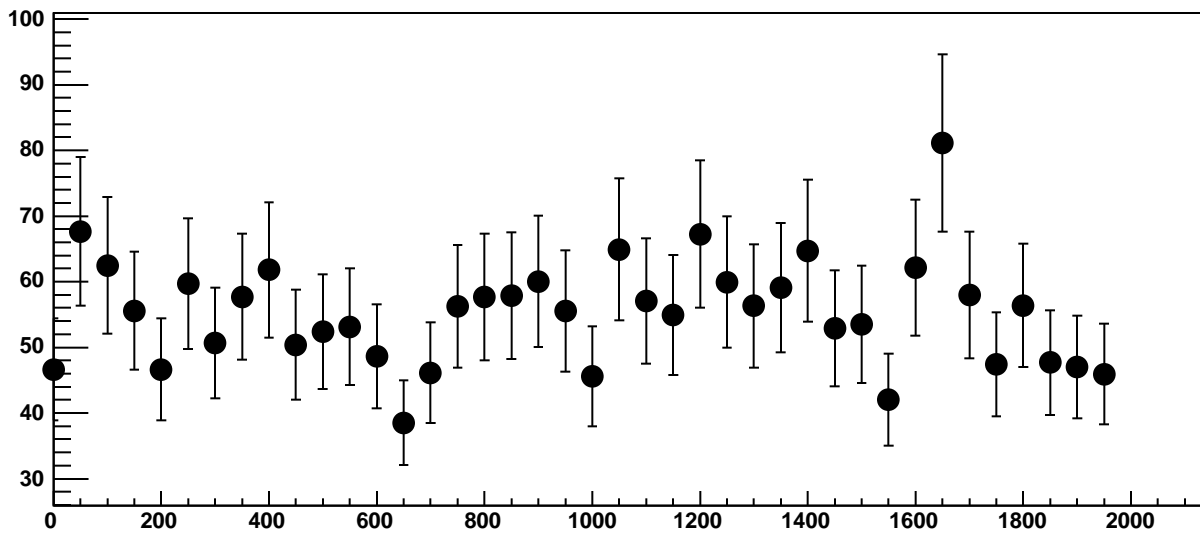


Chip 3, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC

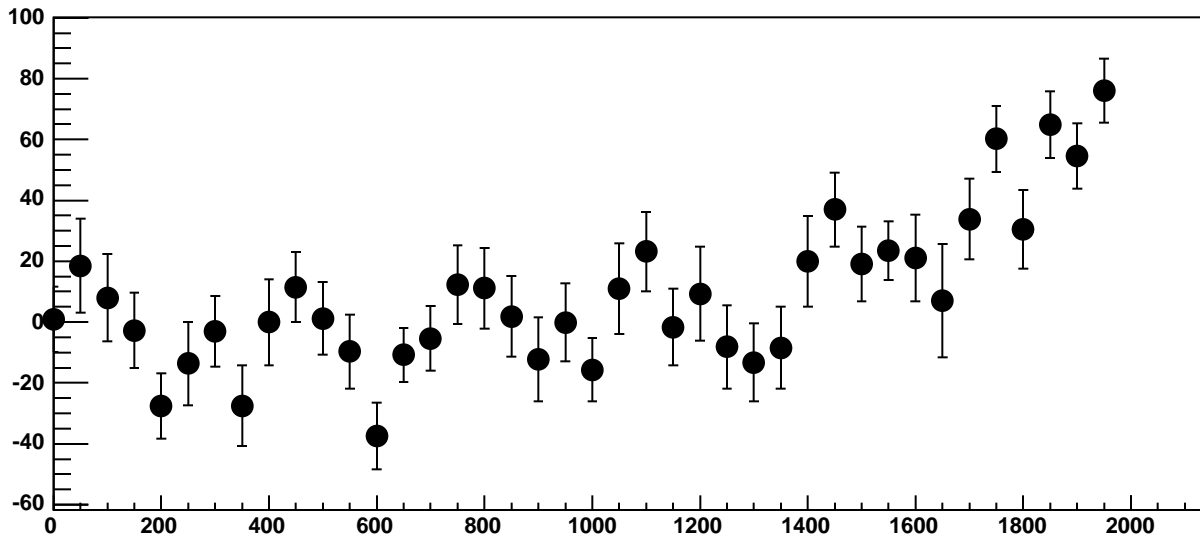


$\chi^2 / \text{ndf}$  11.43 / 11  
p0  $-1821 \pm 22.19$   
p1  $0.009236 \pm 0.02009$

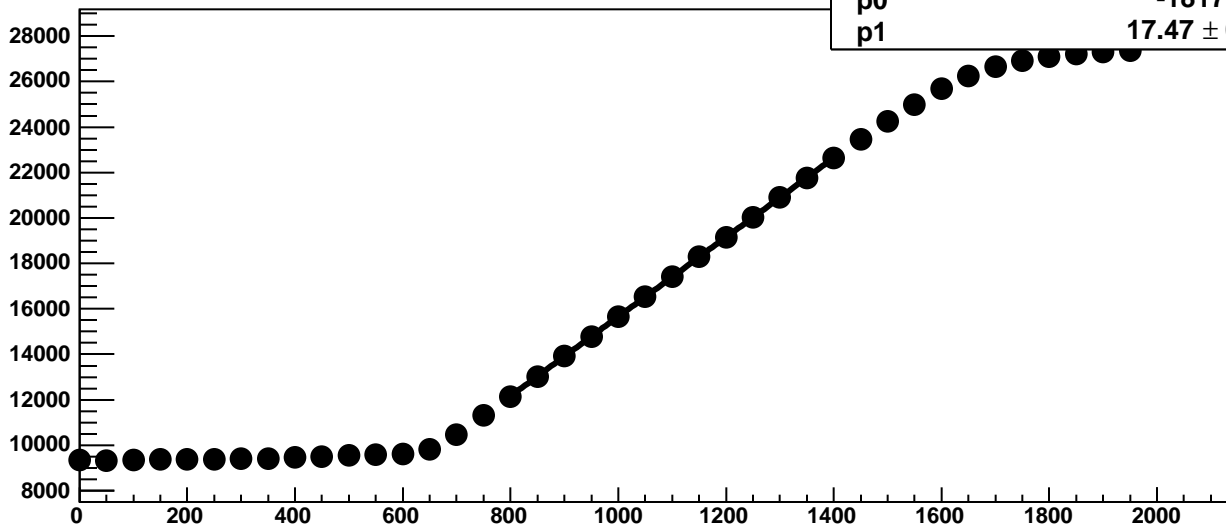
Chip 3, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC



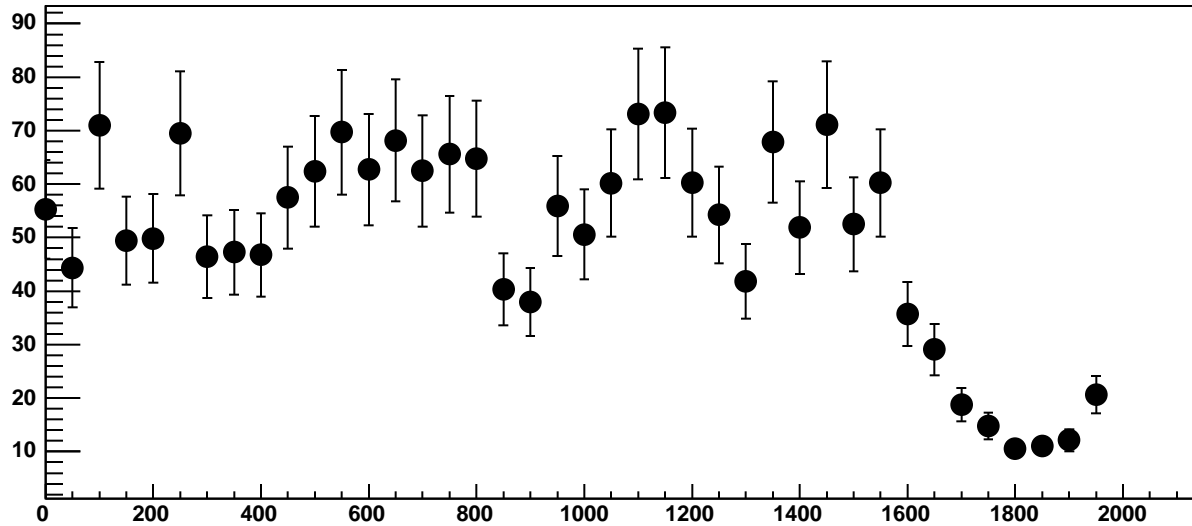
Chip 3, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC



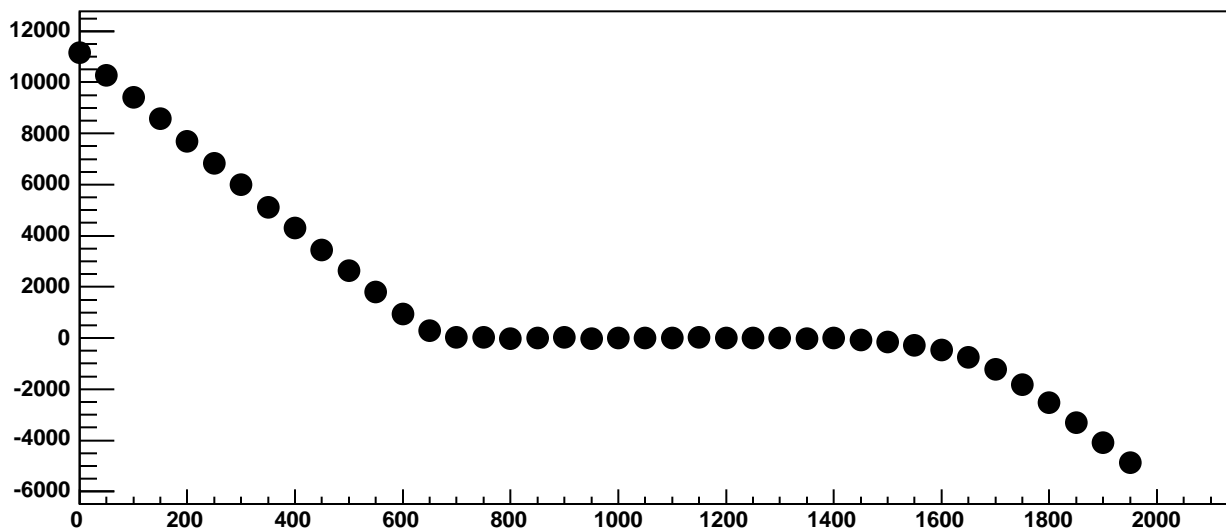
Chip 3, Channel 11, Enable 3!, Hold=30, ADC Mean vs DAC



Chip 3, Channel 11, Enable 3!, Hold=30, ADC Noise vs DAC

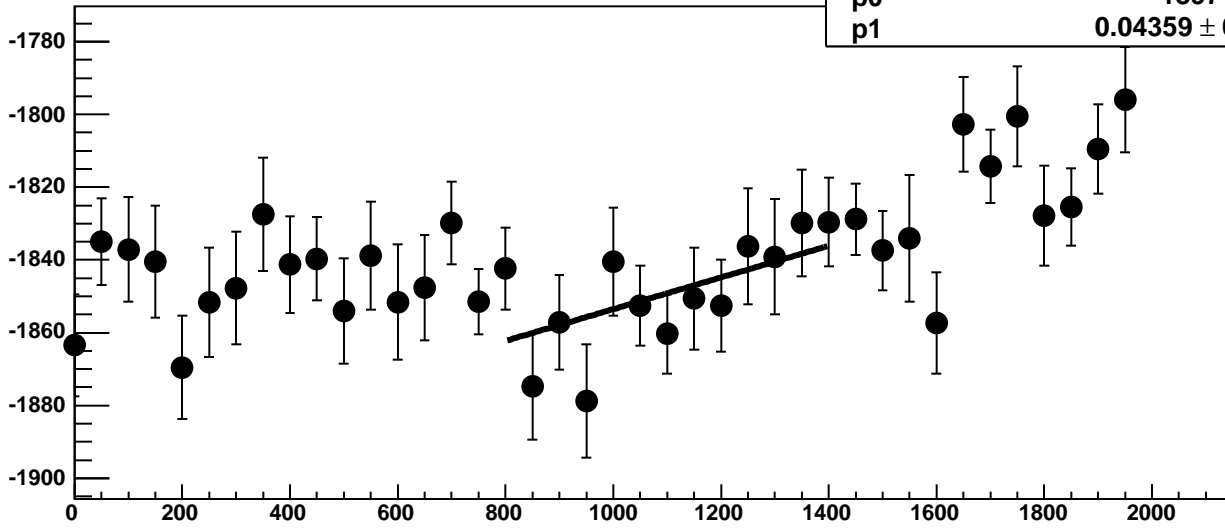


Chip 3, Channel 11, Enable 3!, Hold=30, ADC Residuals vs DAC



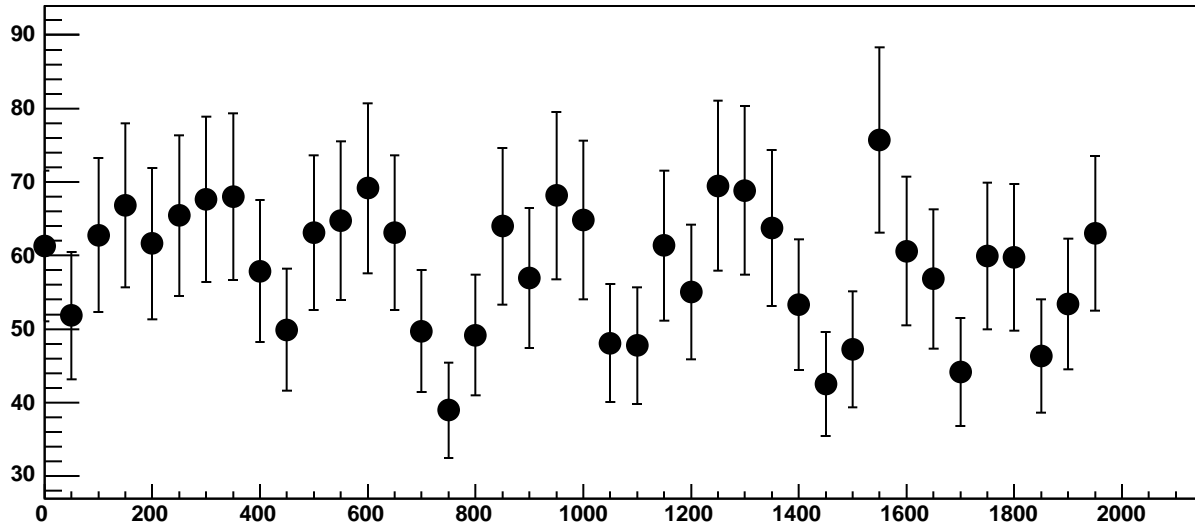


Chip 3, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC

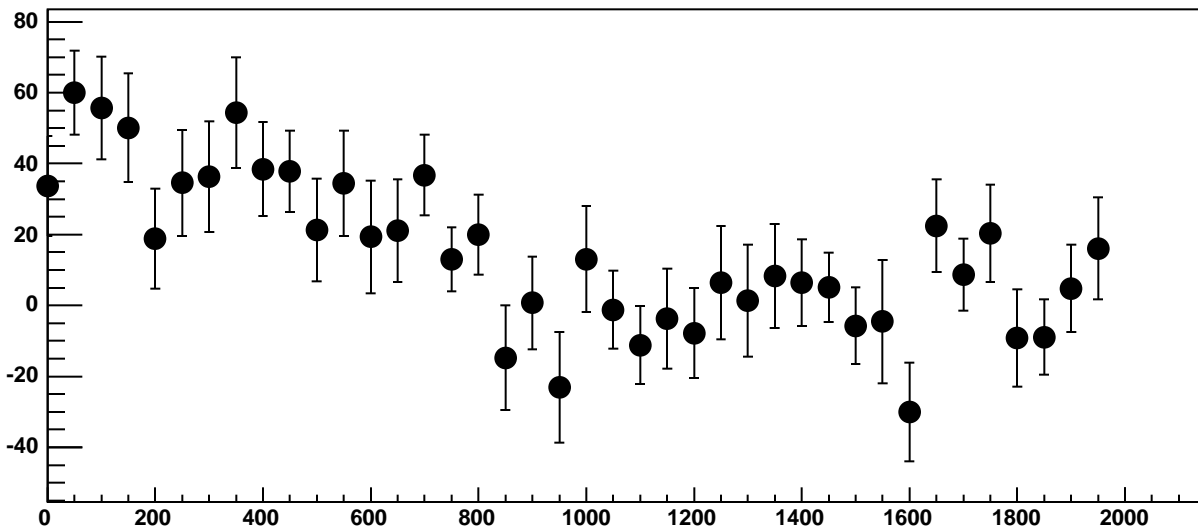


$\chi^2 / \text{ndf}$  9.336 / 11  
p0  $-1897 \pm 21.69$   
p1  $0.04359 \pm 0.01961$

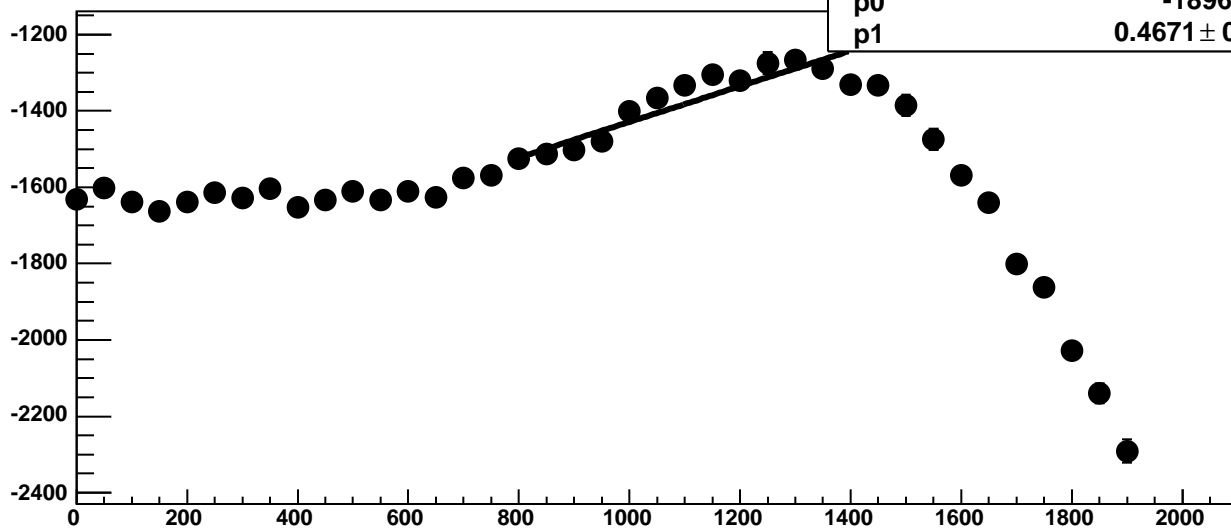
Chip 3, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC



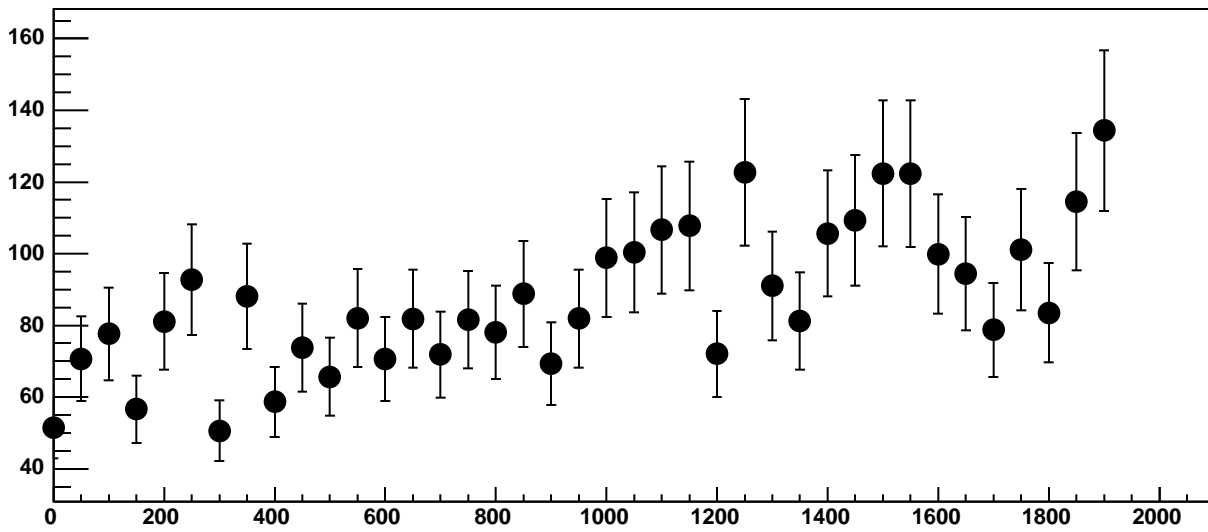
Chip 3, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC



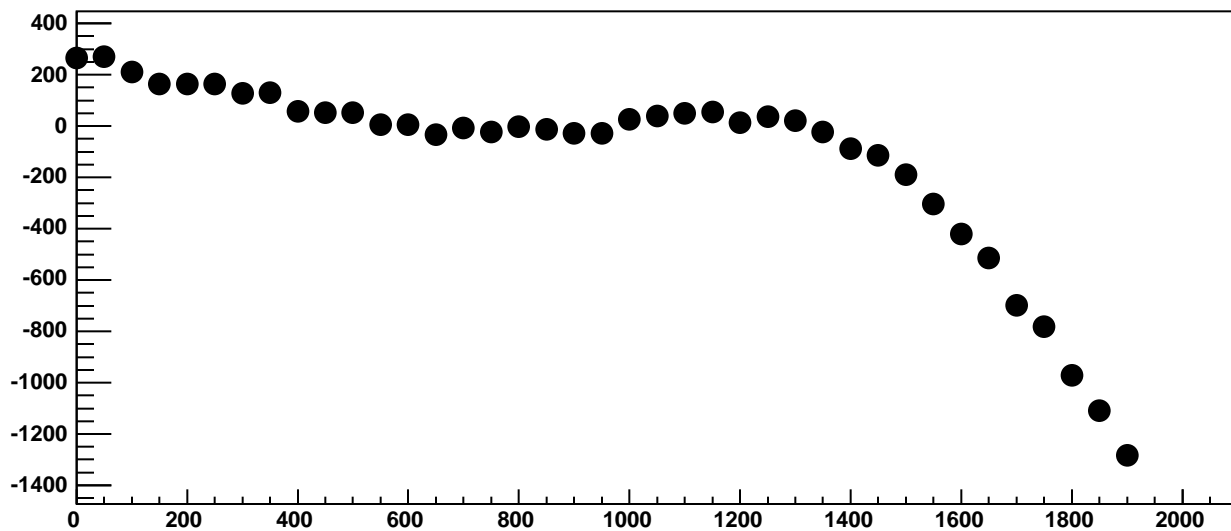
Chip 3, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC



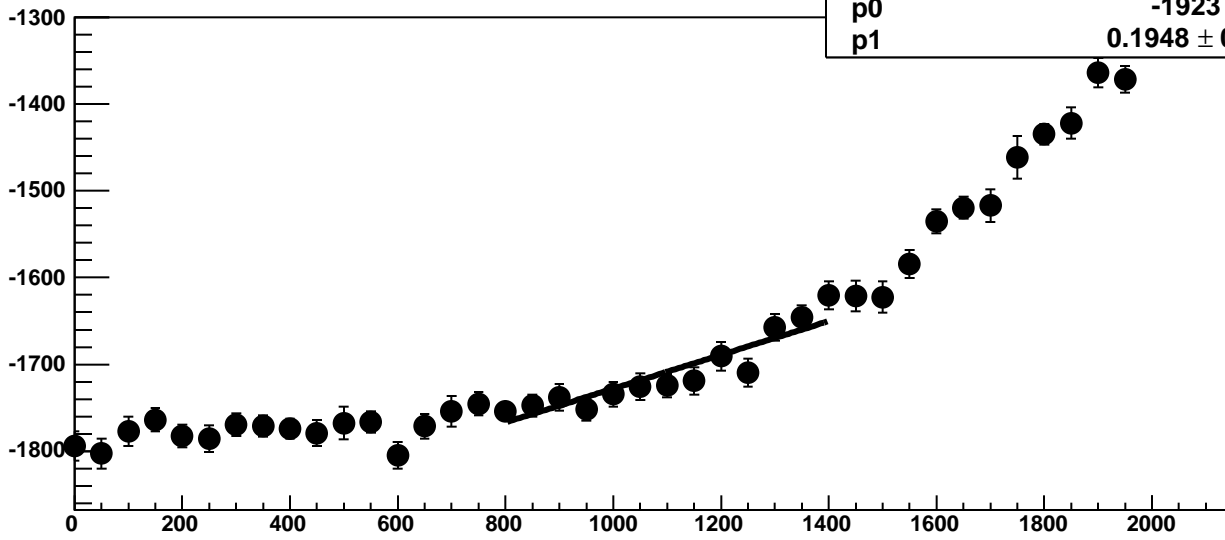
Chip 3, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC



Chip 3, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC

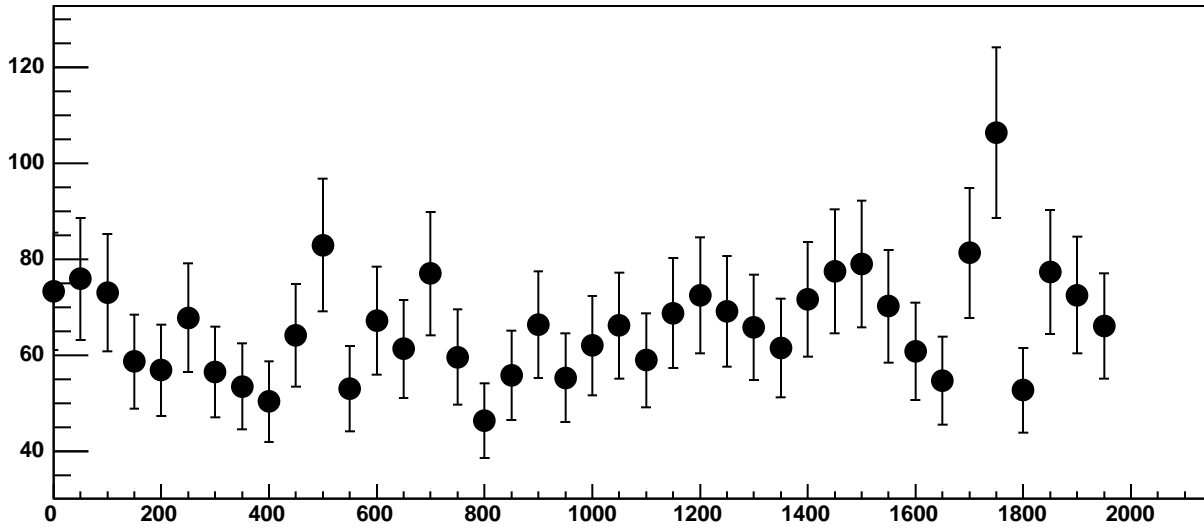


Chip 3, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC

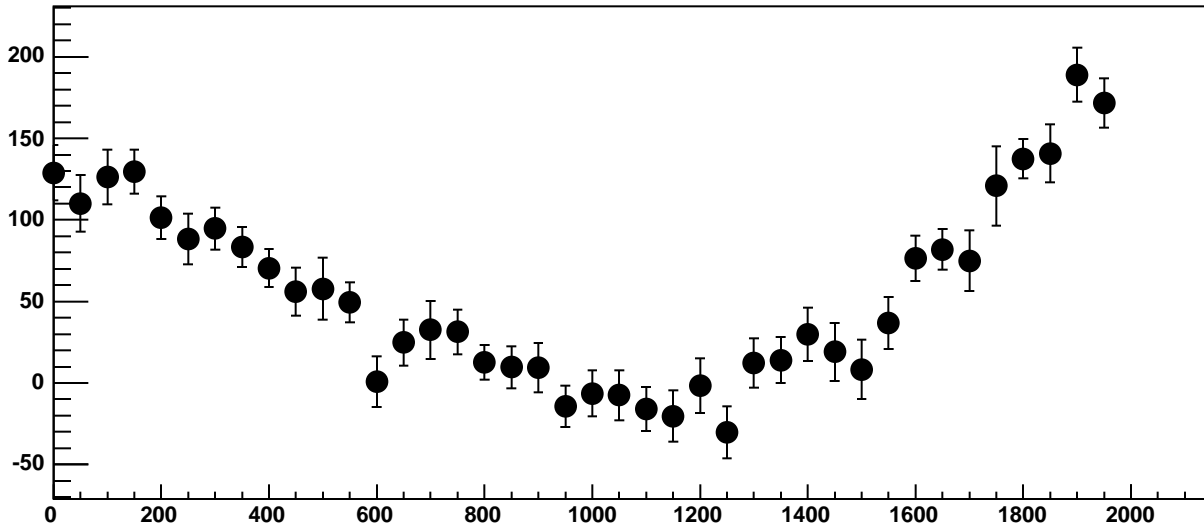


$\chi^2 / \text{ndf}$  15.68 / 11  
p0  $-1923 \pm 21.99$   
p1  $0.1948 \pm 0.02032$

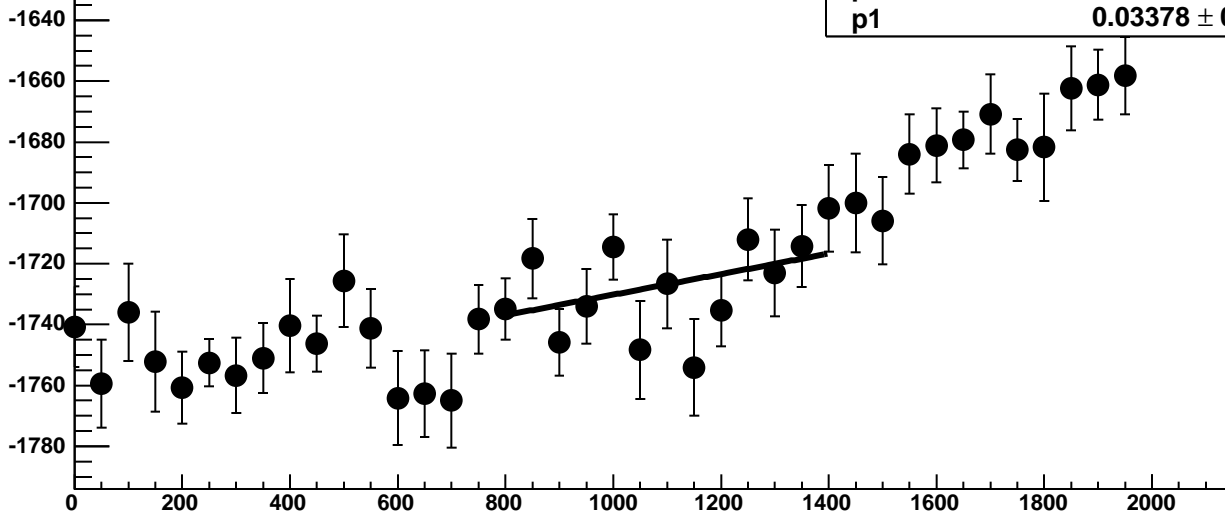
Chip 3, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC



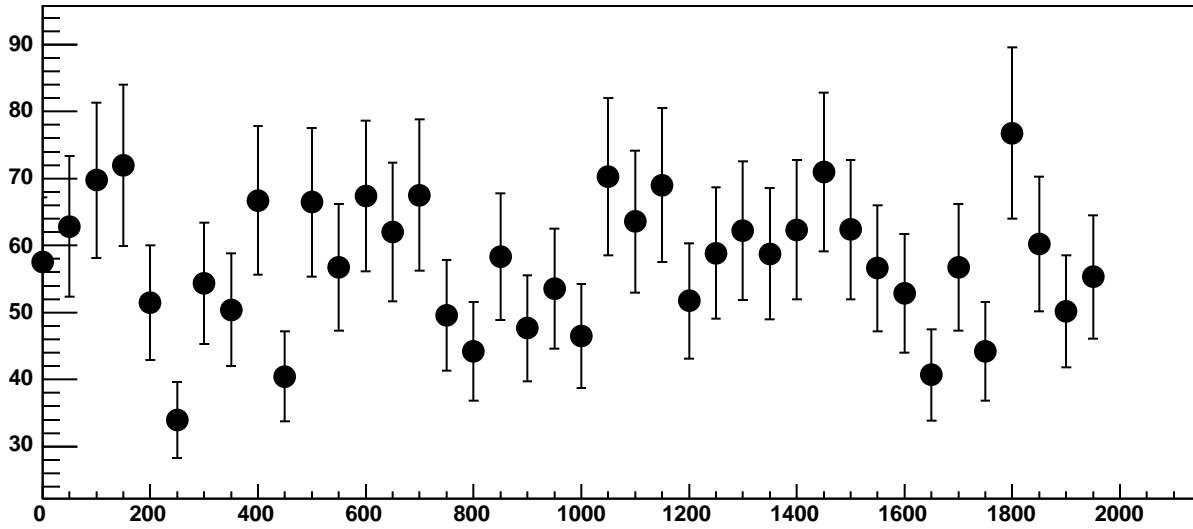
Chip 3, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC



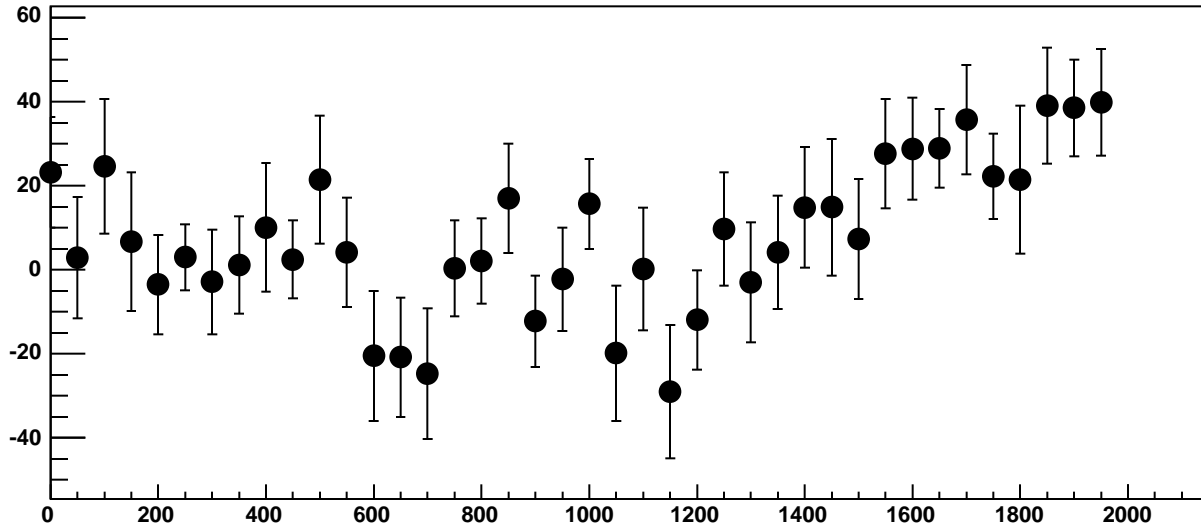
Chip 3, Channel 12, Enable 1, Hold=30, ADC Mean vs DAC



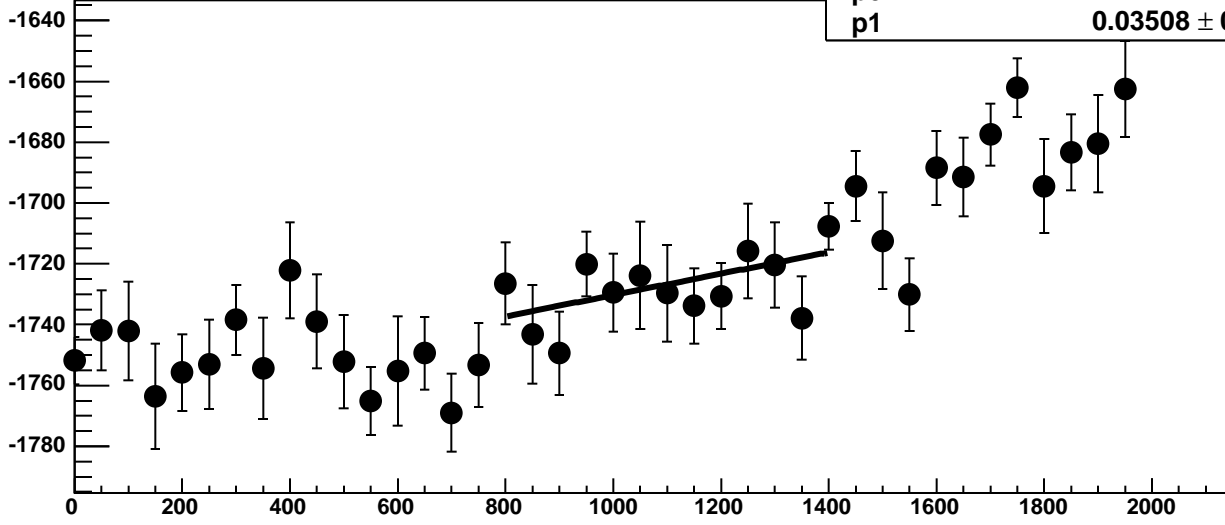
Chip 3, Channel 12, Enable 1, Hold=30, ADC Noise vs DAC



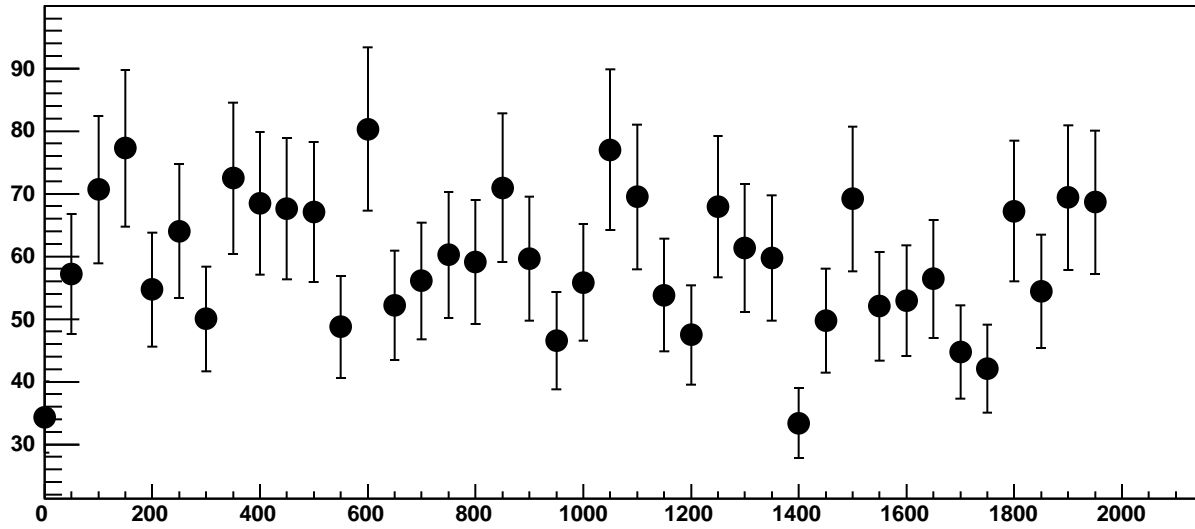
Chip 3, Channel 12, Enable 1, Hold=30, ADC Residuals vs DAC



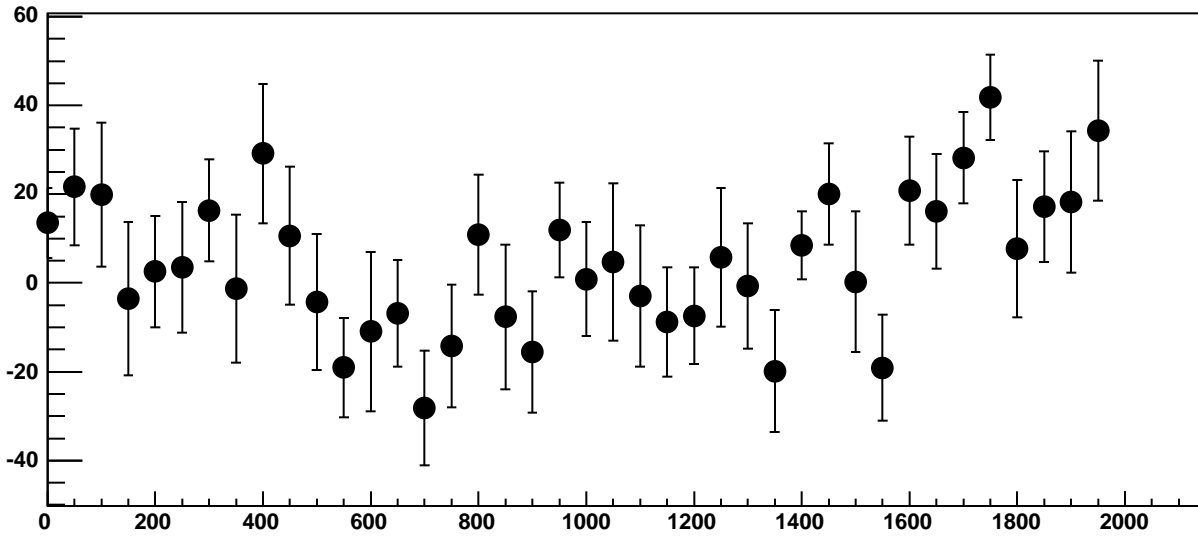
Chip 3, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC



Chip 3, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC



Chip 3, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 3, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

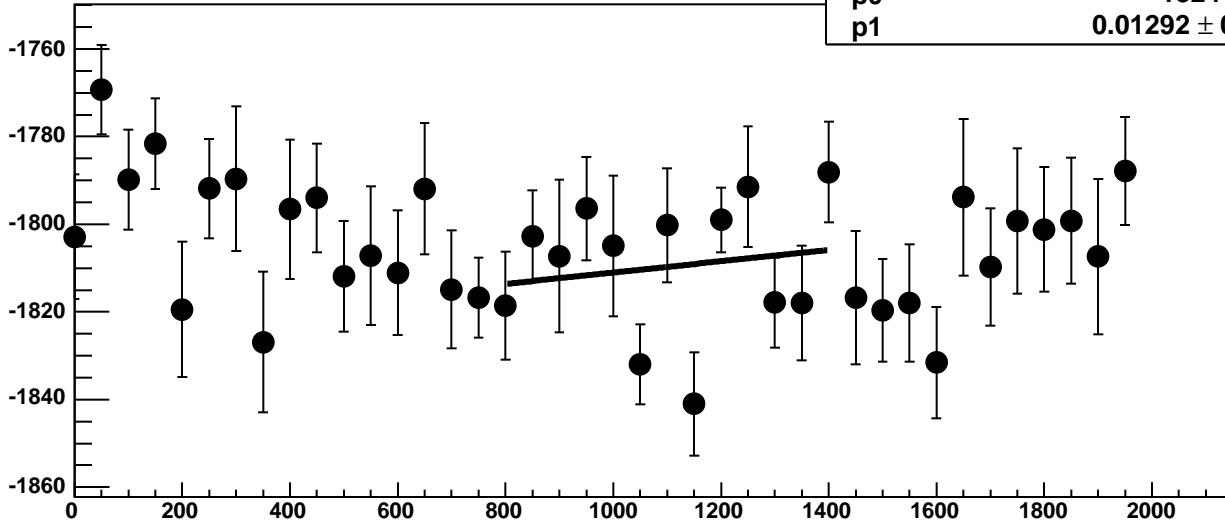
23.72 / 11

p0

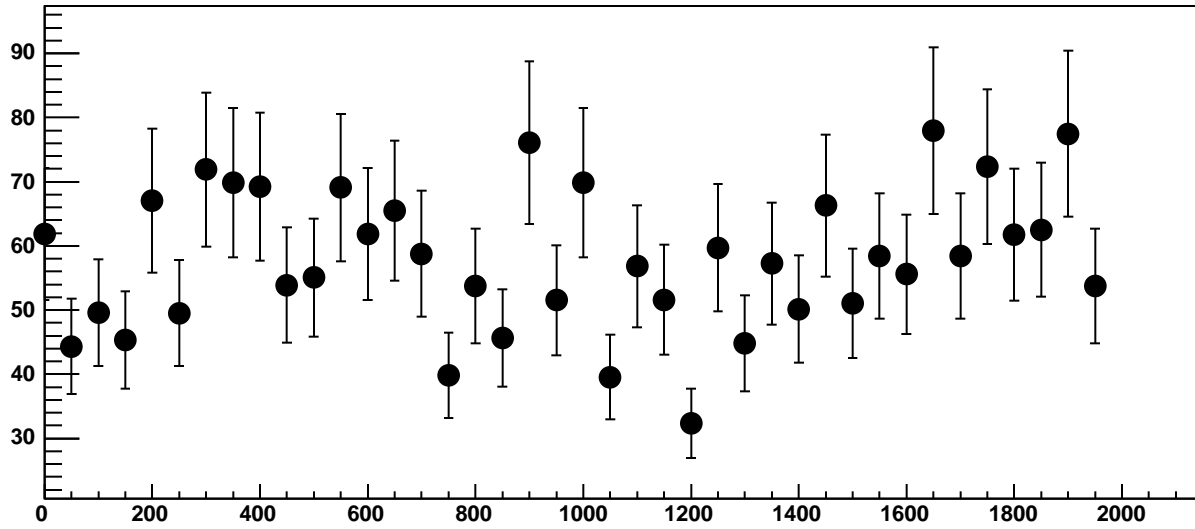
$-1824 \pm 19.86$

p1

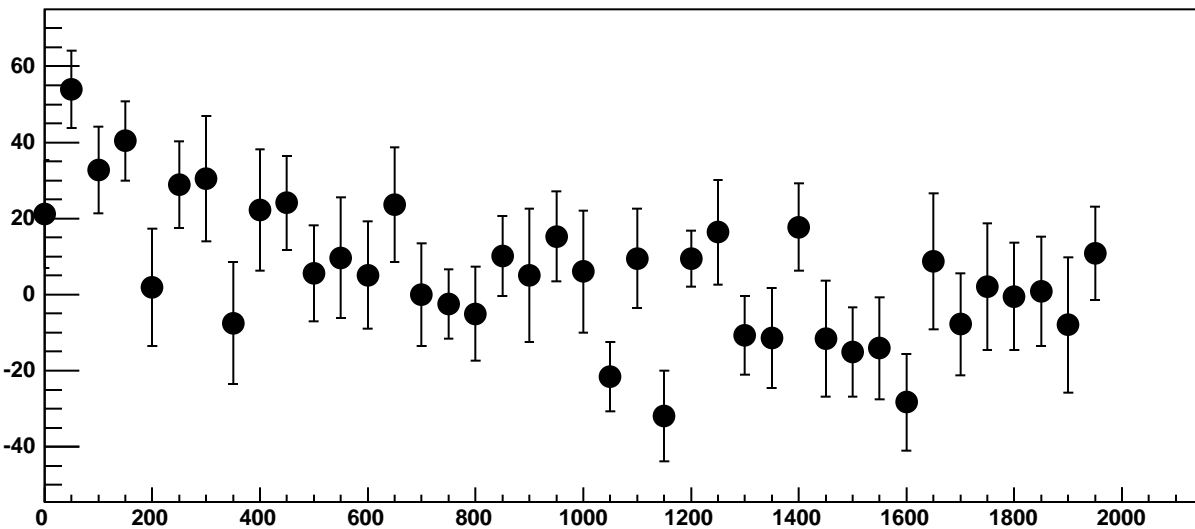
$0.01292 \pm 0.01757$



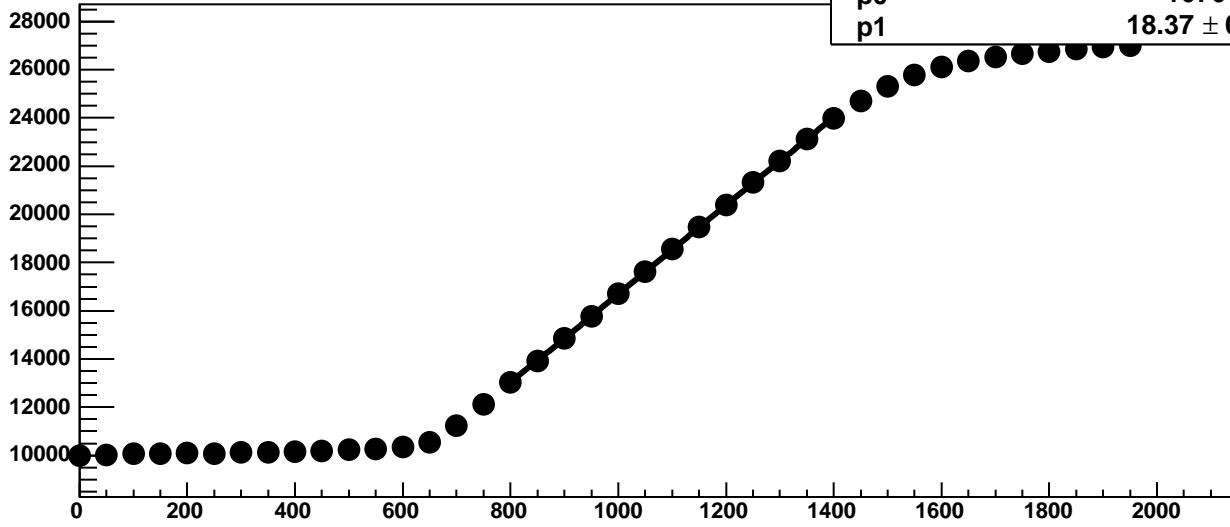
Chip 3, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC



Chip 3, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC

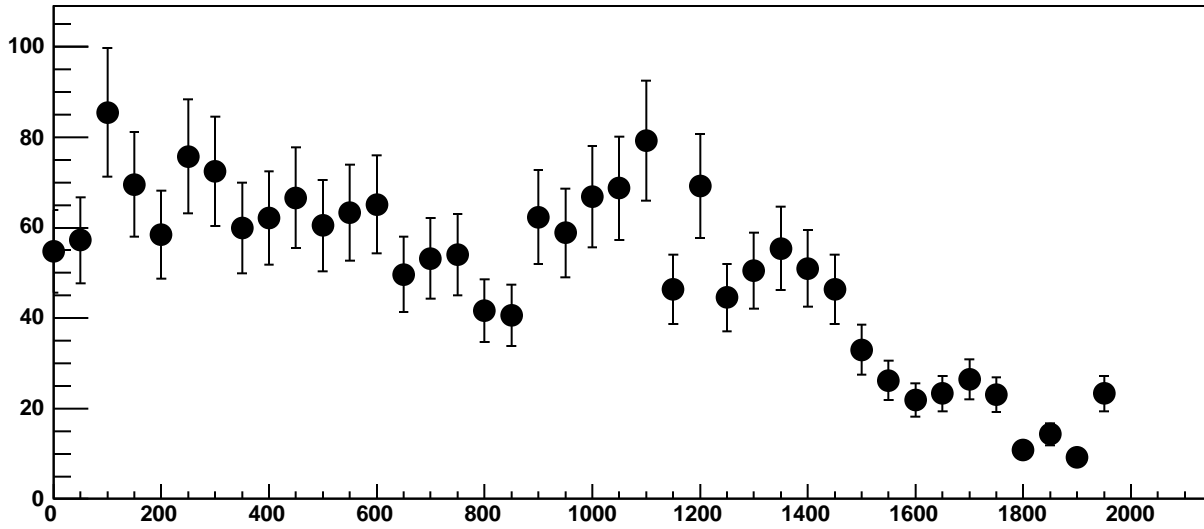


Chip 3, Channel 12, Enable 4!, Hold=30, ADC Mean vs DAC

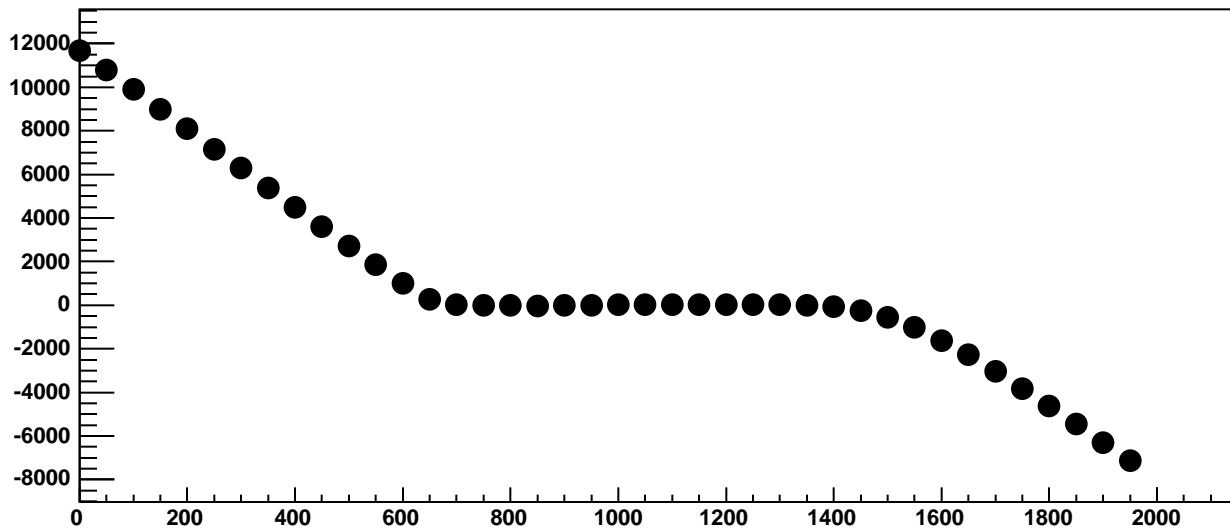


$\chi^2 / \text{ndf}$	77.87 / 11
p0	-1676 ± 18.22
p1	18.37 ± 0.01646

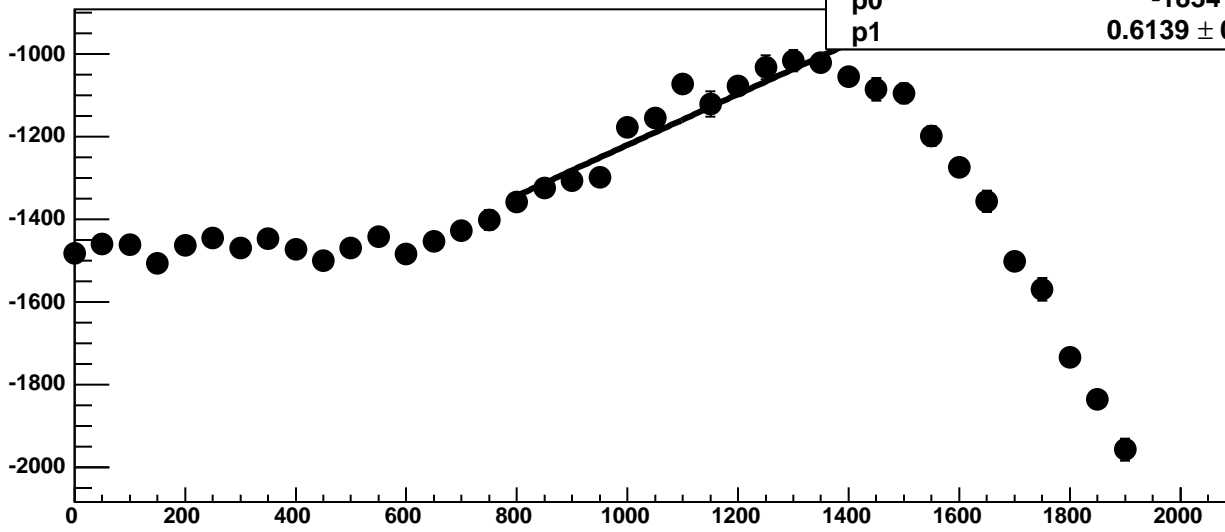
Chip 3, Channel 12, Enable 4!, Hold=30, ADC Noise vs DAC



Chip 3, Channel 12, Enable 4!, Hold=30, ADC Residuals vs DAC



Chip 3, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

59.09 / 11

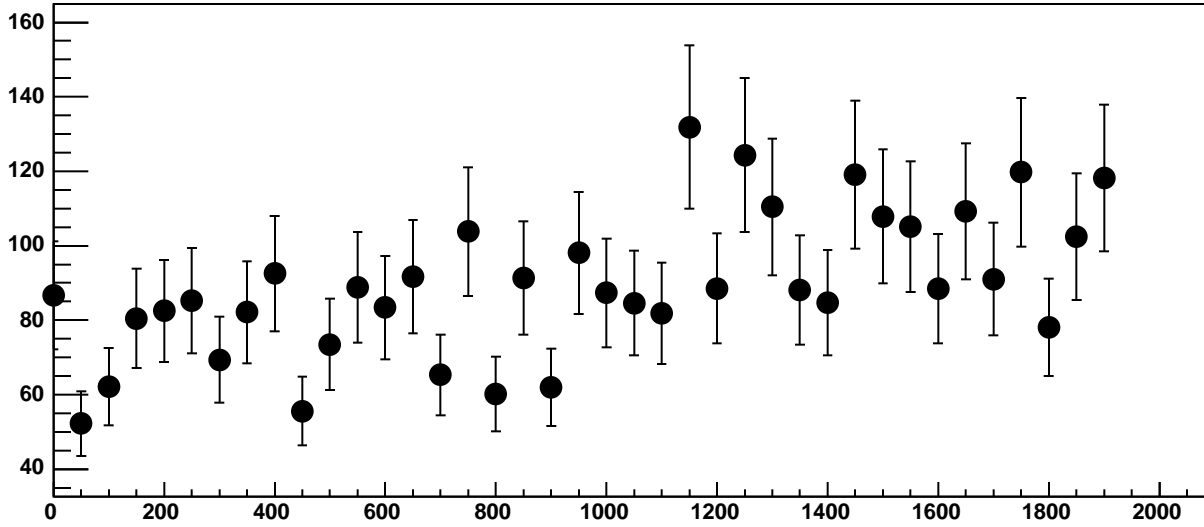
p0

$-1834 \pm 29.46$

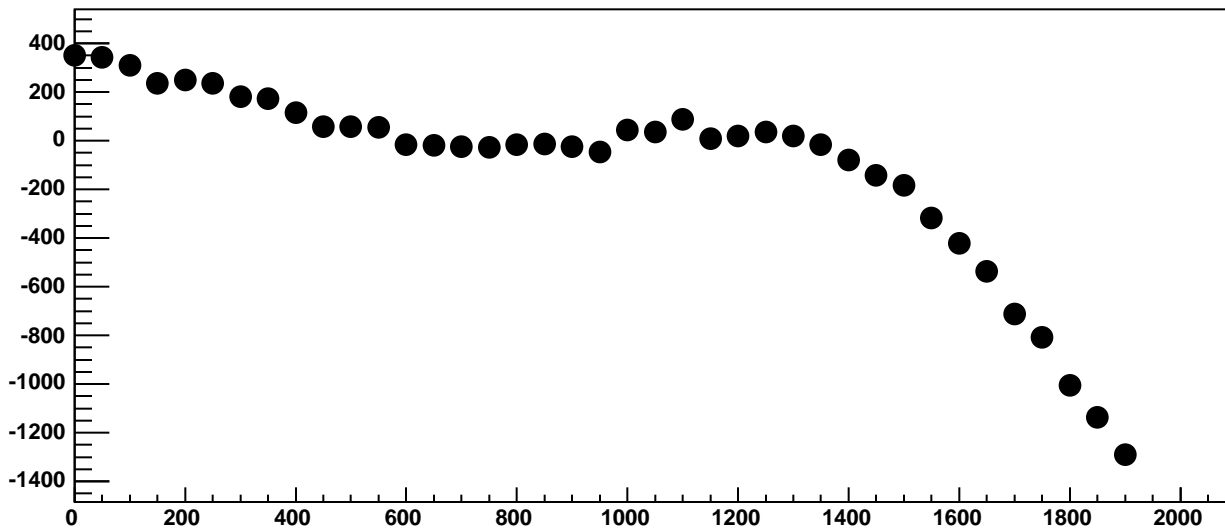
p1

$0.6139 \pm 0.02752$

Chip 3, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC

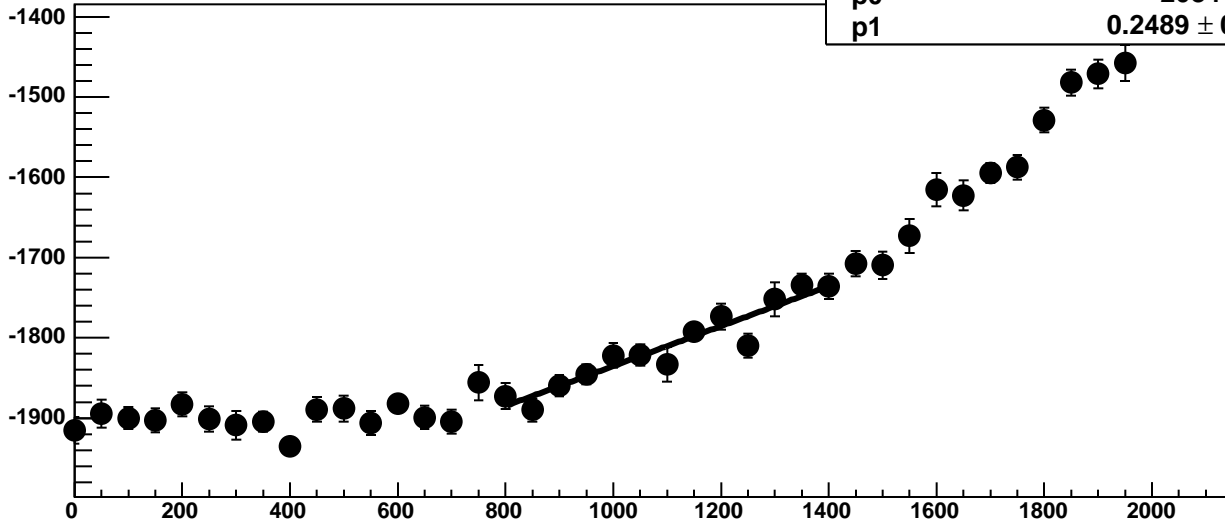


Chip 3, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC



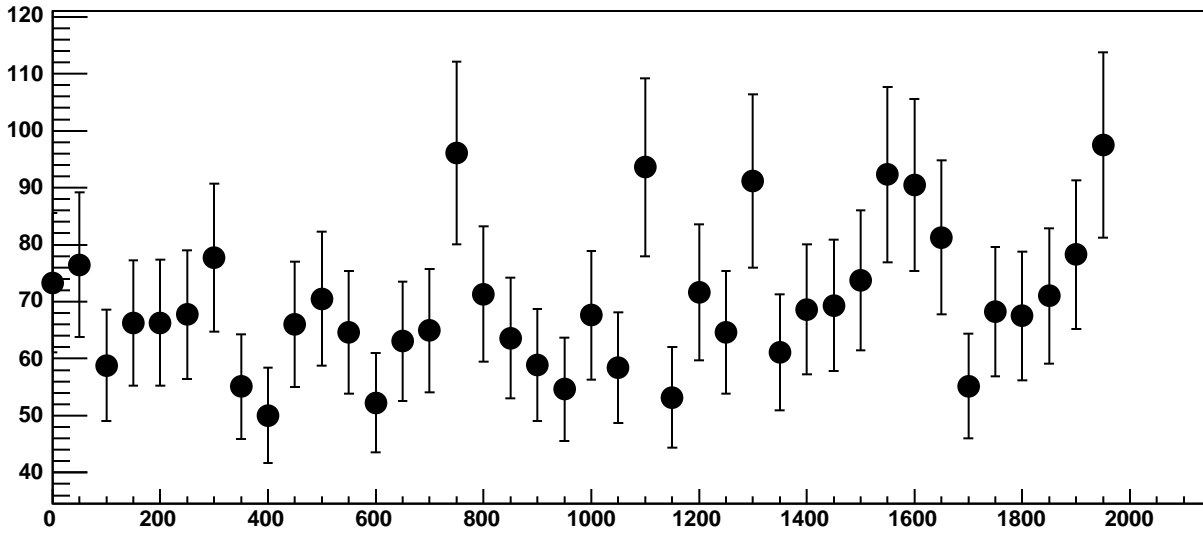


Chip 3, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC

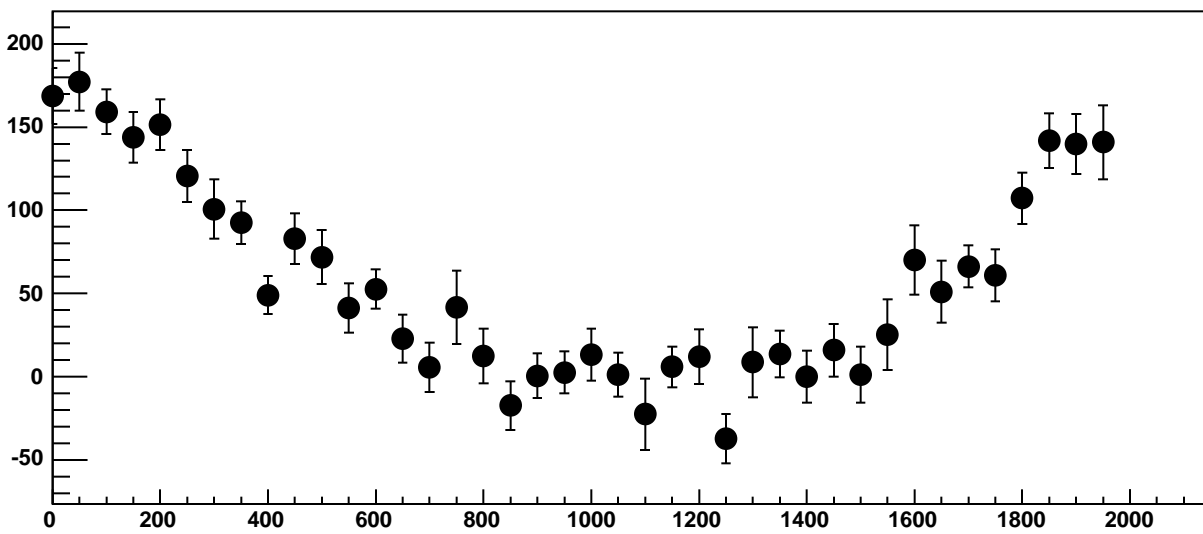


$\chi^2 / \text{ndf}$  11.99 / 11  
p0  $-2084 \pm 24.87$   
p1  $0.2489 \pm 0.02255$

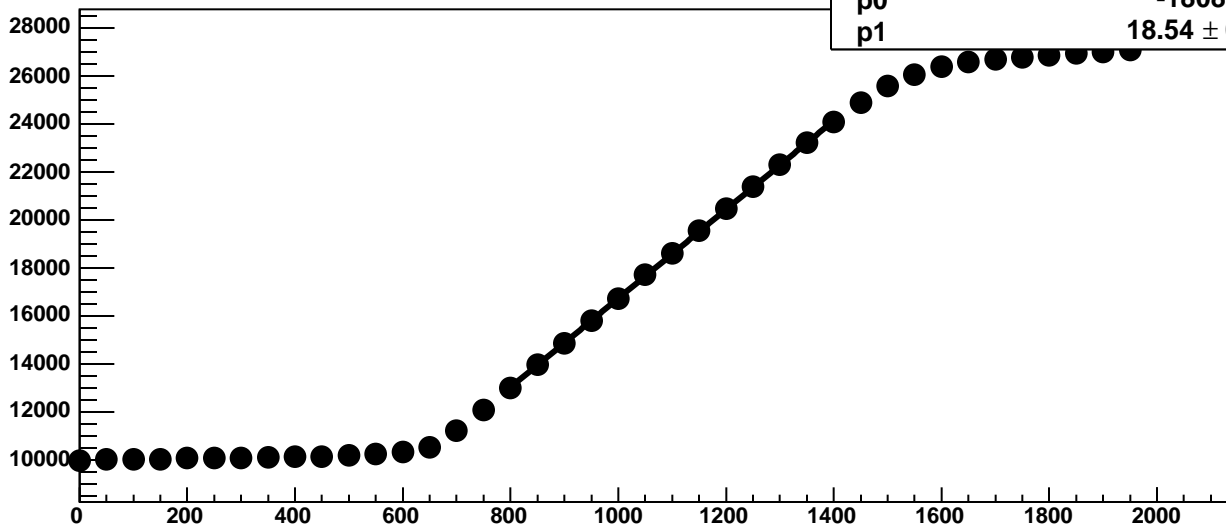
Chip 3, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC



Chip 3, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC

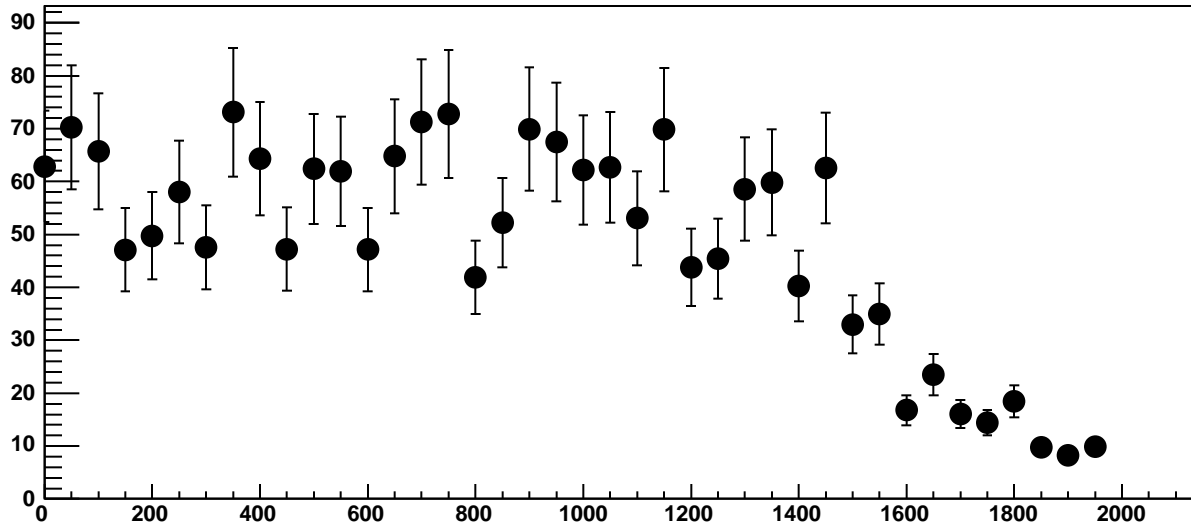


Chip 3, Channel 13, Enable 1!, Hold=30, ADC Mean vs DAC

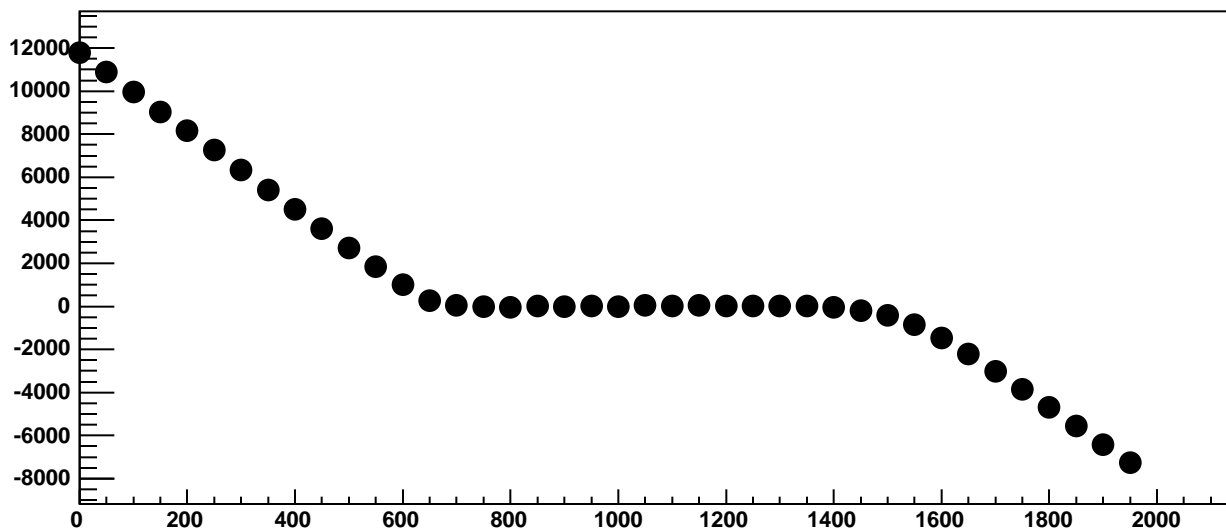


$\chi^2 / \text{ndf}$	99.47 / 11
p0	$-1808 \pm 18.76$
p1	$18.54 \pm 0.01656$

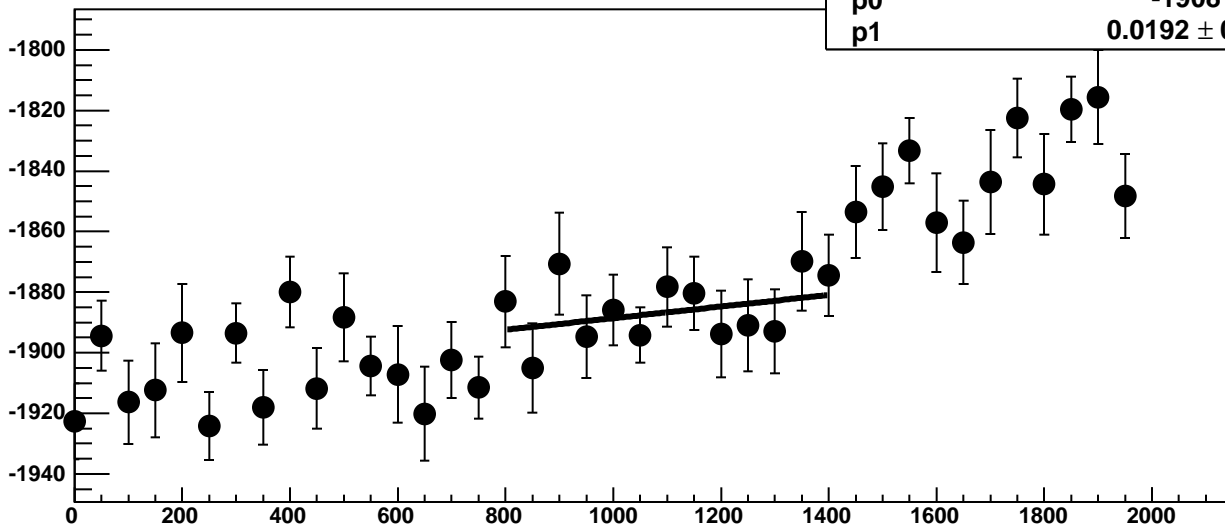
Chip 3, Channel 13, Enable 1!, Hold=30, ADC Noise vs DAC



Chip 3, Channel 13, Enable 1!, Hold=30, ADC Residuals vs DAC

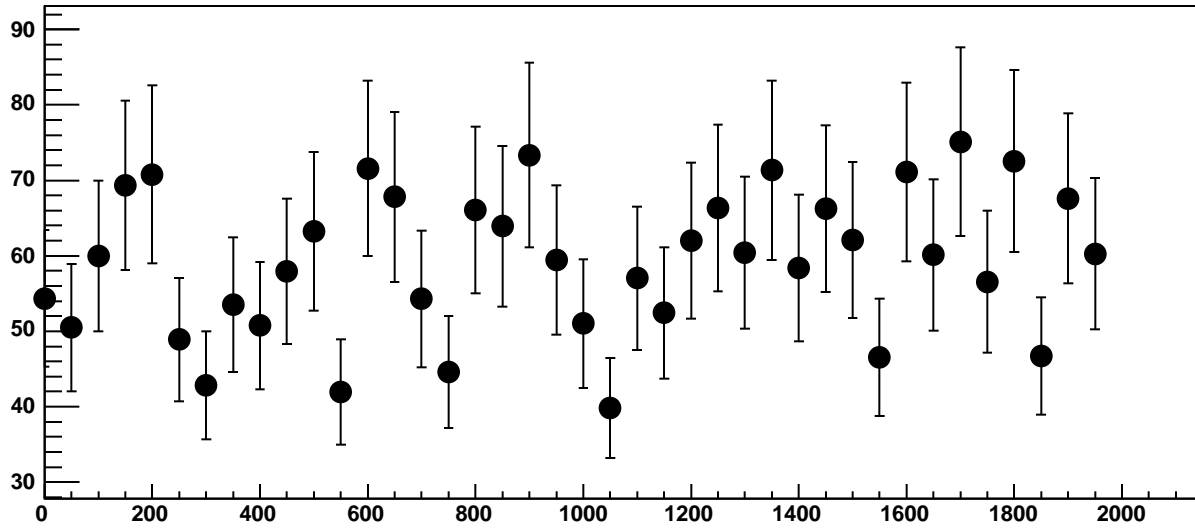


Chip 3, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC

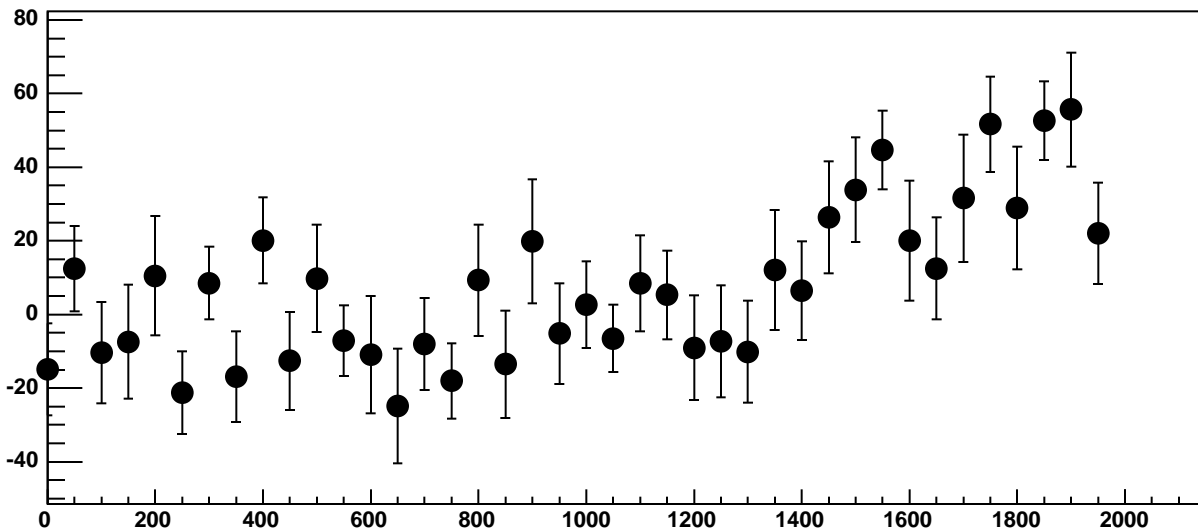


$\chi^2 / \text{ndf}$  5.898 / 11  
p0  $-1908 \pm 23.94$   
p1  $0.0192 \pm 0.02155$

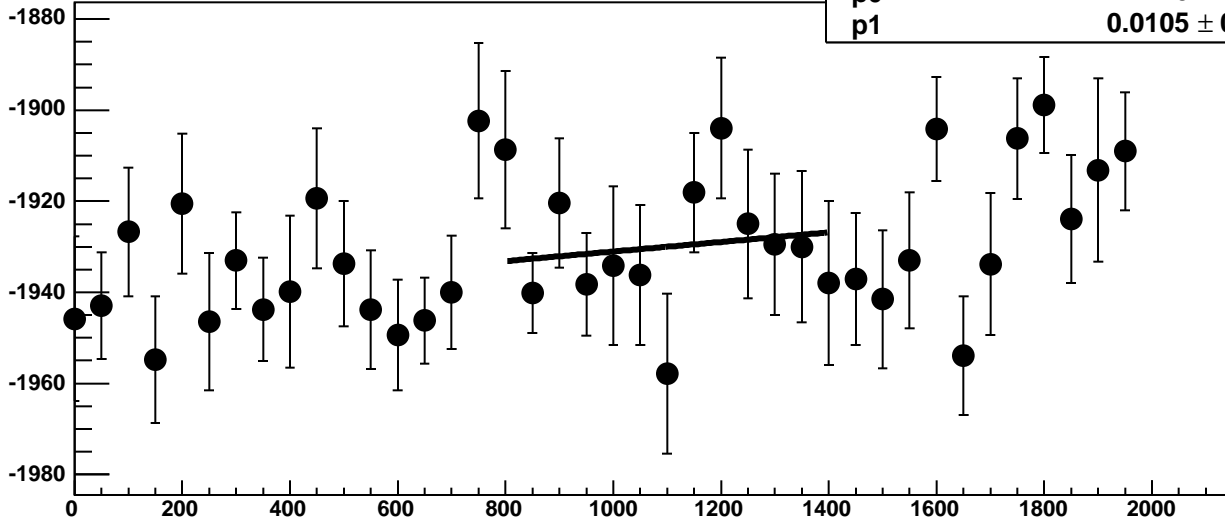
Chip 3, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC



Chip 3, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 3, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

10.28 / 11

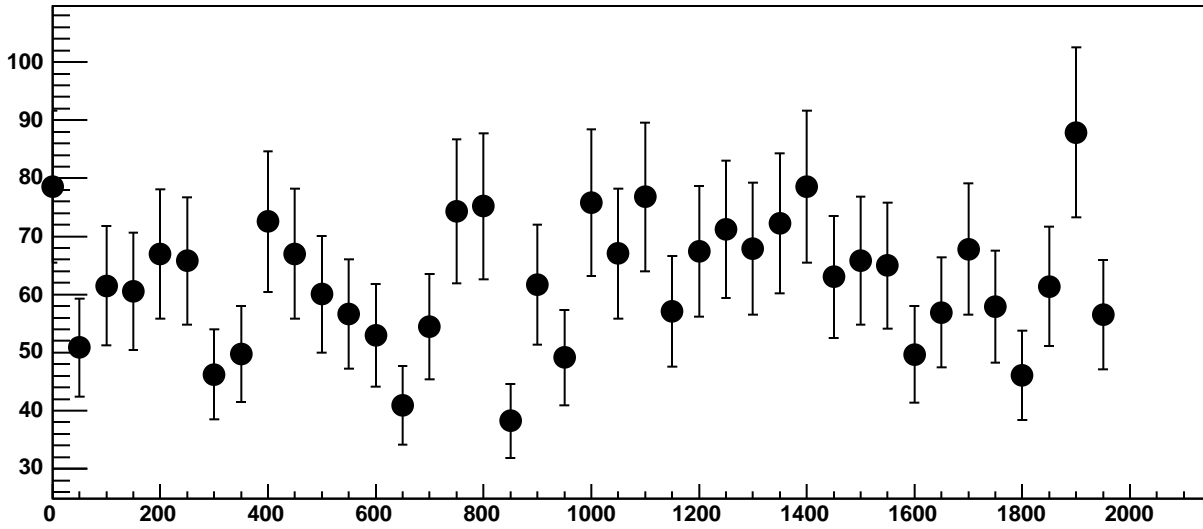
p0

$-1942 \pm 22.78$

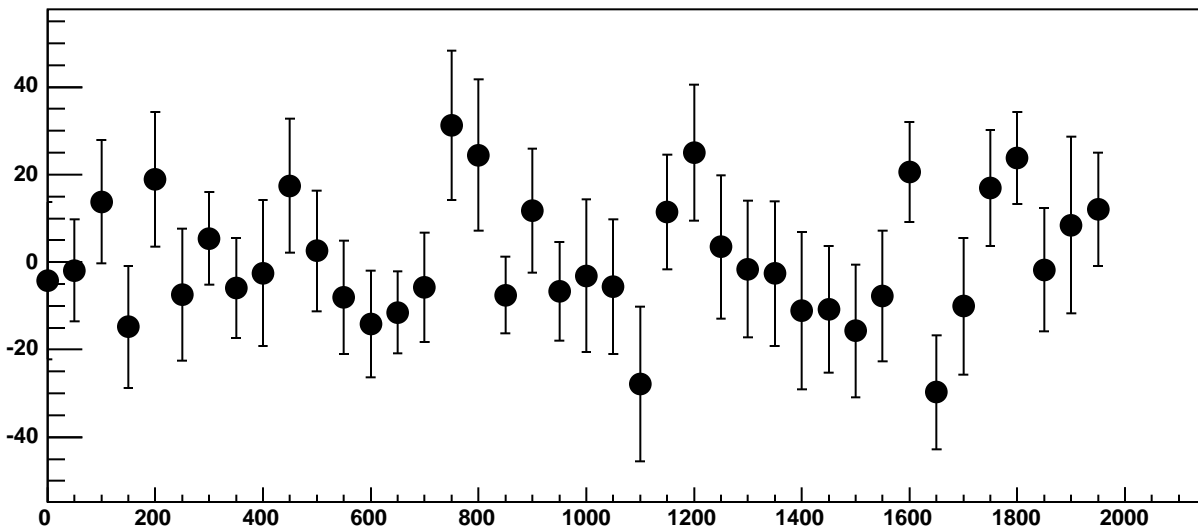
p1

$0.0105 \pm 0.02132$

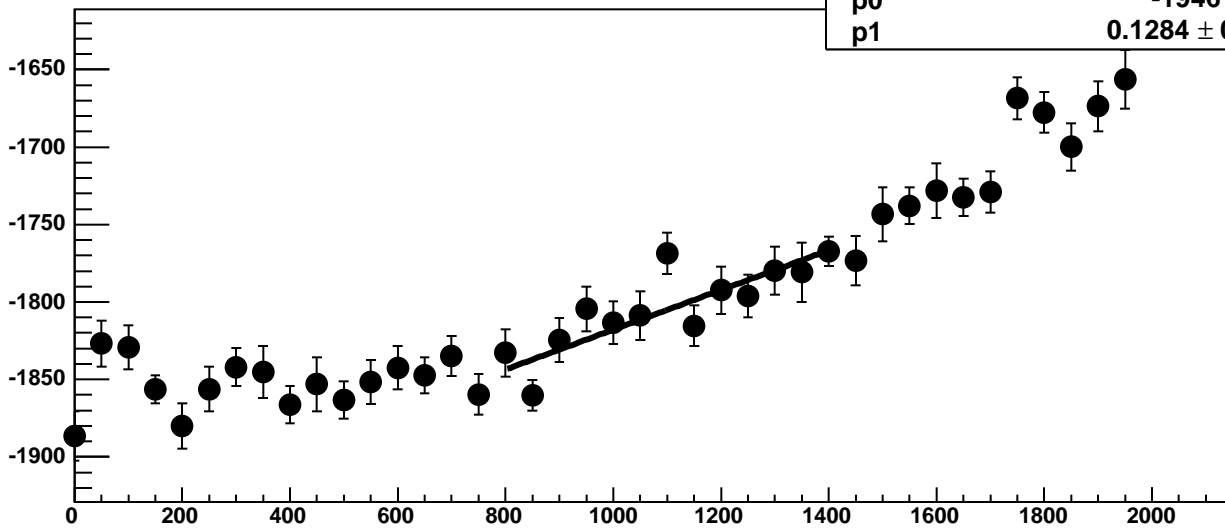
Chip 3, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC



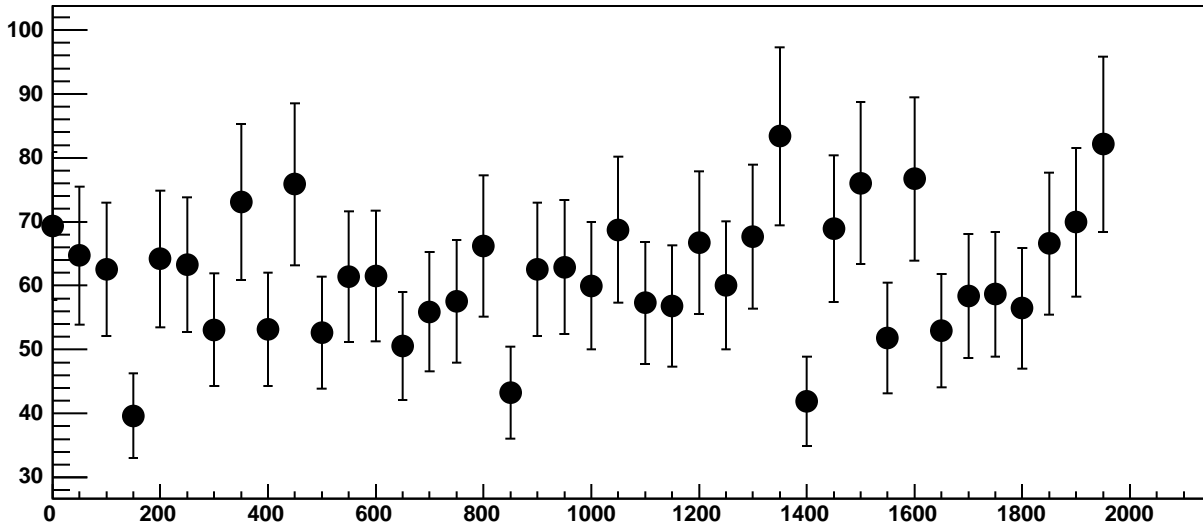
Chip 3, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC



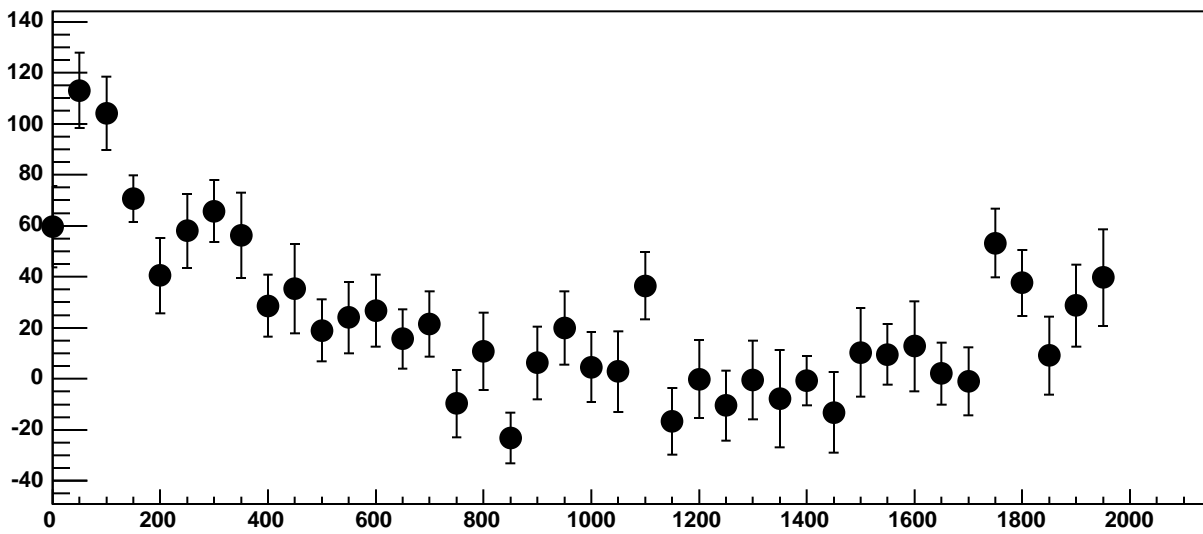
Chip 3, Channel 13, Enable 4, Hold=30, ADC Mean vs DAC



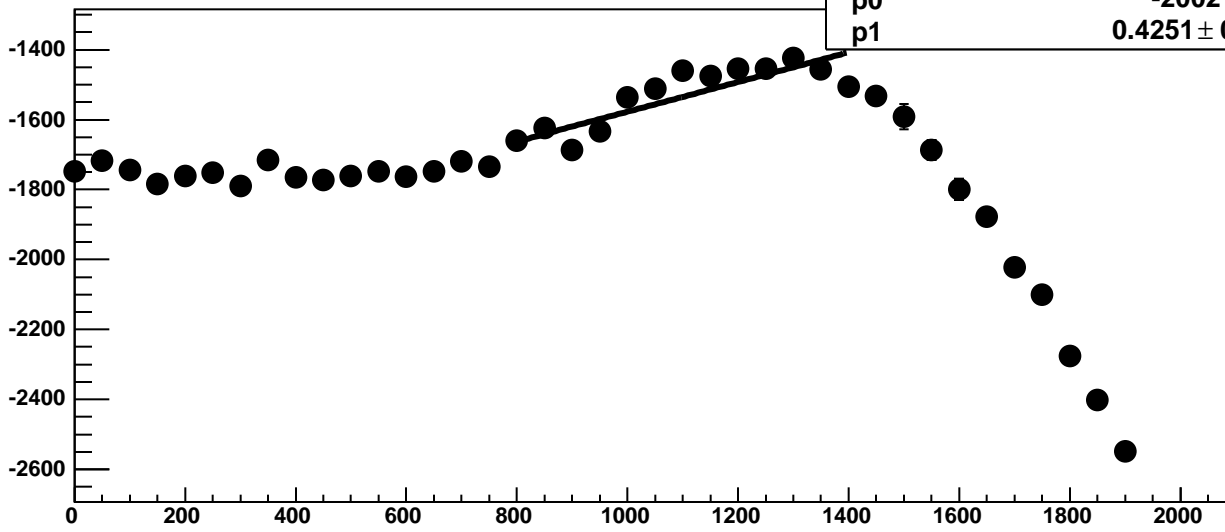
Chip 3, Channel 13, Enable 4, Hold=30, ADC Noise vs DAC



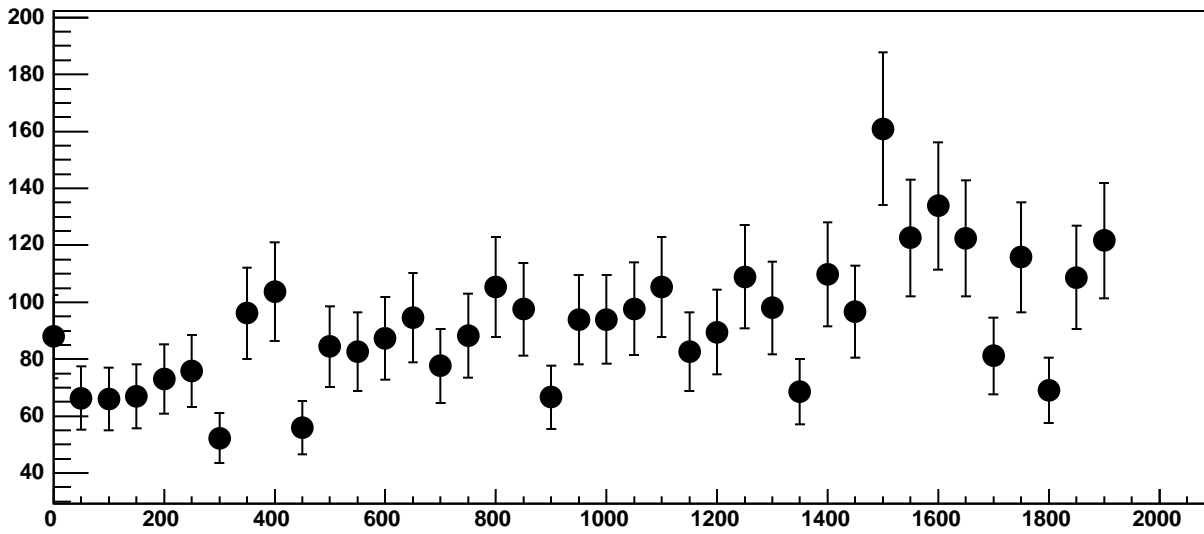
Chip 3, Channel 13, Enable 4, Hold=30, ADC Residuals vs DAC



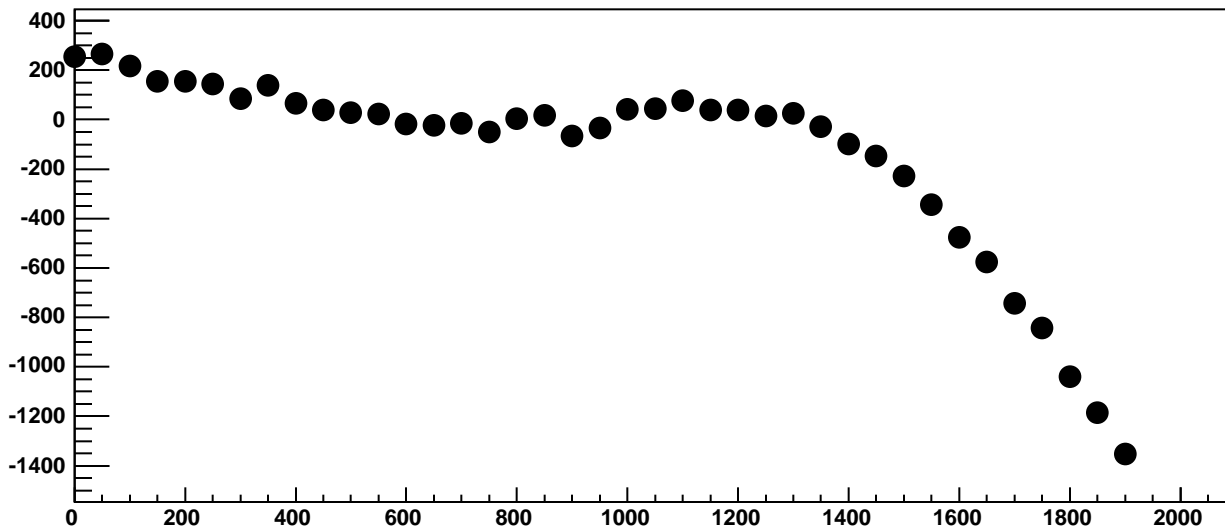
Chip 3, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC



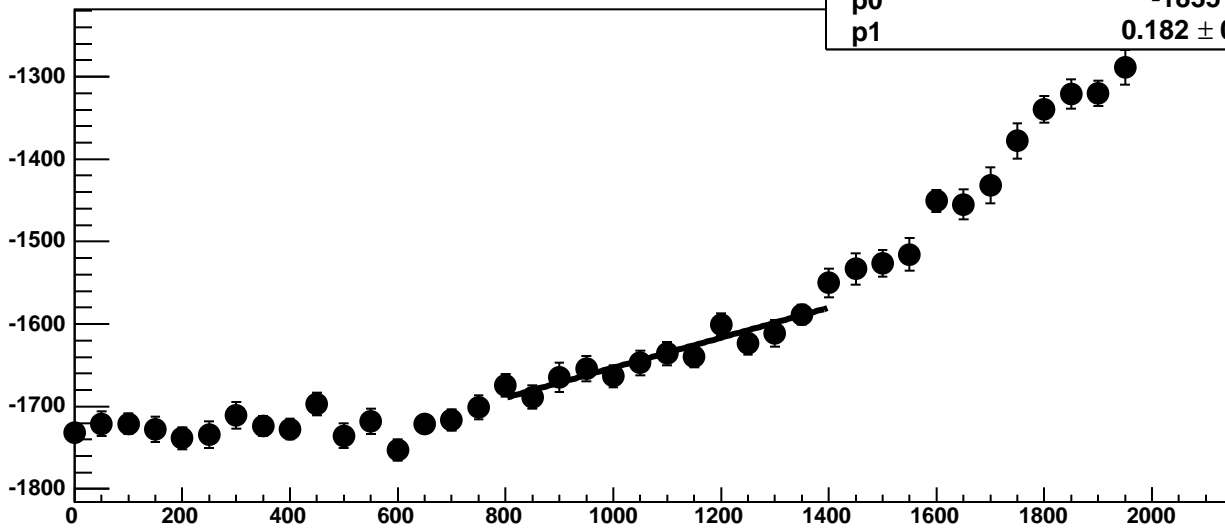
Chip 3, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC



Chip 3, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC

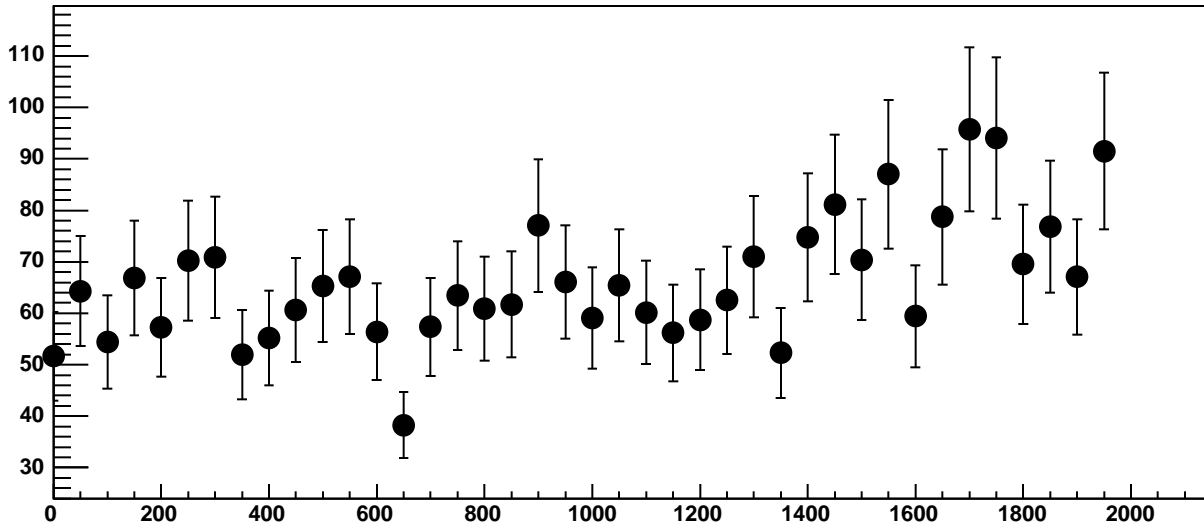


Chip 3, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC

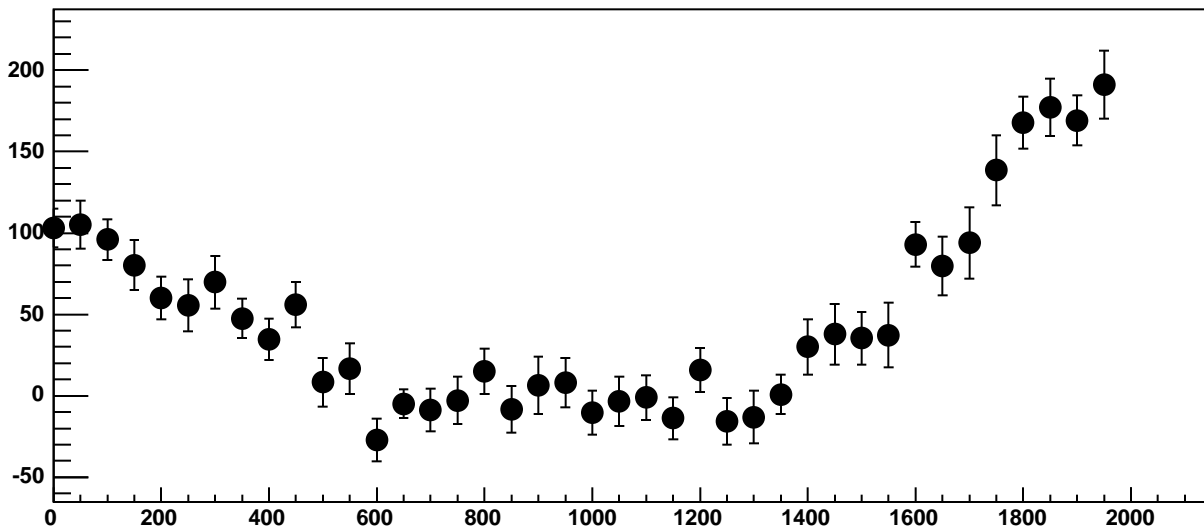


$\chi^2 / \text{ndf}$  9.95 / 11  
p0  $-1835 \pm 24.23$   
p1  $0.182 \pm 0.02165$

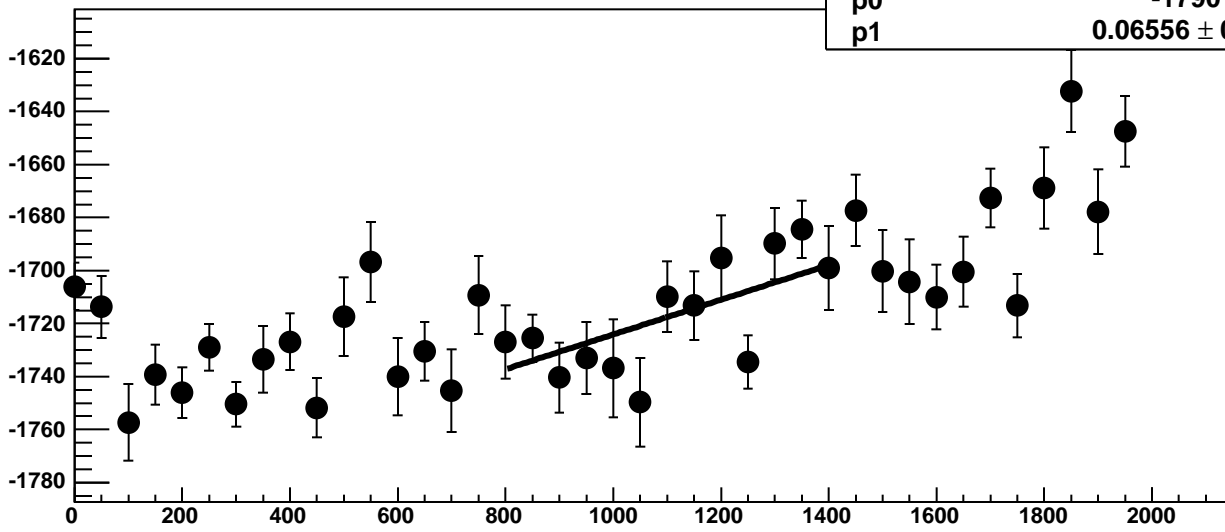
Chip 3, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC



Chip 3, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC

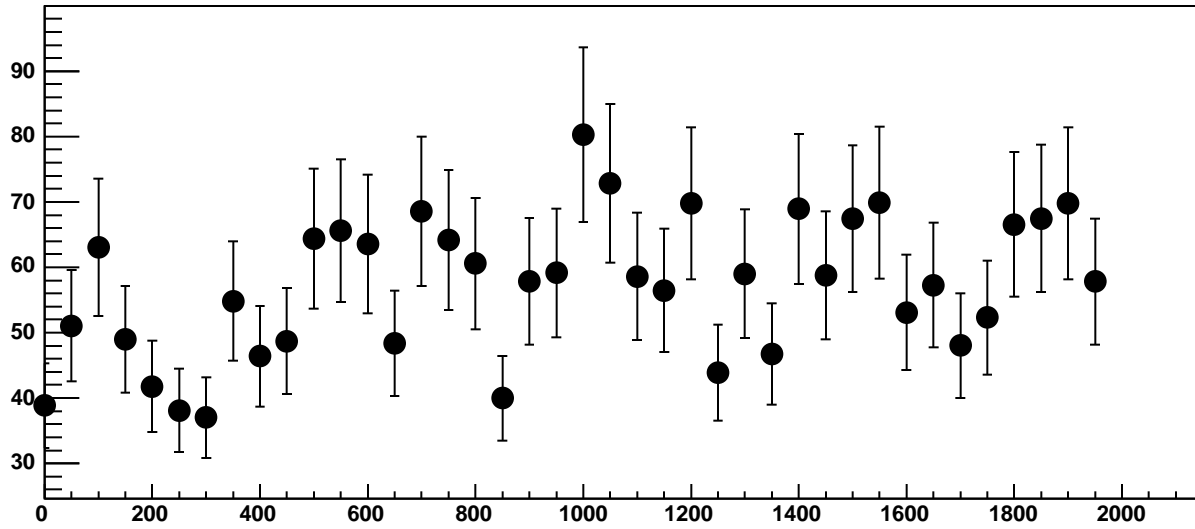


Chip 3, Channel 14, Enable 1, Hold=30, ADC Mean vs DAC

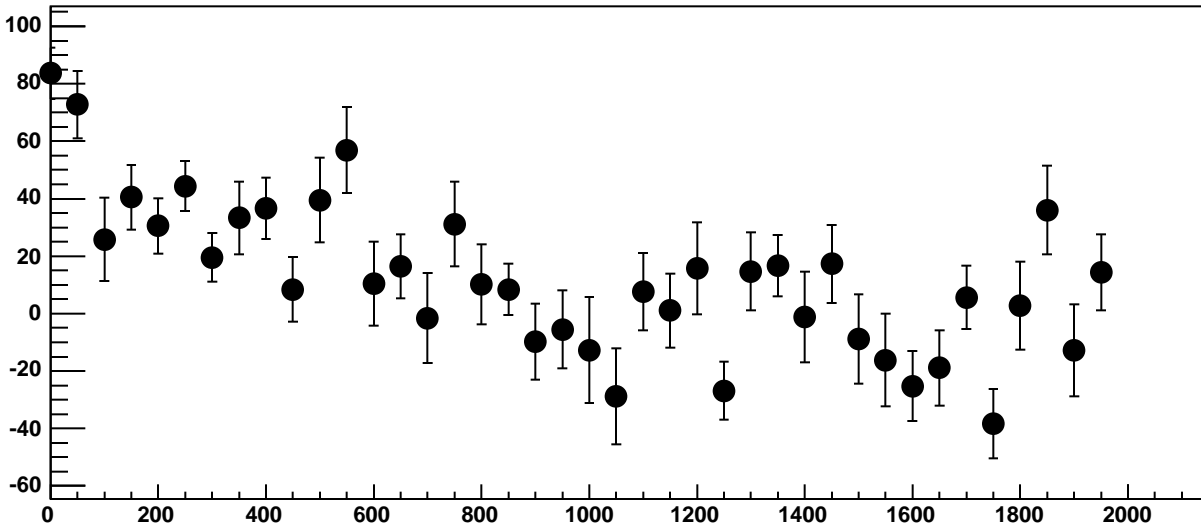


$\chi^2 / \text{ndf}$  17.63 / 11  
p0  $-1790 \pm 20.19$   
p1  $0.06556 \pm 0.01817$

Chip 3, Channel 14, Enable 1, Hold=30, ADC Noise vs DAC

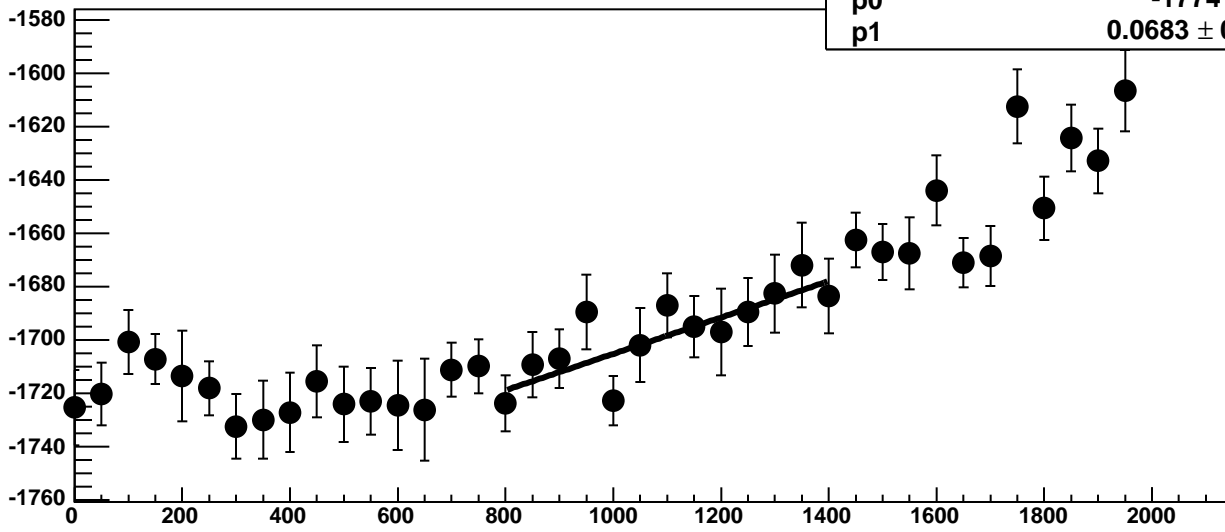


Chip 3, Channel 14, Enable 1, Hold=30, ADC Residuals vs DAC

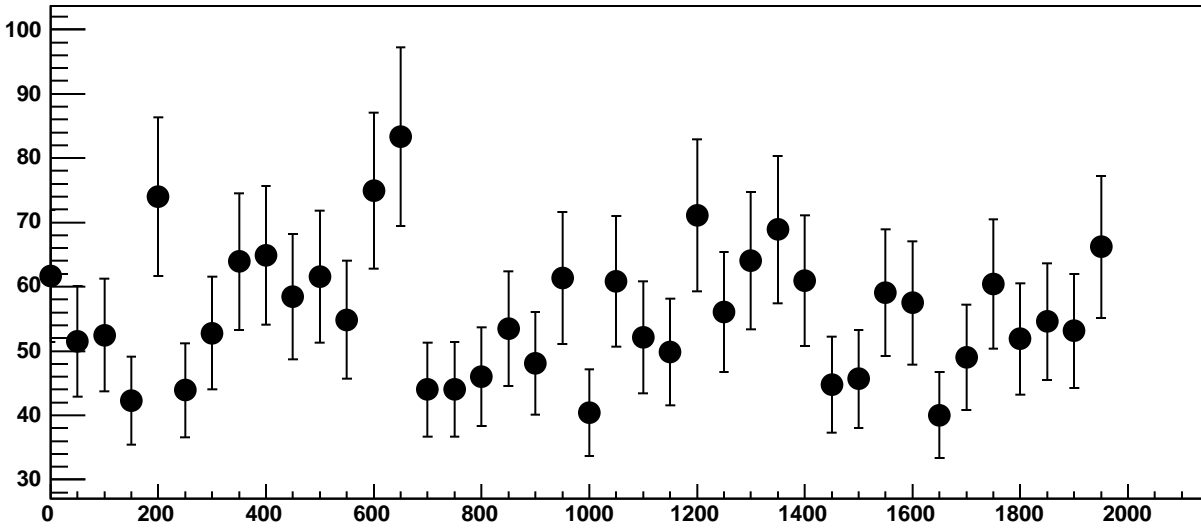




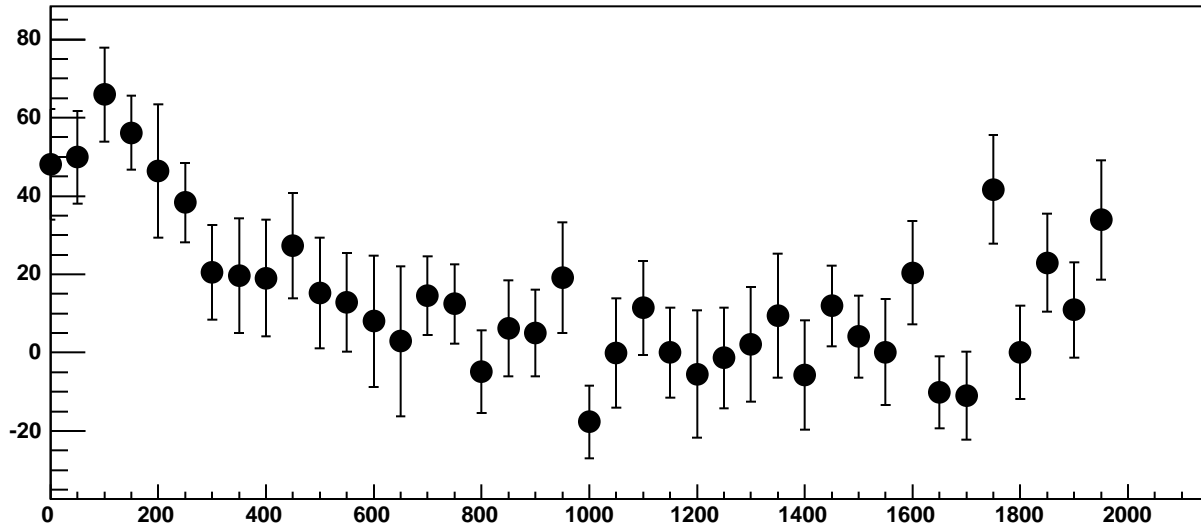
Chip 3, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC



Chip 3, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC

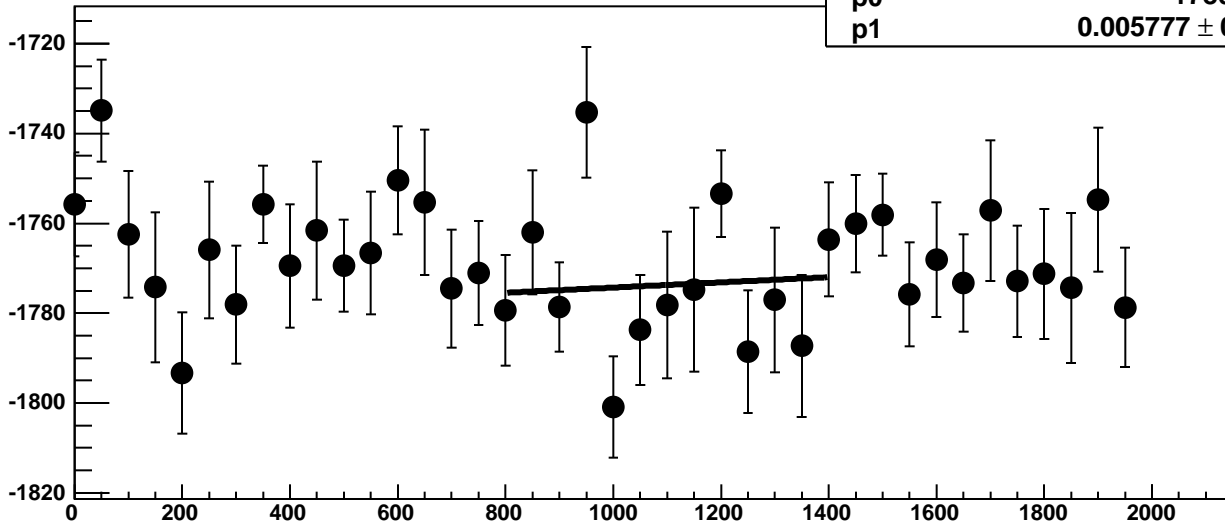


Chip 3, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC

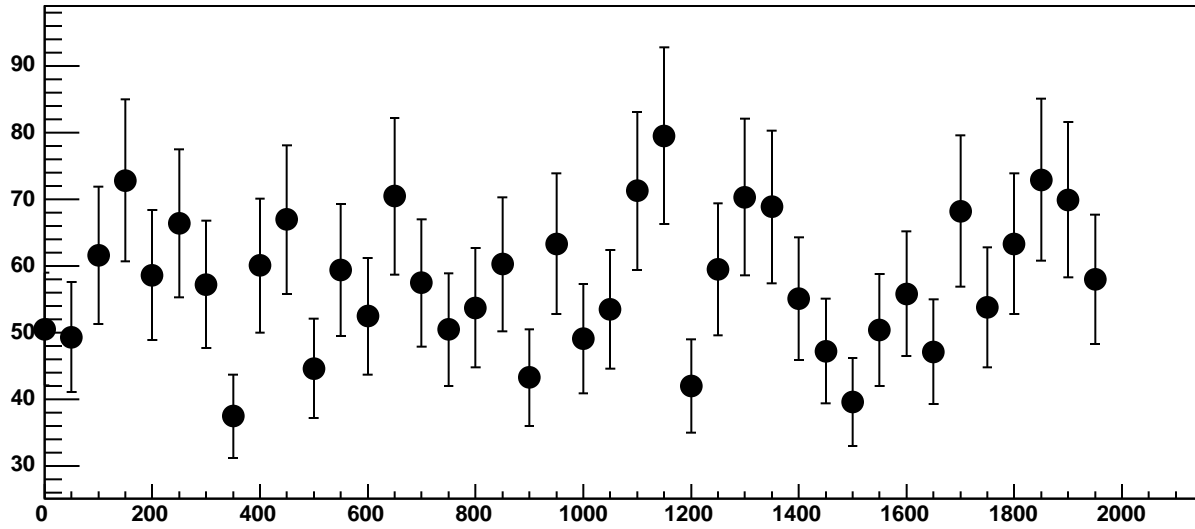


Chip 3, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC

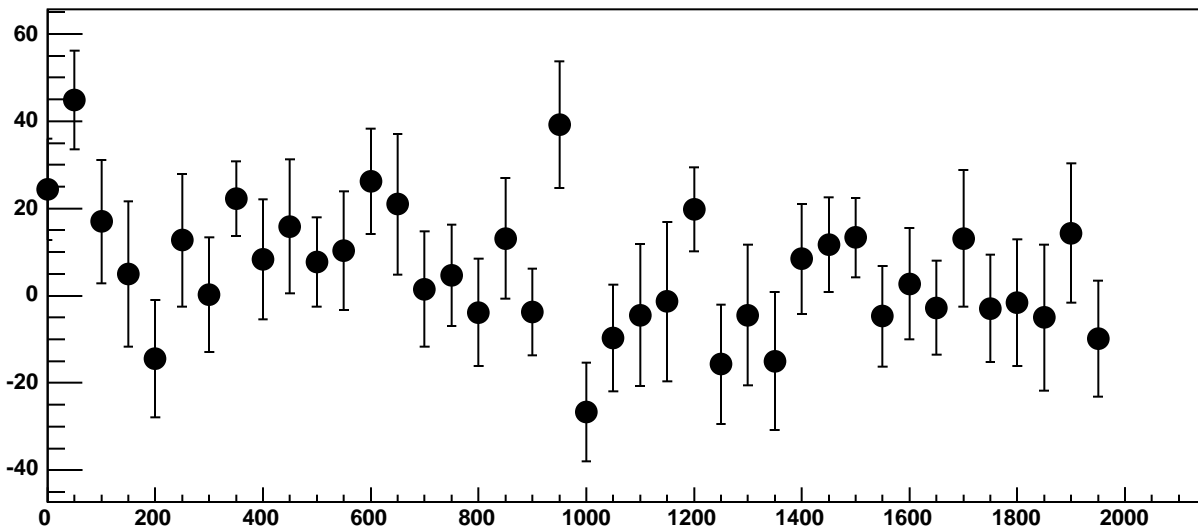
$\chi^2 / \text{ndf}$  21.7 / 11  
p0  $-1780 \pm 21.1$   
p1  $0.005777 \pm 0.01923$



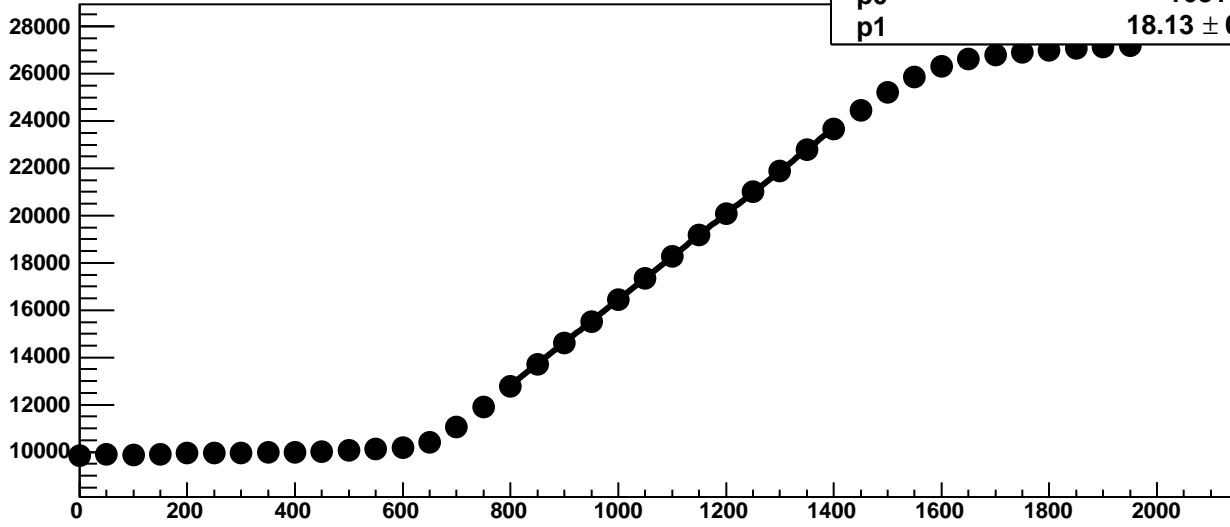
Chip 3, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC



Chip 3, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC

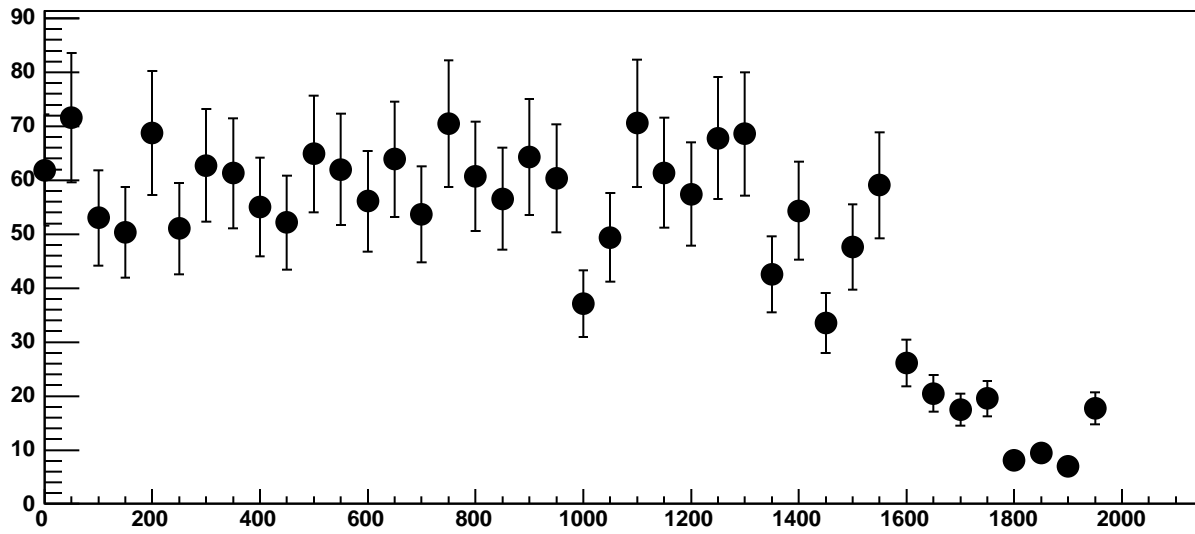


Chip 3, Channel 14, Enable 4!, Hold=30, ADC Mean vs DAC

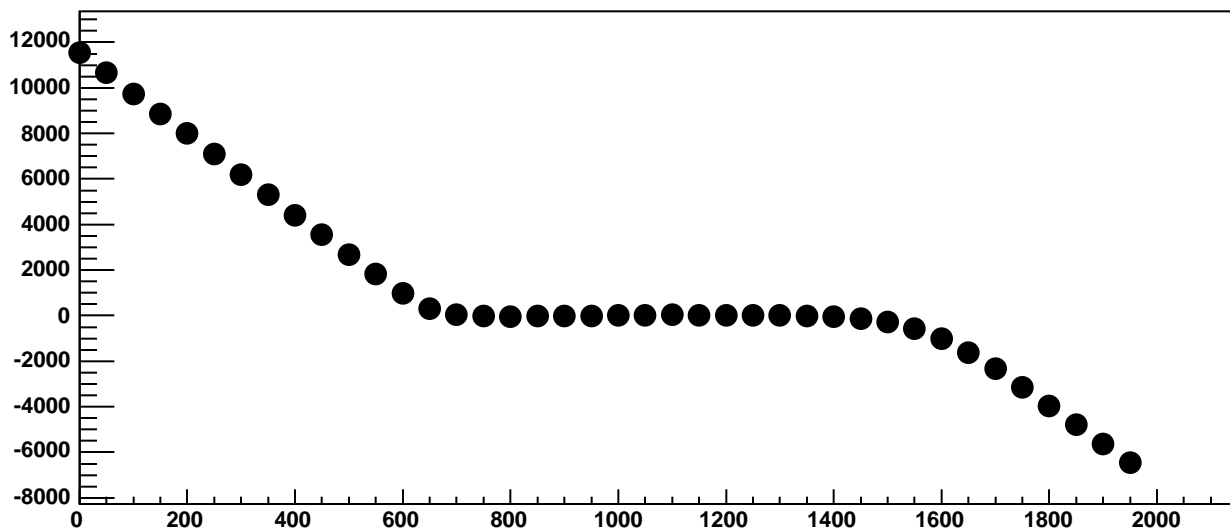


$\chi^2 / \text{ndf}$  37.13 / 11  
p0  $-1681 \pm 20.96$   
p1  $18.13 \pm 0.01873$

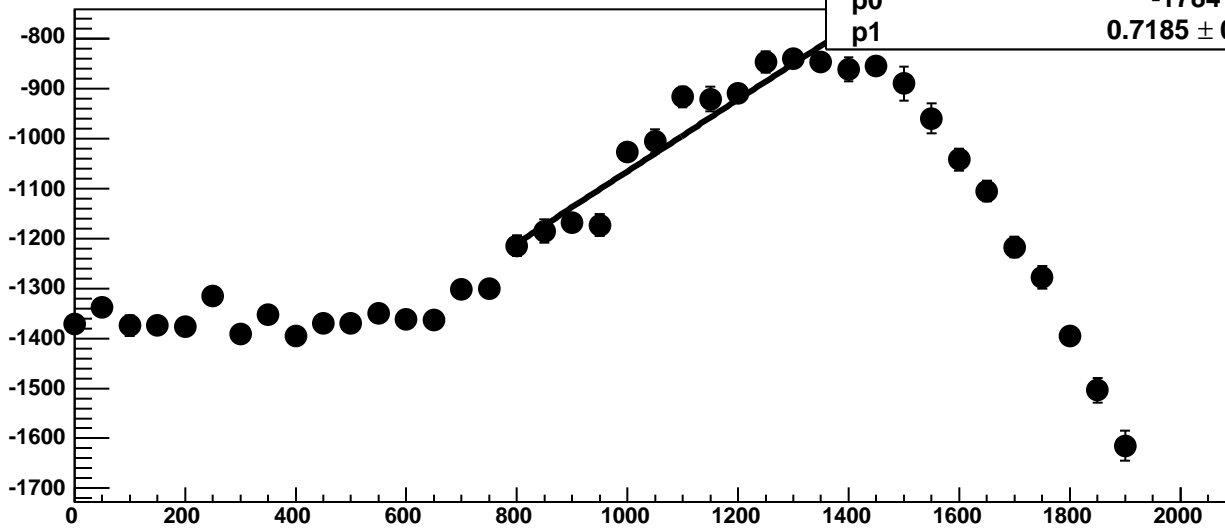
Chip 3, Channel 14, Enable 4!, Hold=30, ADC Noise vs DAC



Chip 3, Channel 14, Enable 4!, Hold=30, ADC Residuals vs DAC



Chip 3, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

55.12 / 11

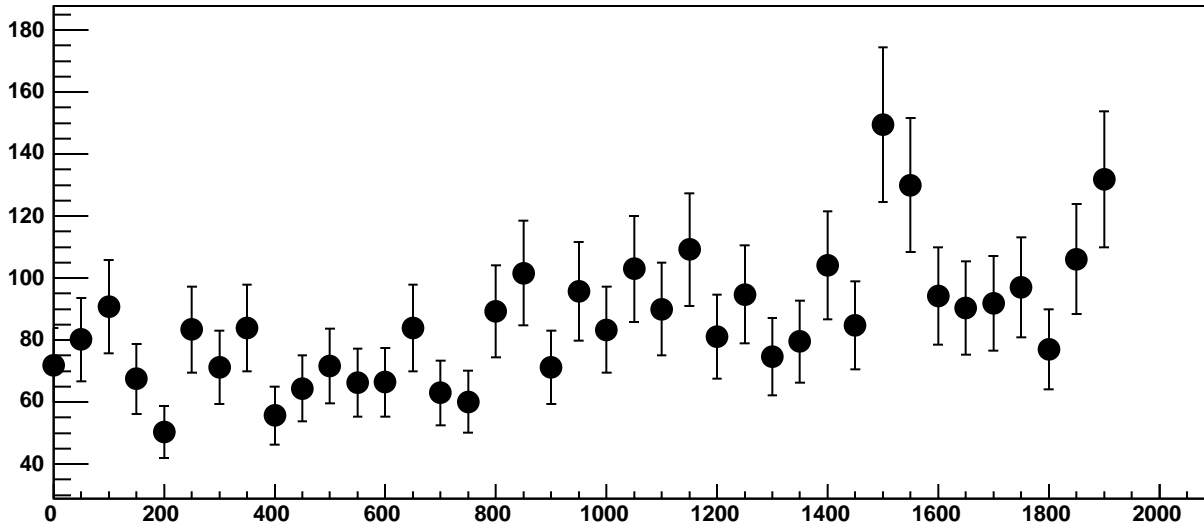
p0

$-1784 \pm 33.38$

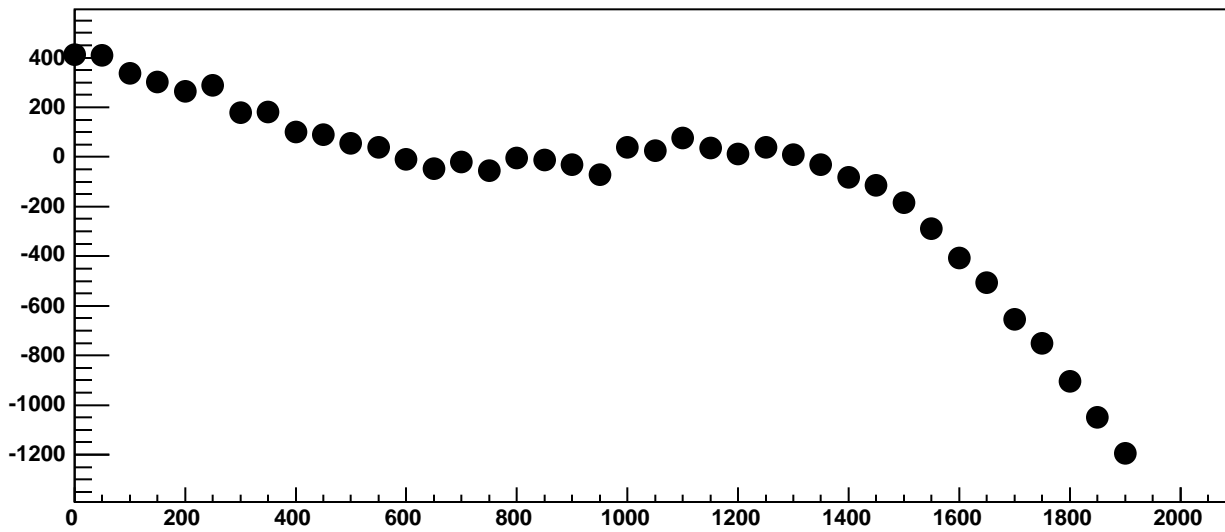
p1

$0.7185 \pm 0.02987$

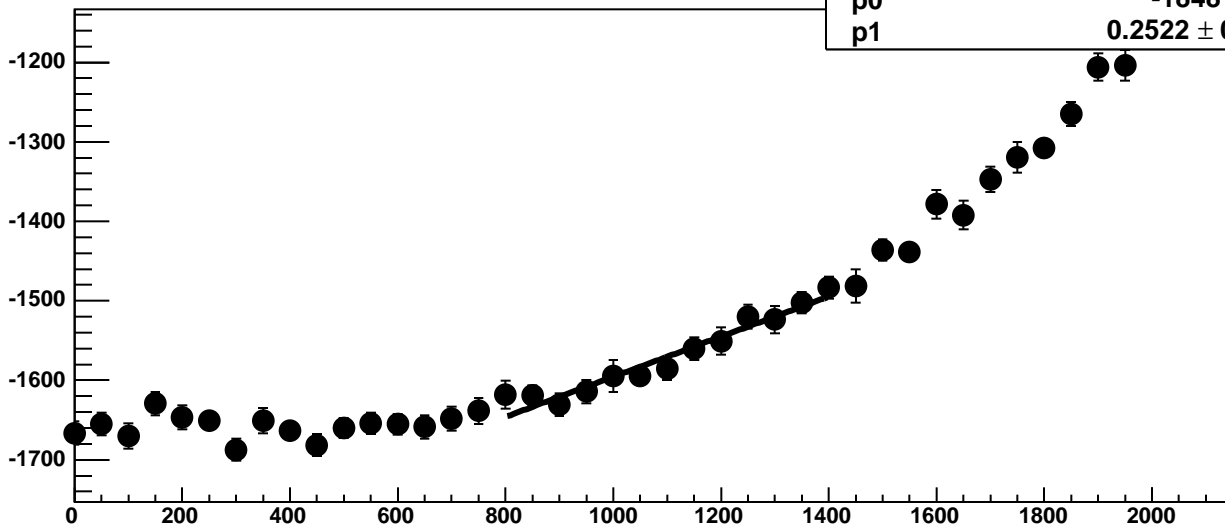
Chip 3, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC



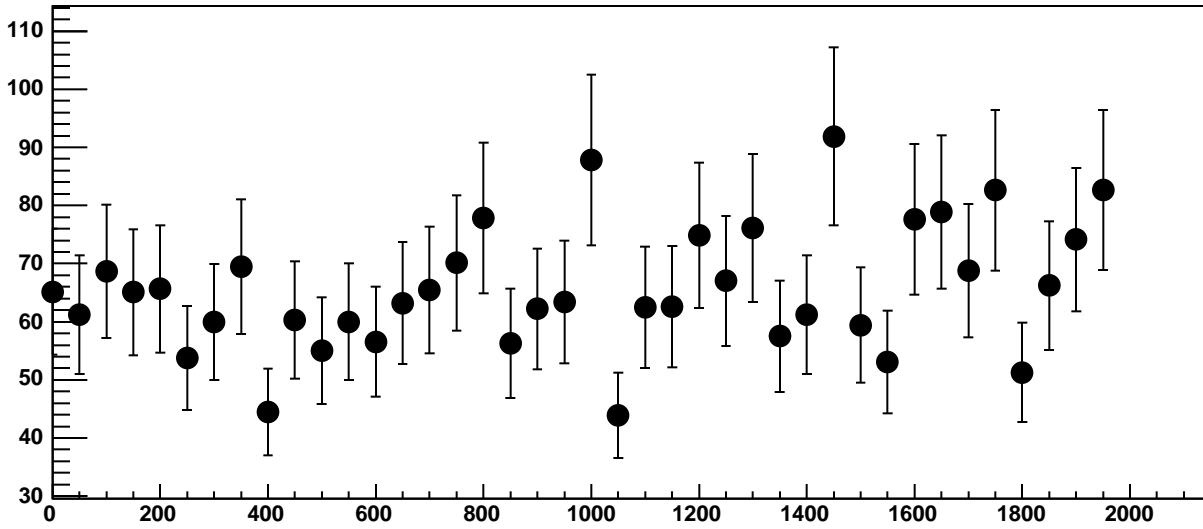
Chip 3, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC



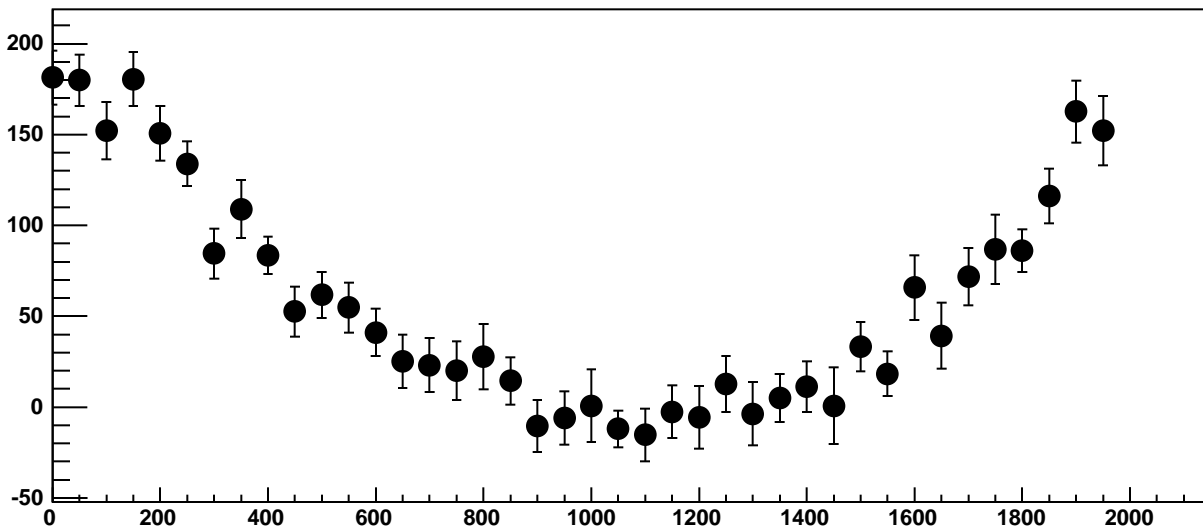
Chip 3, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC



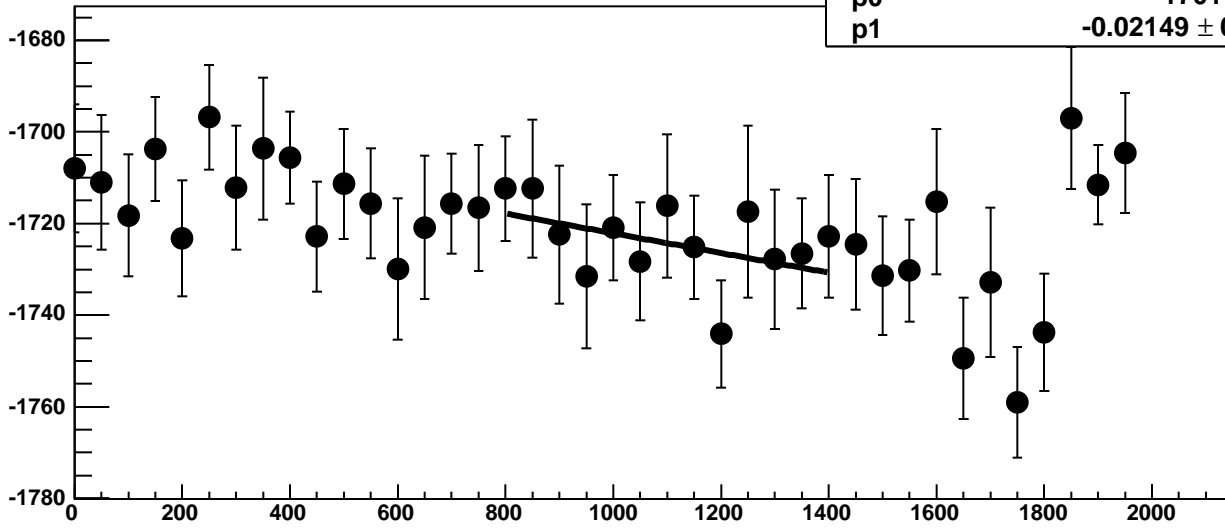
Chip 3, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC



Chip 3, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC

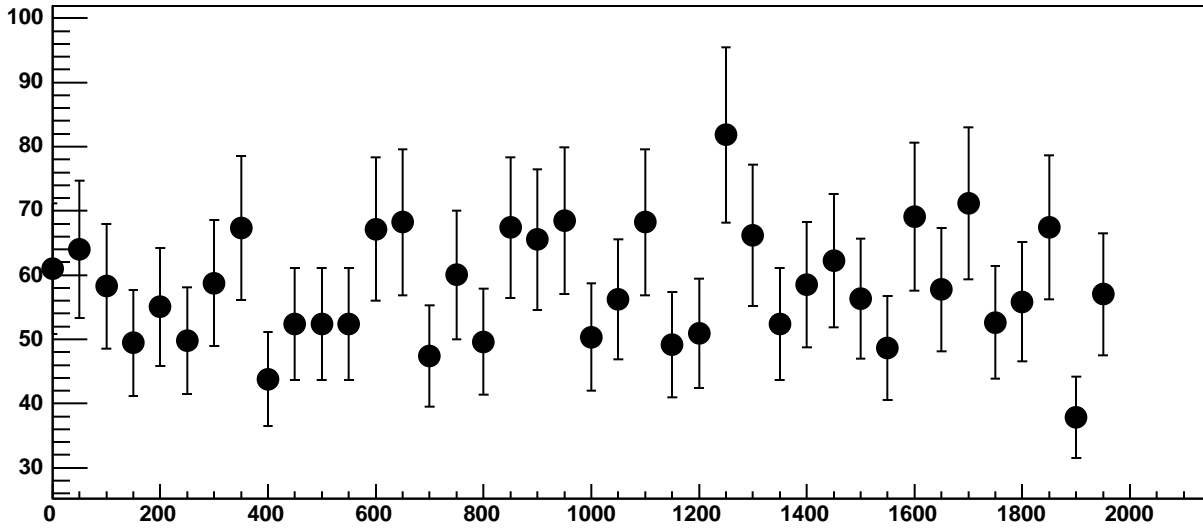


Chip 3, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC

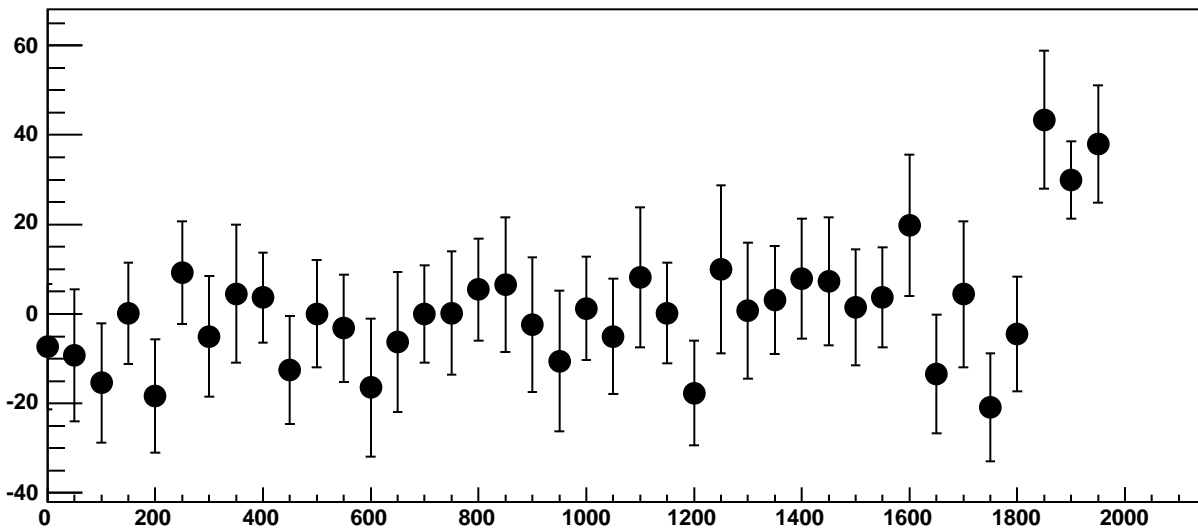


$\chi^2 / \text{ndf}$  4.321 / 11  
p0  $-1701 \pm 21.89$   
p1  $-0.02149 \pm 0.01965$

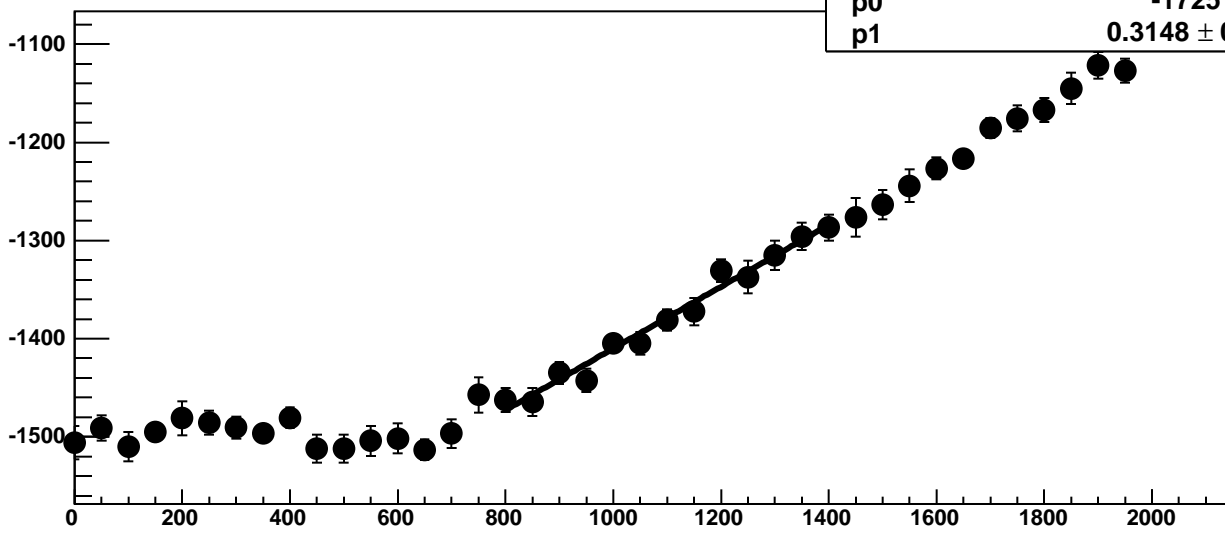
Chip 3, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC



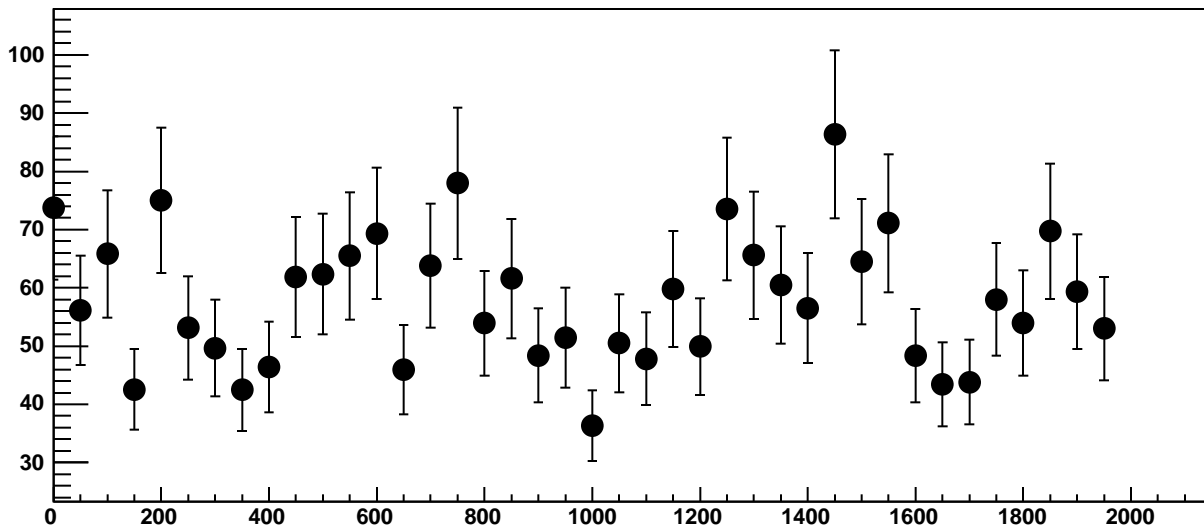
Chip 3, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC



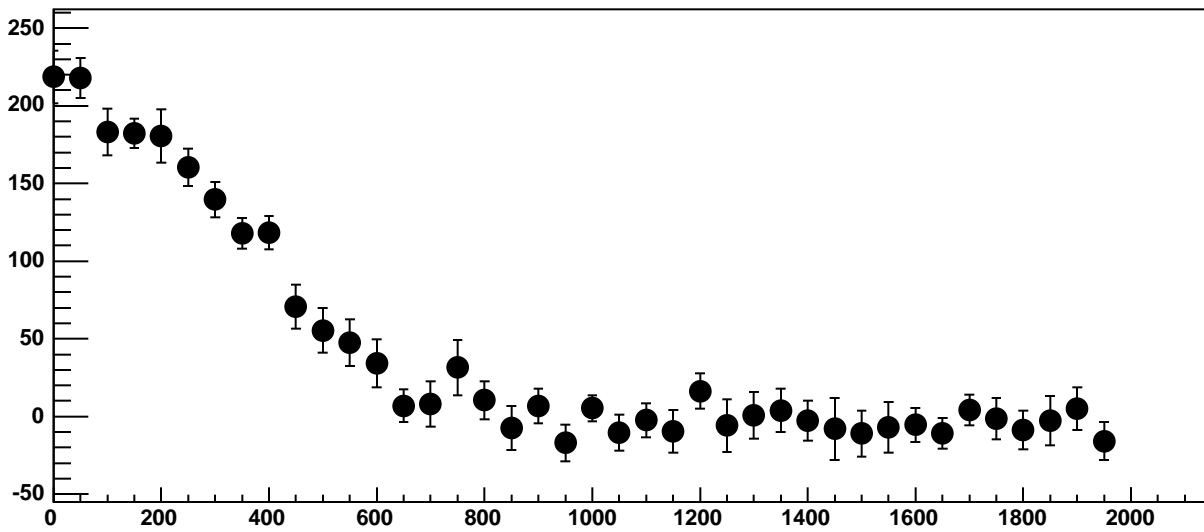
Chip 3, Channel 15, Enable 2, Hold=30, ADC Mean vs DAC



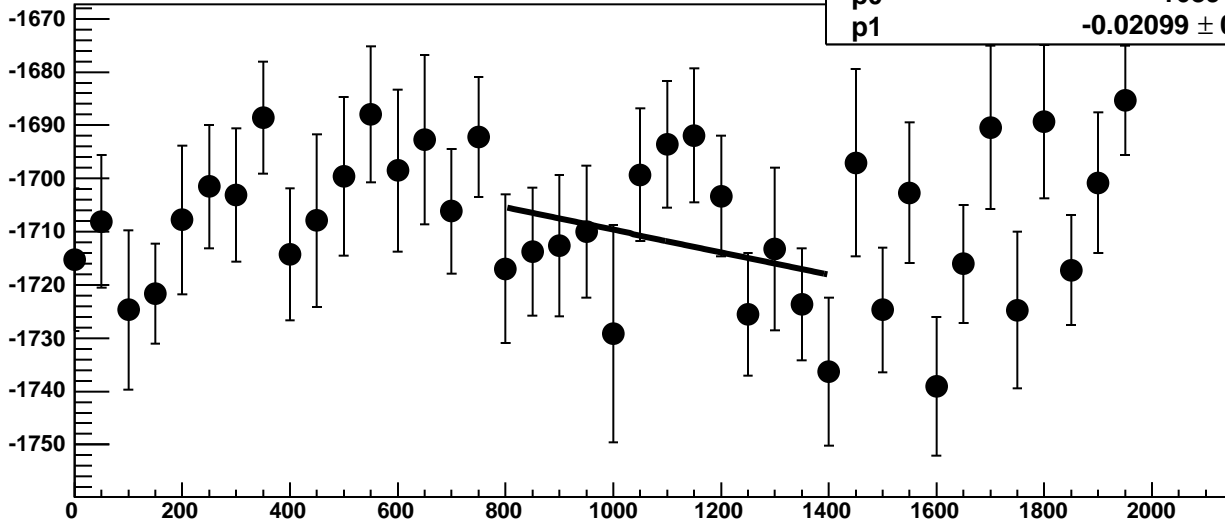
Chip 3, Channel 15, Enable 2, Hold=30, ADC Noise vs DAC



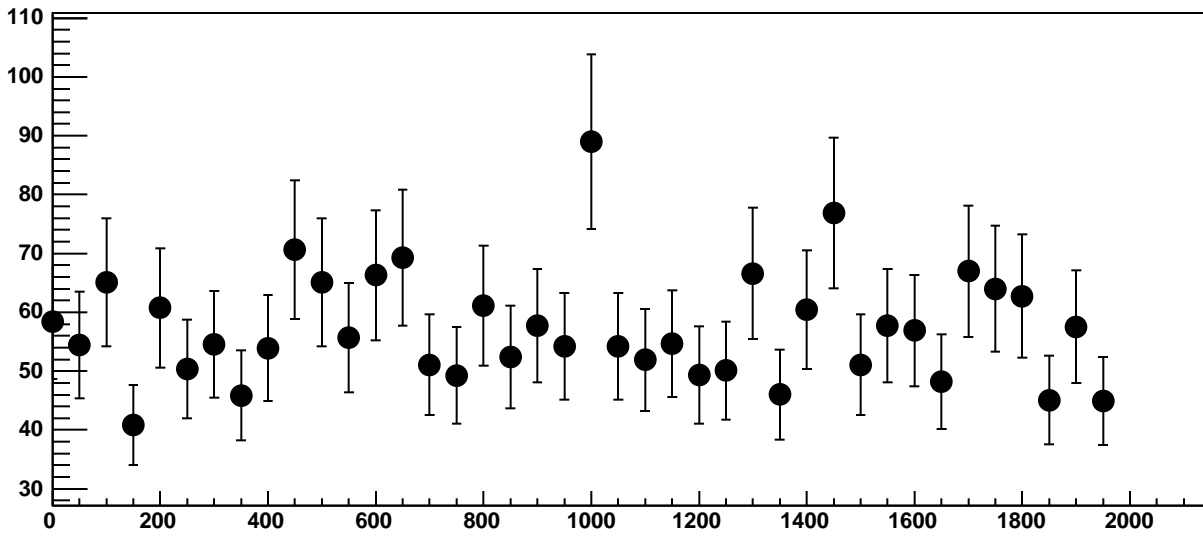
Chip 3, Channel 15, Enable 2, Hold=30, ADC Residuals vs DAC



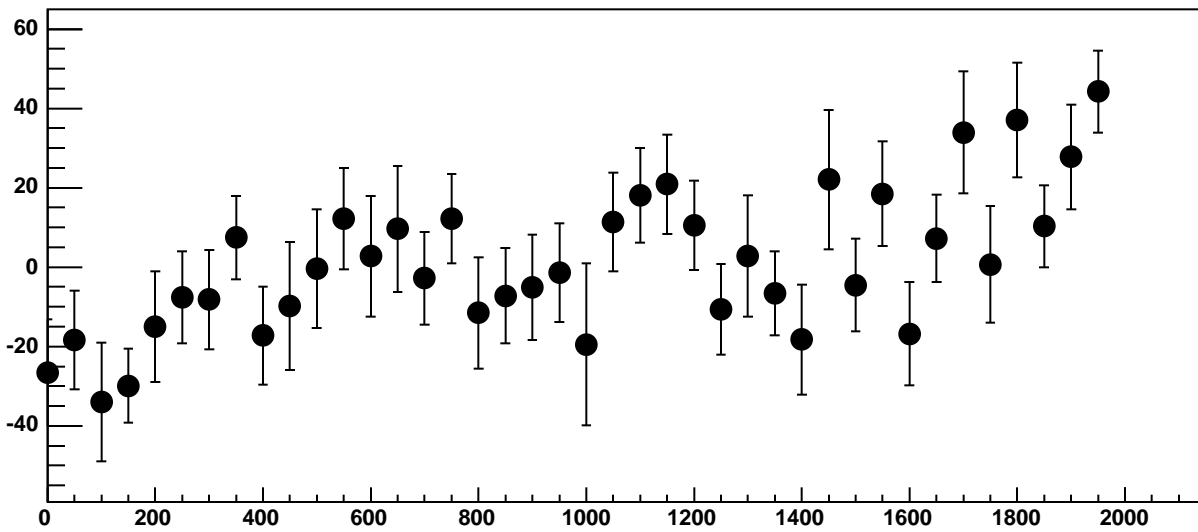
Chip 3, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC



Chip 3, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC

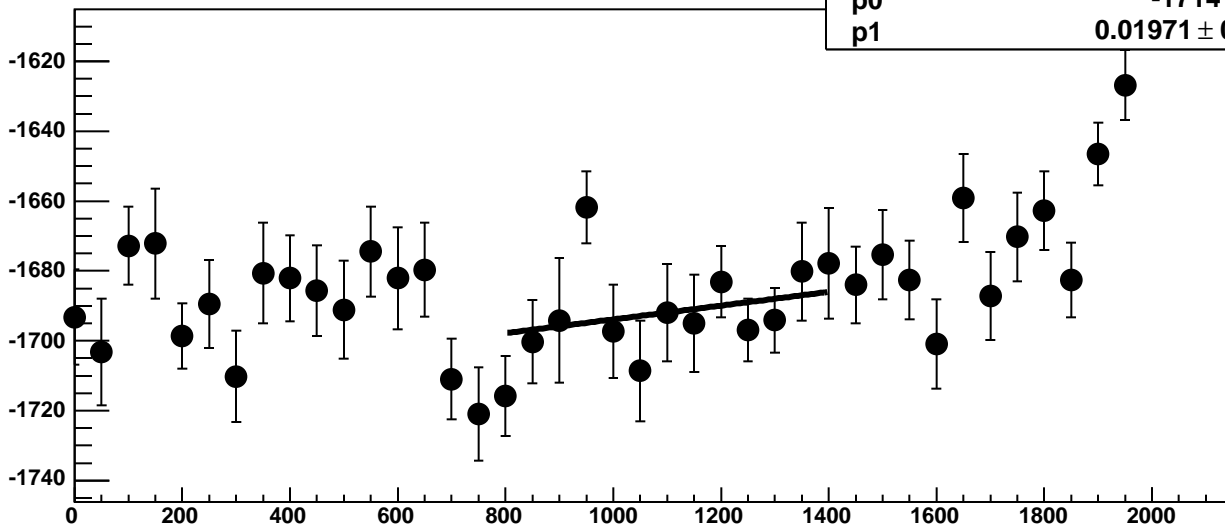


Chip 3, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC





Chip 3, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

16.3 / 11

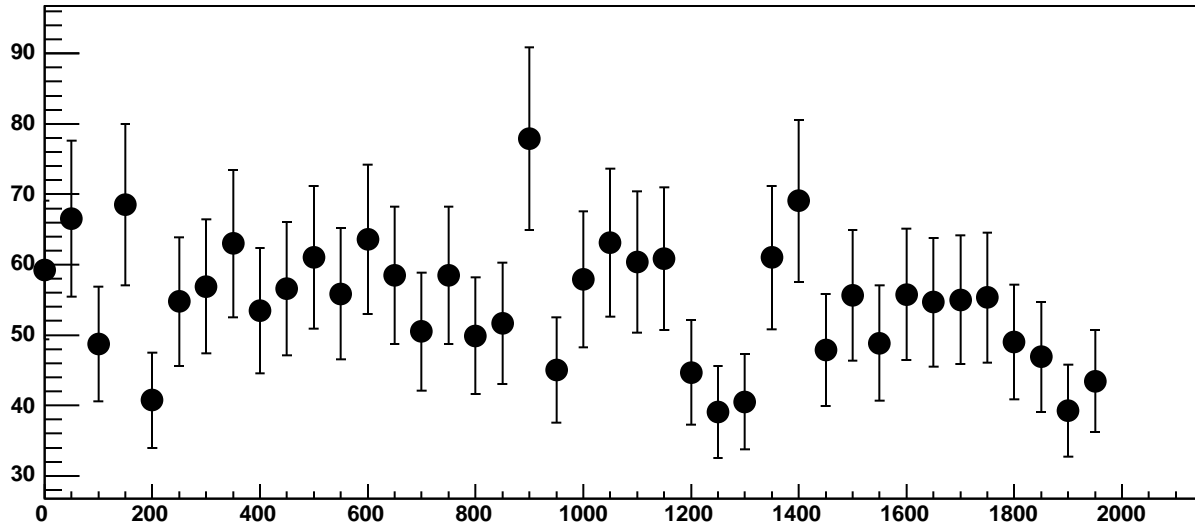
p0

$-1714 \pm 20.37$

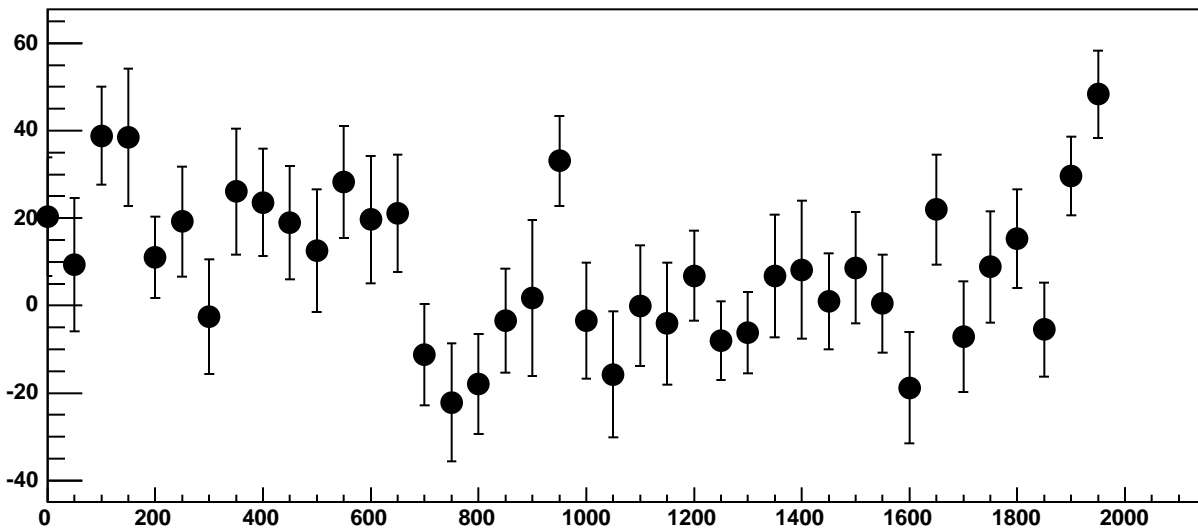
p1

$0.01971 \pm 0.01809$

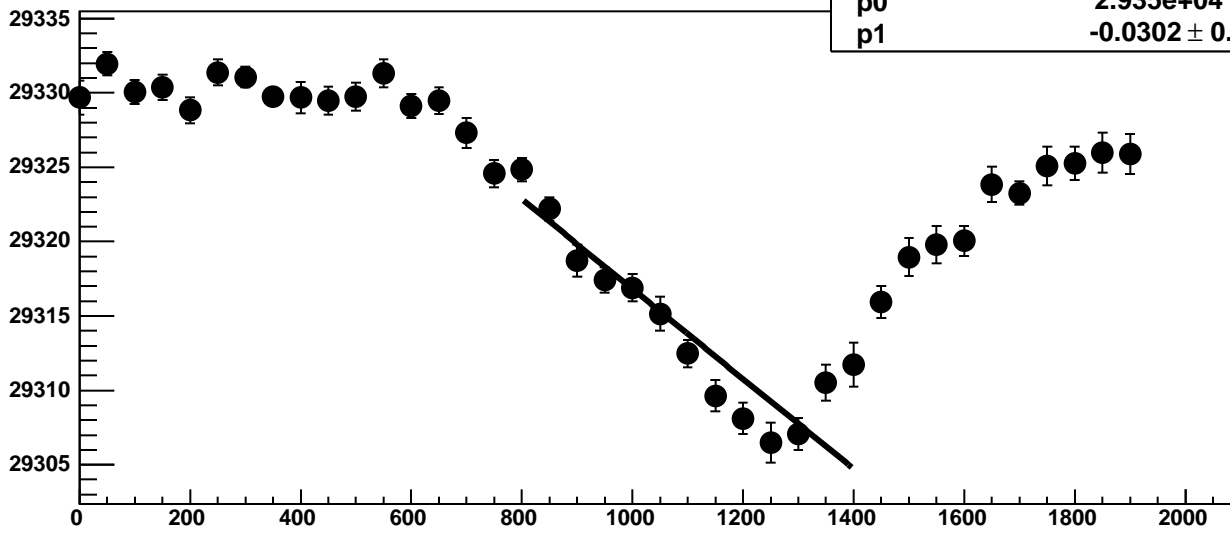
Chip 3, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC



Chip 3, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC

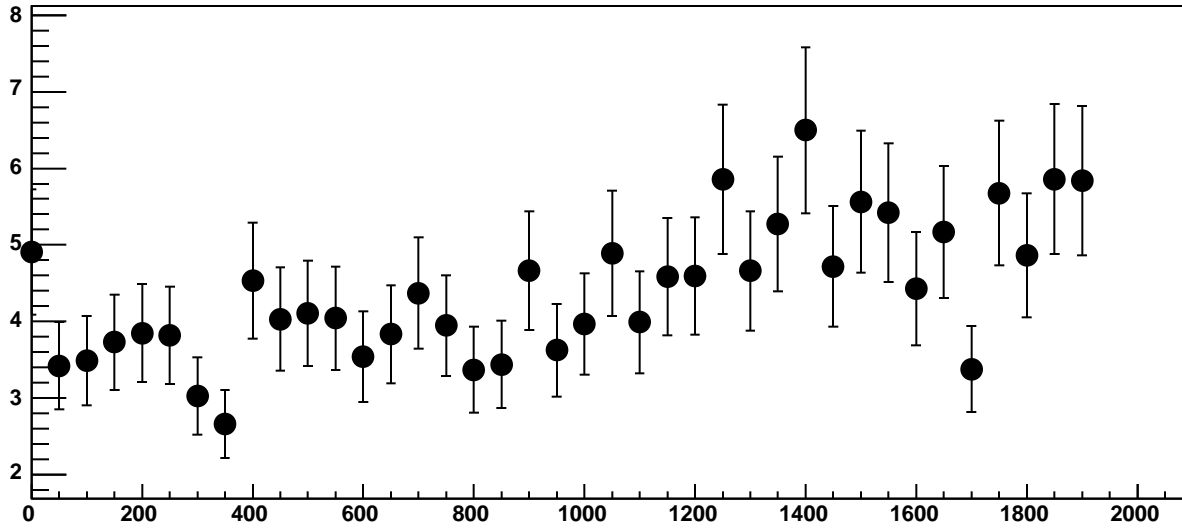


Chip 3, Channel 15, Enable 5!, Hold=30, ADC Mean vs DAC

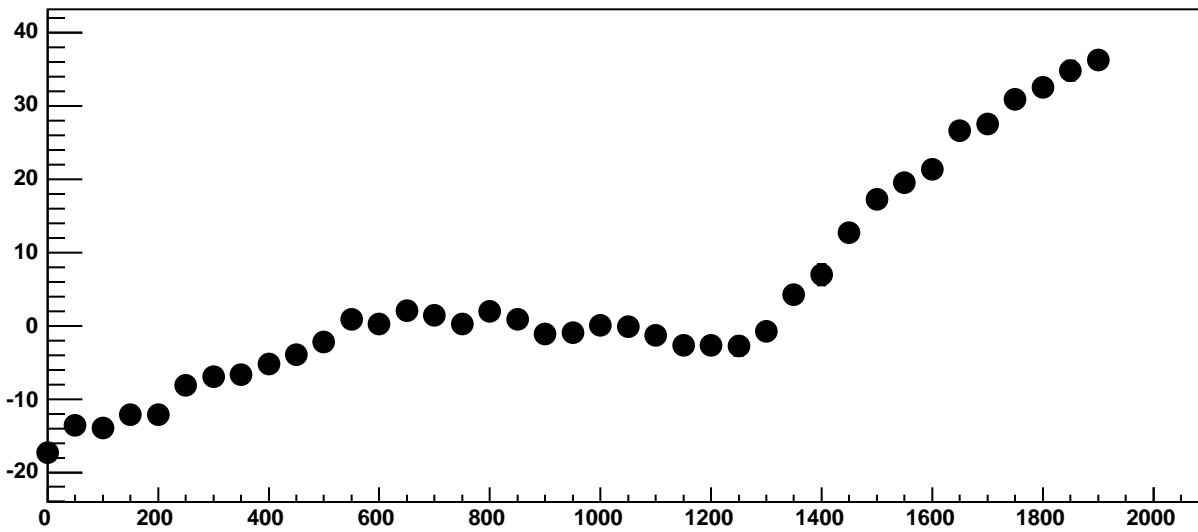


$\chi^2 / \text{ndf}$	64.27 / 11
p0	$2.935\text{e}+04 \pm 1.612$
p1	$-0.0302 \pm 0.001524$

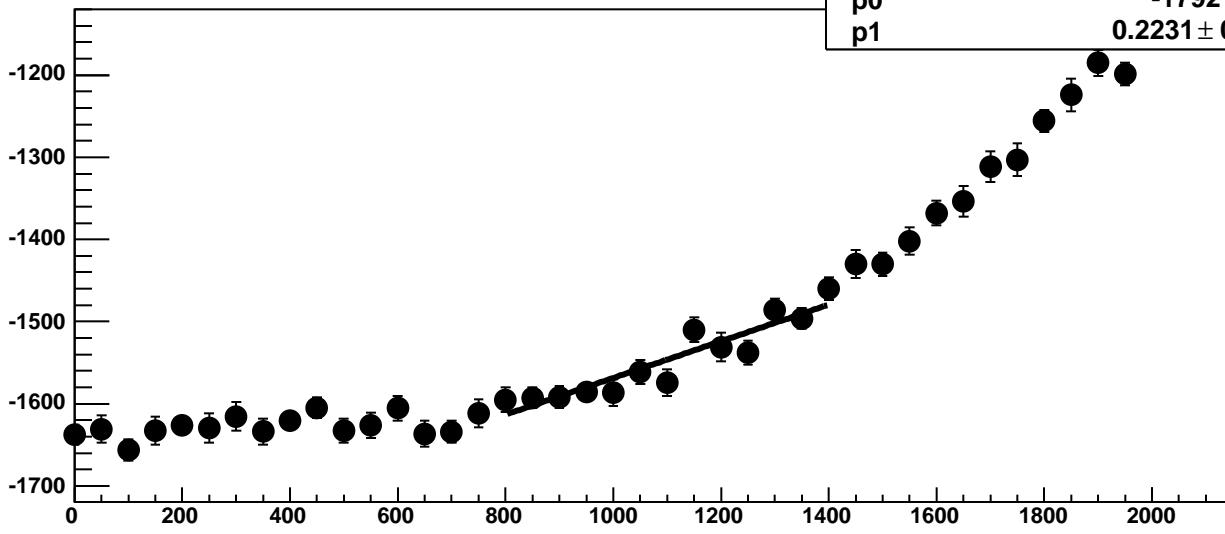
Chip 3, Channel 15, Enable 5!, Hold=30, ADC Noise vs DAC



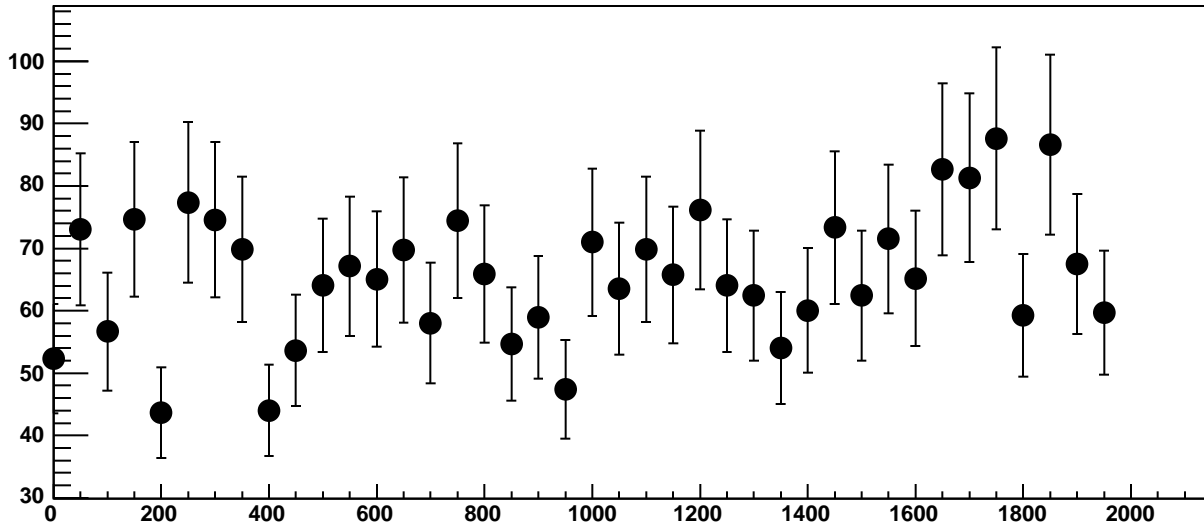
Chip 3, Channel 15, Enable 5!, Hold=30, ADC Residuals vs DAC



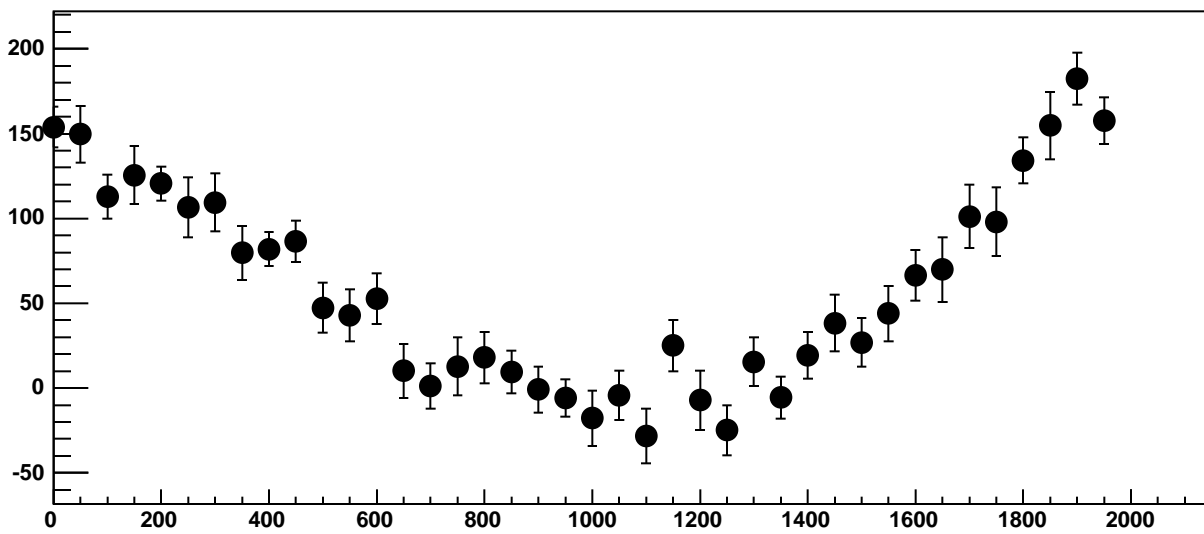
Chip 3, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC



Chip 3, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC

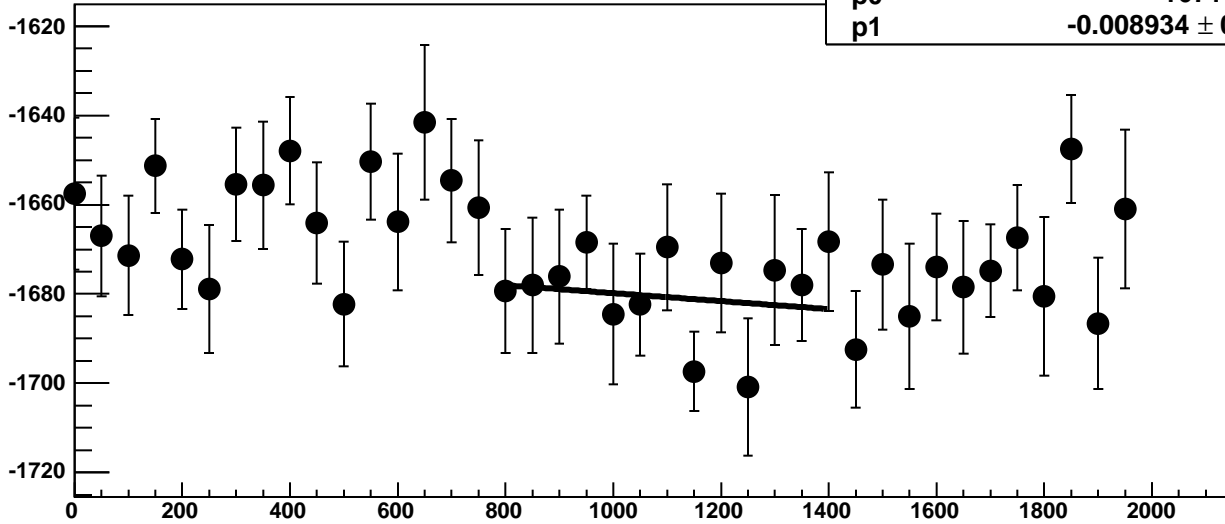


Chip 3, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC

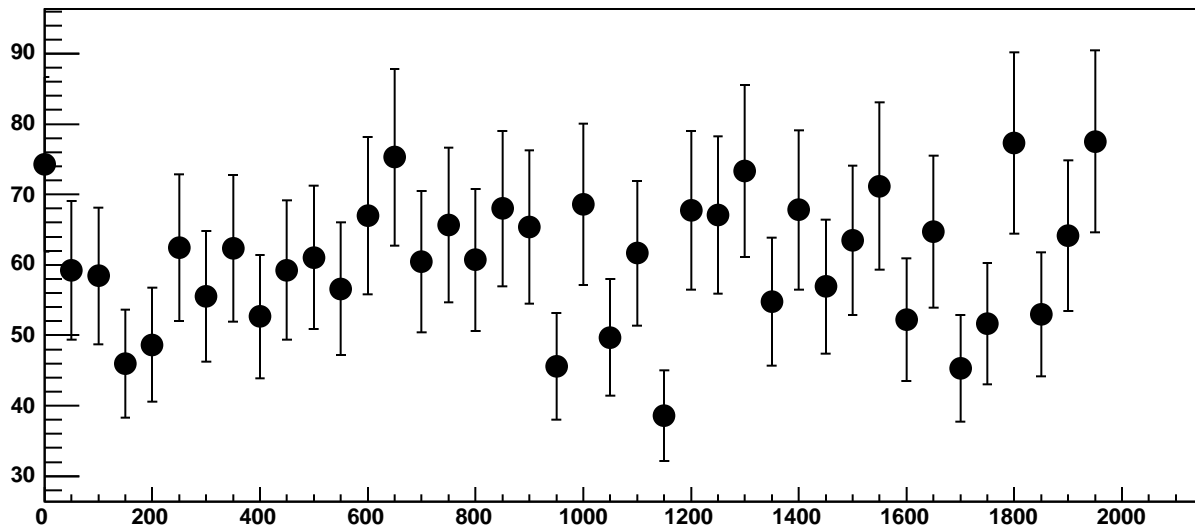


Chip 3, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC

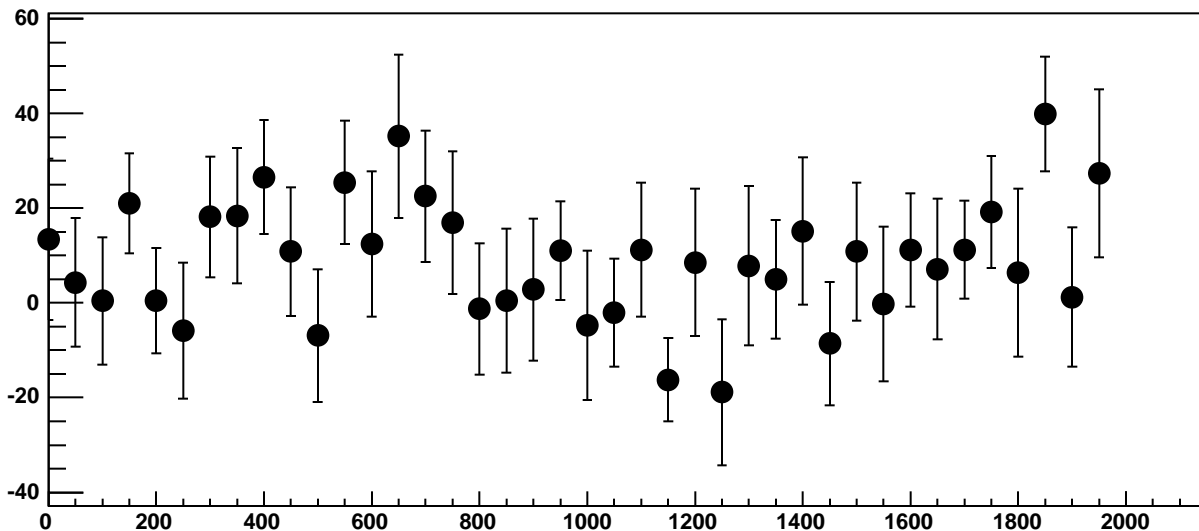
$\chi^2 / \text{ndf}$  8.371 / 11  
p0  $-1671 \pm 23.34$   
p1  $-0.008934 \pm 0.02107$



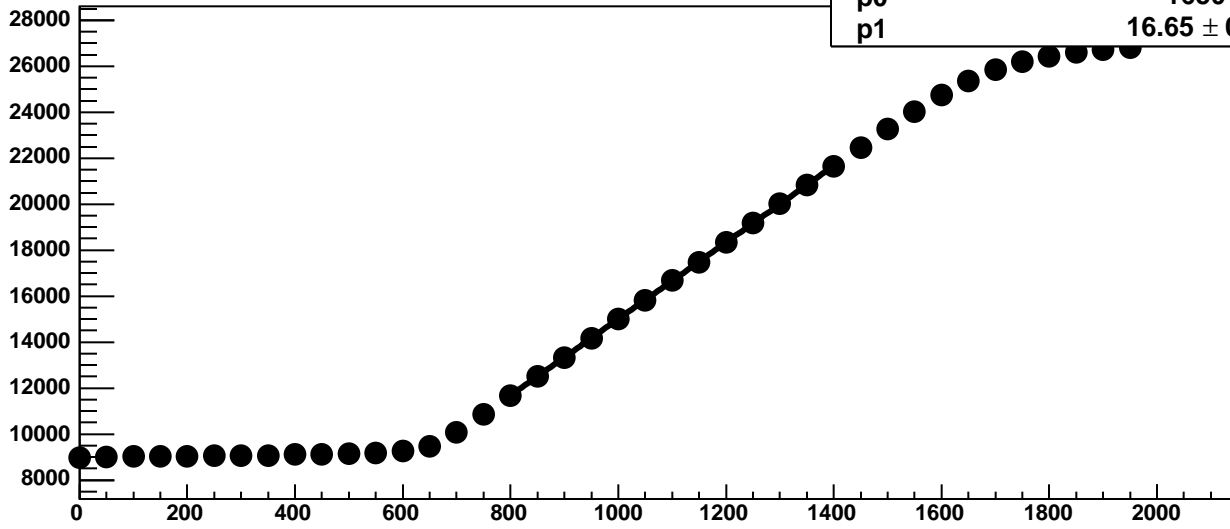
Chip 3, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC



Chip 3, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC

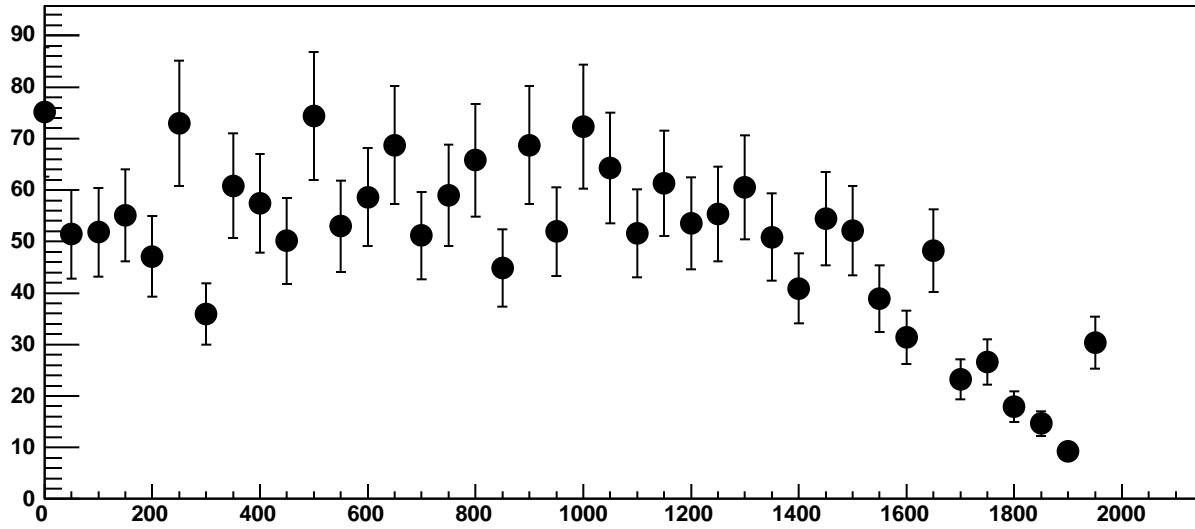


Chip 3, Channel 16, Enable 2!, Hold=30, ADC Mean vs DAC

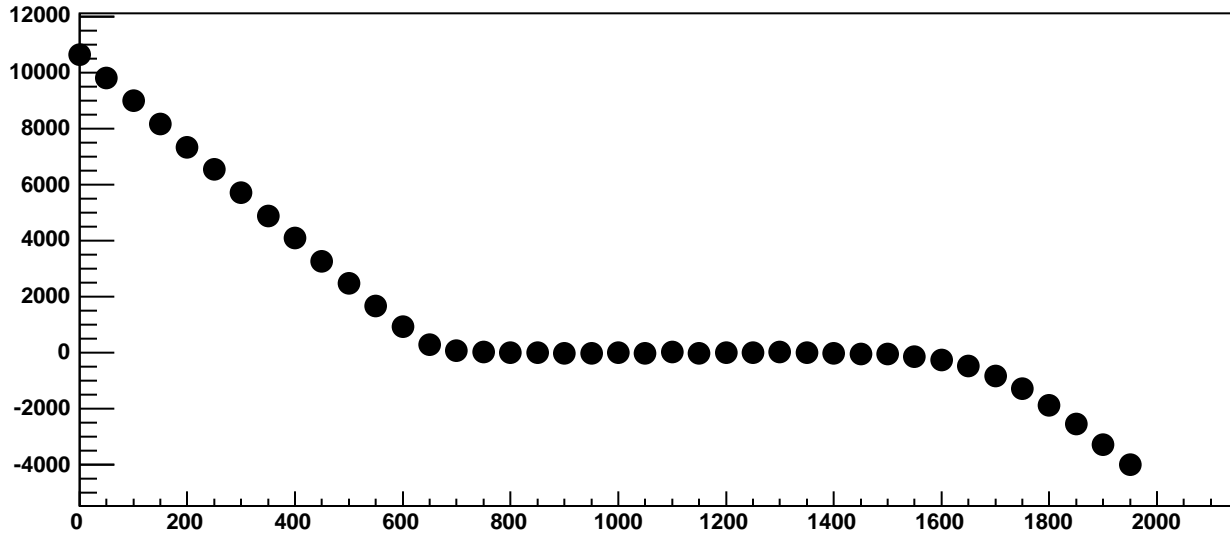


$\chi^2 / \text{ndf}$  23.29 / 11  
p0  $-1650 \pm 20.15$   
p1  $16.65 \pm 0.01765$

Chip 3, Channel 16, Enable 2!, Hold=30, ADC Noise vs DAC

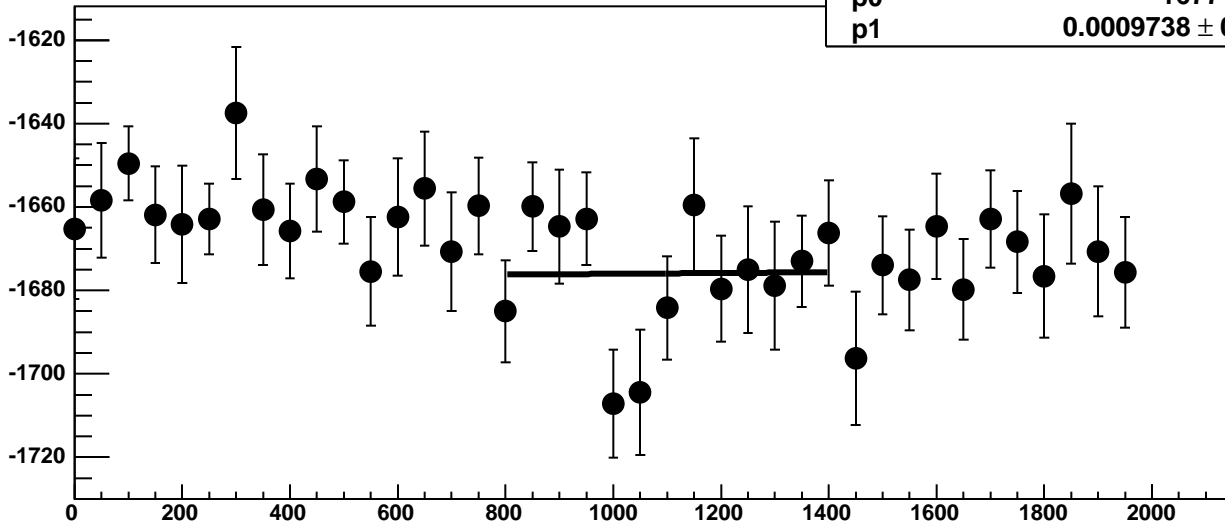


Chip 3, Channel 16, Enable 2!, Hold=30, ADC Residuals vs DAC

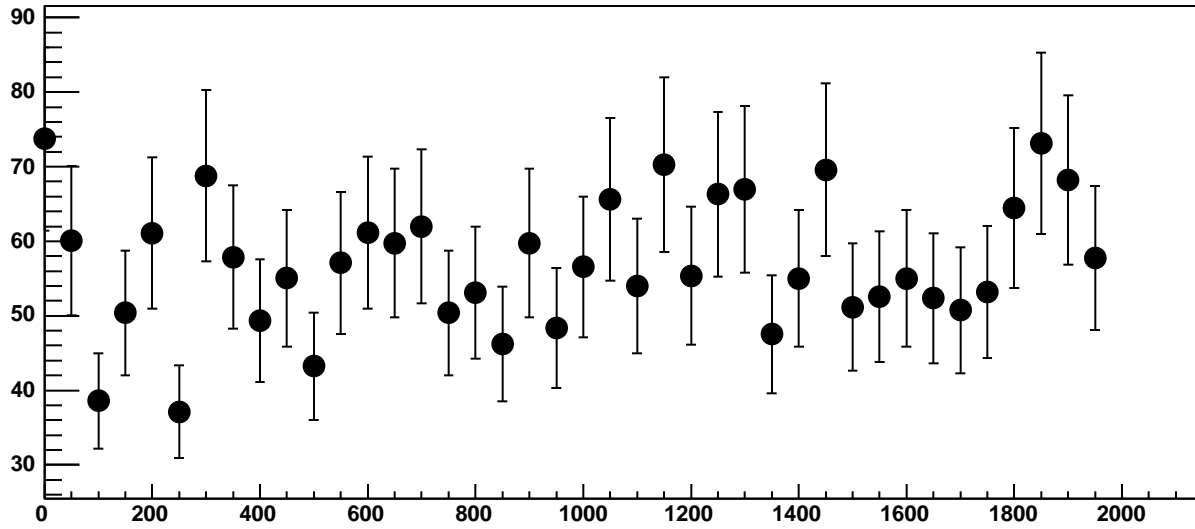


Chip 3, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC

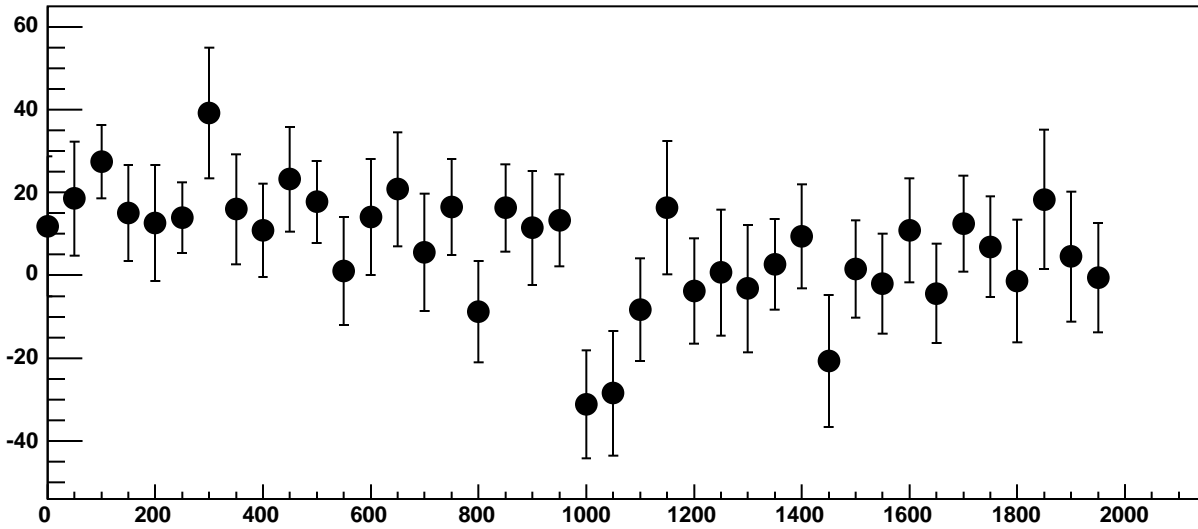
$\chi^2 / \text{ndf}$  16.54 / 11  
p0  $-1677 \pm 20.02$   
p1  $0.0009738 \pm 0.01813$



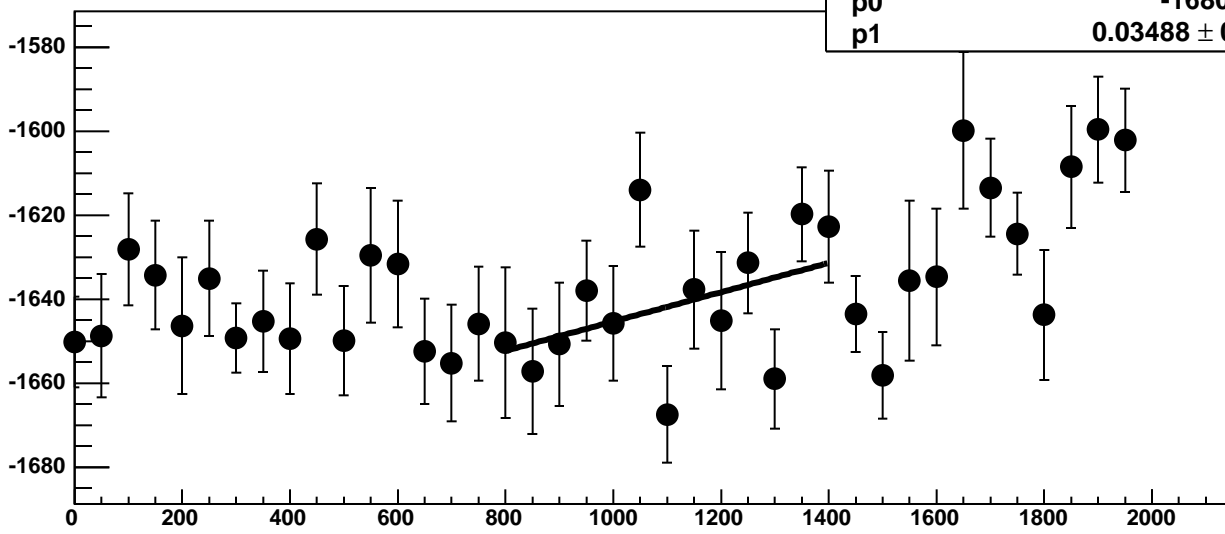
Chip 3, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC



Chip 3, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC

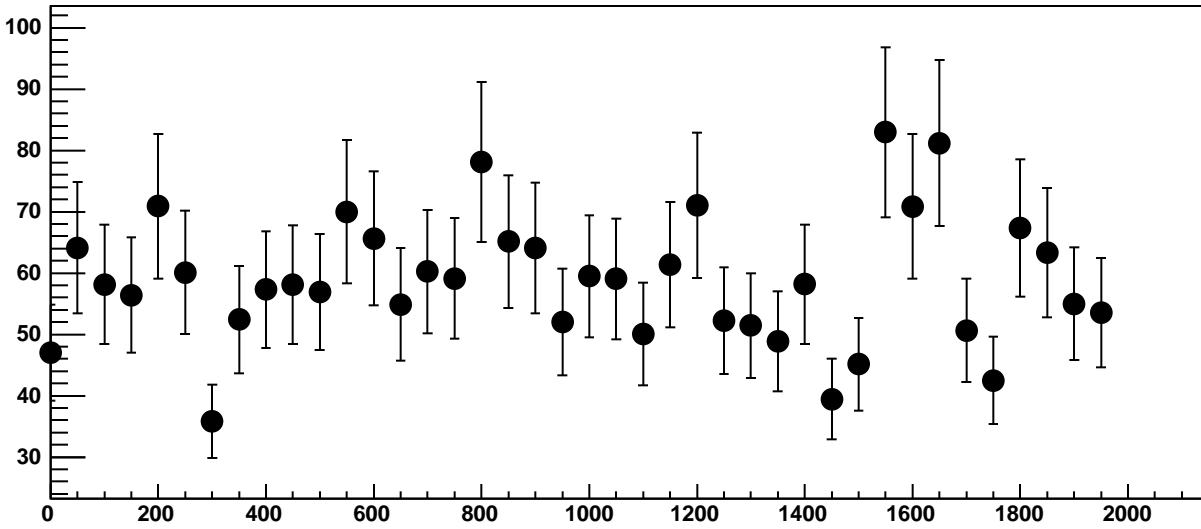


Chip 3, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC

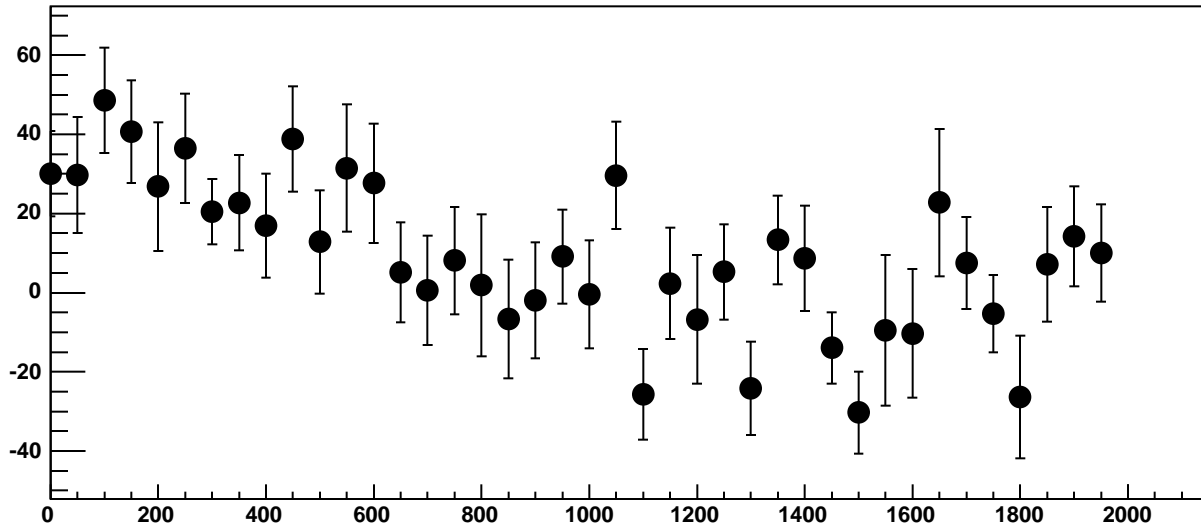


$\chi^2 / \text{ndf}$  16.99 / 11  
p0  $-1680 \pm 23.2$   
p1  $0.03488 \pm 0.02034$

Chip 3, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC

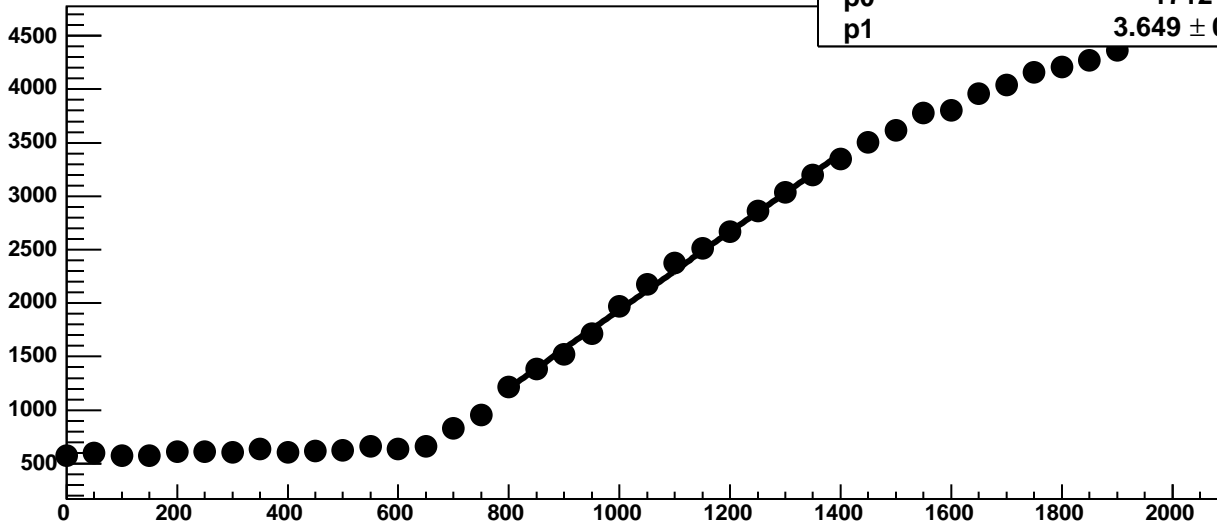


Chip 3, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC

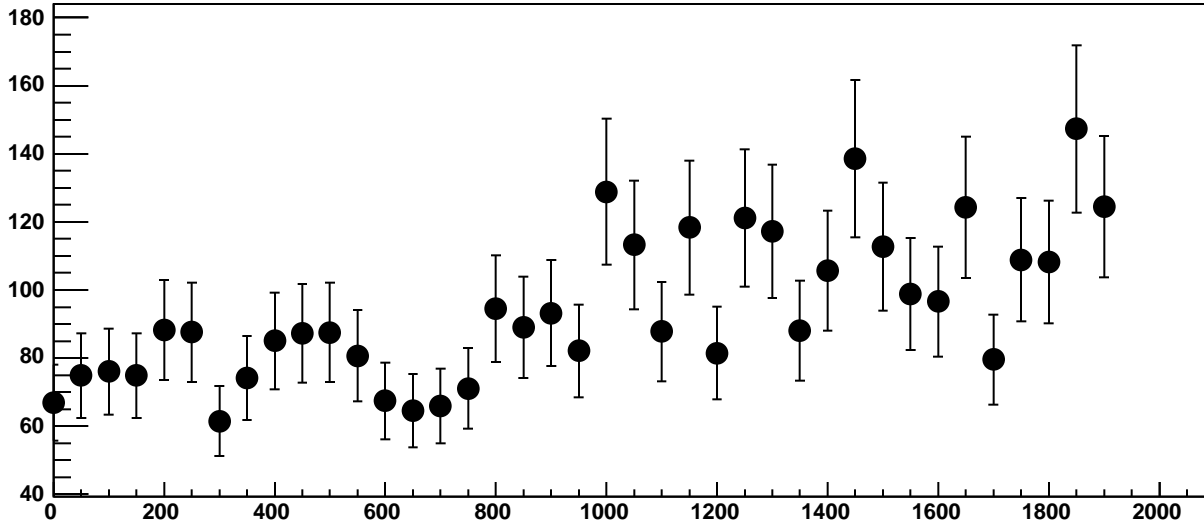


Chip 3, Channel 16, Enable 5, Hold=30, ADC Mean vs DAC

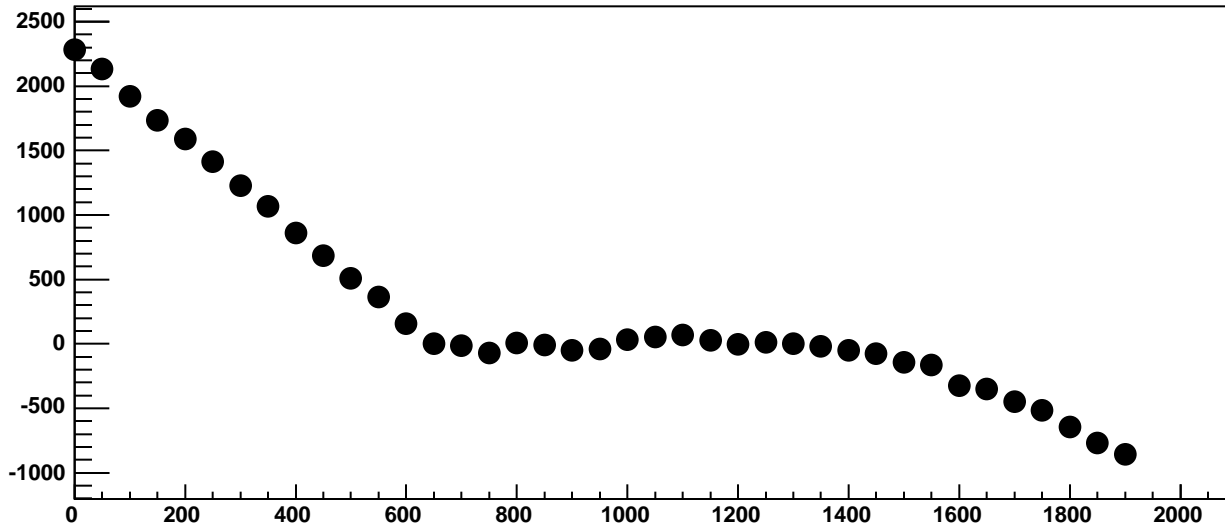
$\chi^2 / \text{ndf}$  34.56 / 11  
p0  $-1712 \pm 36.27$   
p1  $3.649 \pm 0.03287$



Chip 3, Channel 16, Enable 5, Hold=30, ADC Noise vs DAC

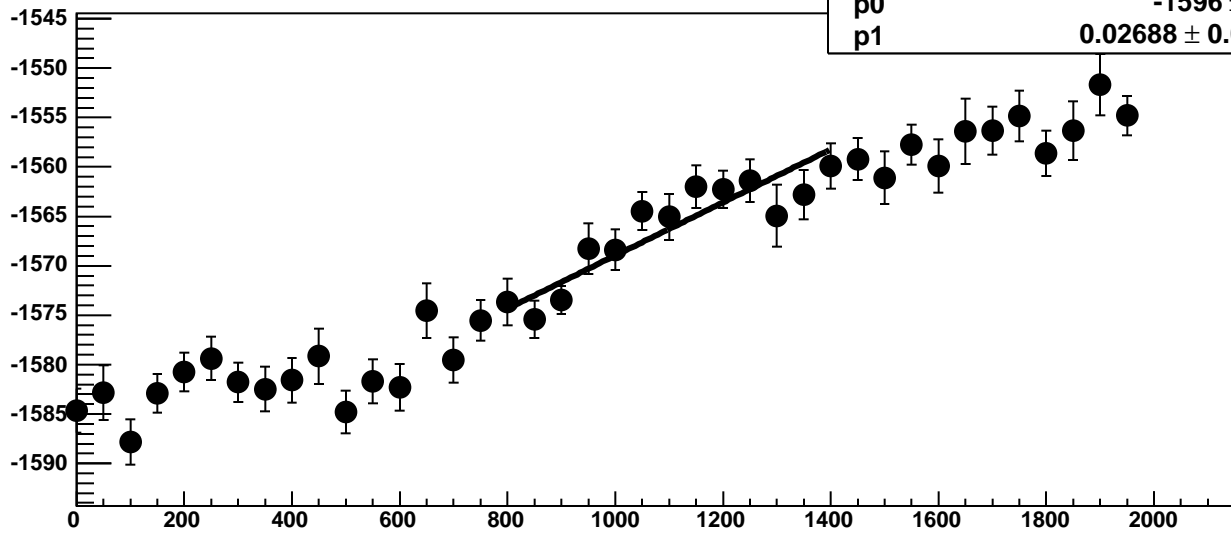


Chip 3, Channel 16, Enable 5, Hold=30, ADC Residuals vs DAC



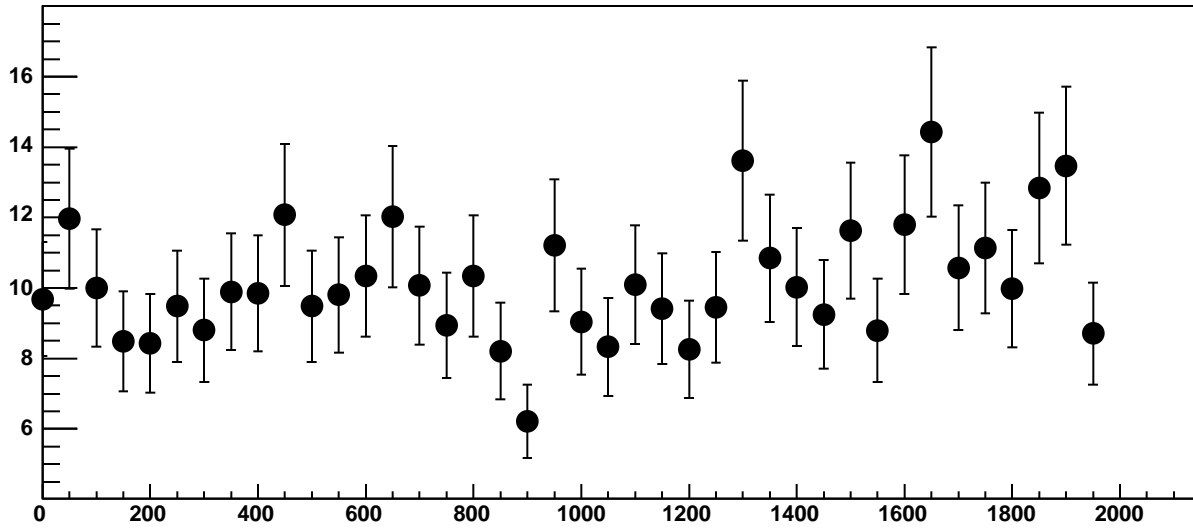


Chip 3, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC

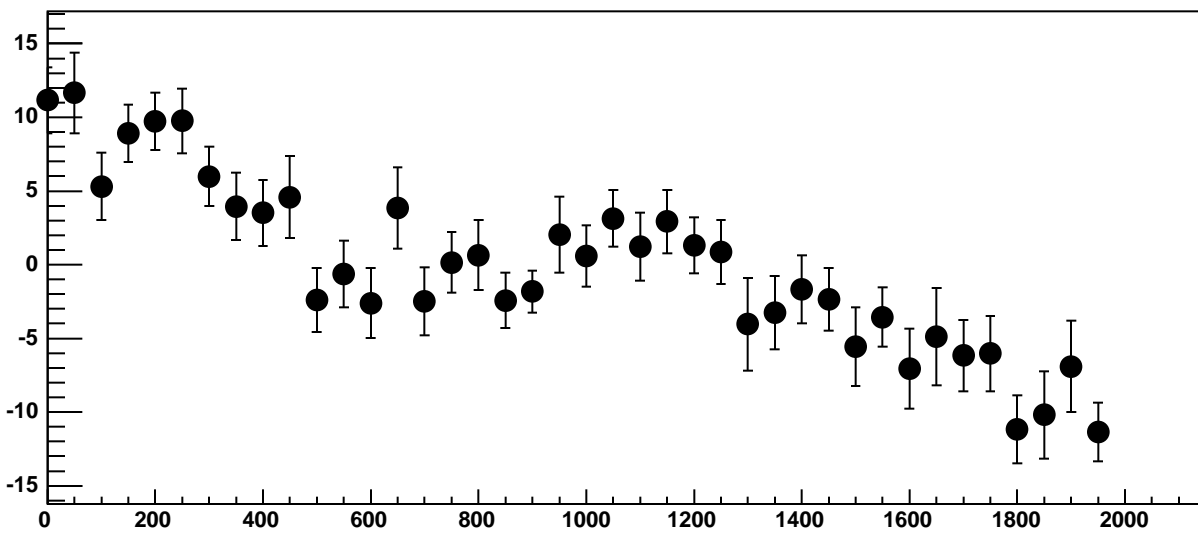


$\chi^2 / \text{ndf}$  13.47 / 11  
p0  $-1596 \pm 3.495$   
p1  $0.02688 \pm 0.003226$

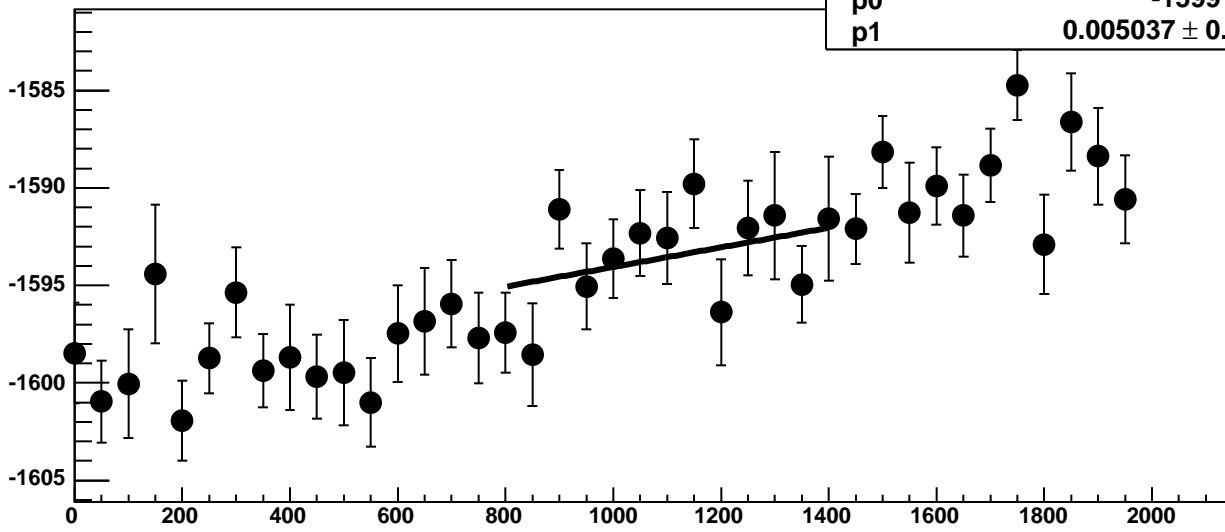
Chip 3, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC



Chip 3, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC

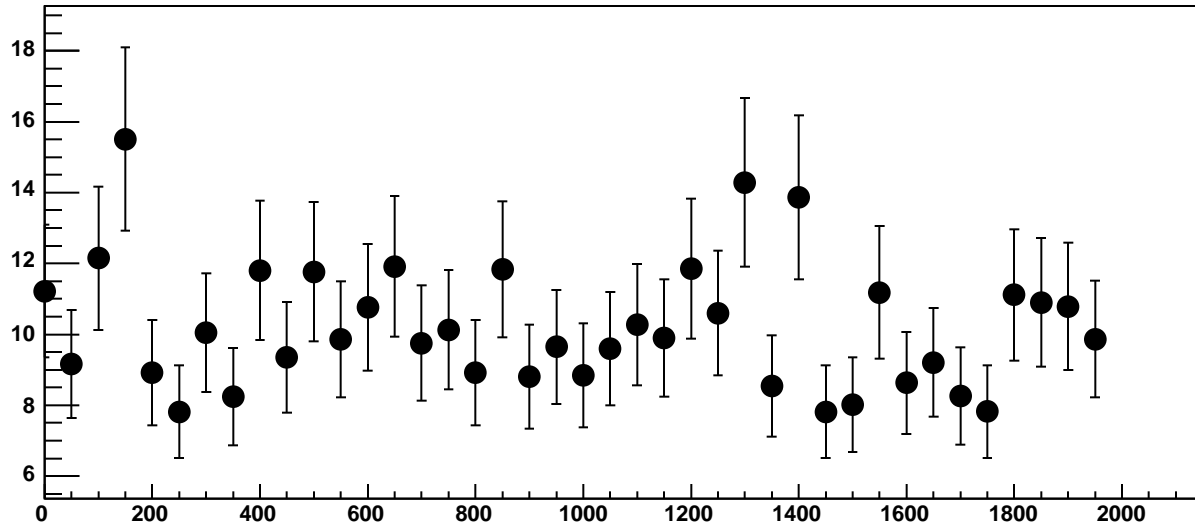


Chip 3, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC

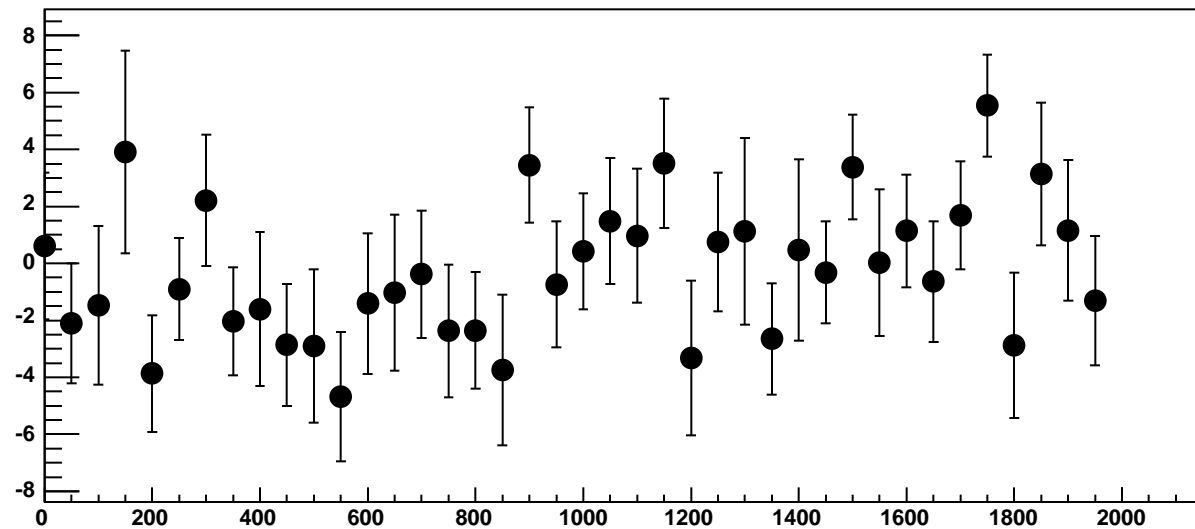


$\chi^2 / \text{ndf}$  12.99 / 11  
p0  $-1599 \pm 3.836$   
p1  $0.005037 \pm 0.003517$

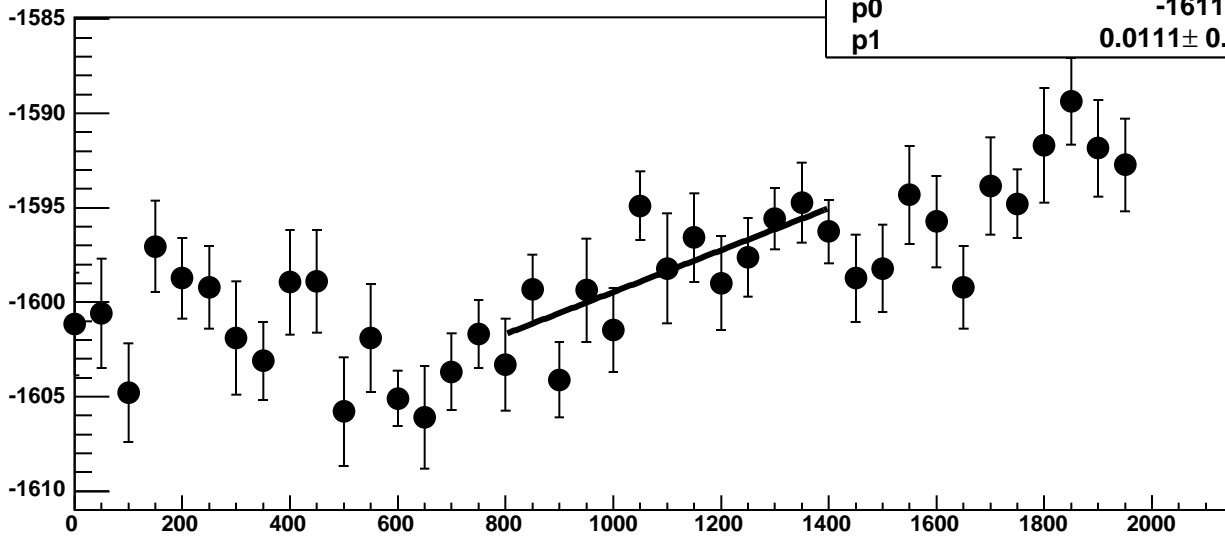
Chip 3, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC



Chip 3, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC

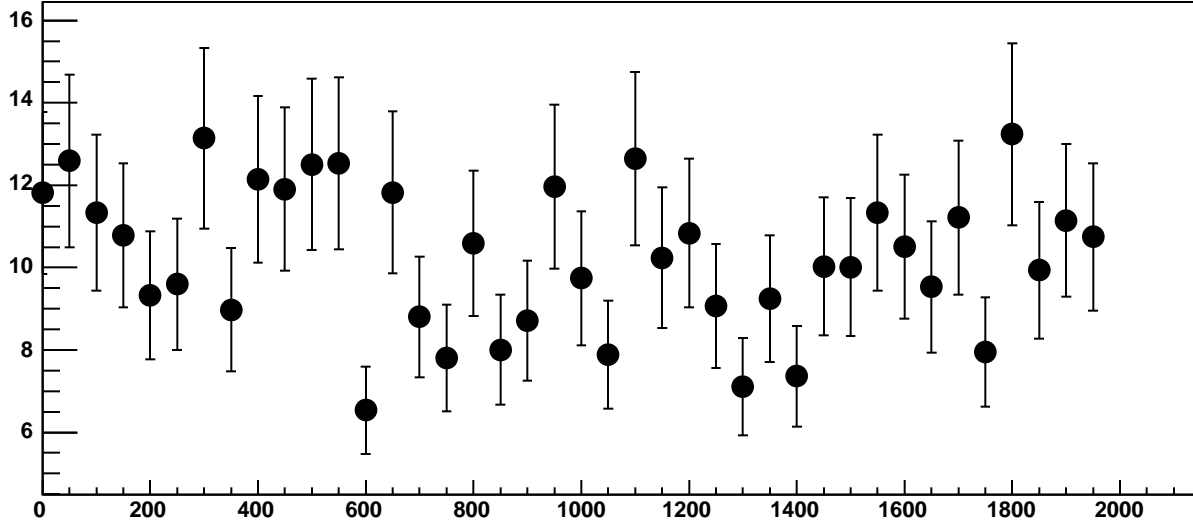


Chip 3, Channel 17, Enable 2, Hold=30, ADC Mean vs DAC

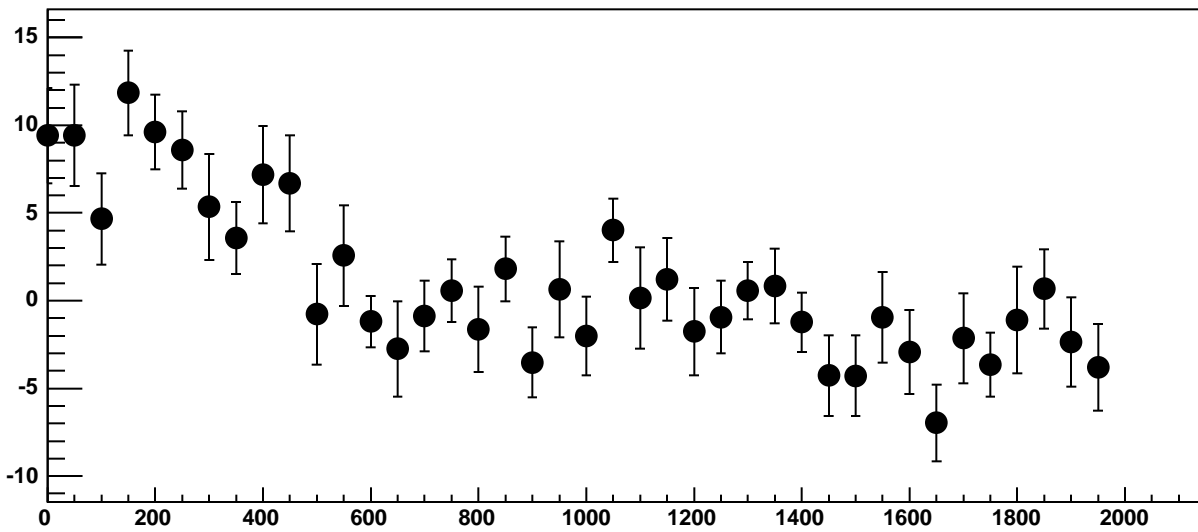


$\chi^2 / \text{ndf}$  12.12 / 11  
p0  $-1611 \pm 3.347$   
p1  $0.0111 \pm 0.002938$

Chip 3, Channel 17, Enable 2, Hold=30, ADC Noise vs DAC

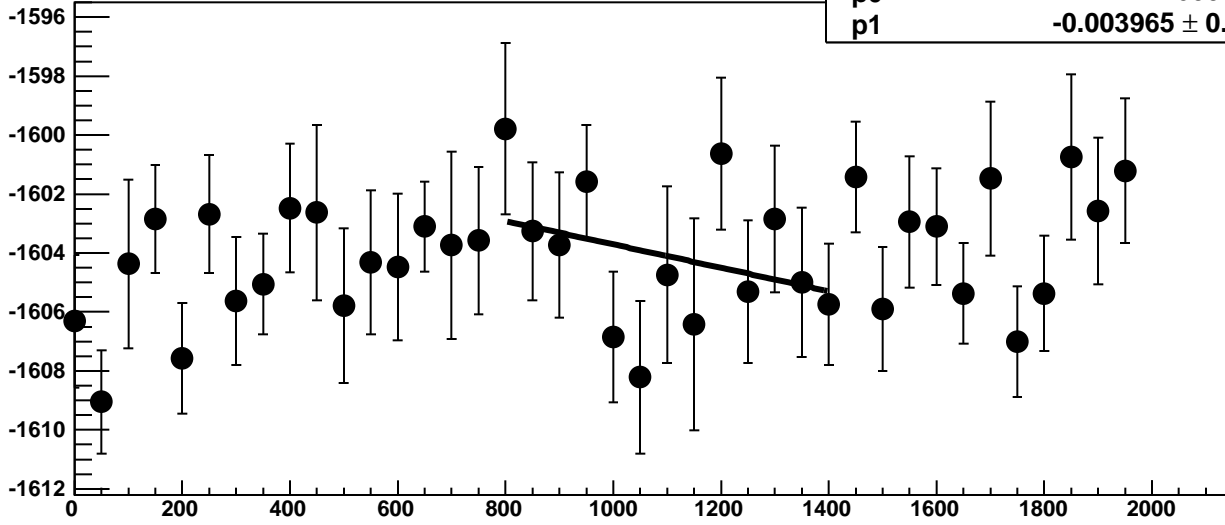


Chip 3, Channel 17, Enable 2, Hold=30, ADC Residuals vs DAC

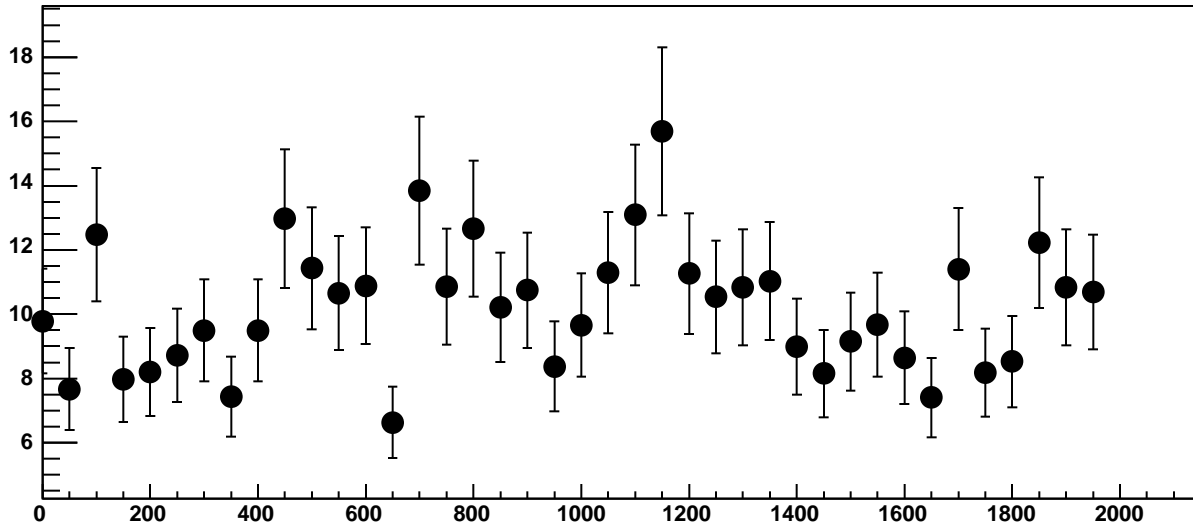


Chip 3, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC

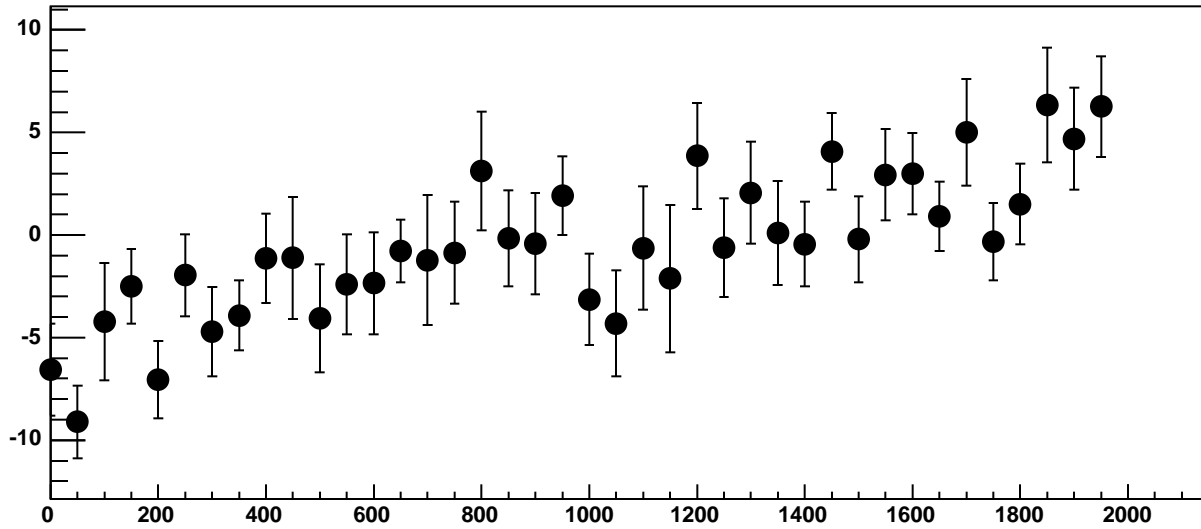
$\chi^2 / \text{ndf}$  10.39 / 11  
p0  $-1600 \pm 3.954$   
p1  $-0.003965 \pm 0.003534$



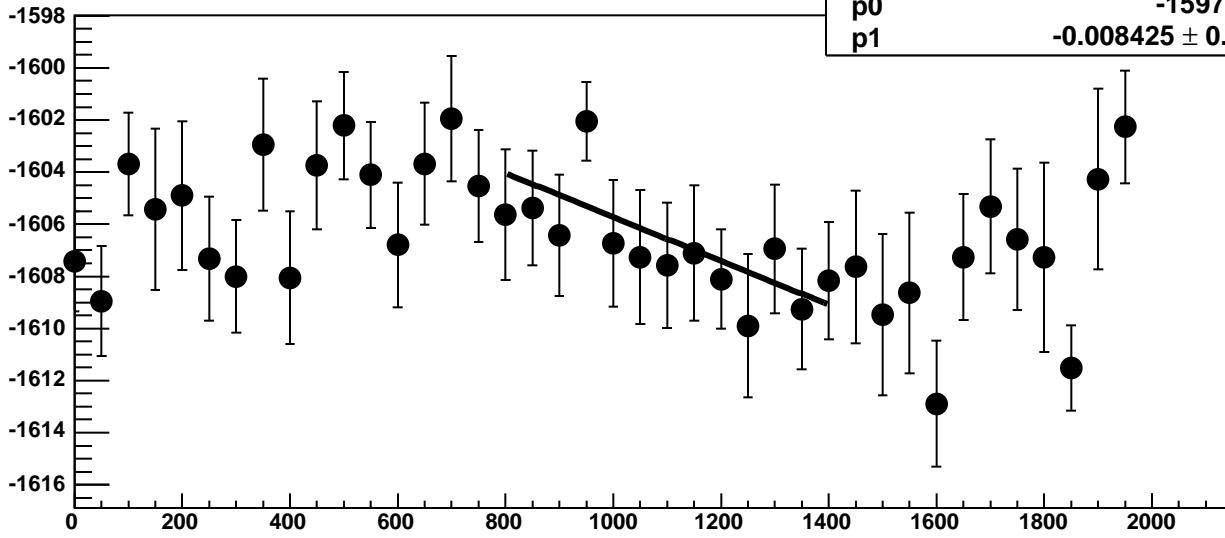
Chip 3, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC



Chip 3, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC

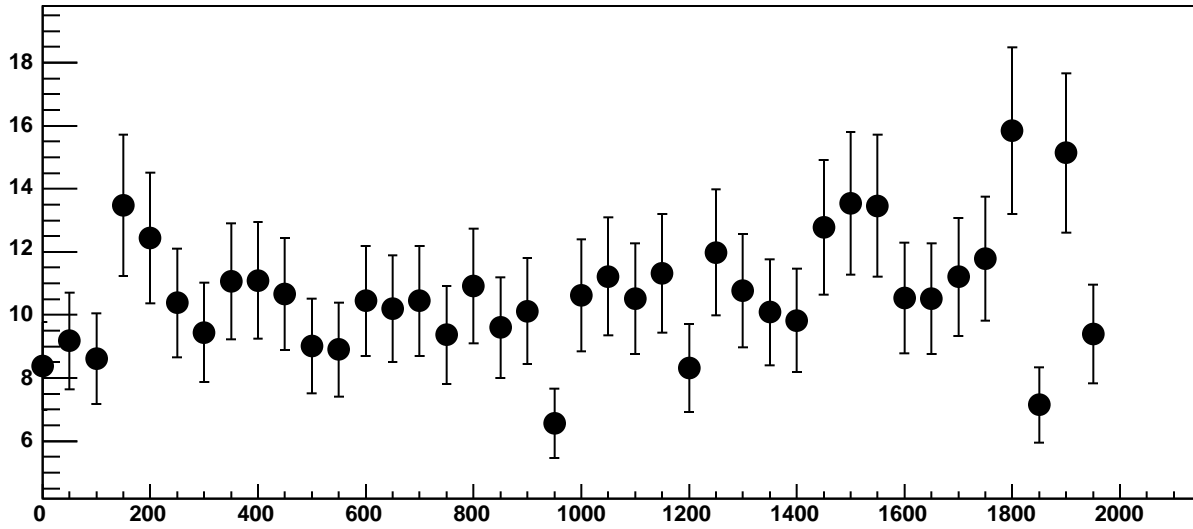


Chip 3, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC

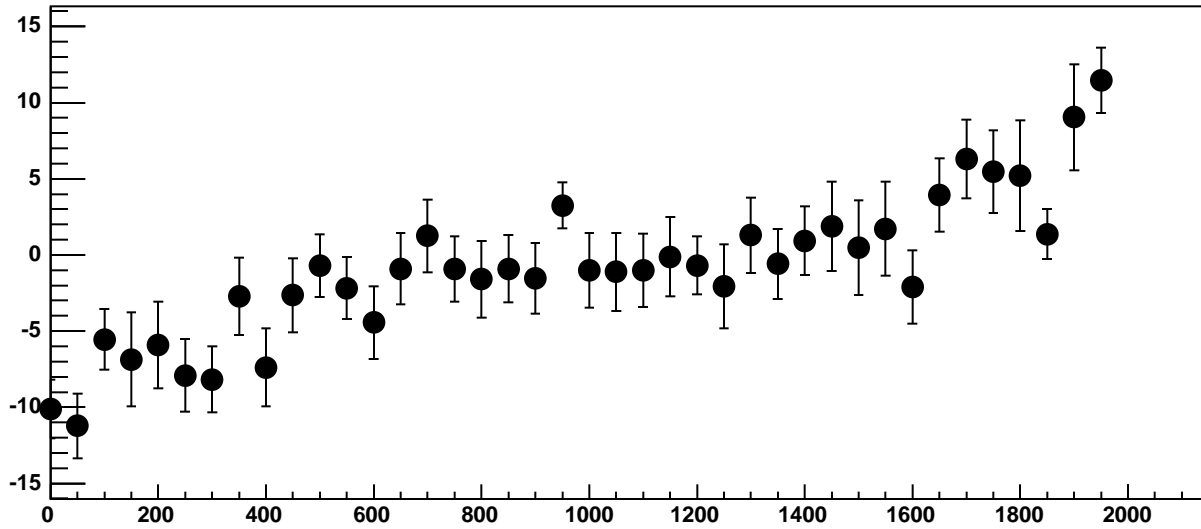


$\chi^2 / \text{ndf}$  7.429 / 11  
p0  $-1597 \pm 3.711$   
p1  $-0.008425 \pm 0.003366$

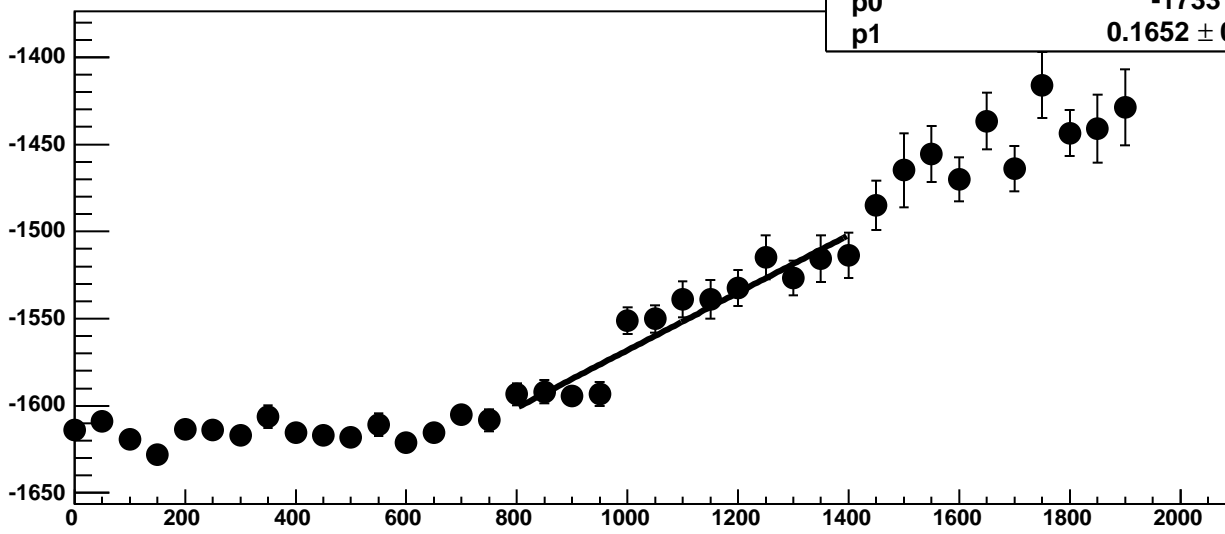
Chip 3, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC



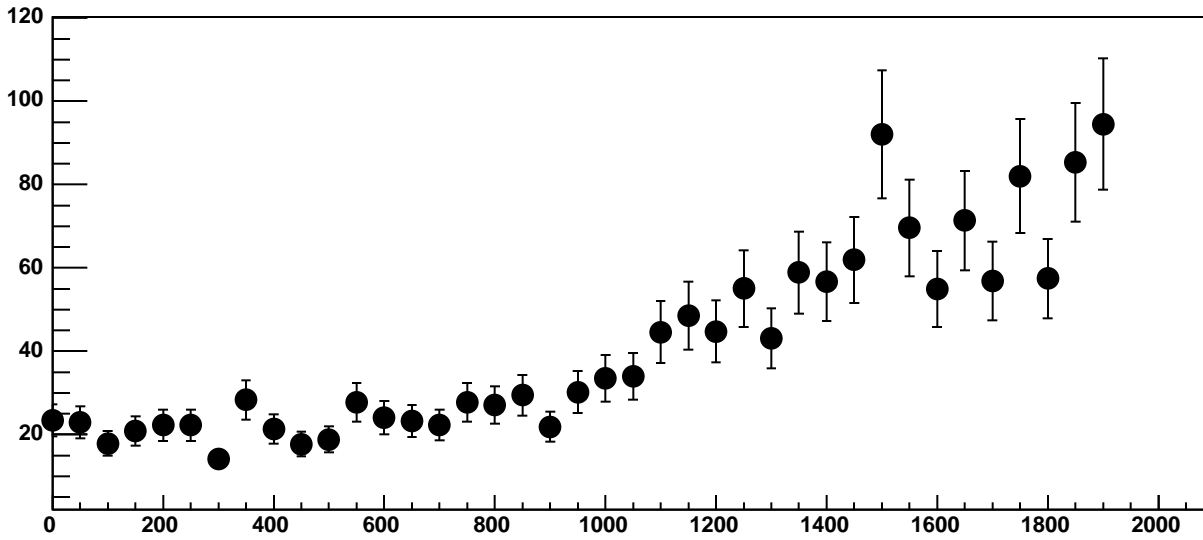
Chip 3, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC



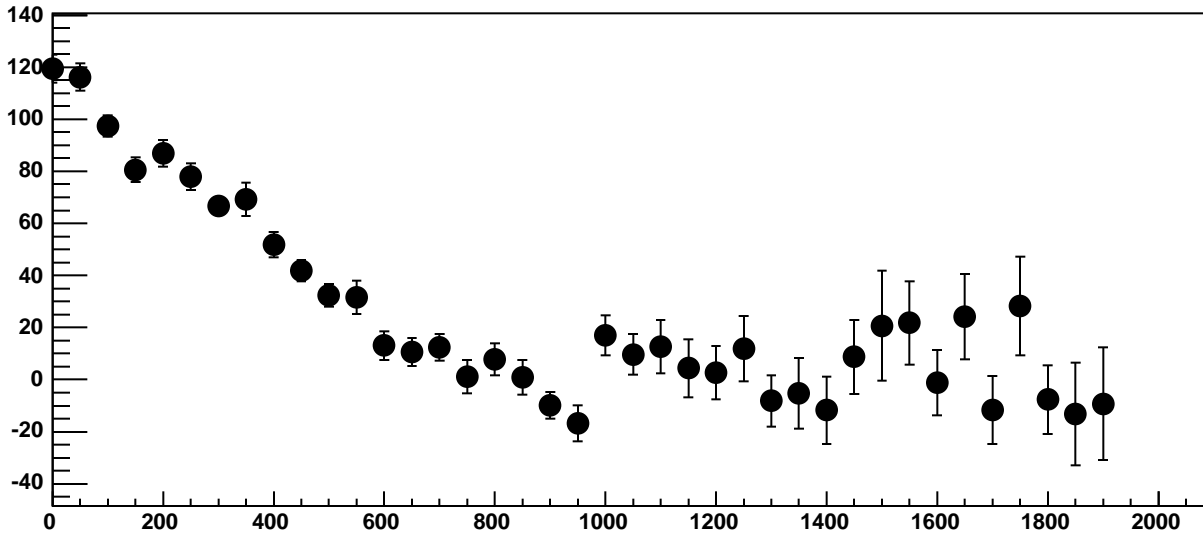
Chip 3, Channel 17, Enable 5!, Hold=30, ADC Mean vs DAC



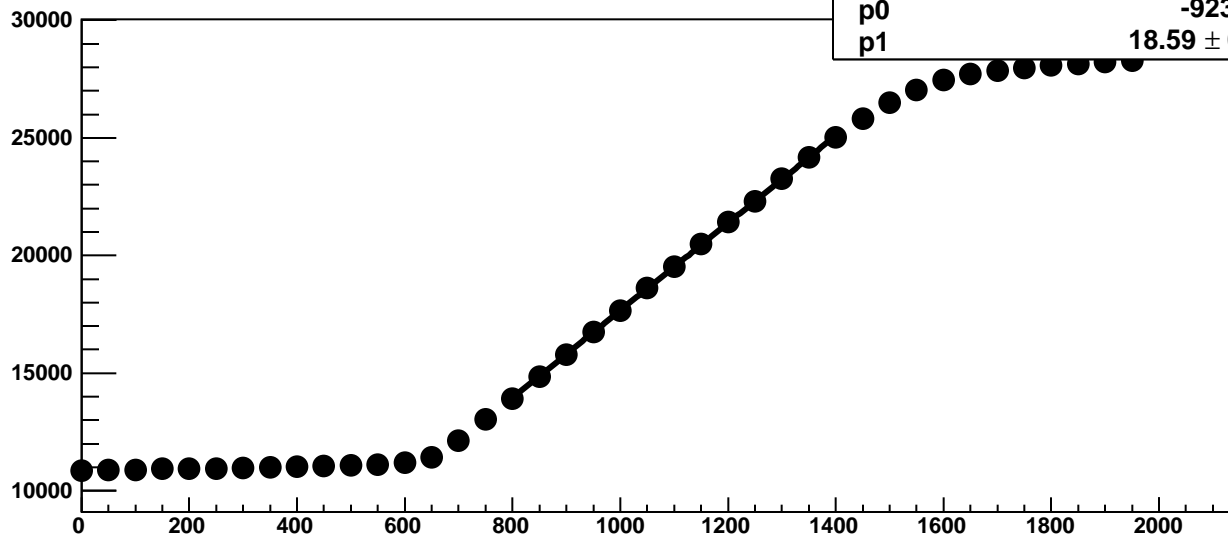
Chip 3, Channel 17, Enable 5!, Hold=30, ADC Noise vs DAC



Chip 3, Channel 17, Enable 5!, Hold=30, ADC Residuals vs DAC

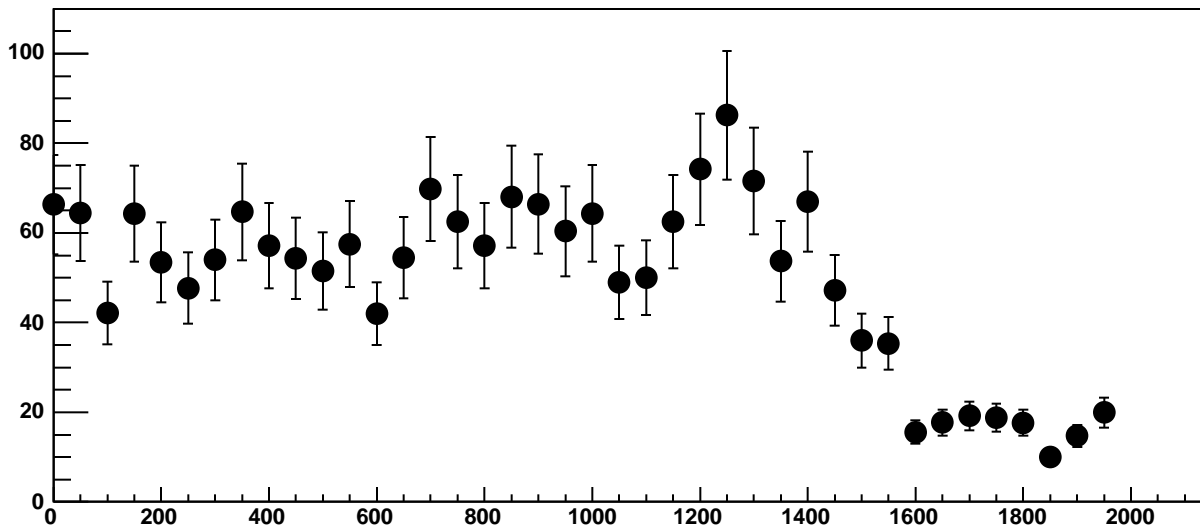


Chip 4, Channel 0, Enable 0!, Hold=30, ADC Mean vs DAC

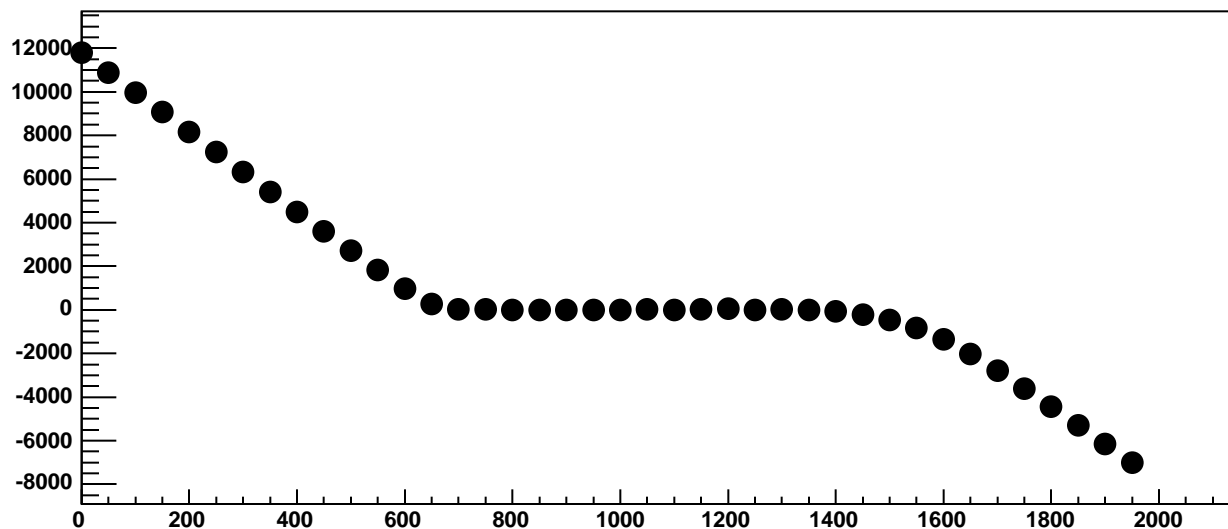


$\chi^2 / \text{ndf}$  57.42 / 11  
p0  $-923 \pm 23.7$   
p1  $18.59 \pm 0.02144$

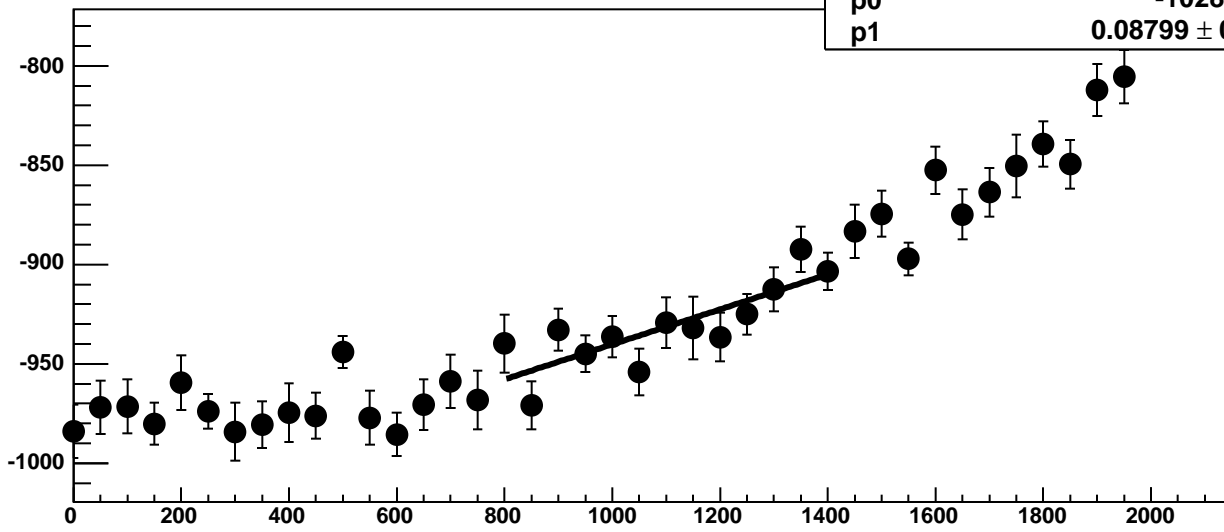
Chip 4, Channel 0, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 4, Channel 0, Enable 0!, Hold=30, ADC Residuals vs DAC

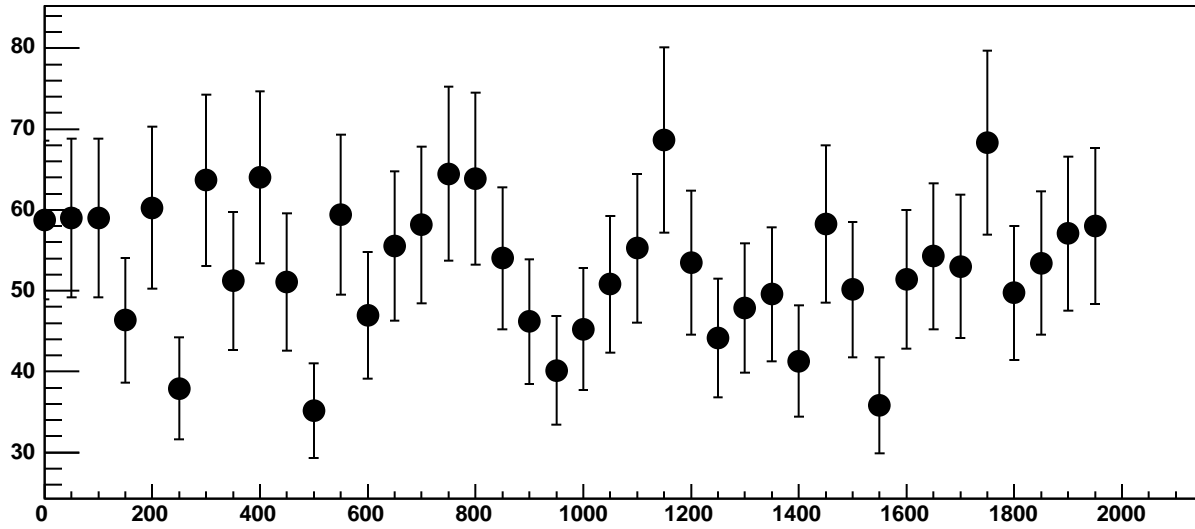


Chip 4, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC

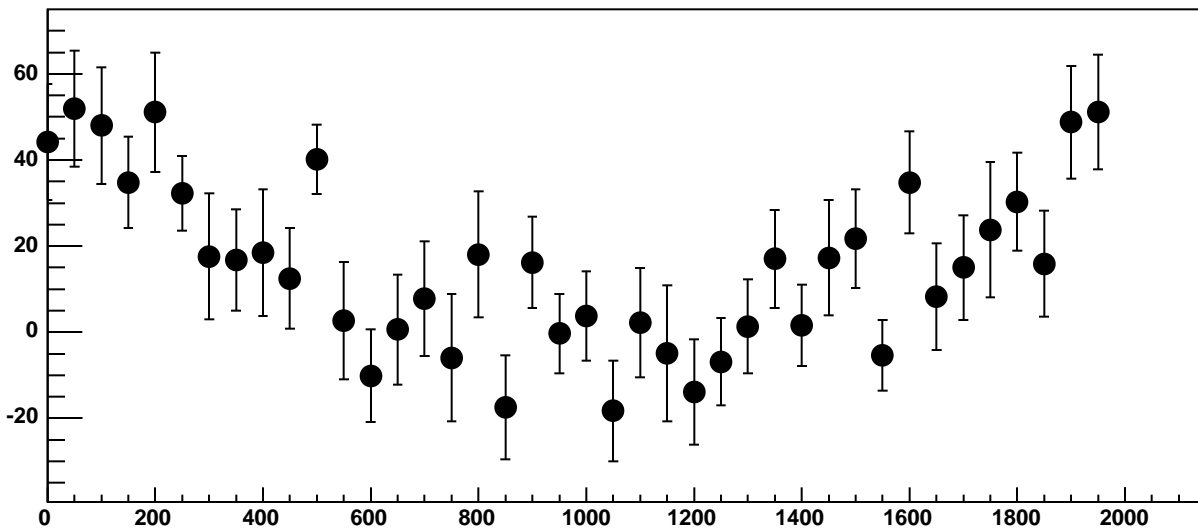


$\chi^2 / \text{ndf}$  12.69 / 11  
p0  $-1028 \pm 18.61$   
p1  $0.08799 \pm 0.01649$

Chip 4, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC

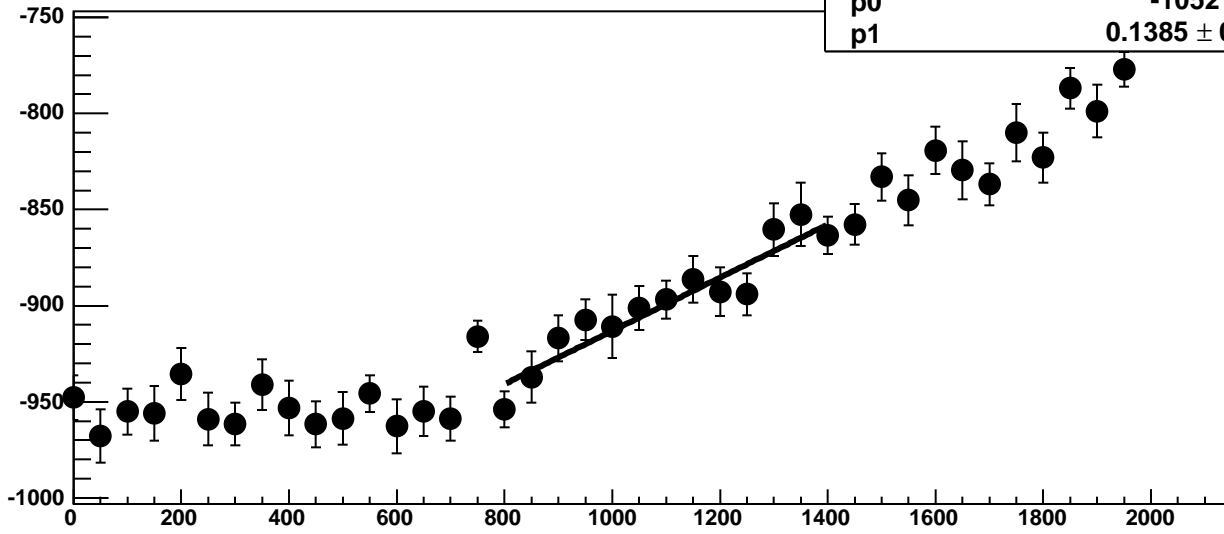


Chip 4, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC

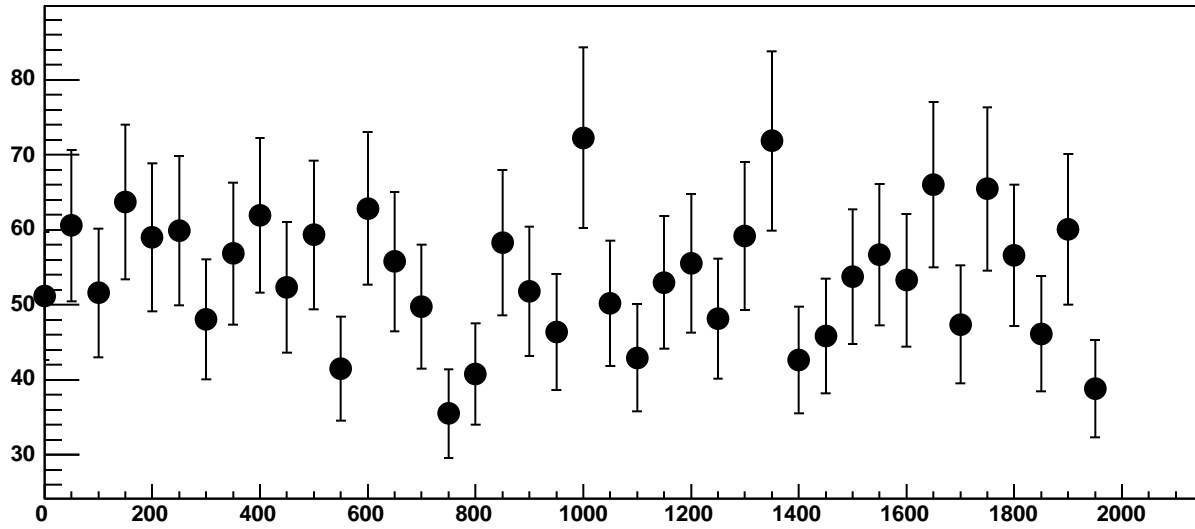




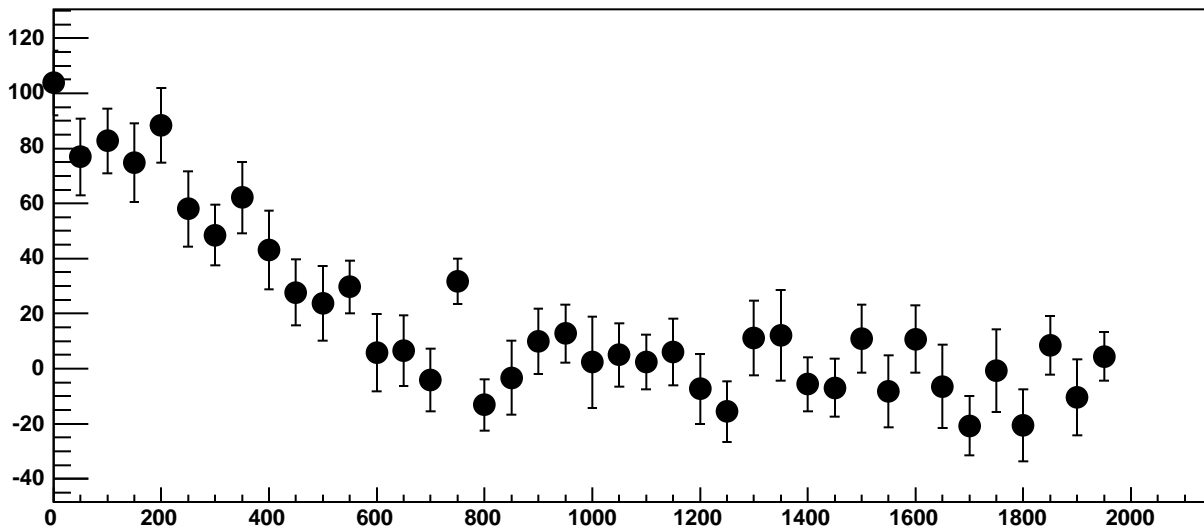
Chip 4, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC



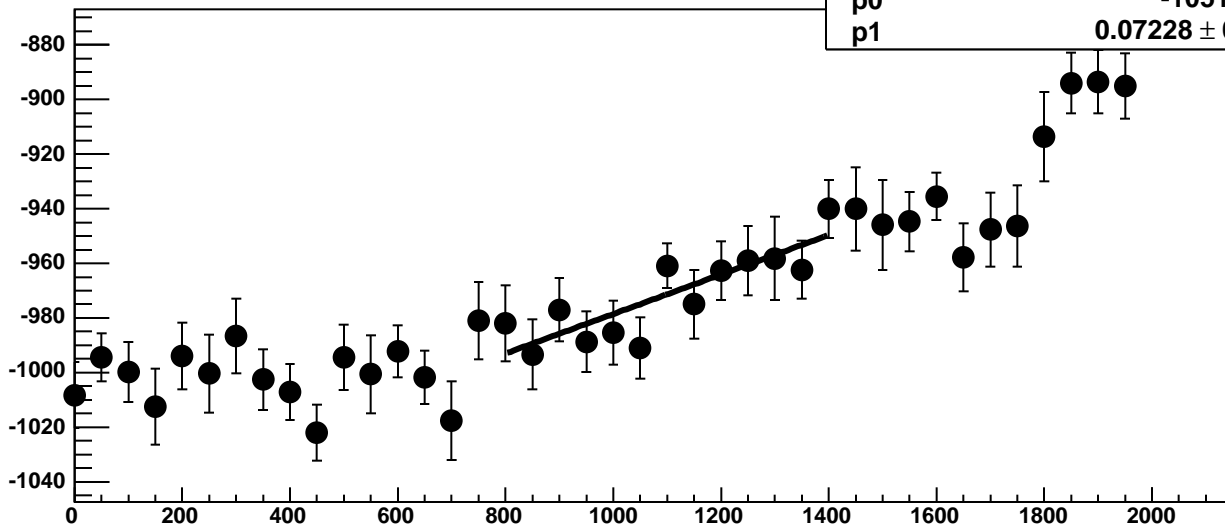
Chip 4, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC



Chip 4, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC

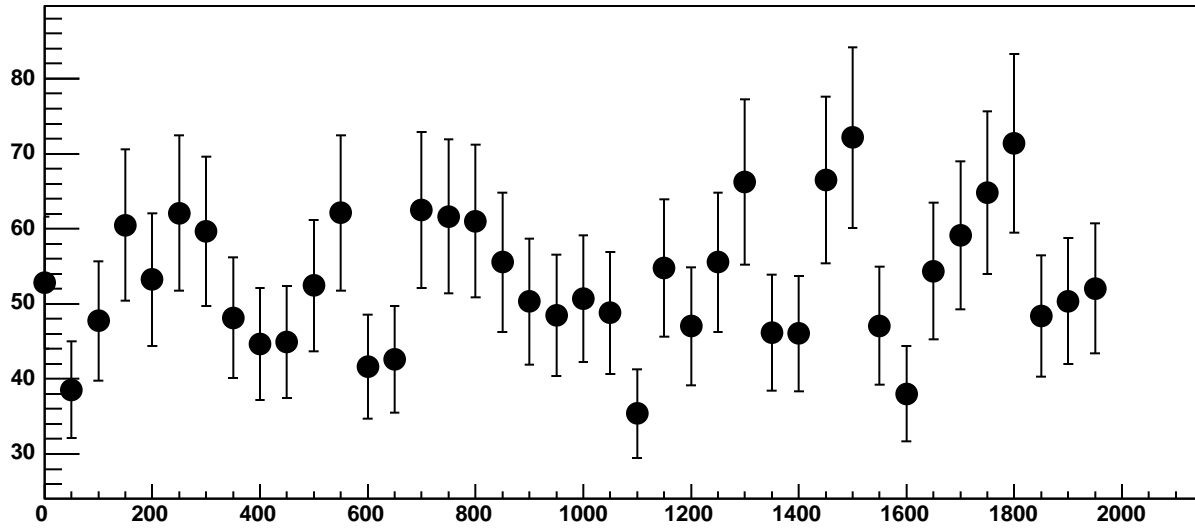


Chip 4, Channel 0, Enable 3, Hold=30, ADC Mean vs DAC

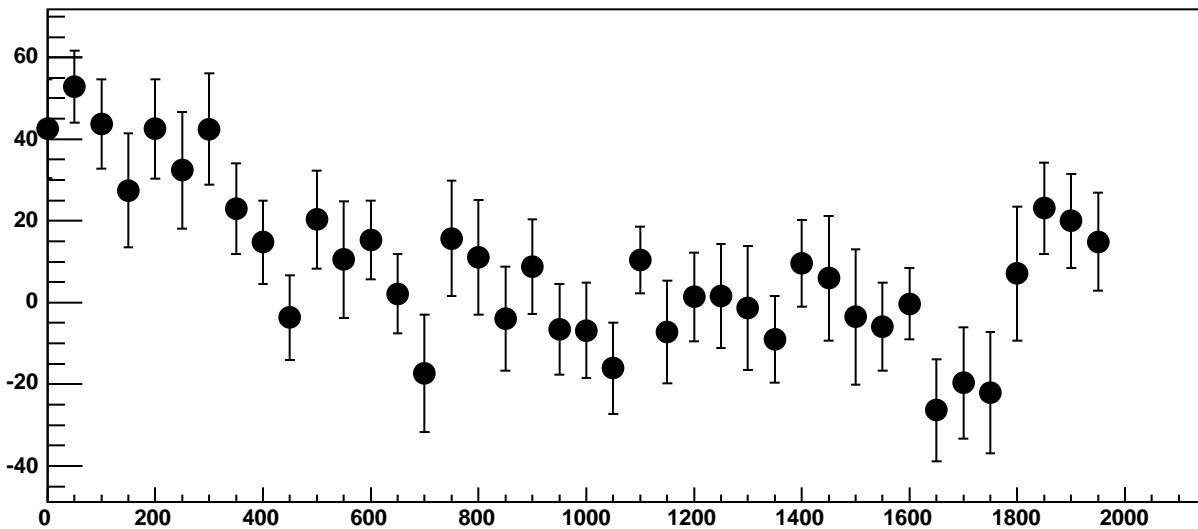


$\chi^2 / \text{ndf}$  7.622 / 11  
p0  $-1051 \pm 19.9$   
p1  $0.07228 \pm 0.01771$

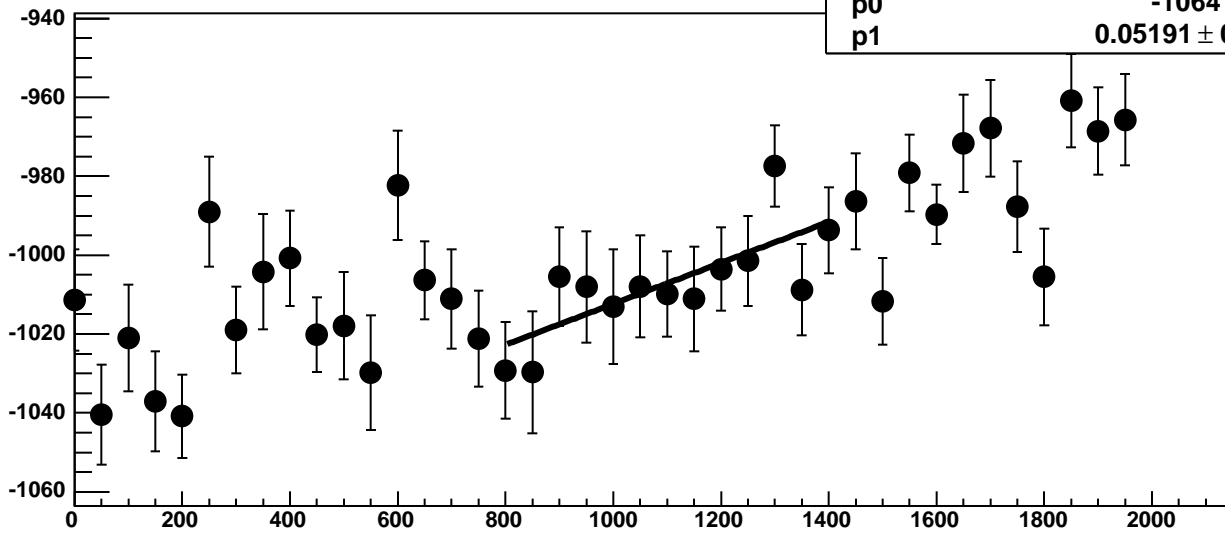
Chip 4, Channel 0, Enable 3, Hold=30, ADC Noise vs DAC



Chip 4, Channel 0, Enable 3, Hold=30, ADC Residuals vs DAC

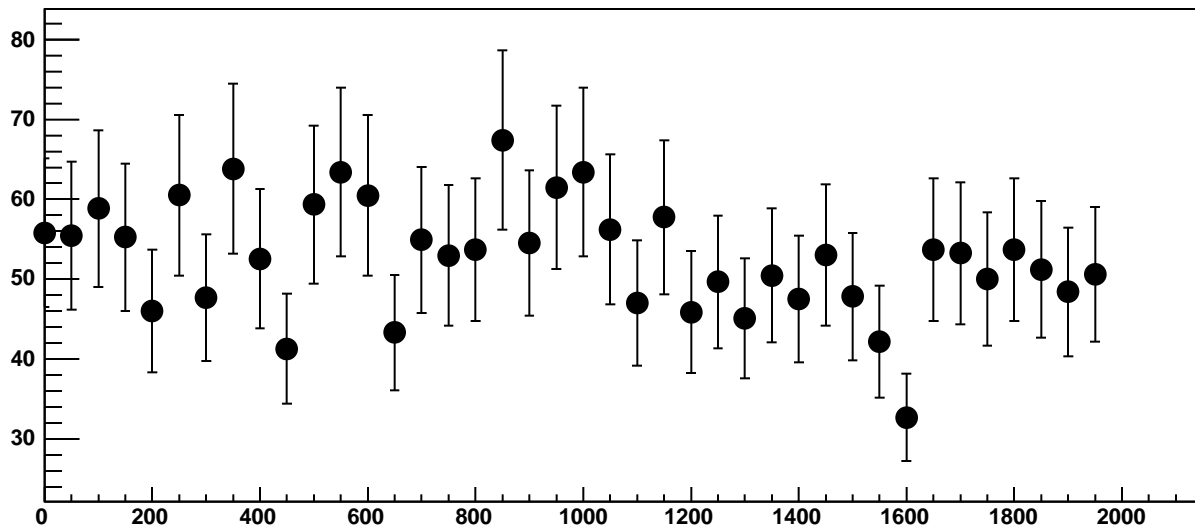


Chip 4, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC

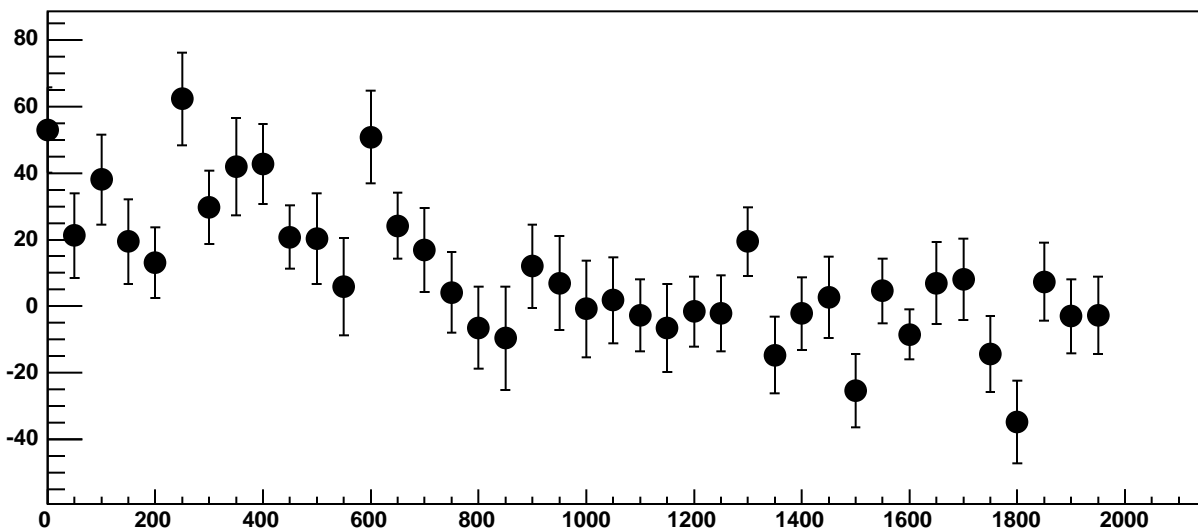


$\chi^2 / \text{ndf}$  7.37 / 11  
p0  $-1064 \pm 20.67$   
p1  $0.05191 \pm 0.01803$

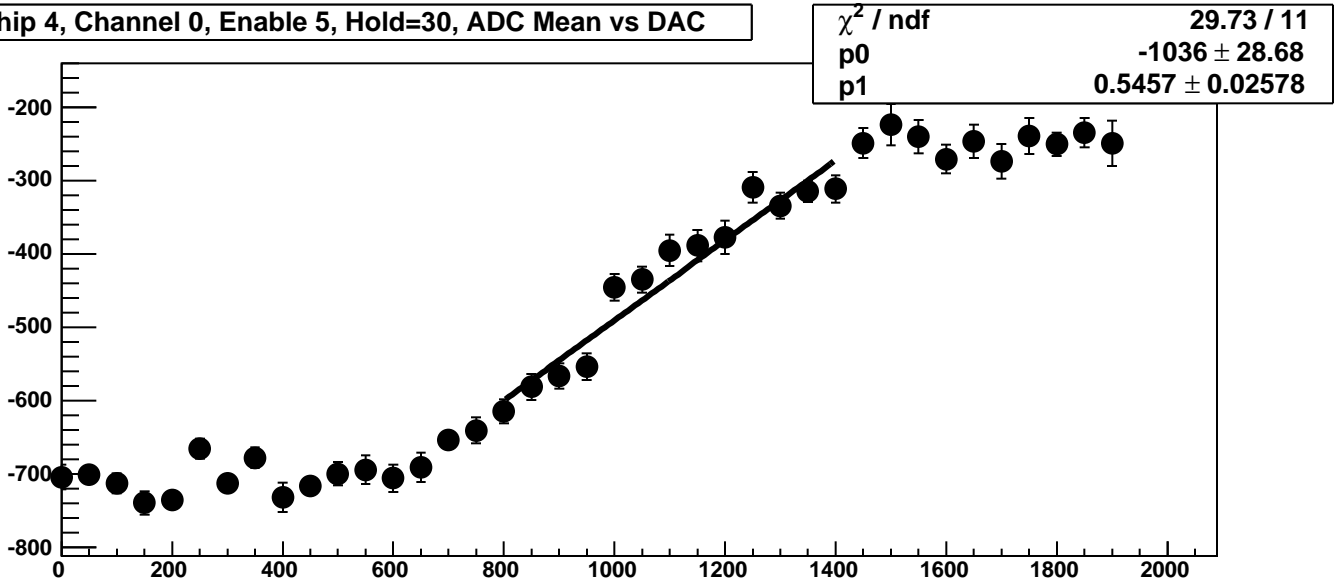
Chip 4, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC



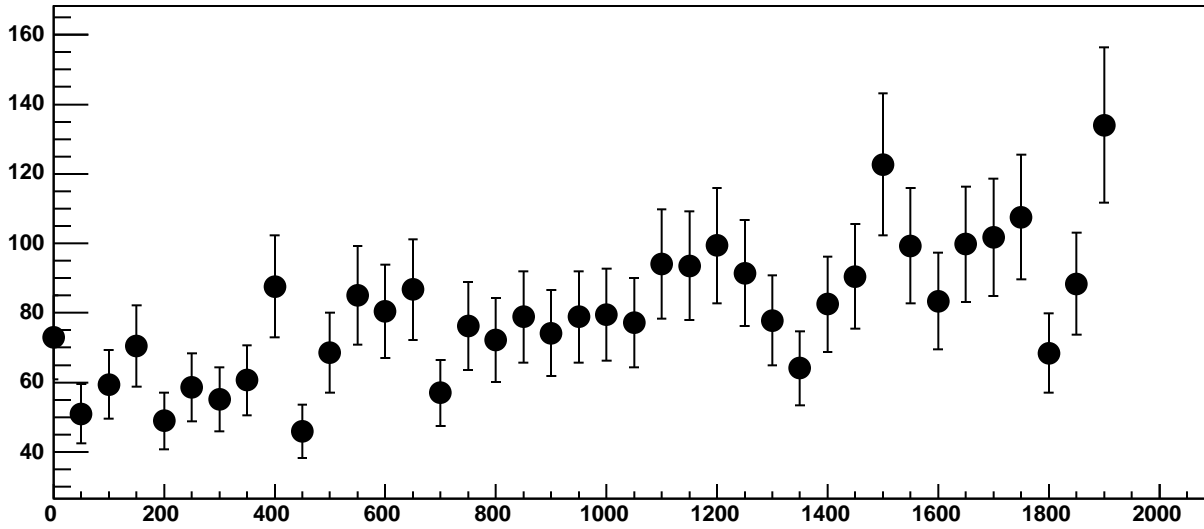
Chip 4, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC



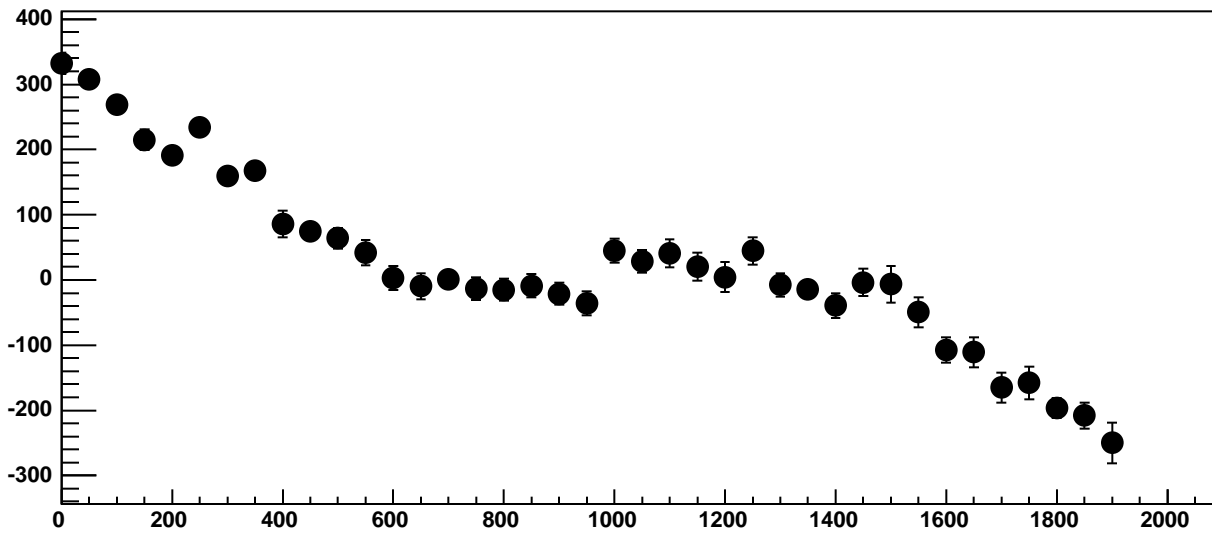
Chip 4, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC



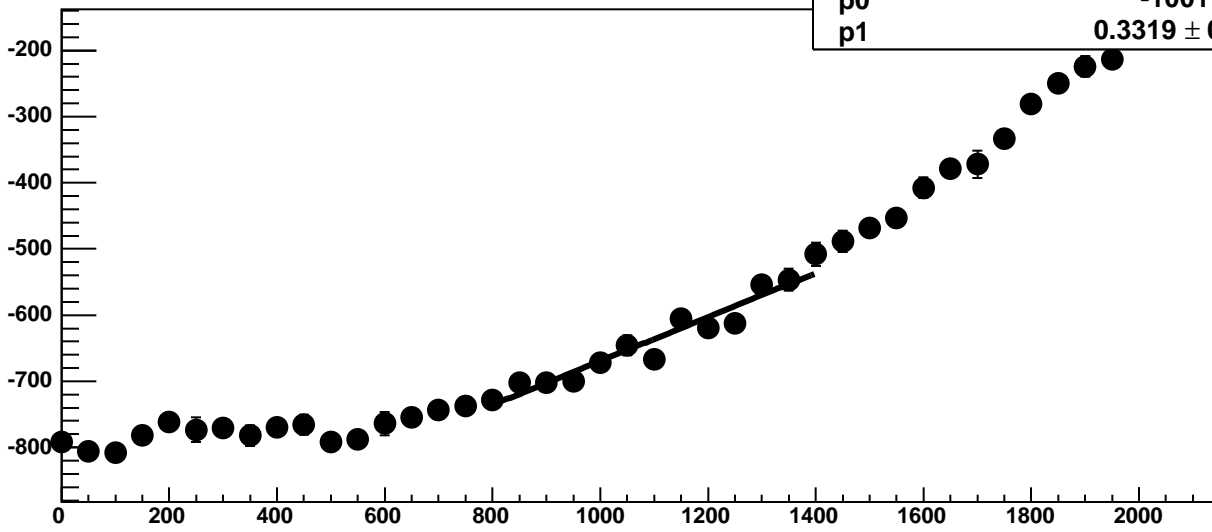
Chip 4, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC



Chip 4, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC

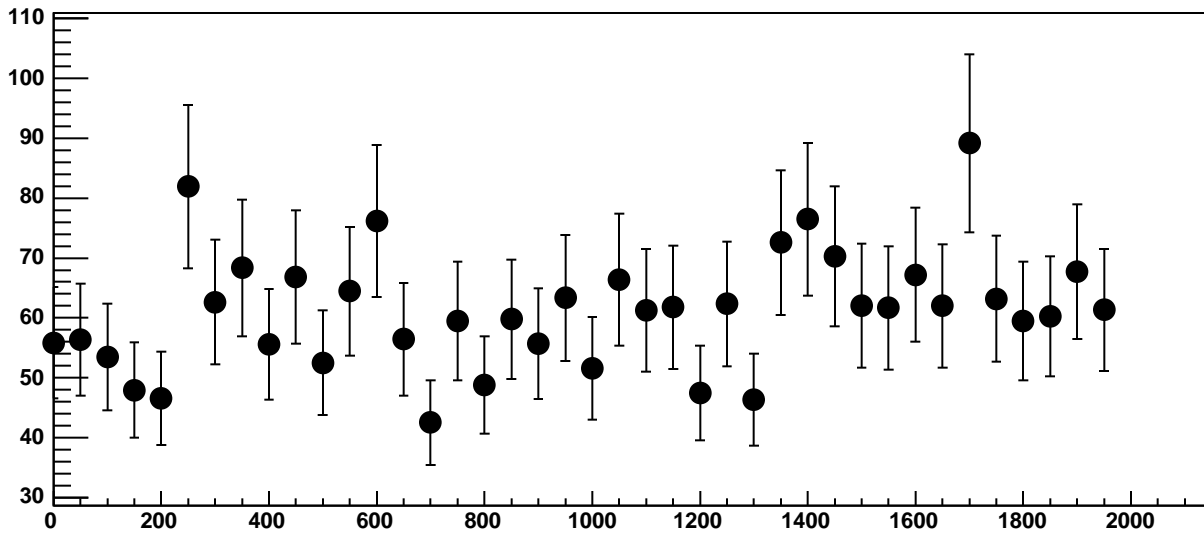


Chip 4, Channel 1, Enable 0, Hold=30, ADC Mean vs DAC

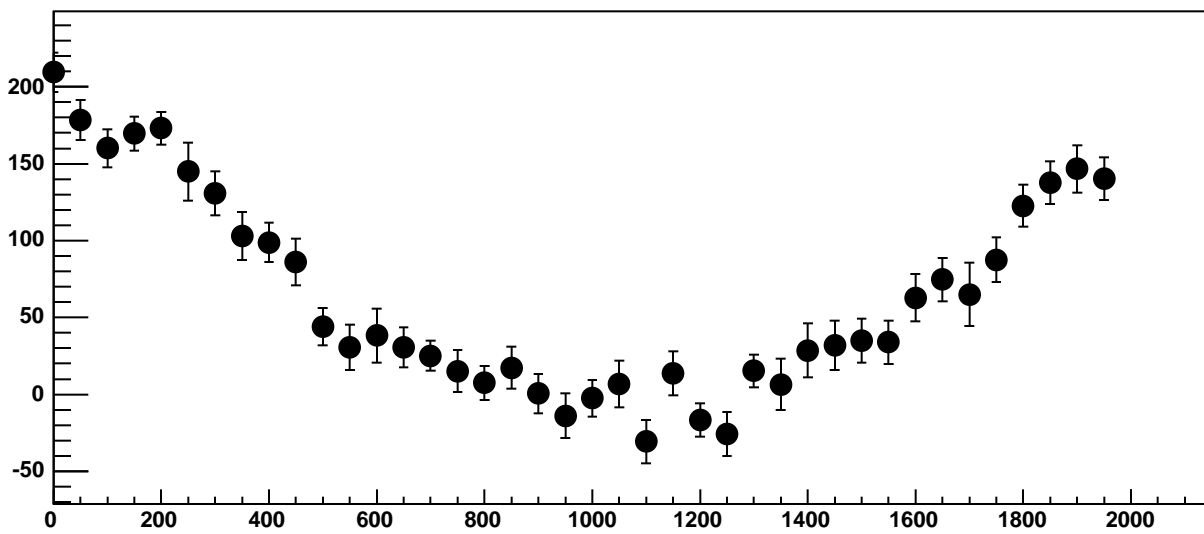


$\chi^2 / \text{ndf}$  19.38 / 11  
p0  $-1001 \pm 21.82$   
p1  $0.3319 \pm 0.01982$

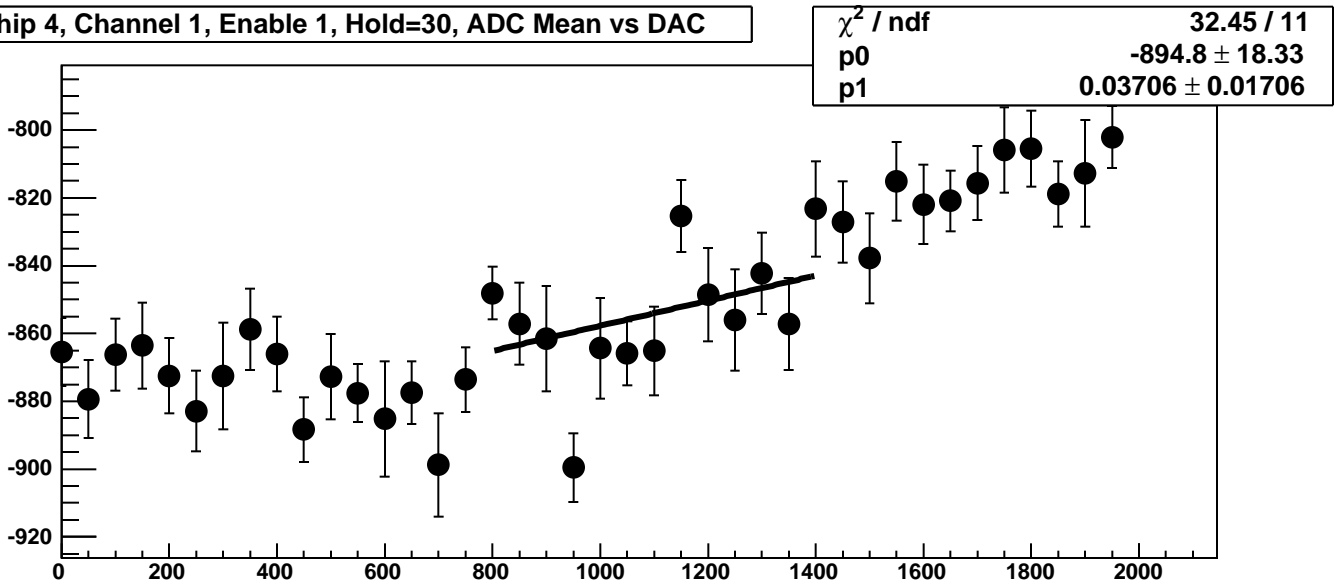
Chip 4, Channel 1, Enable 0, Hold=30, ADC Noise vs DAC



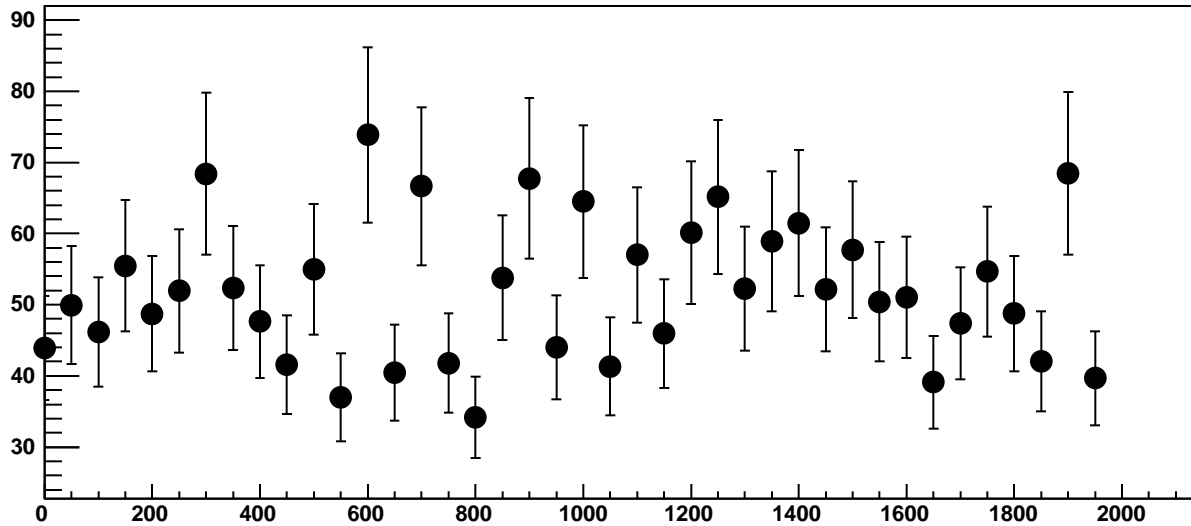
Chip 4, Channel 1, Enable 0, Hold=30, ADC Residuals vs DAC



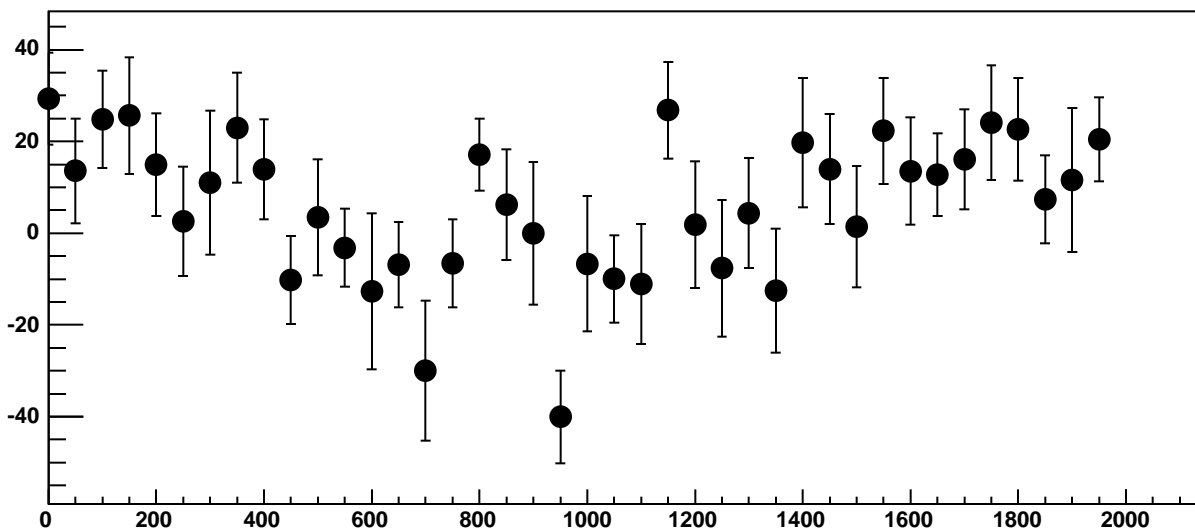
Chip 4, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC



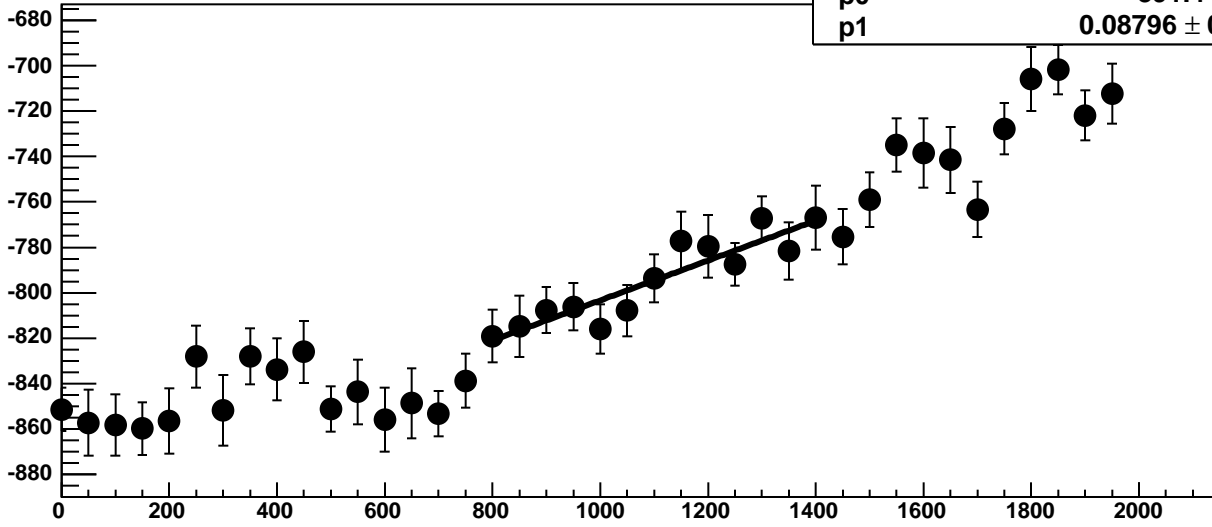
Chip 4, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC



Chip 4, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC

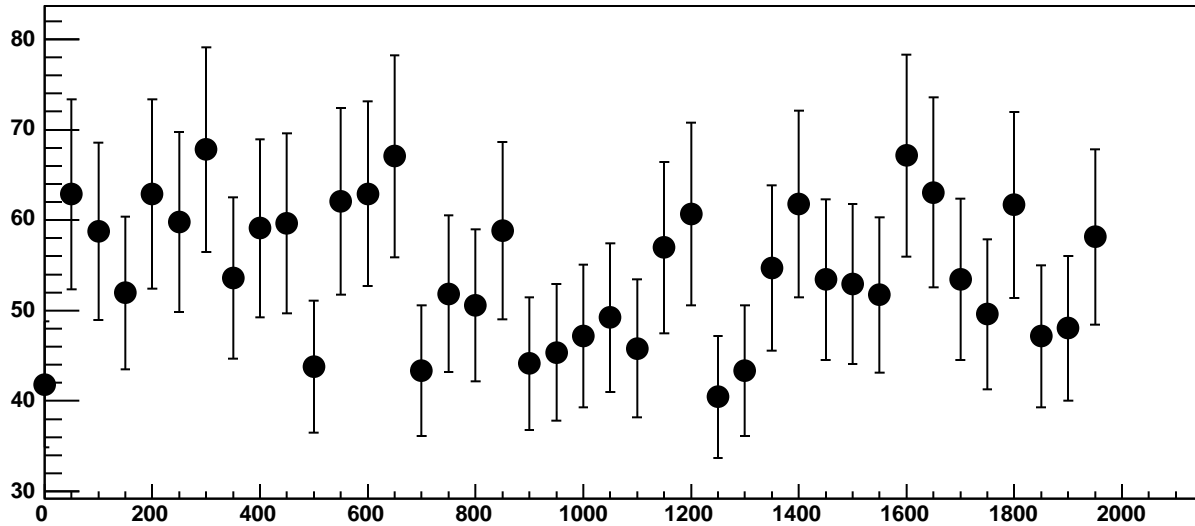


Chip 4, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC

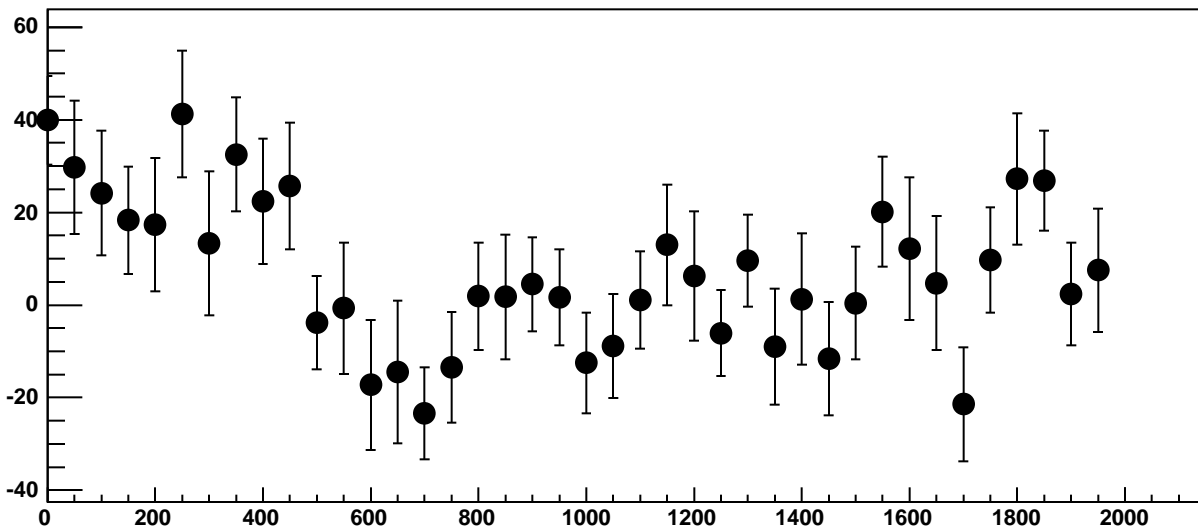


$\chi^2 / \text{ndf}$  5.305 / 11  
p0  $-891.4 \pm 19.35$   
p1  $0.08796 \pm 0.01744$

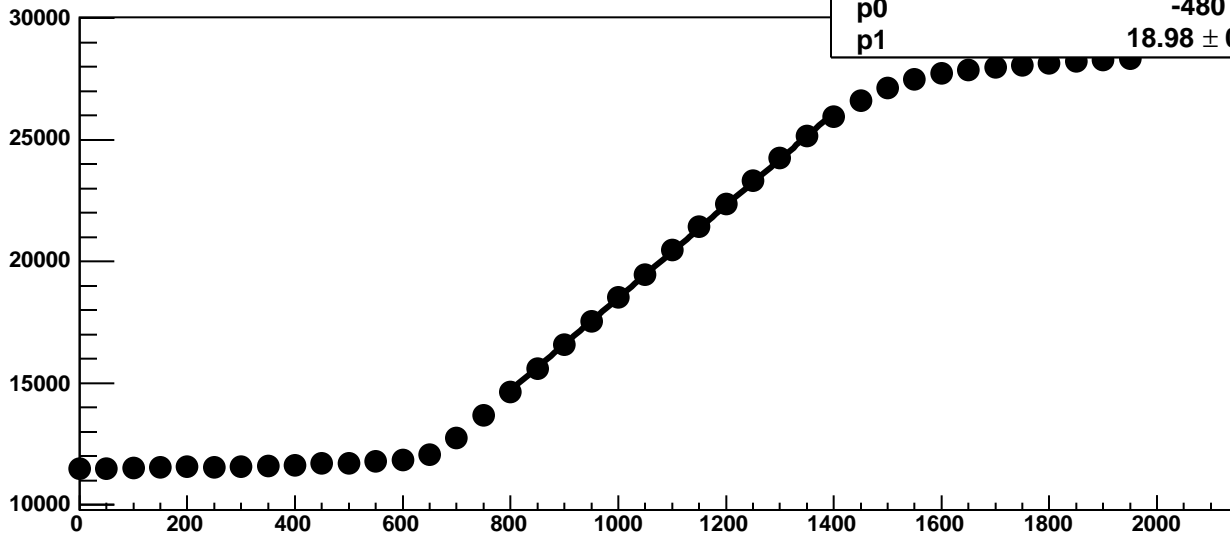
Chip 4, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC



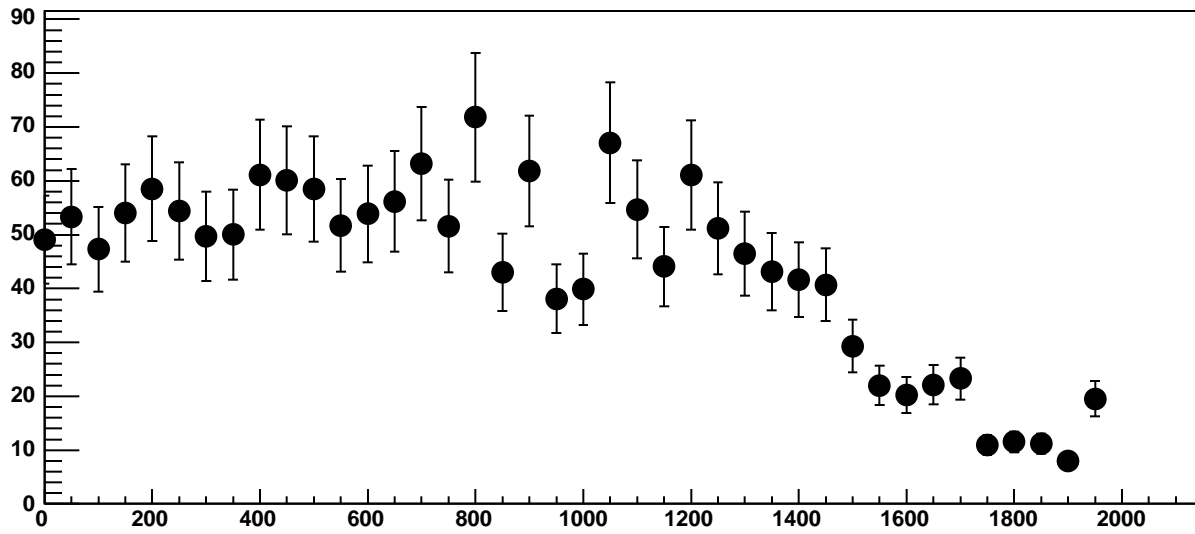
Chip 4, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC



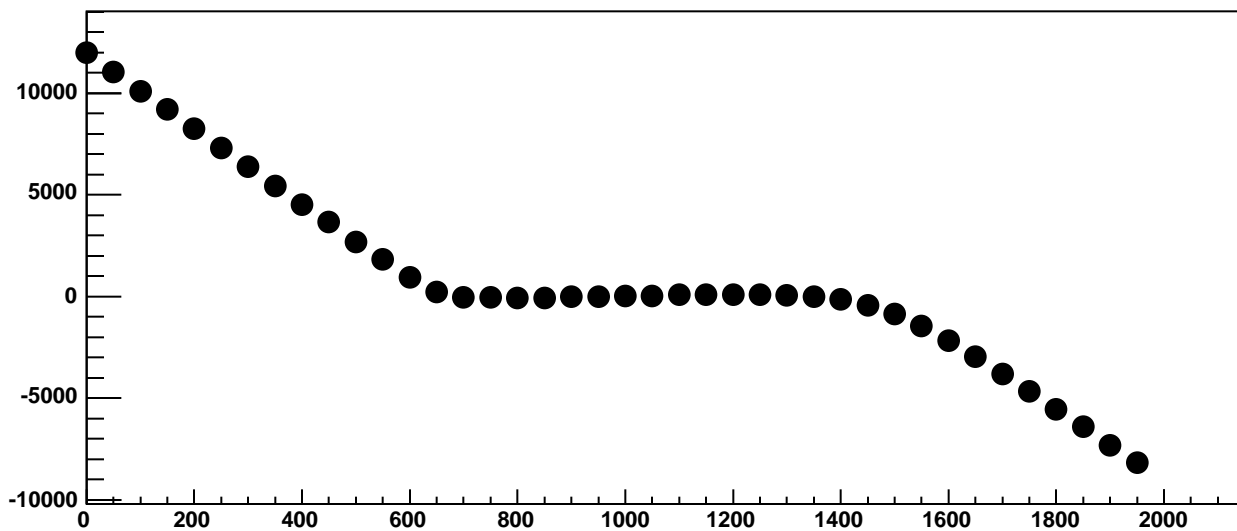
Chip 4, Channel 1, Enable 3!, Hold=30, ADC Mean vs DAC



Chip 4, Channel 1, Enable 3!, Hold=30, ADC Noise vs DAC

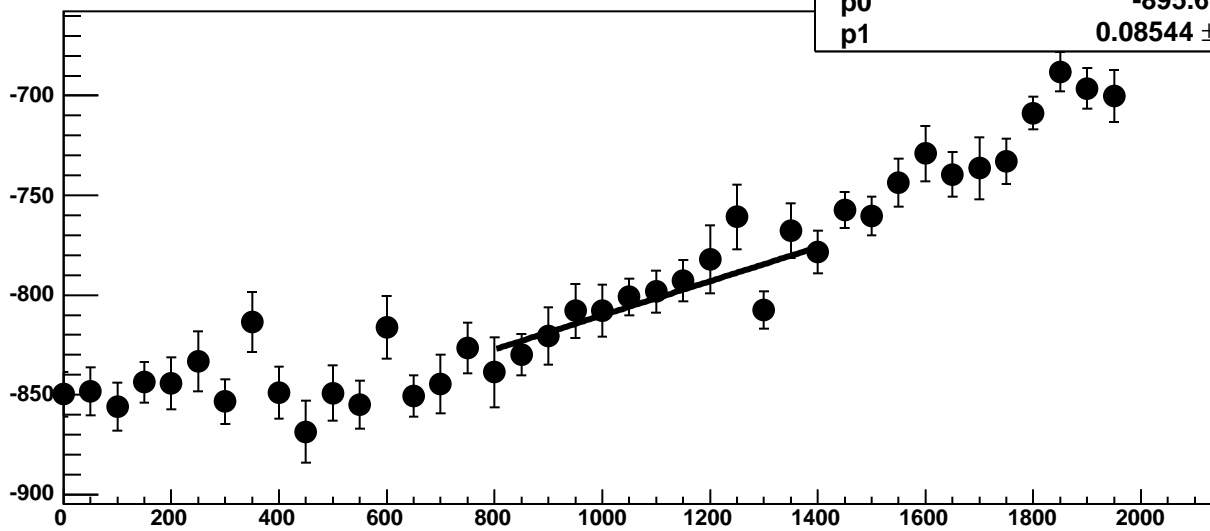


Chip 4, Channel 1, Enable 3!, Hold=30, ADC Residuals vs DAC



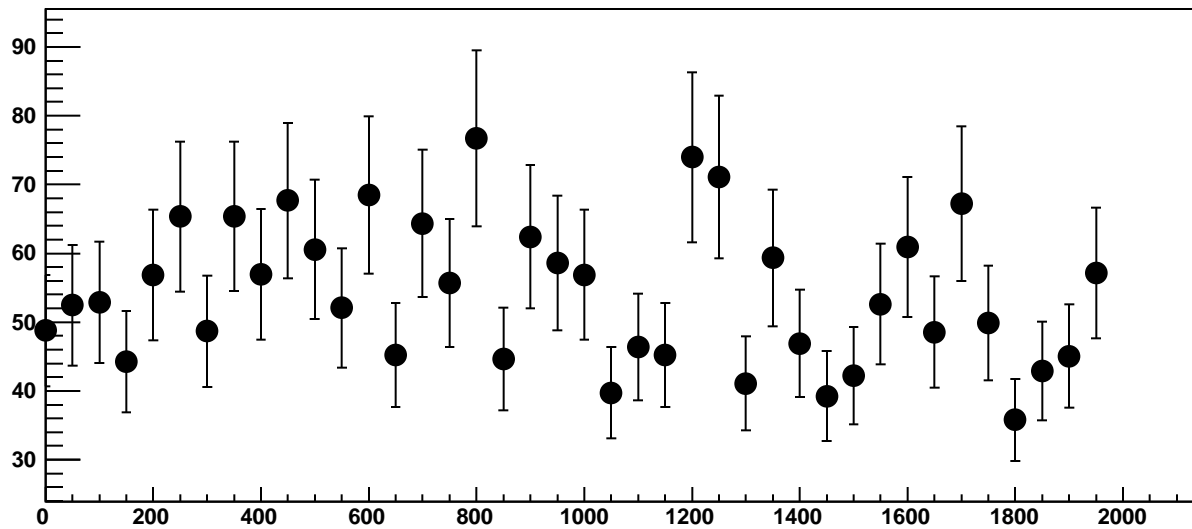


Chip 4, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC

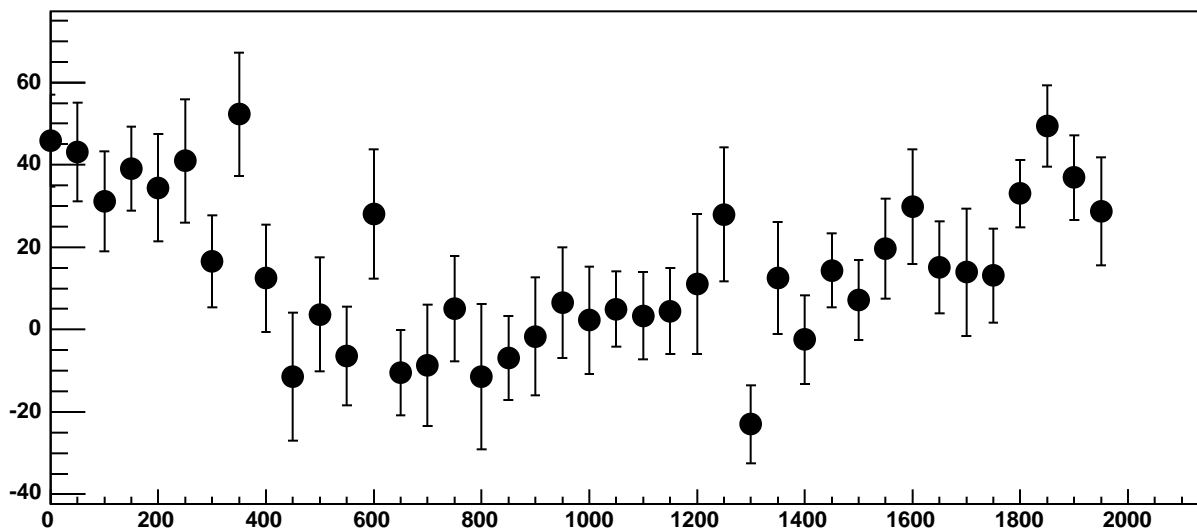


$\chi^2 / \text{ndf}$  11.95 / 11  
p0  $-895.6 \pm 20.64$   
p1  $0.08544 \pm 0.0183$

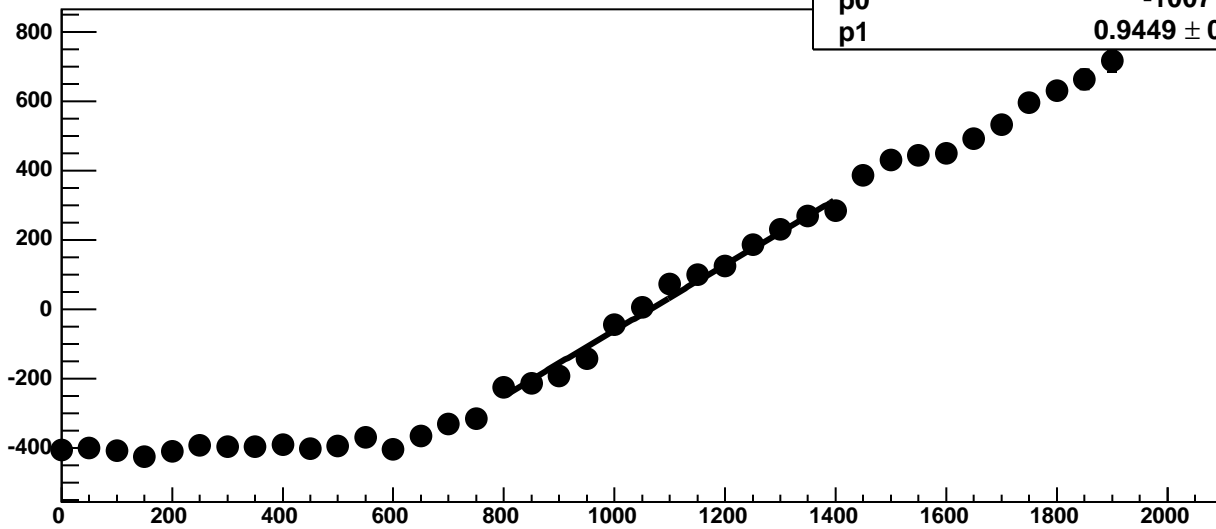
Chip 4, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC



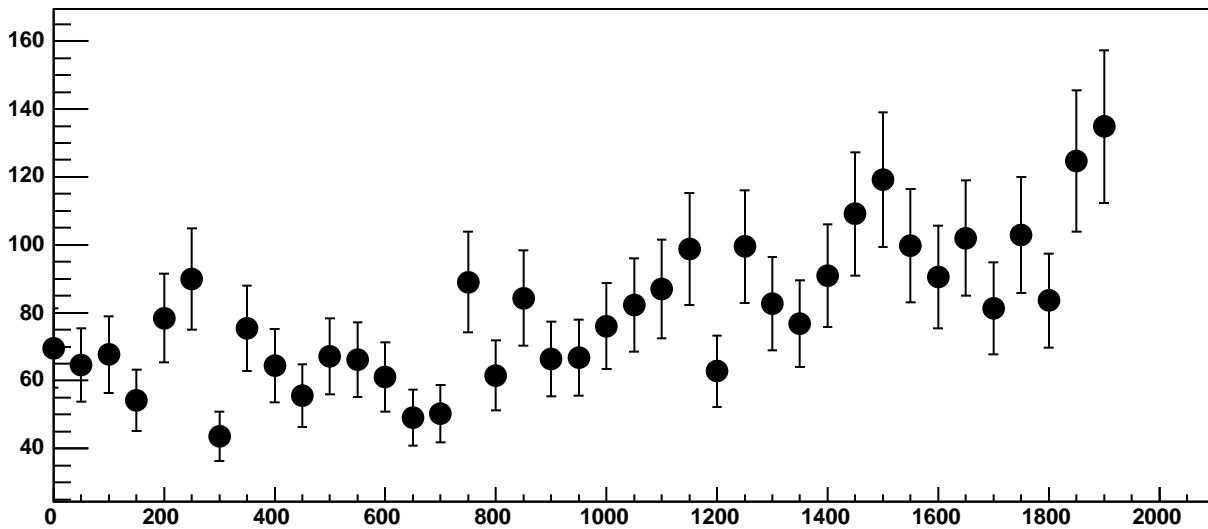
Chip 4, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC



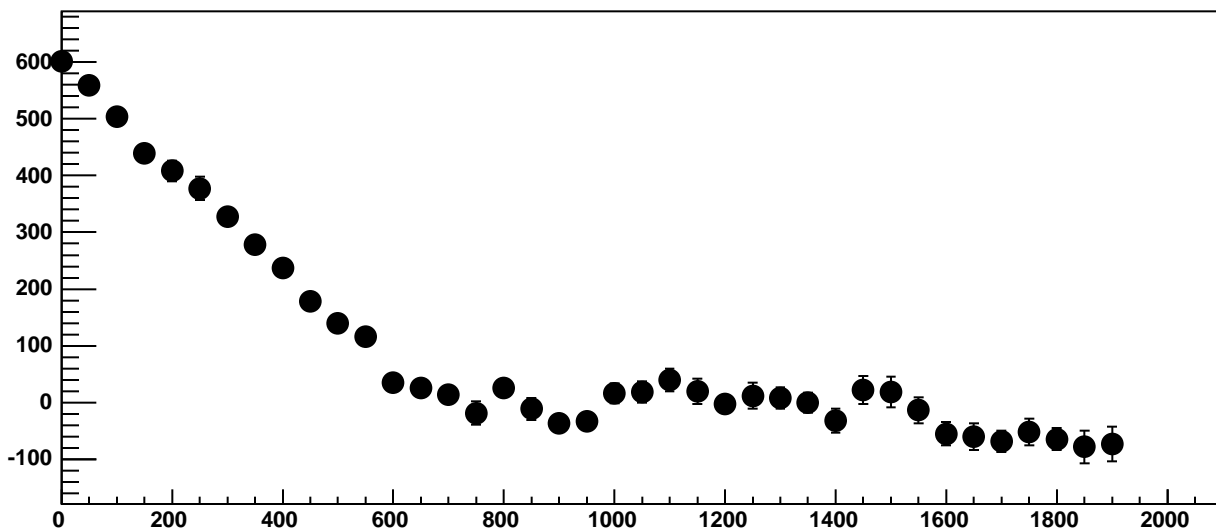
Chip 4, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC



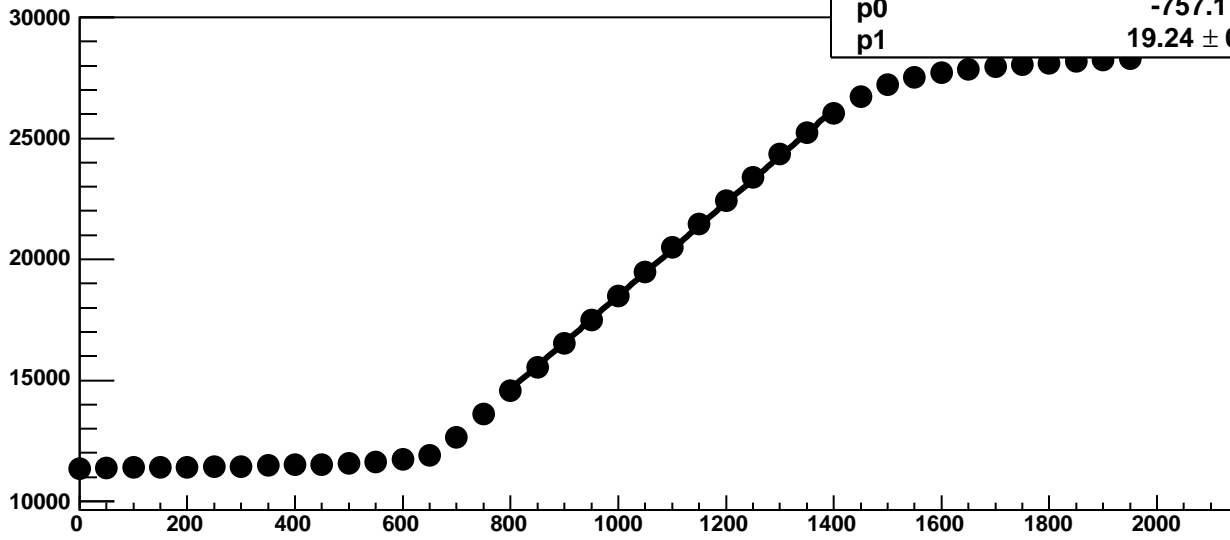
Chip 4, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC



Chip 4, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC

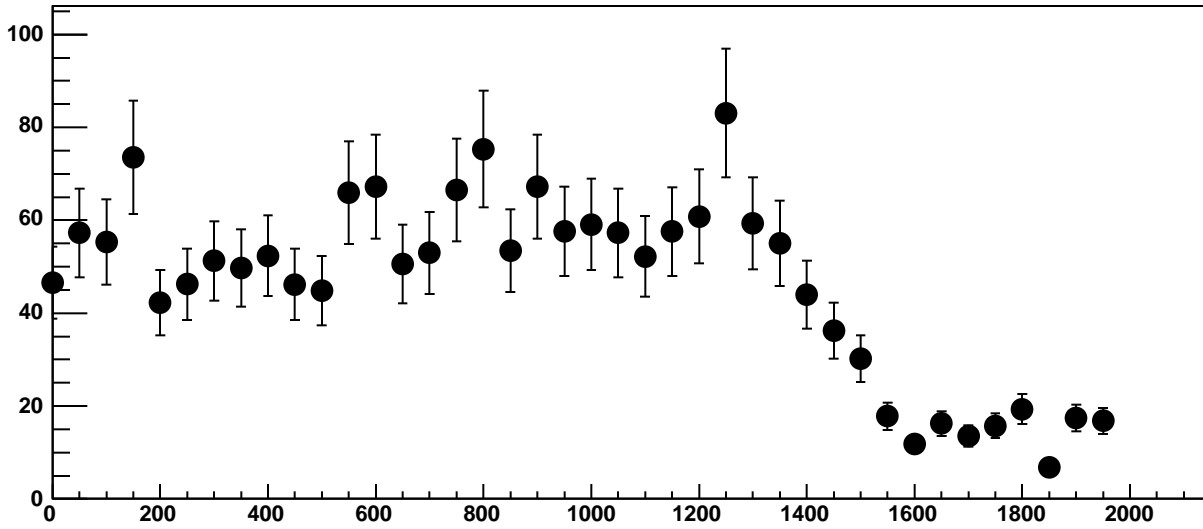


Chip 4, Channel 2, Enable 0!, Hold=30, ADC Mean vs DAC

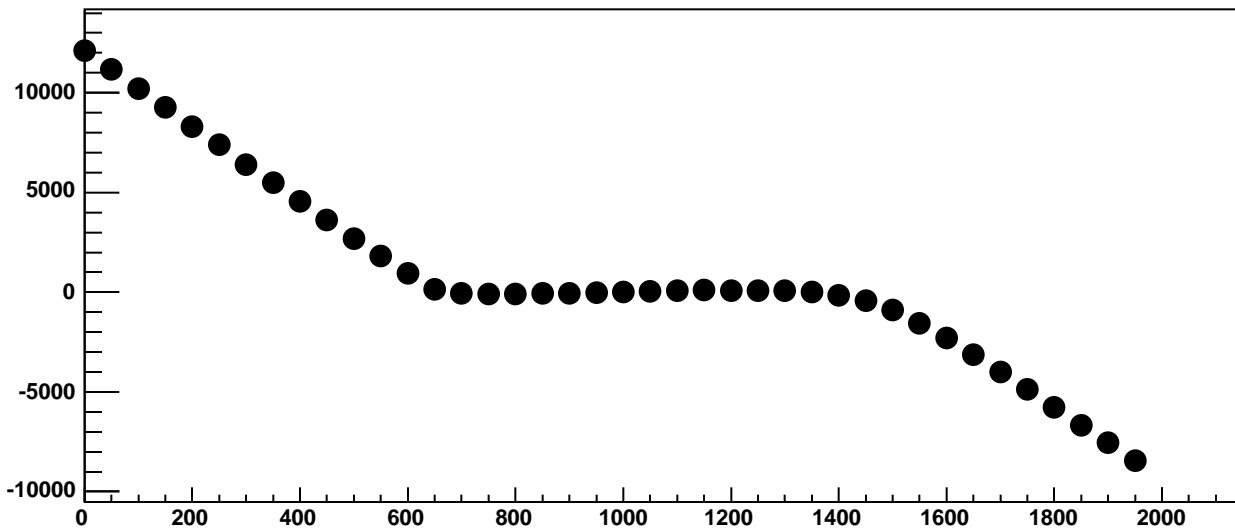


$\chi^2 / \text{ndf}$	481.9 / 11
p0	-757.1 ± 22.04
p1	19.24 ± 0.01936

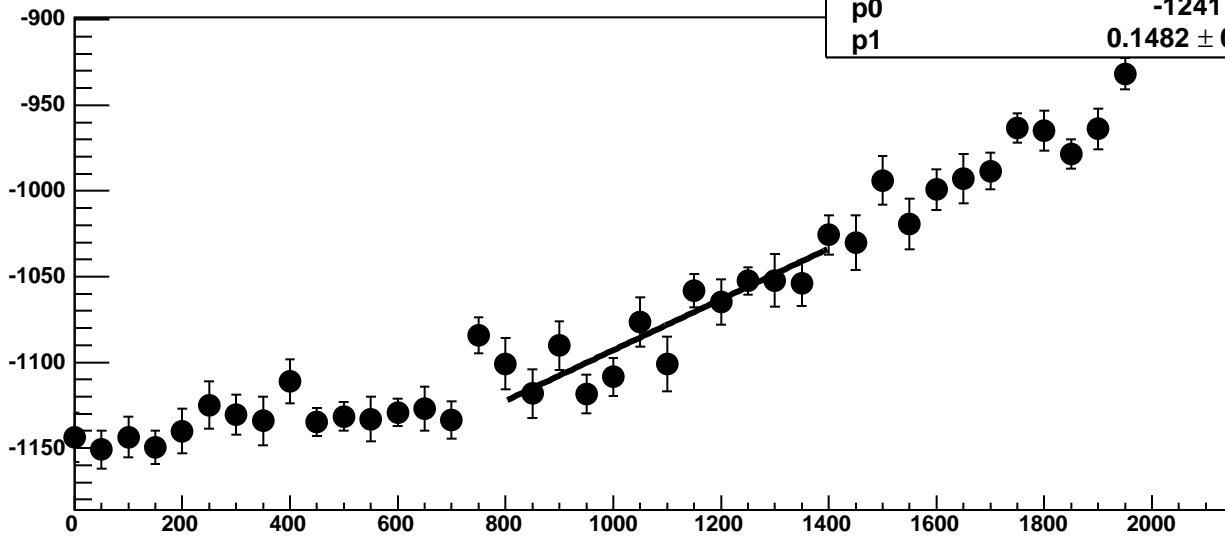
Chip 4, Channel 2, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 4, Channel 2, Enable 0!, Hold=30, ADC Residuals vs DAC

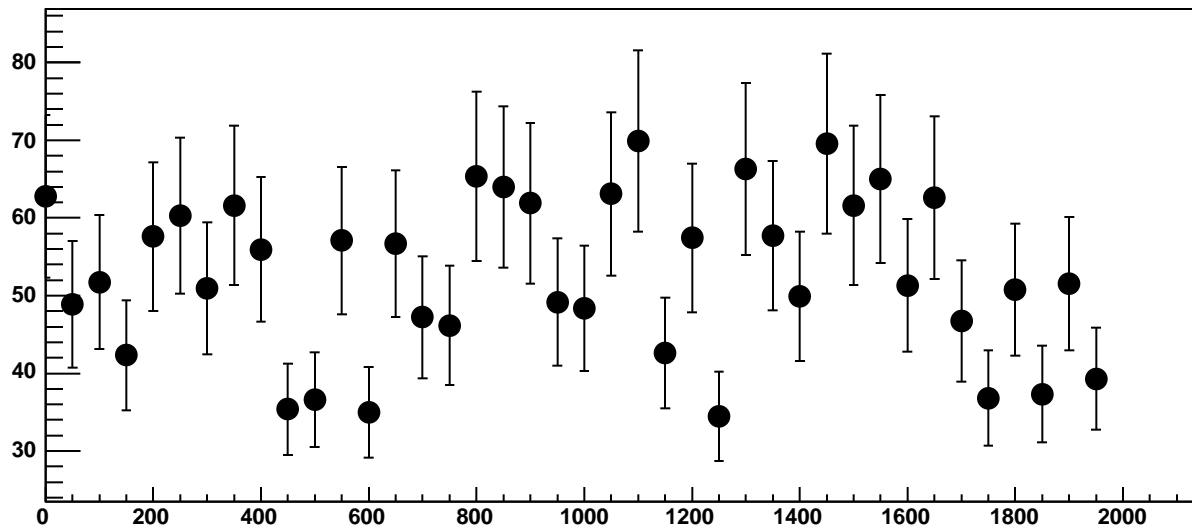


Chip 4, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC

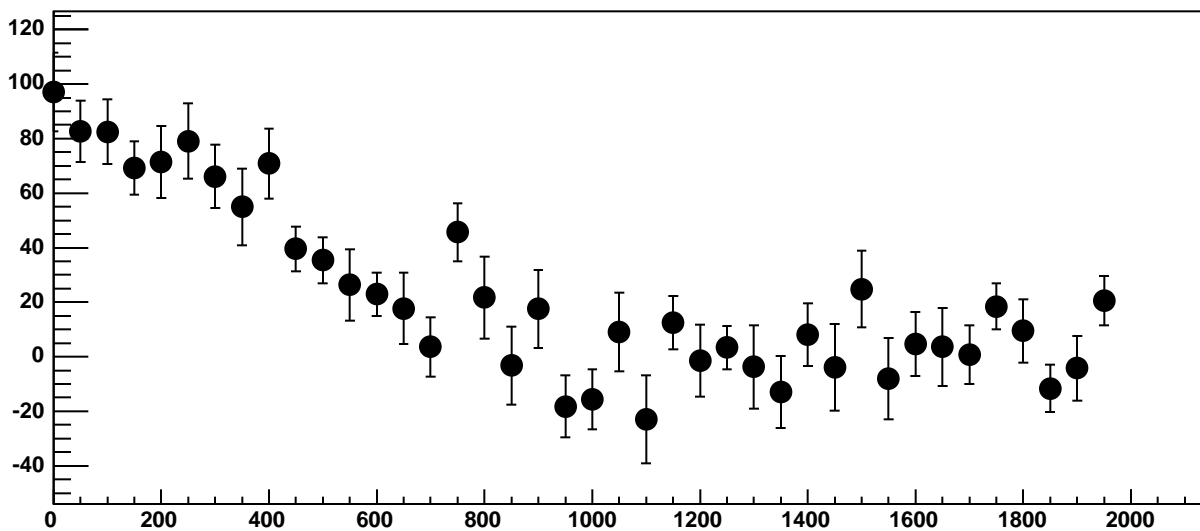


$\chi^2 / \text{ndf}$  14.04 / 11  
p0  $-1241 \pm 21.64$   
p1  $0.1482 \pm 0.01899$

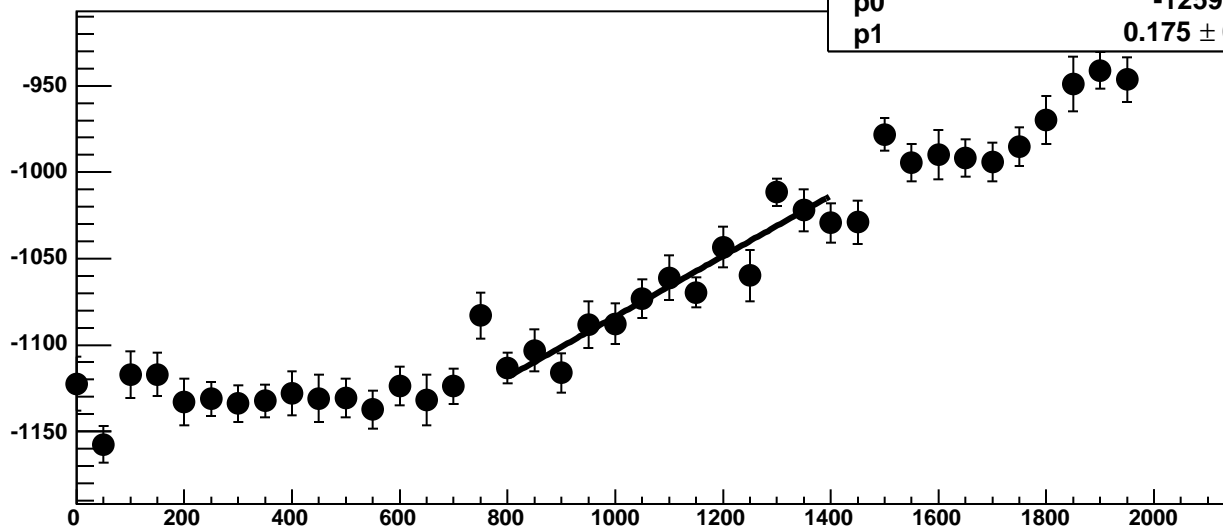
Chip 4, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC



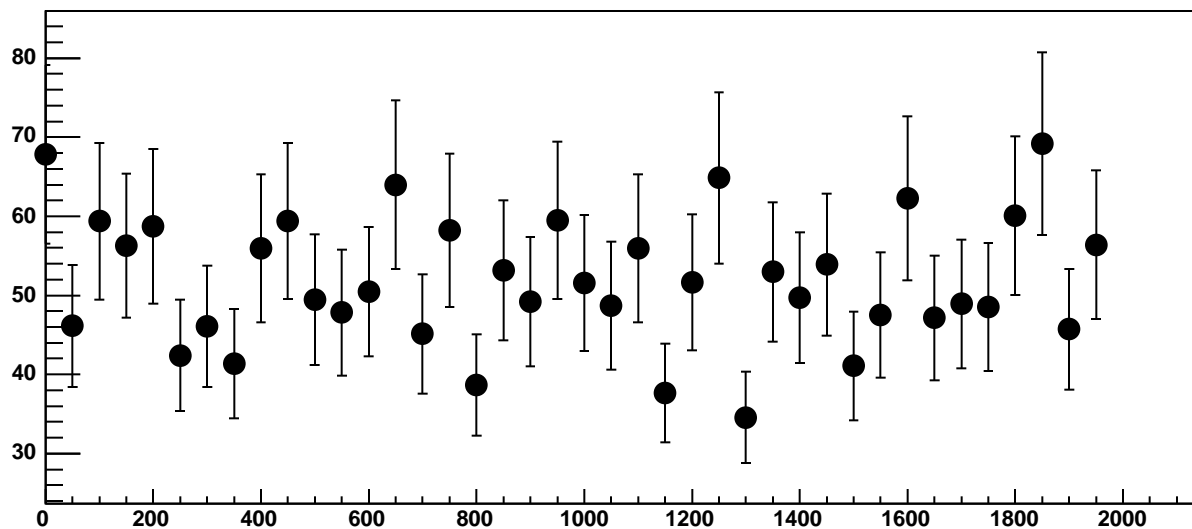
Chip 4, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC



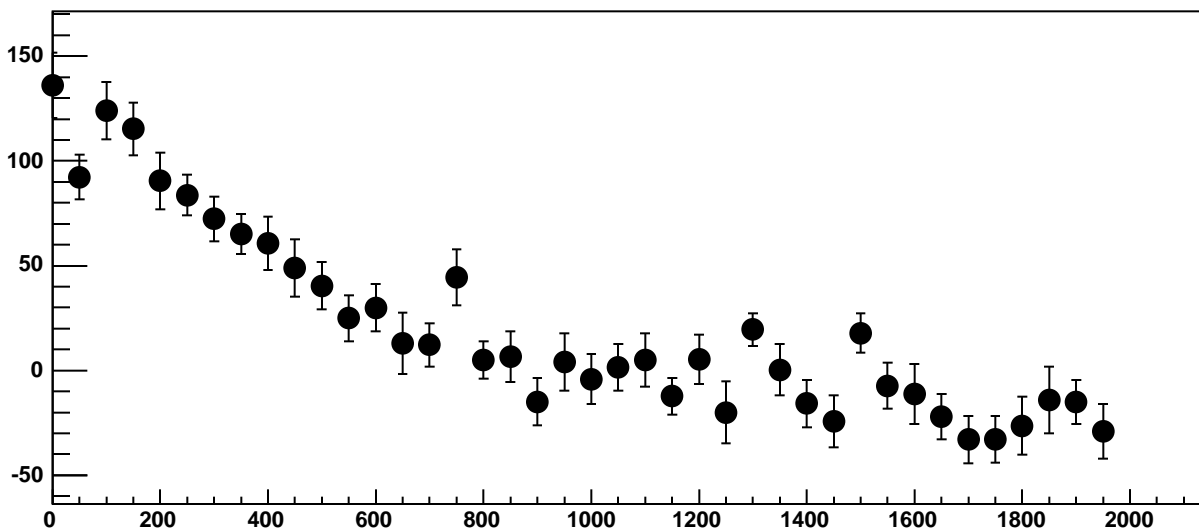
Chip 4, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC



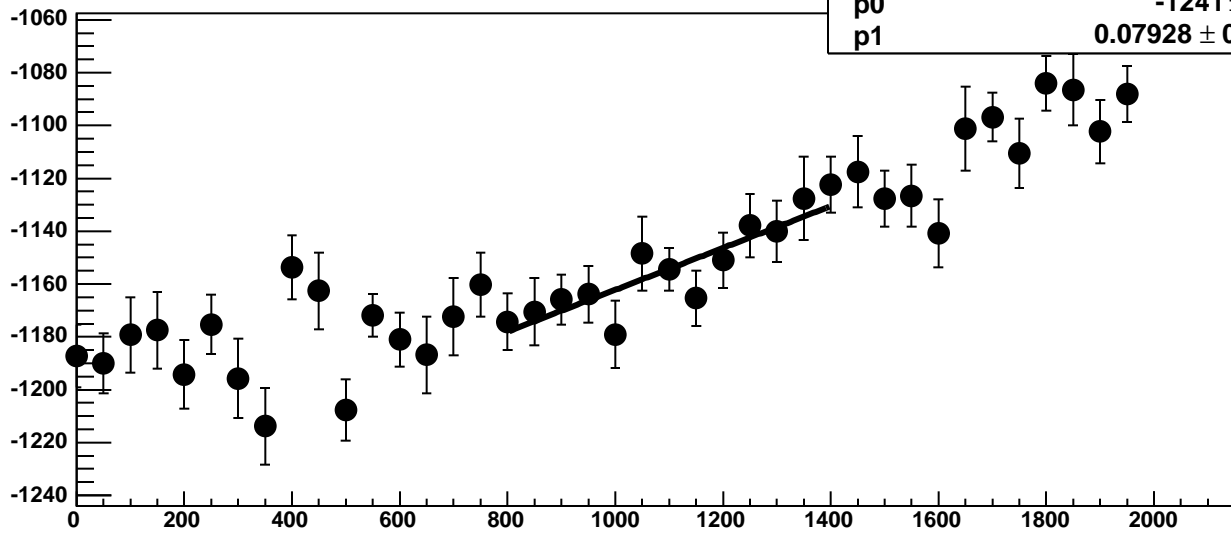
Chip 4, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC



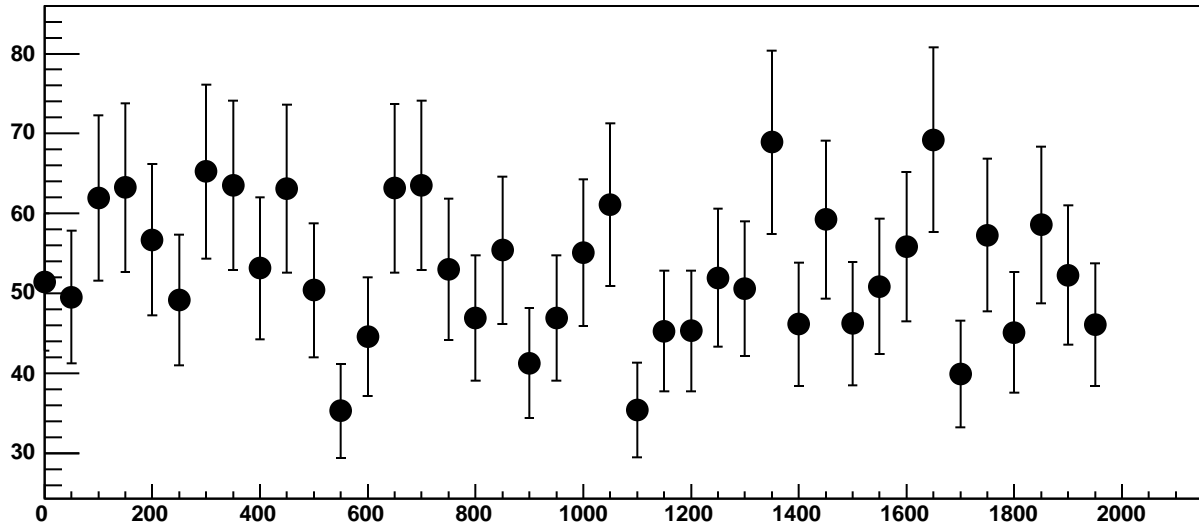
Chip 4, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC



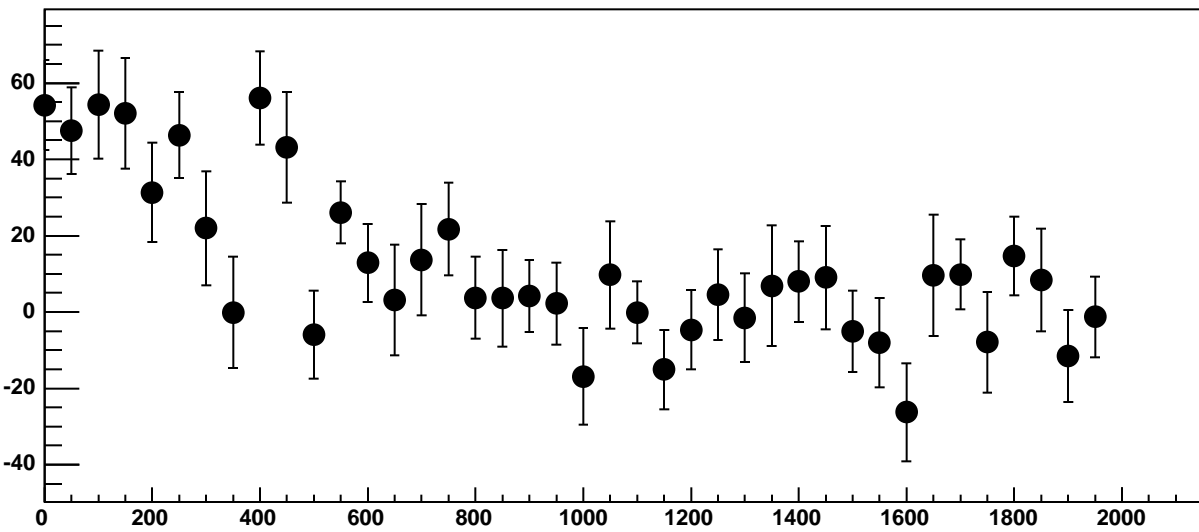
Chip 4, Channel 2, Enable 3, Hold=30, ADC Mean vs DAC



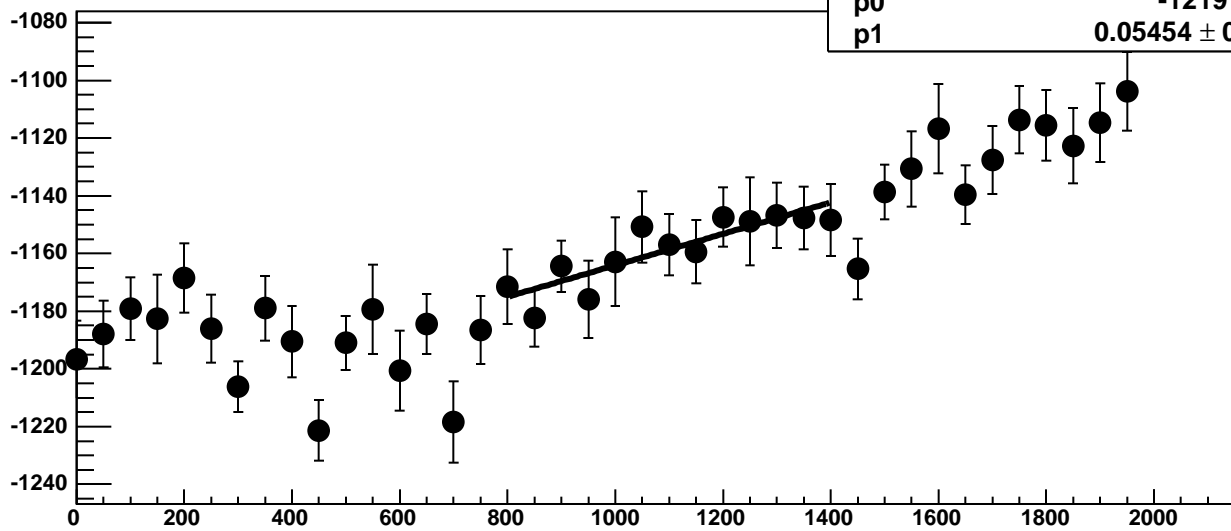
Chip 4, Channel 2, Enable 3, Hold=30, ADC Noise vs DAC



Chip 4, Channel 2, Enable 3, Hold=30, ADC Residuals vs DAC

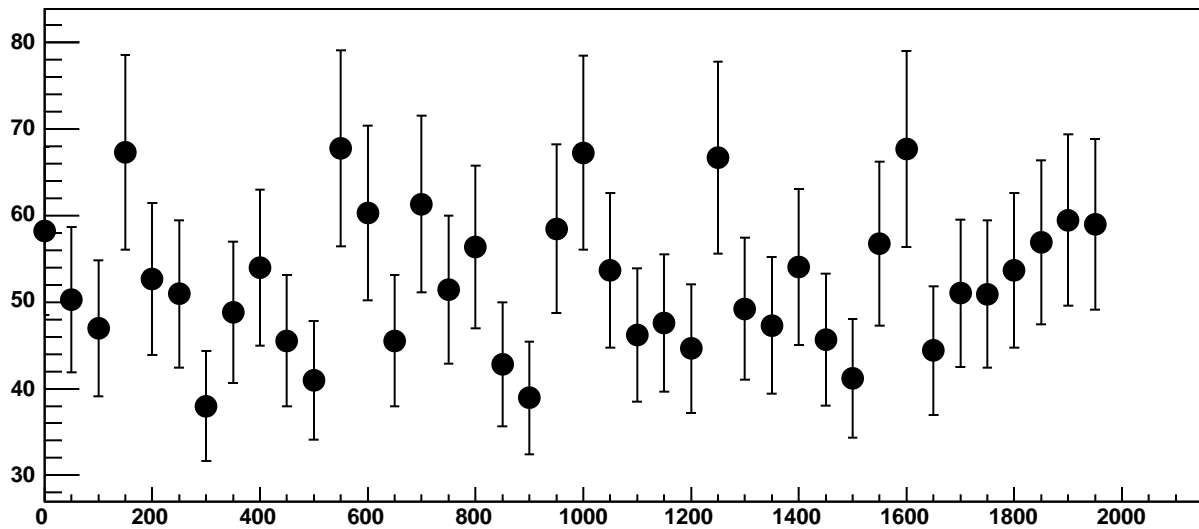


Chip 4, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC

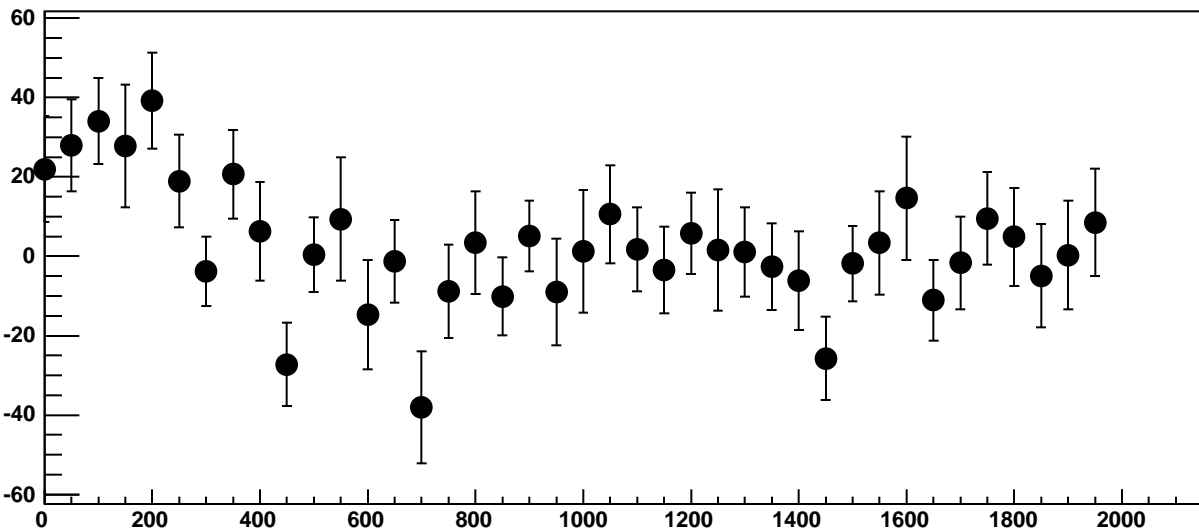


$\chi^2 / \text{ndf}$  3.431 / 11  
p0  $-1219 \pm 18.71$   
p1  $0.05454 \pm 0.01689$

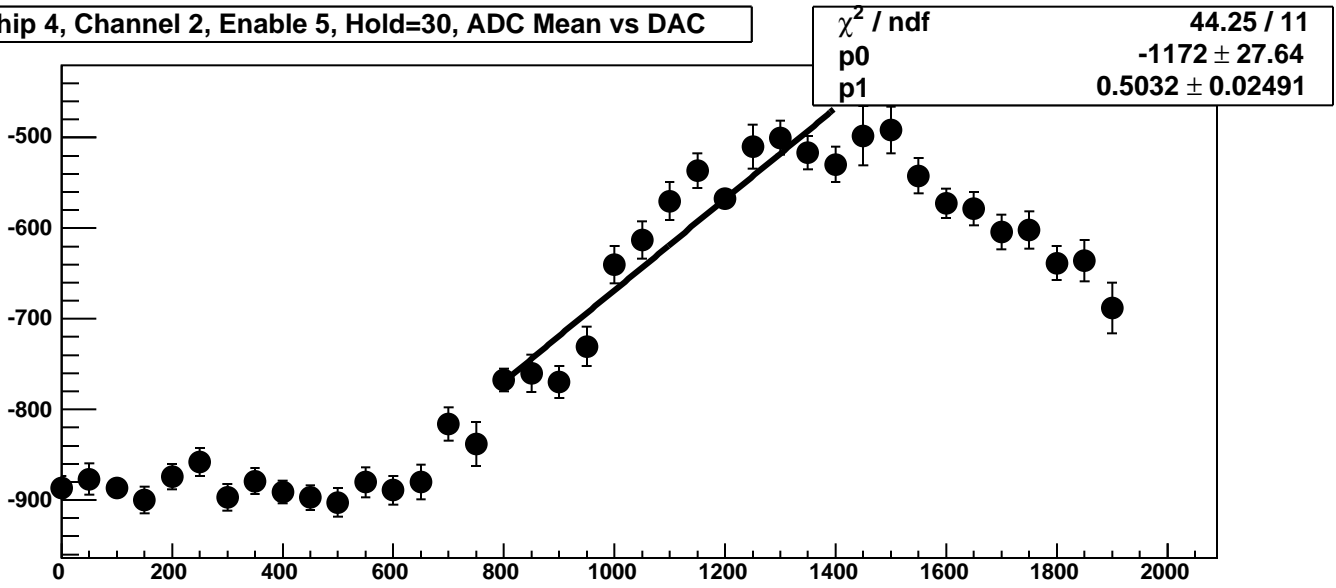
Chip 4, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC



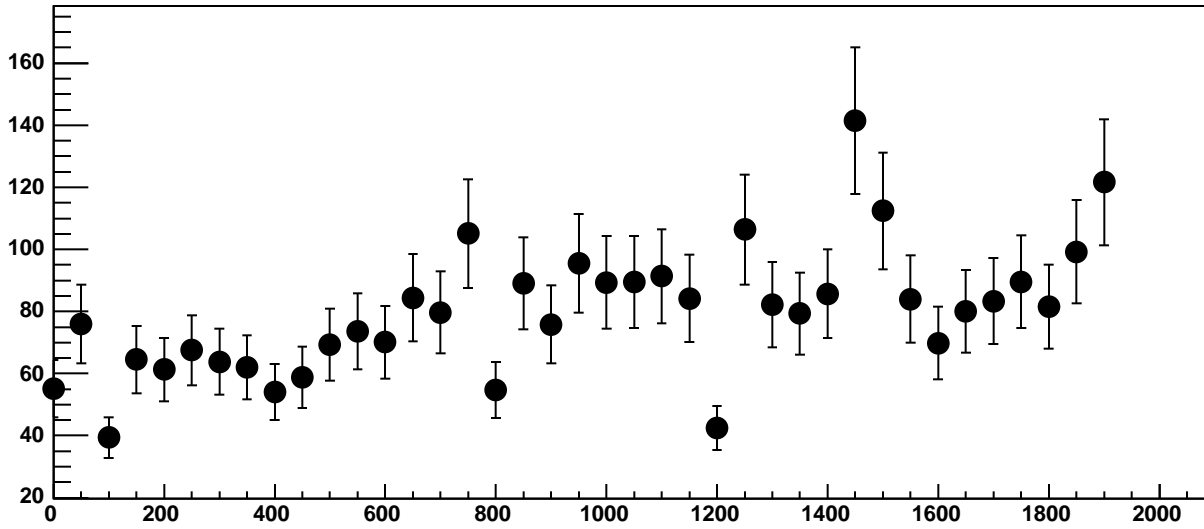
Chip 4, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC



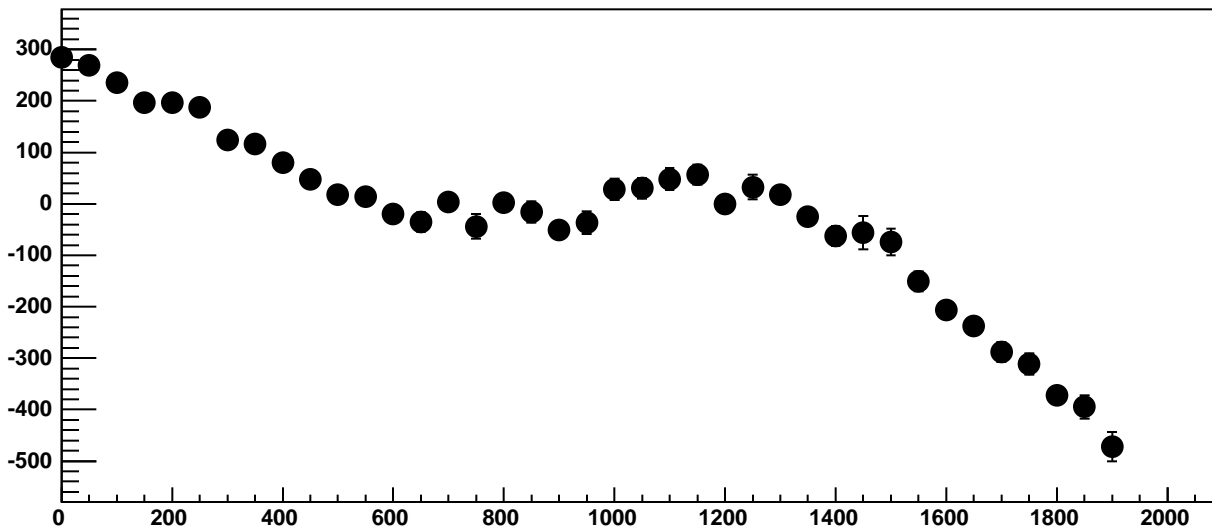
Chip 4, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC



Chip 4, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC

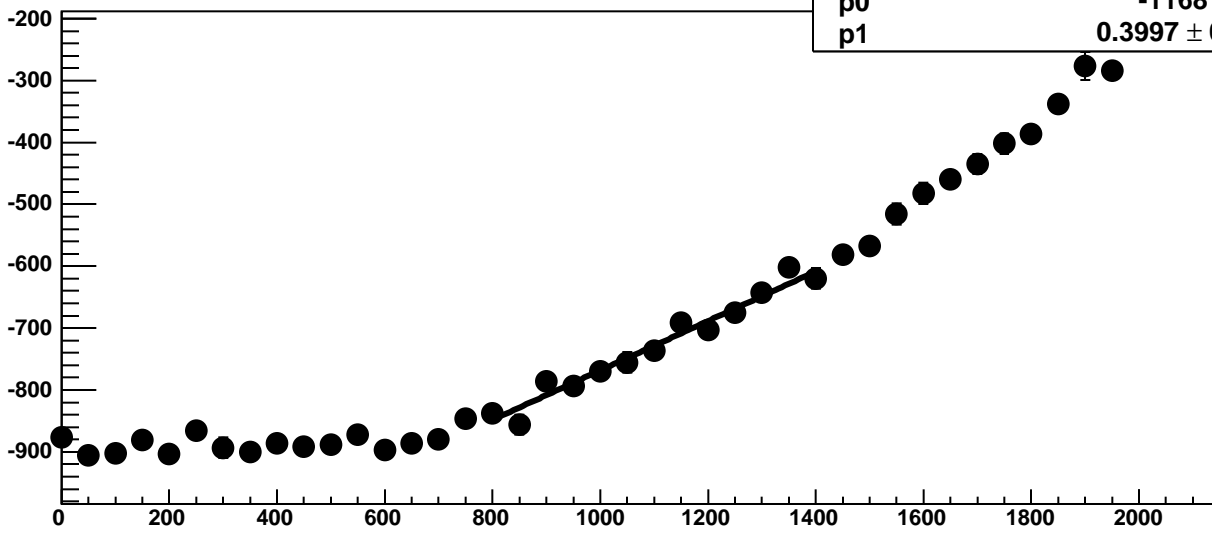


Chip 4, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC

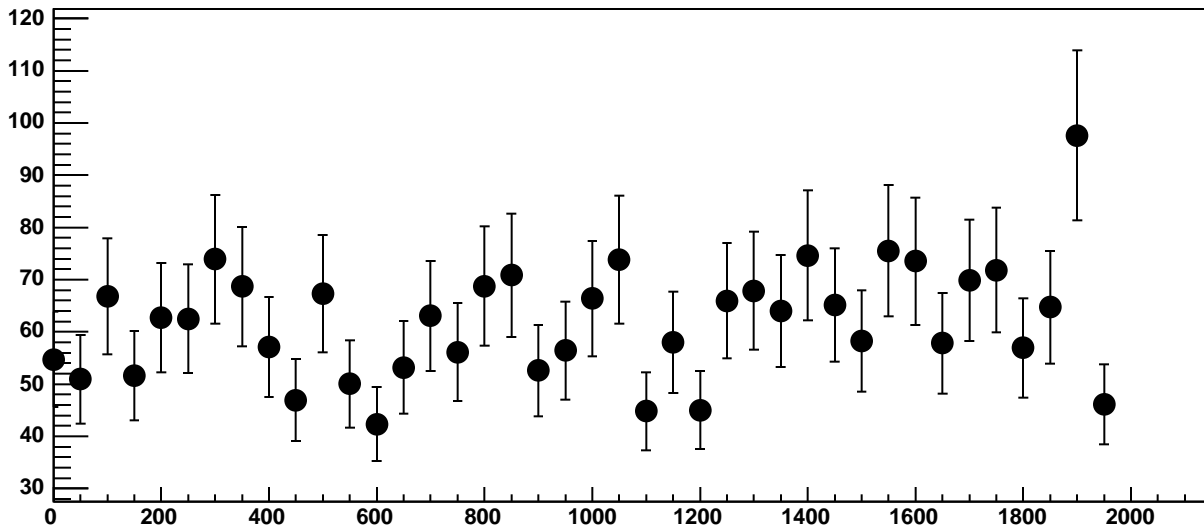




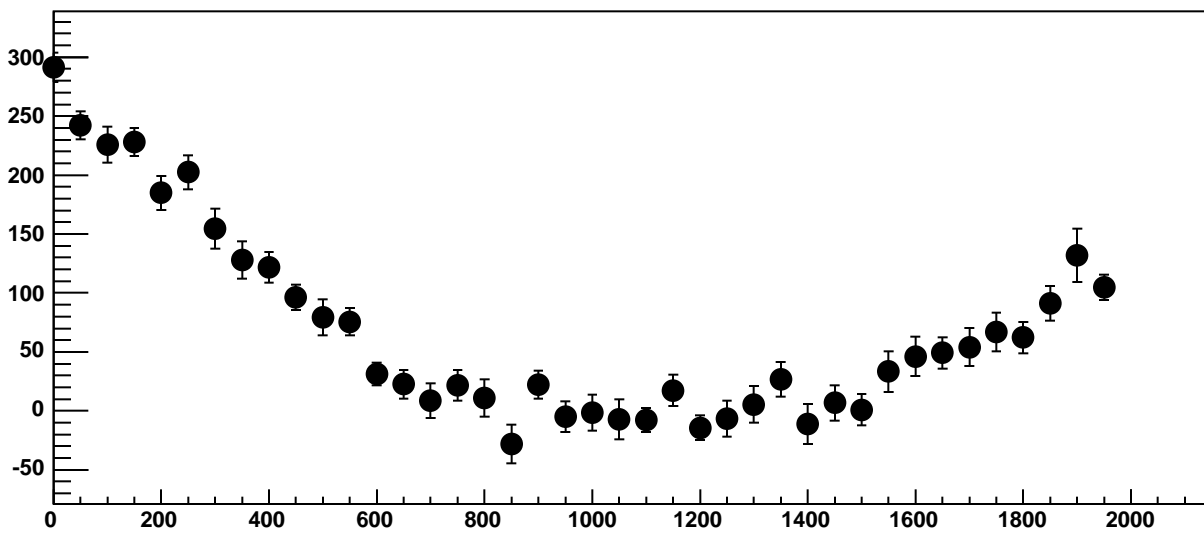
Chip 4, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC



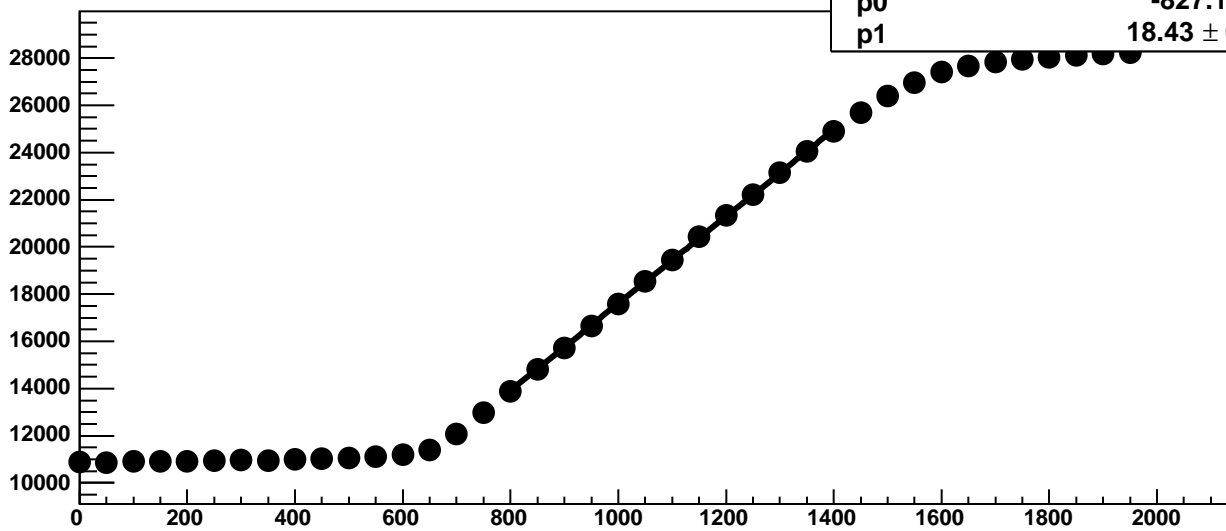
Chip 4, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC



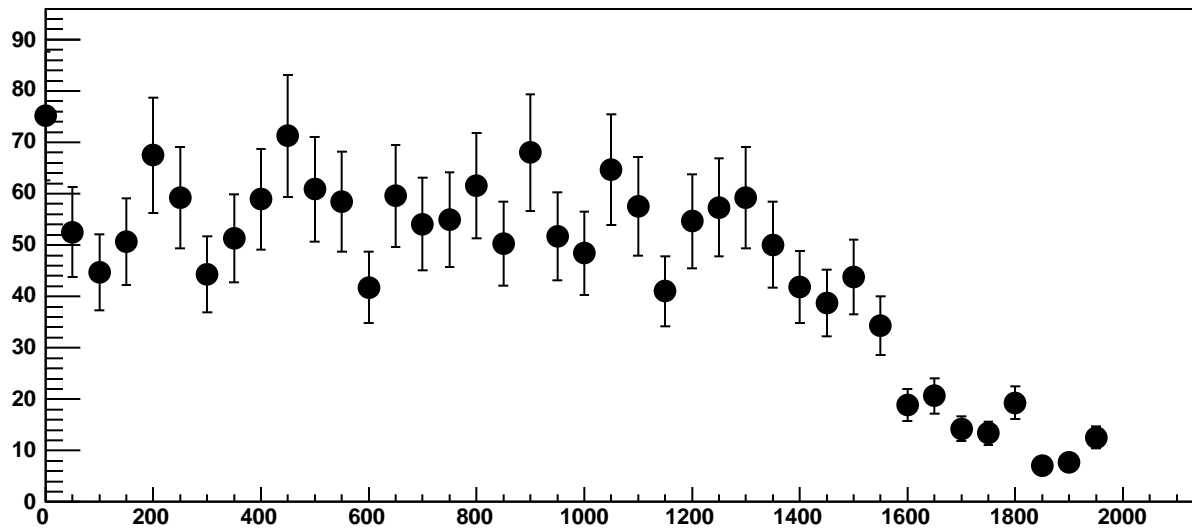
Chip 4, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC



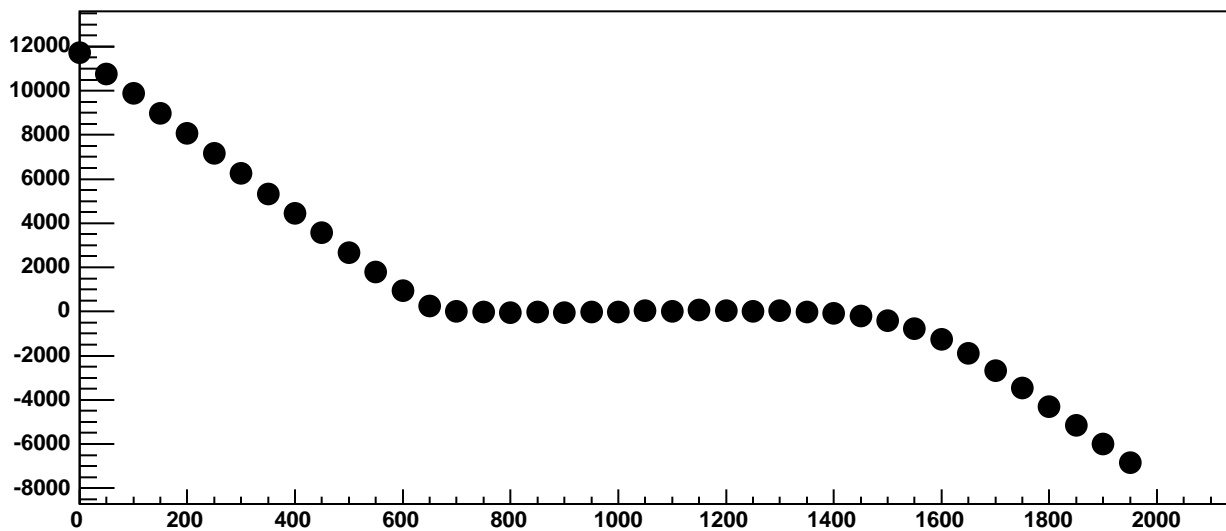
Chip 4, Channel 3, Enable 1!, Hold=30, ADC Mean vs DAC



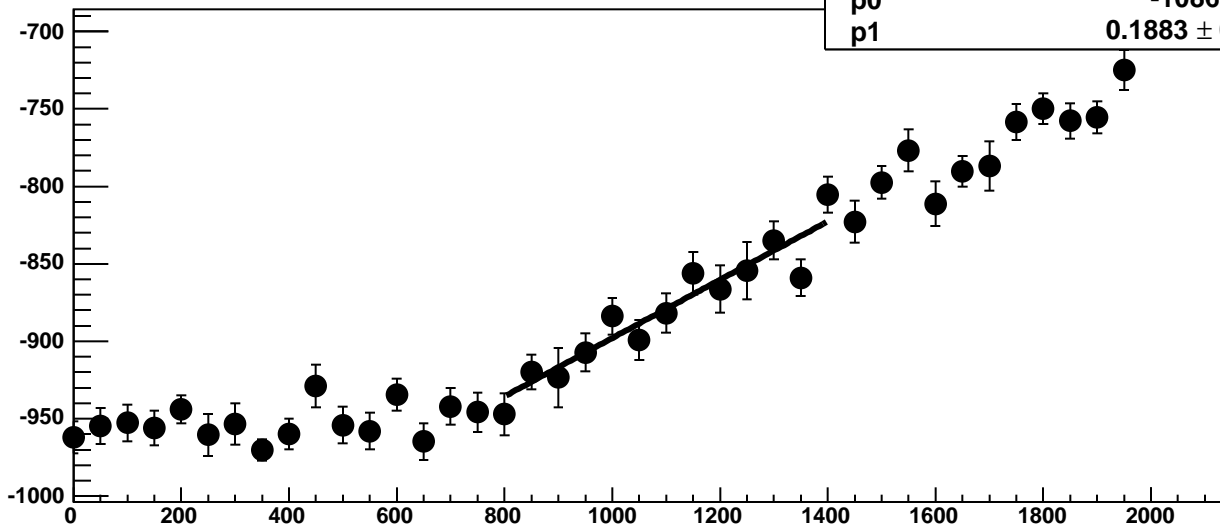
Chip 4, Channel 3, Enable 1!, Hold=30, ADC Noise vs DAC



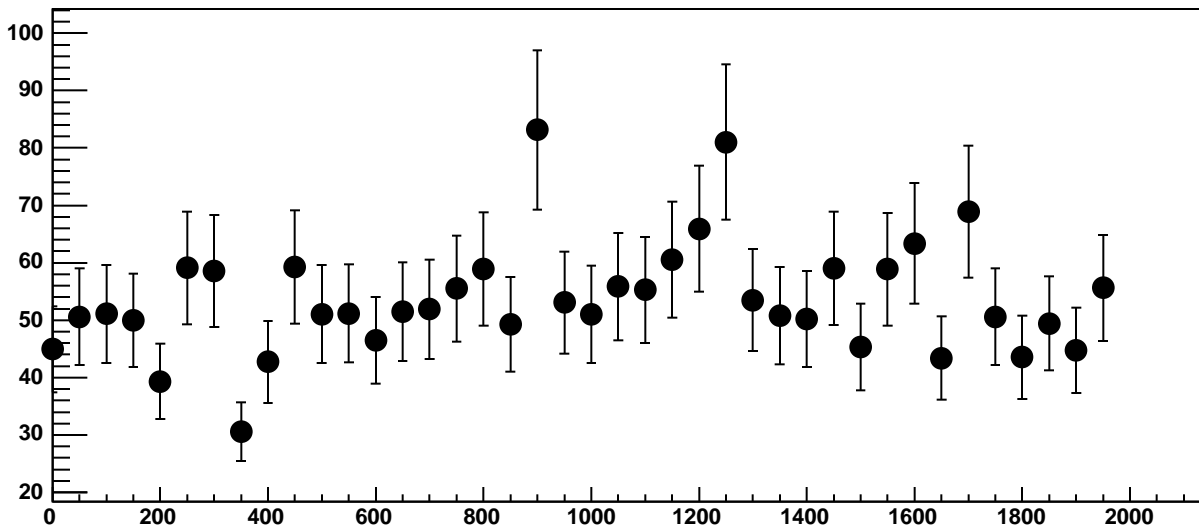
Chip 4, Channel 3, Enable 1!, Hold=30, ADC Residuals vs DAC



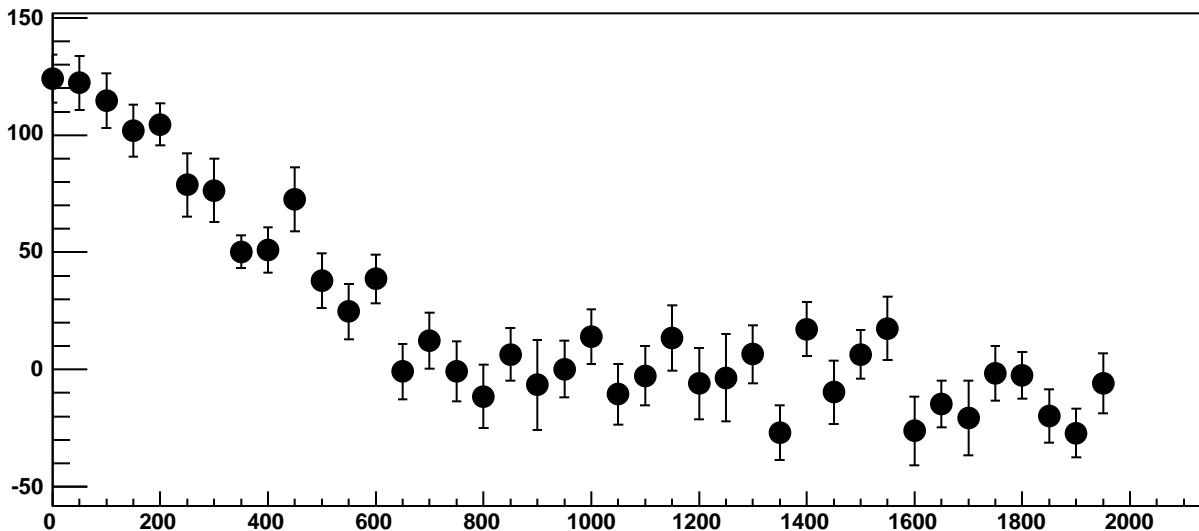
Chip 4, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC



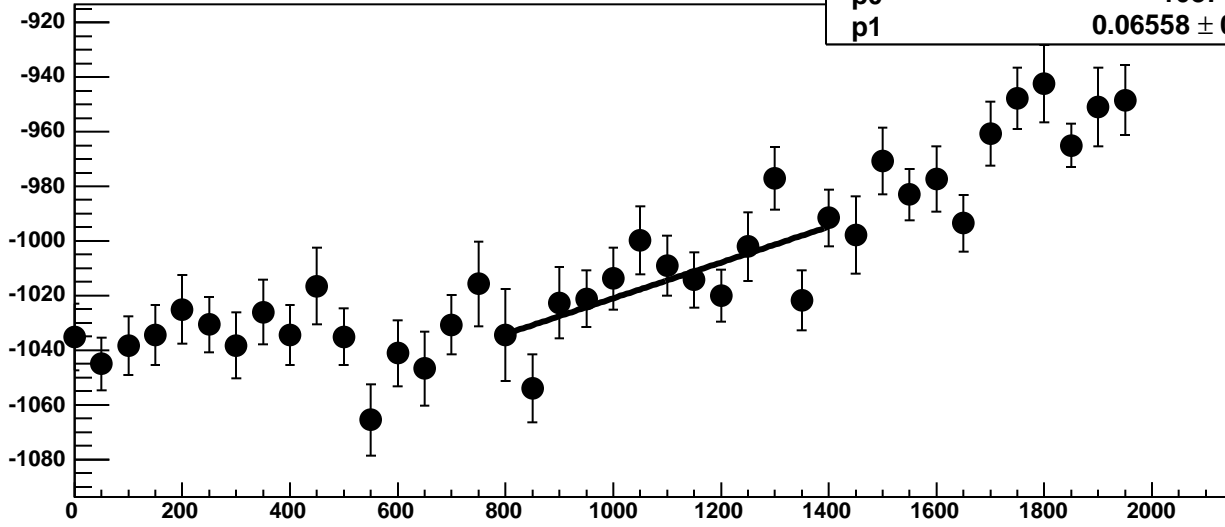
Chip 4, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC



Chip 4, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC

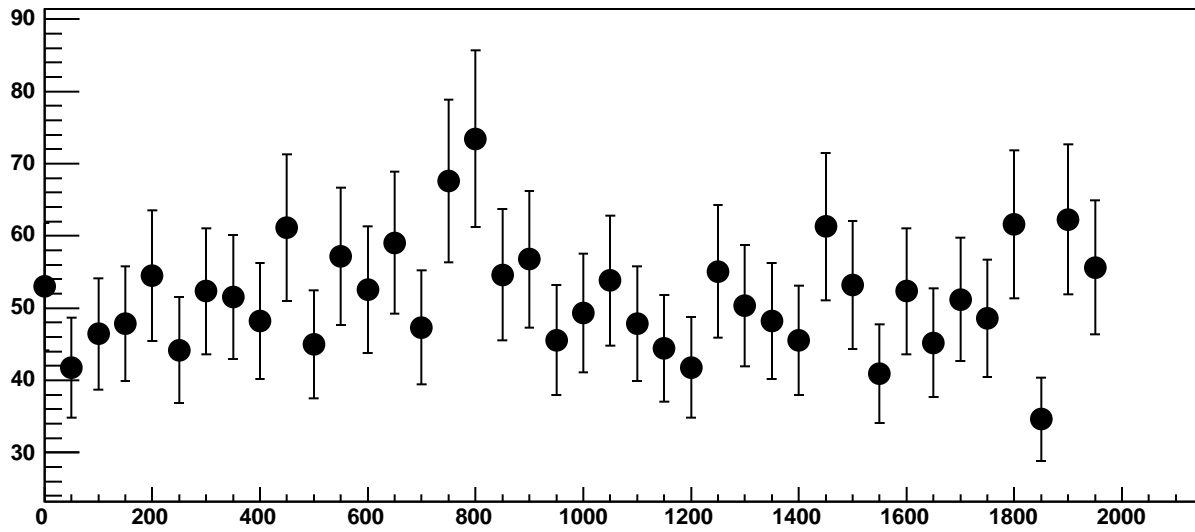


Chip 4, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC

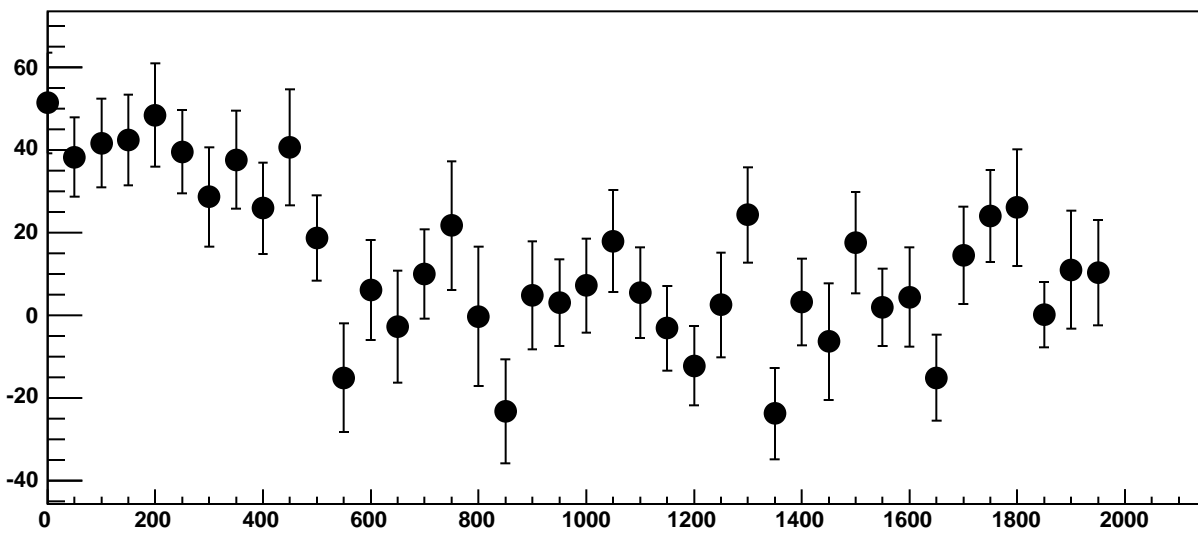


$\chi^2 / \text{ndf}$  17.27 / 11  
p0  $-1087 \pm 20.46$   
p1  $0.06558 \pm 0.01797$

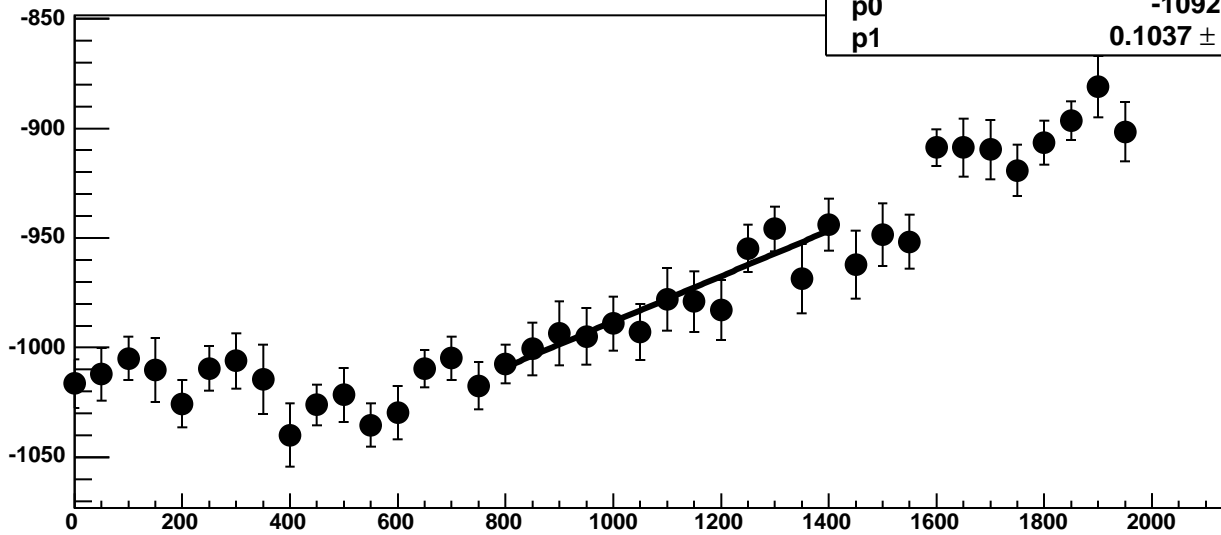
Chip 4, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC



Chip 4, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC



Chip 4, Channel 3, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

5.184 / 11

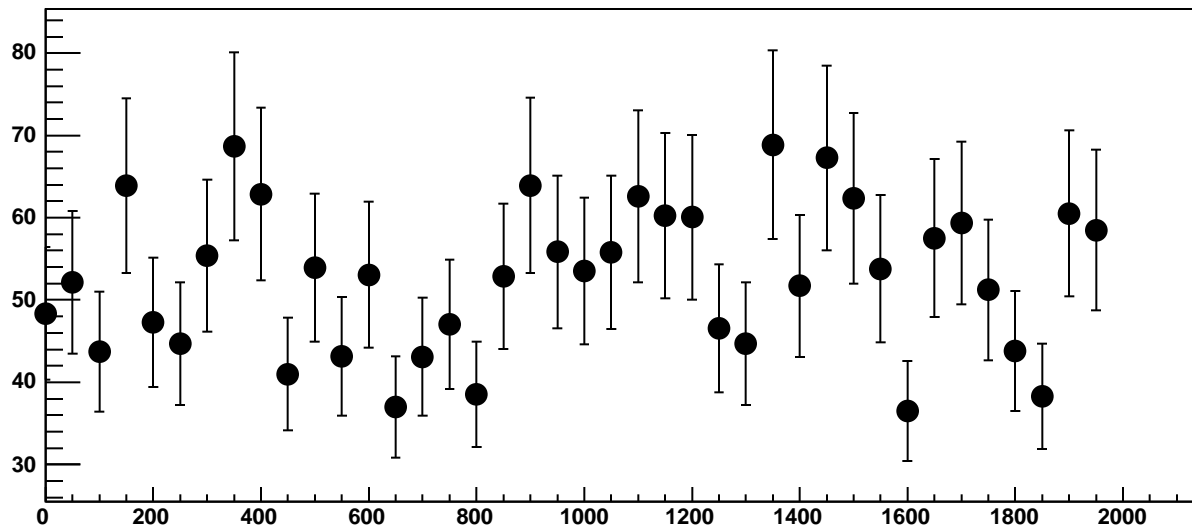
p0

$-1092 \pm 18.67$

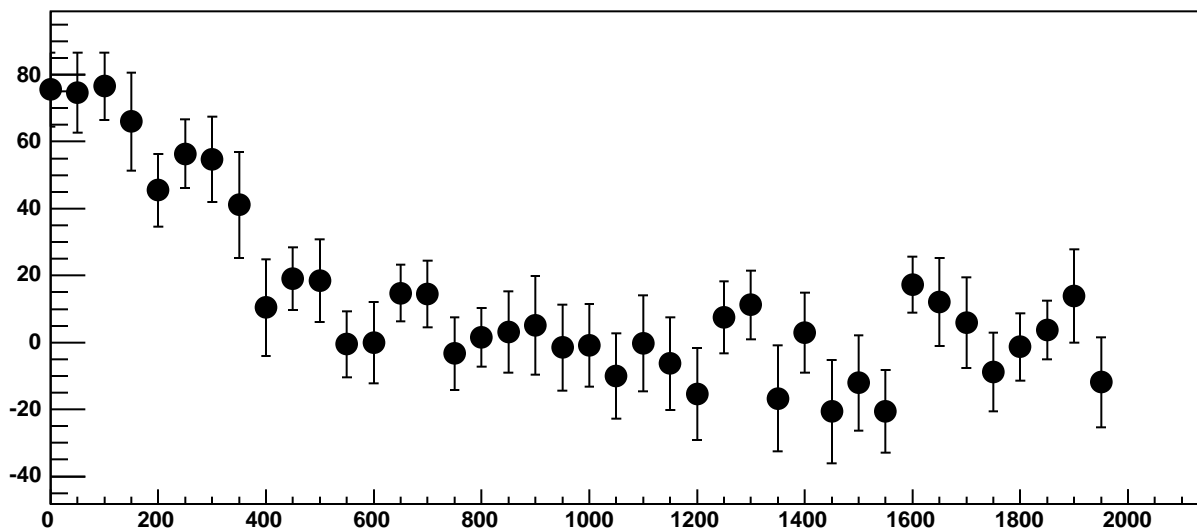
p1

$0.1037 \pm 0.01691$

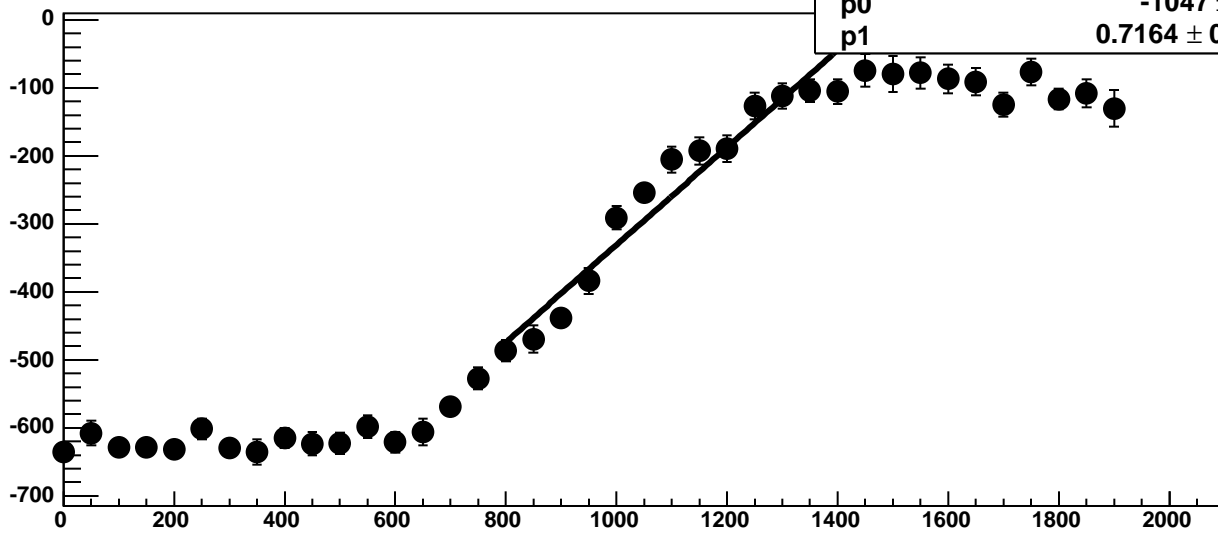
Chip 4, Channel 3, Enable 4, Hold=30, ADC Noise vs DAC



Chip 4, Channel 3, Enable 4, Hold=30, ADC Residuals vs DAC

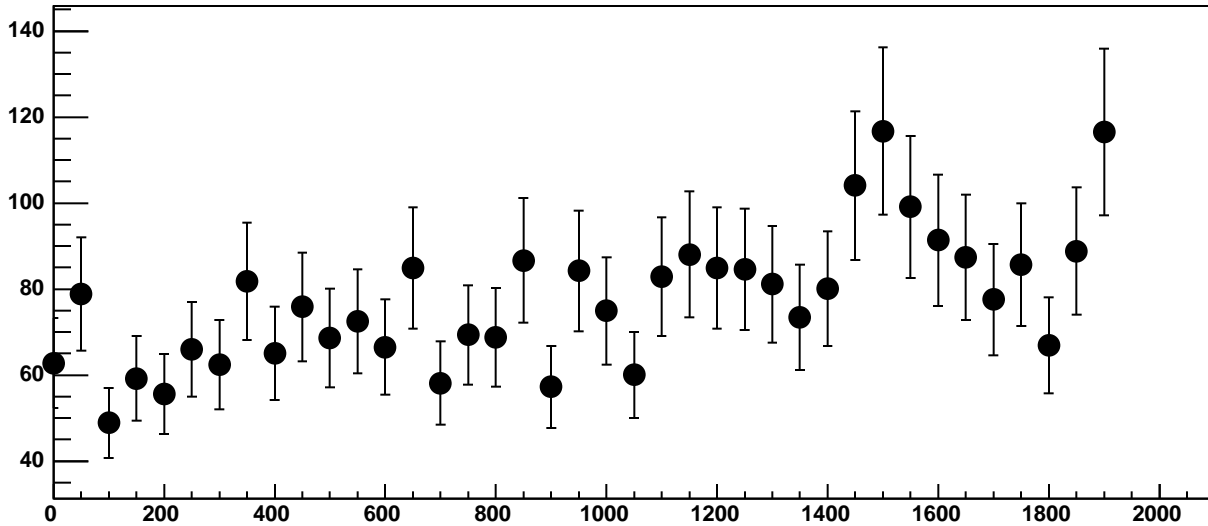


Chip 4, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC

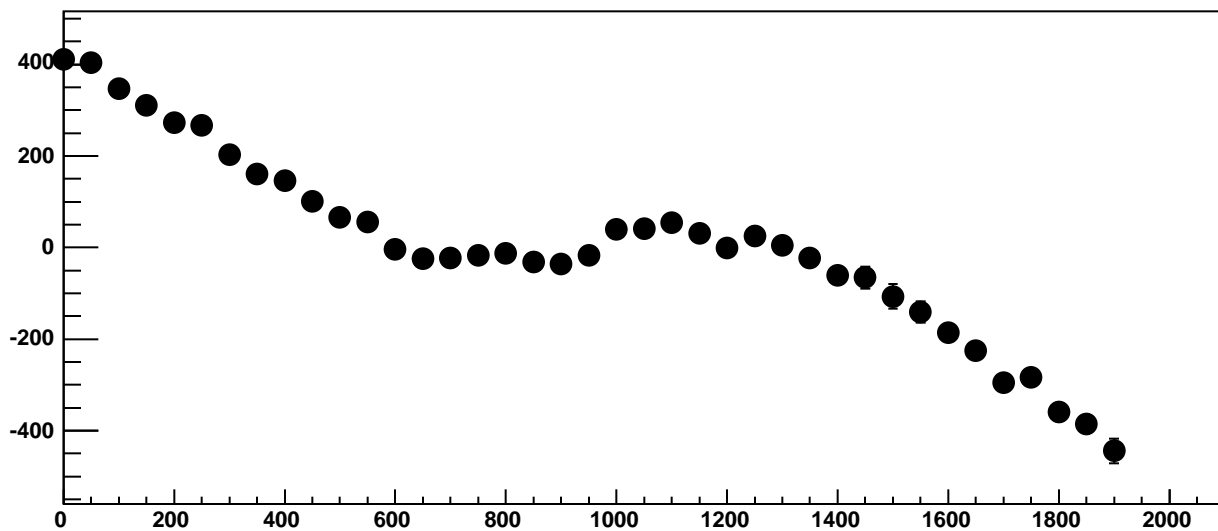


$\chi^2 / \text{ndf}$  50.65 / 11  
p0  $-1047 \pm 27.98$   
p1  $0.7164 \pm 0.02552$

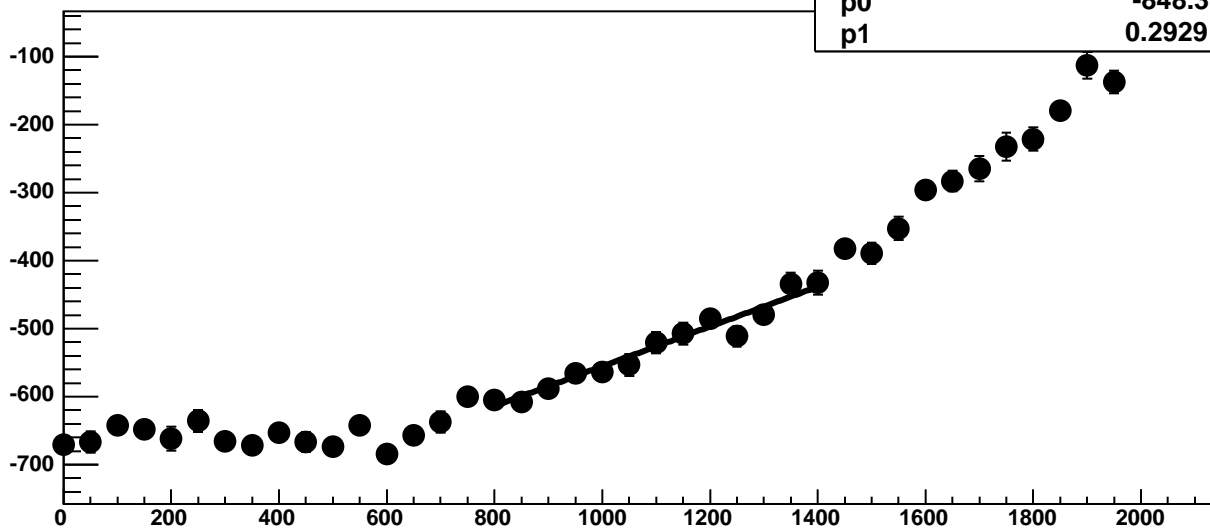
Chip 4, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC



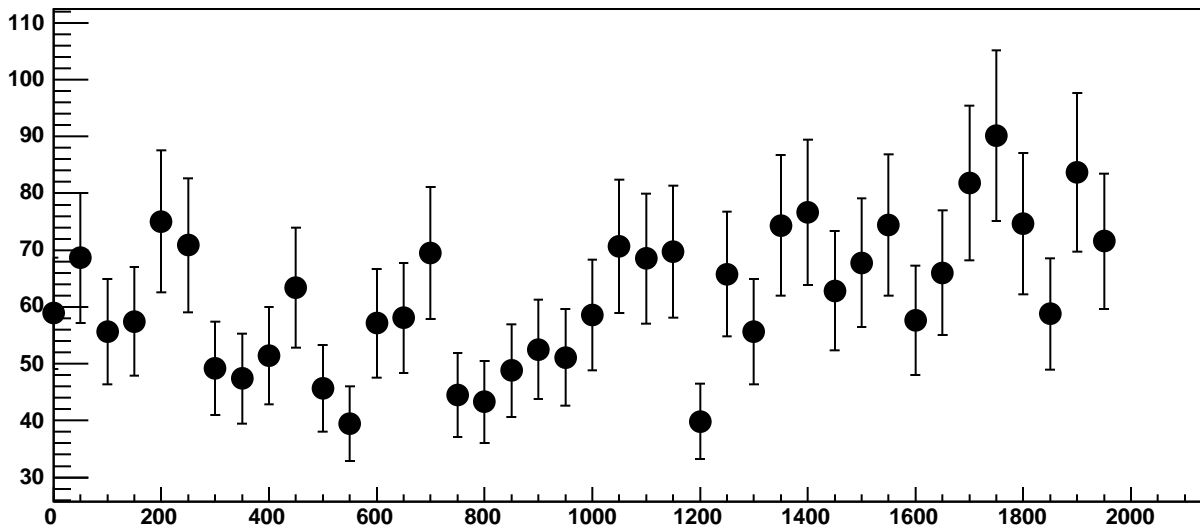
Chip 4, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC



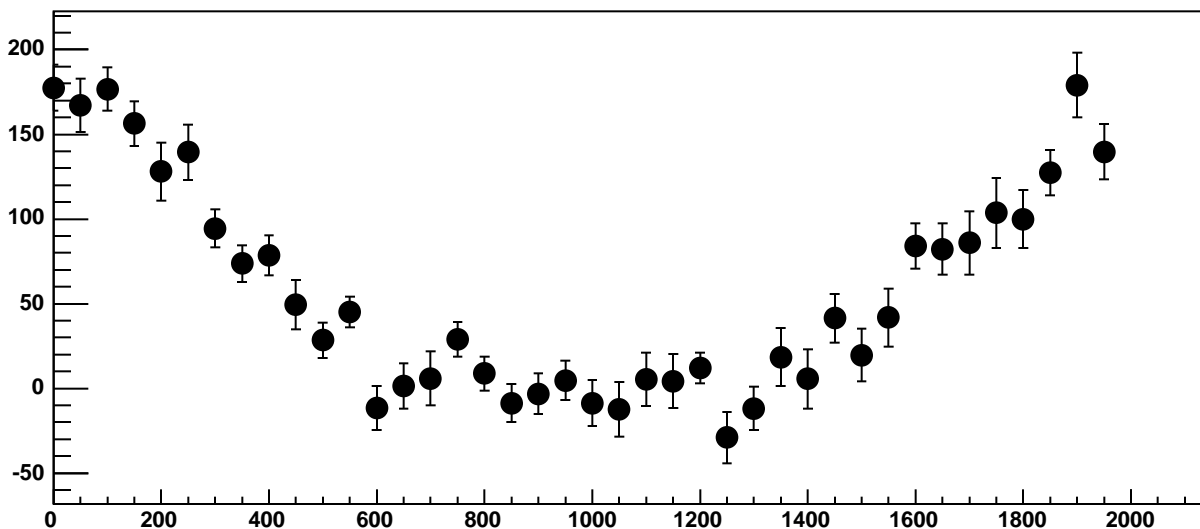
Chip 4, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC



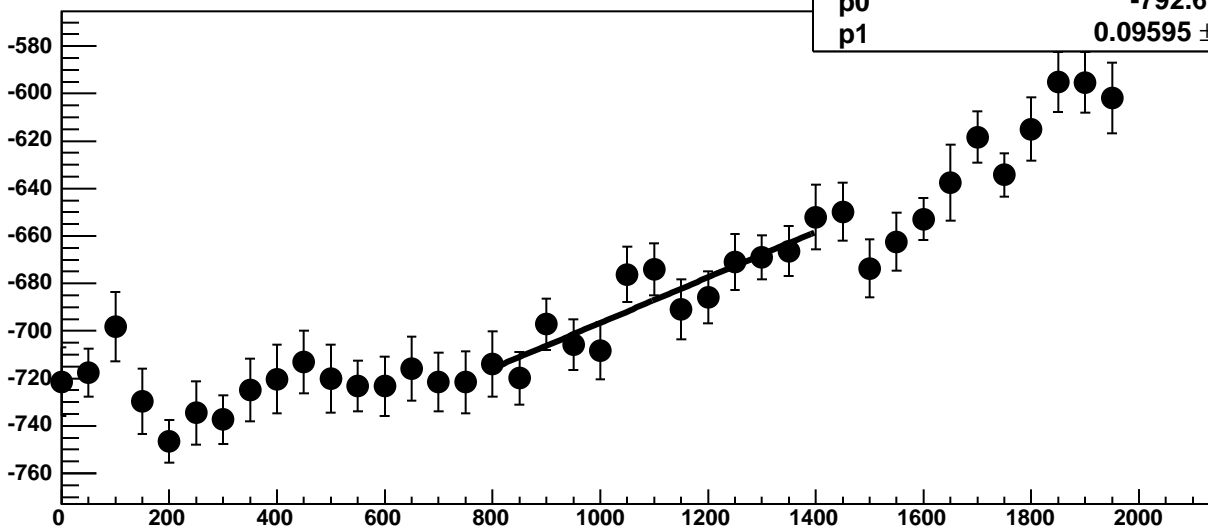
Chip 4, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC



Chip 4, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC

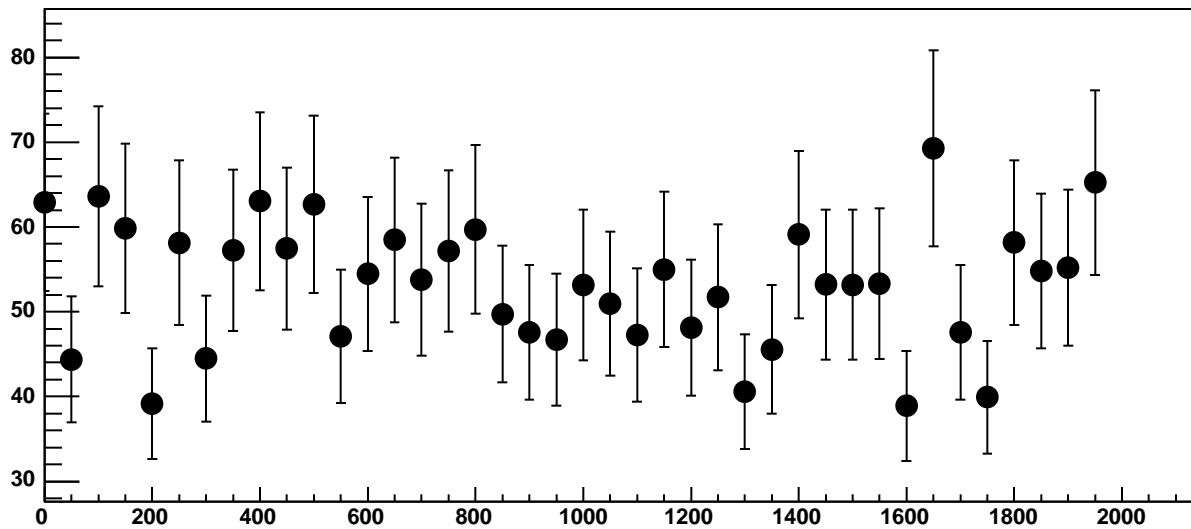


Chip 4, Channel 4, Enable 1, Hold=30, ADC Mean vs DAC

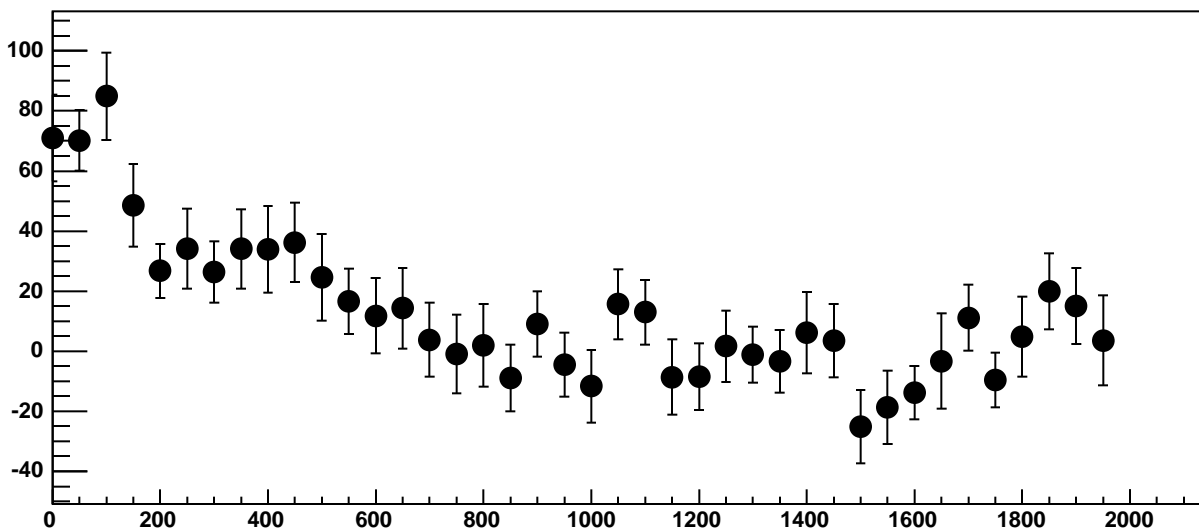


$\chi^2 / \text{ndf}$  7.064 / 11  
p0  $-792.6 \pm 19.32$   
p1  $0.09595 \pm 0.0172$

Chip 4, Channel 4, Enable 1, Hold=30, ADC Noise vs DAC

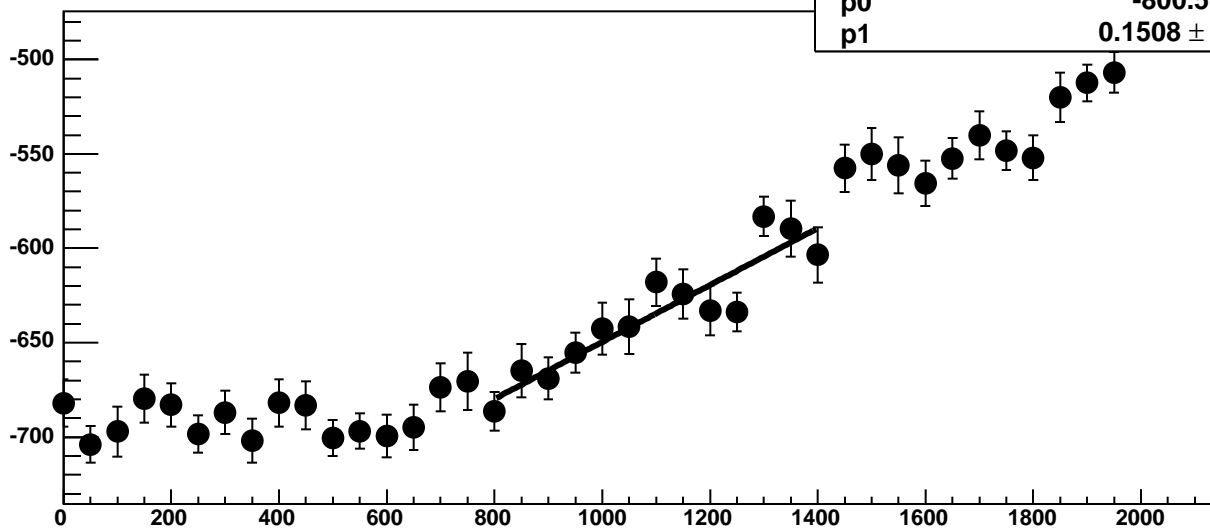


Chip 4, Channel 4, Enable 1, Hold=30, ADC Residuals vs DAC



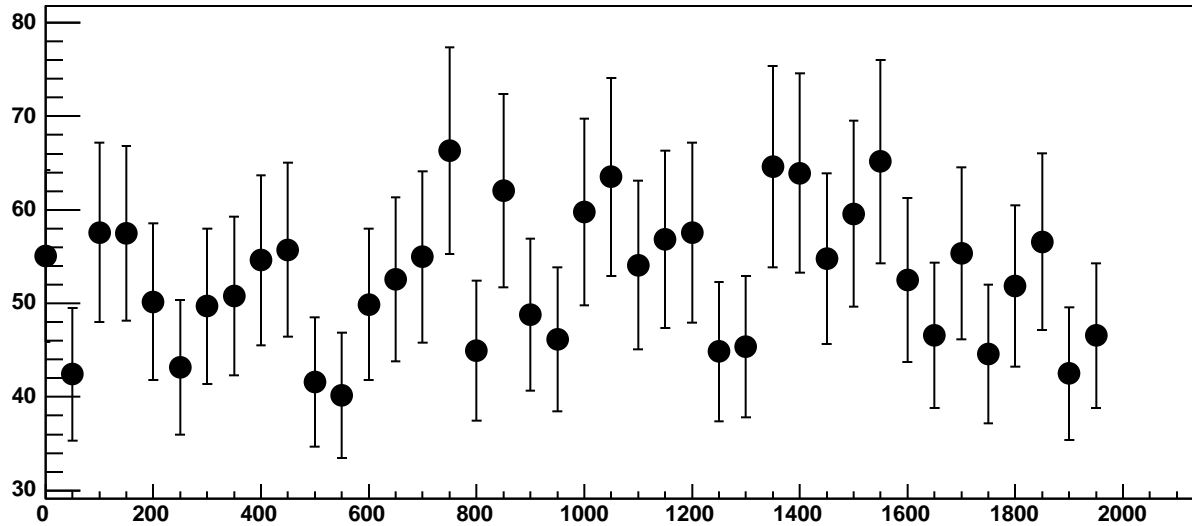


Chip 4, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC

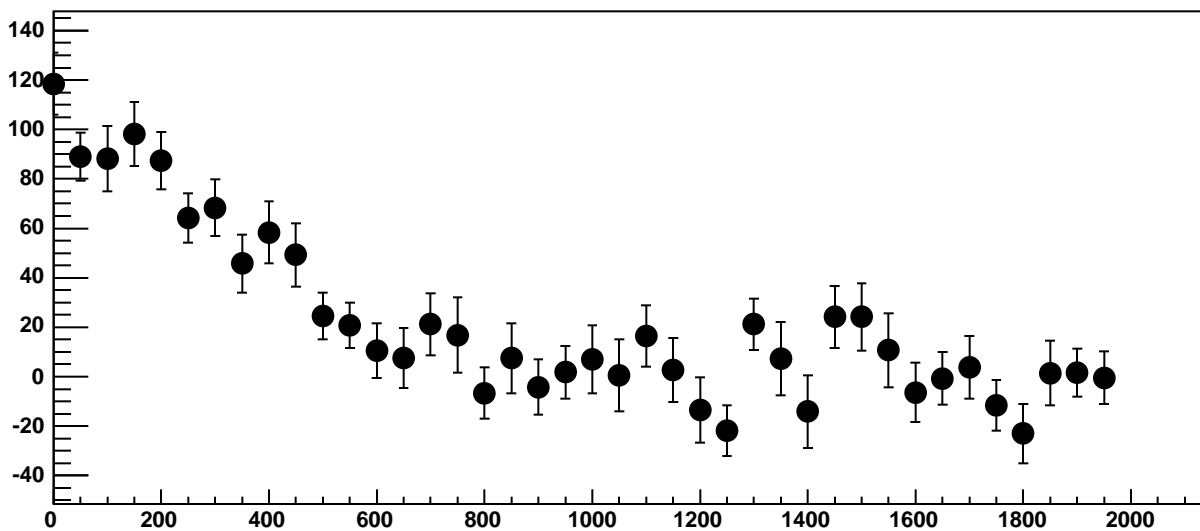


$\chi^2 / \text{ndf}$  13.79 / 11  
p0  $-800.5 \pm 19.88$   
p1  $0.1508 \pm 0.01801$

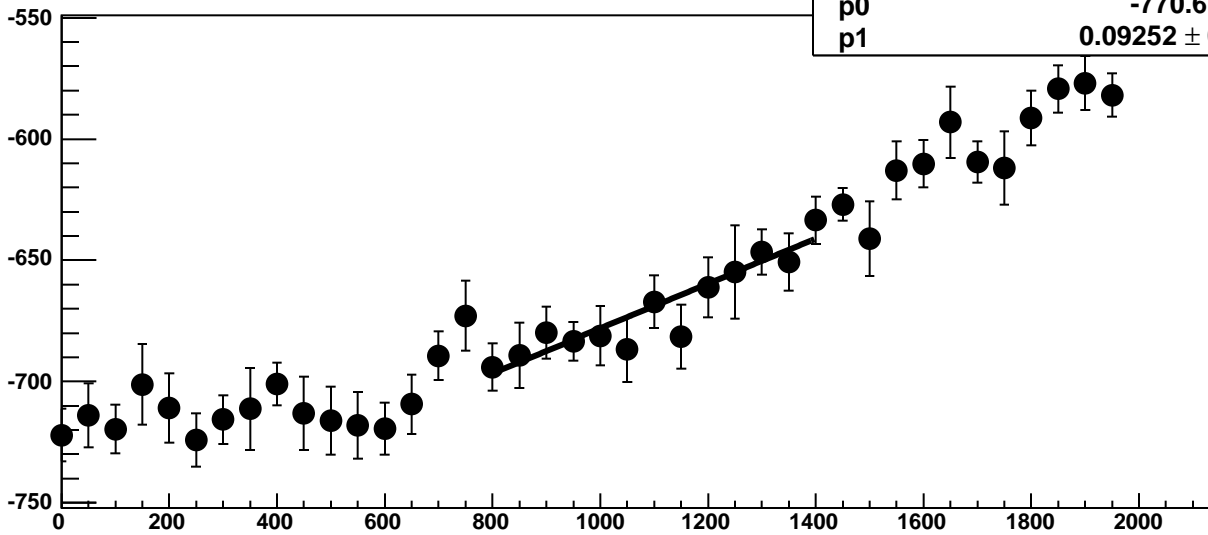
Chip 4, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC



Chip 4, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC

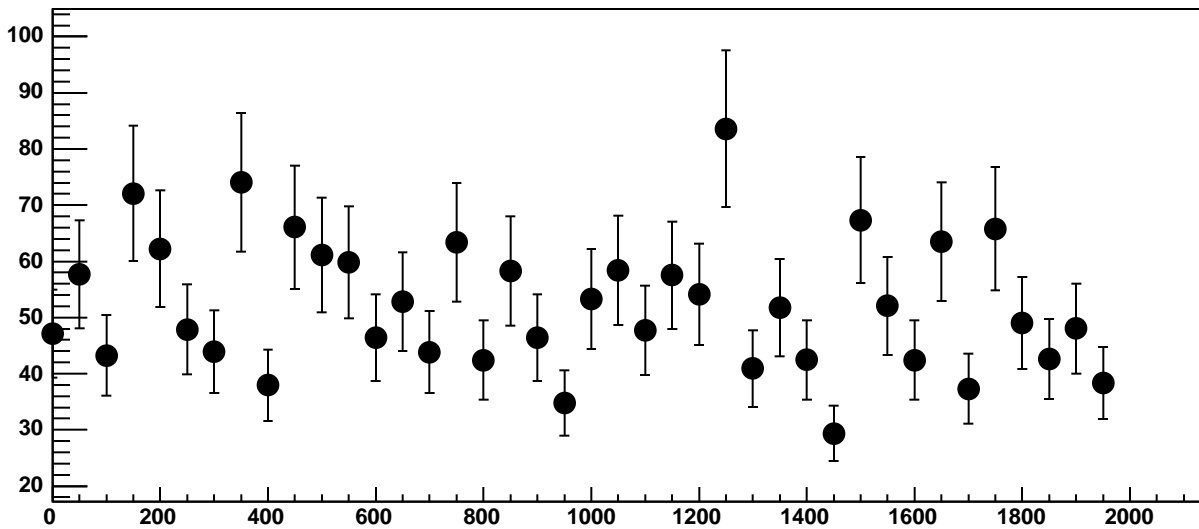


Chip 4, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC

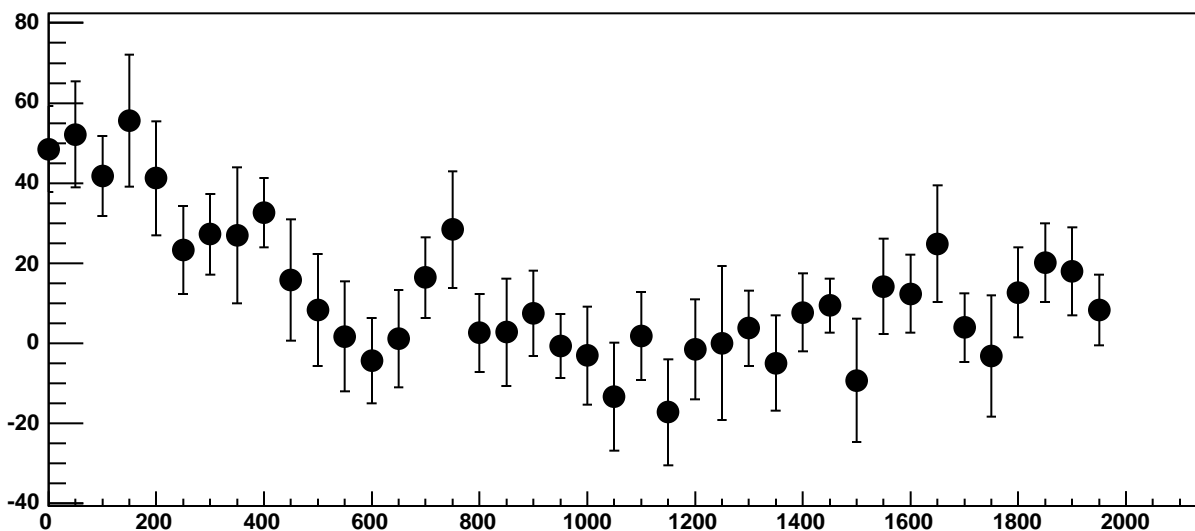


$\chi^2 / \text{ndf}$  4.363 / 11  
p0  $-770.6 \pm 17.37$   
p1  $0.09252 \pm 0.01569$

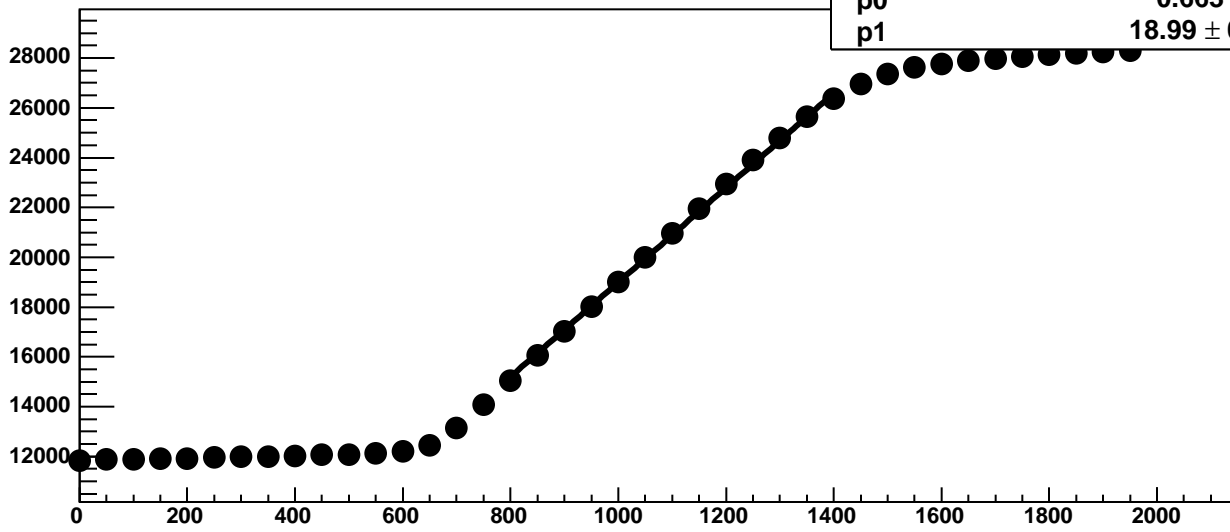
Chip 4, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC



Chip 4, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC

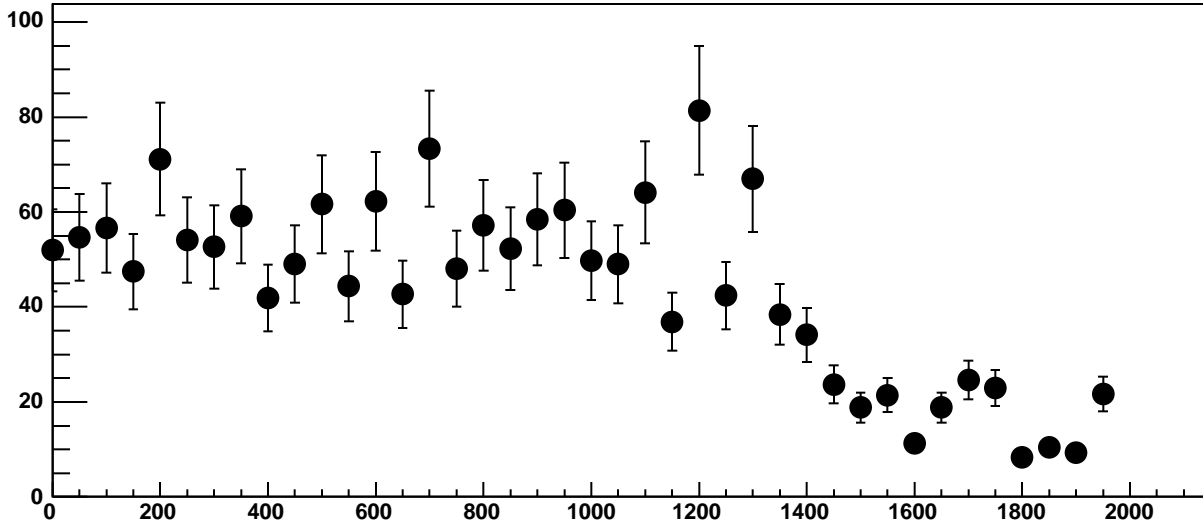


Chip 4, Channel 4, Enable 4!, Hold=30, ADC Mean vs DAC

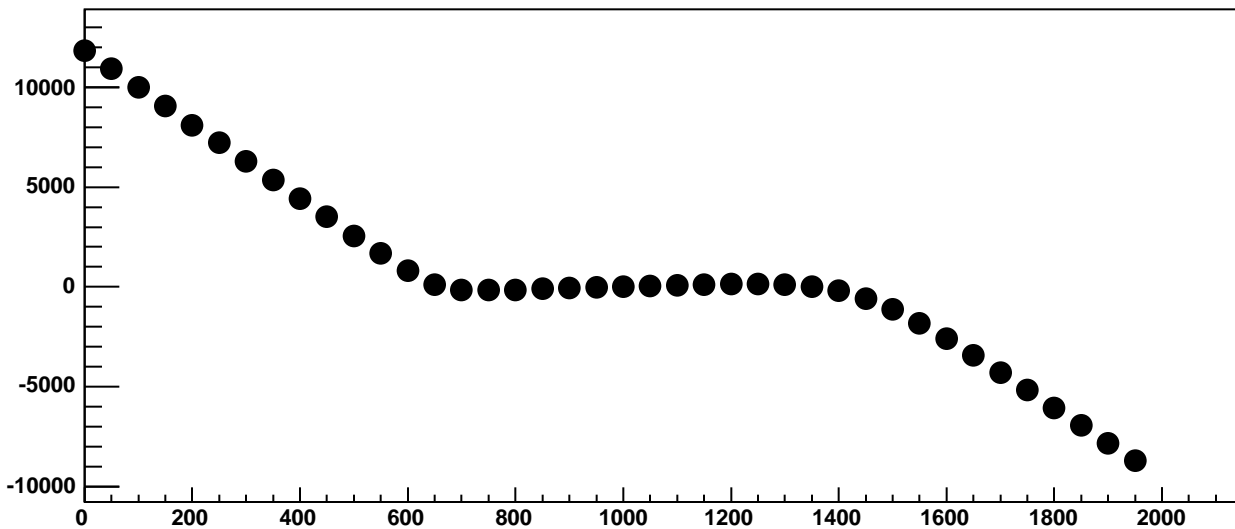


$\chi^2 / \text{ndf}$  1525 / 11  
p0  $0.663 \pm 18.64$   
p1  $18.99 \pm 0.01601$

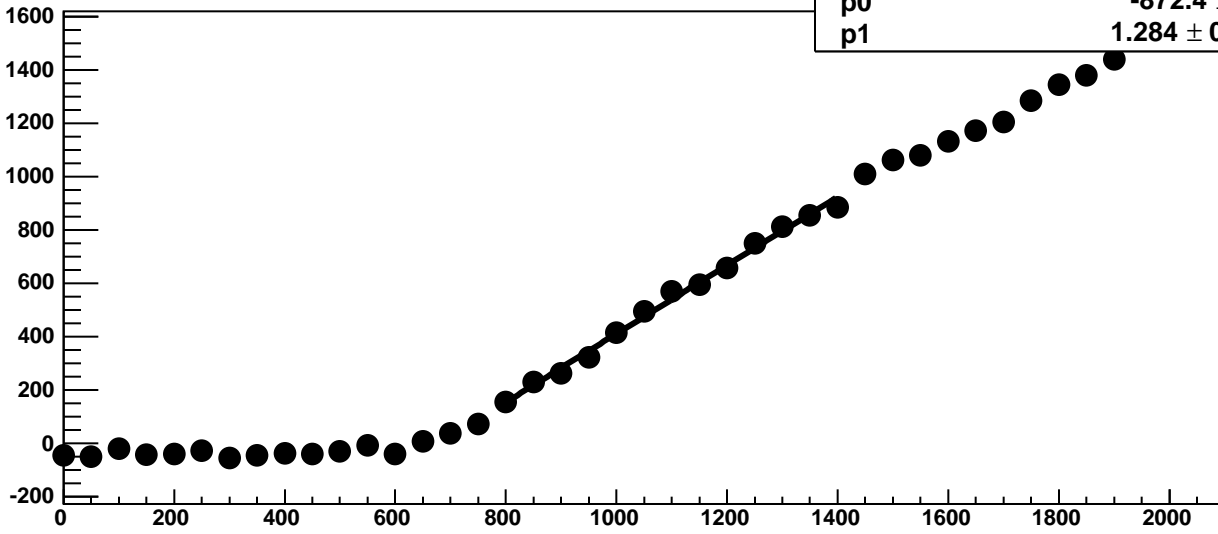
Chip 4, Channel 4, Enable 4!, Hold=30, ADC Noise vs DAC



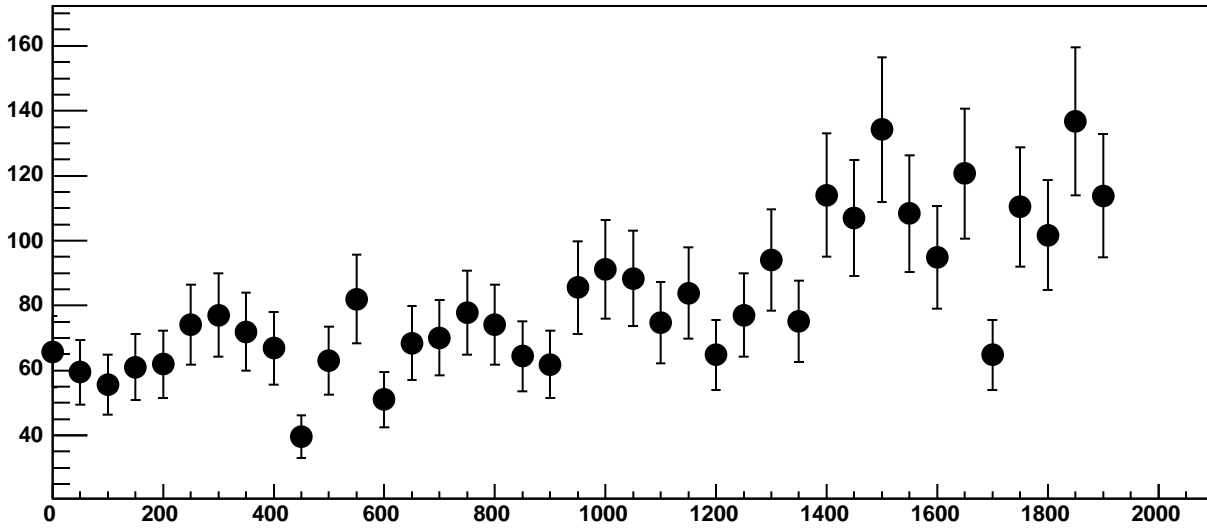
Chip 4, Channel 4, Enable 4!, Hold=30, ADC Residuals vs DAC



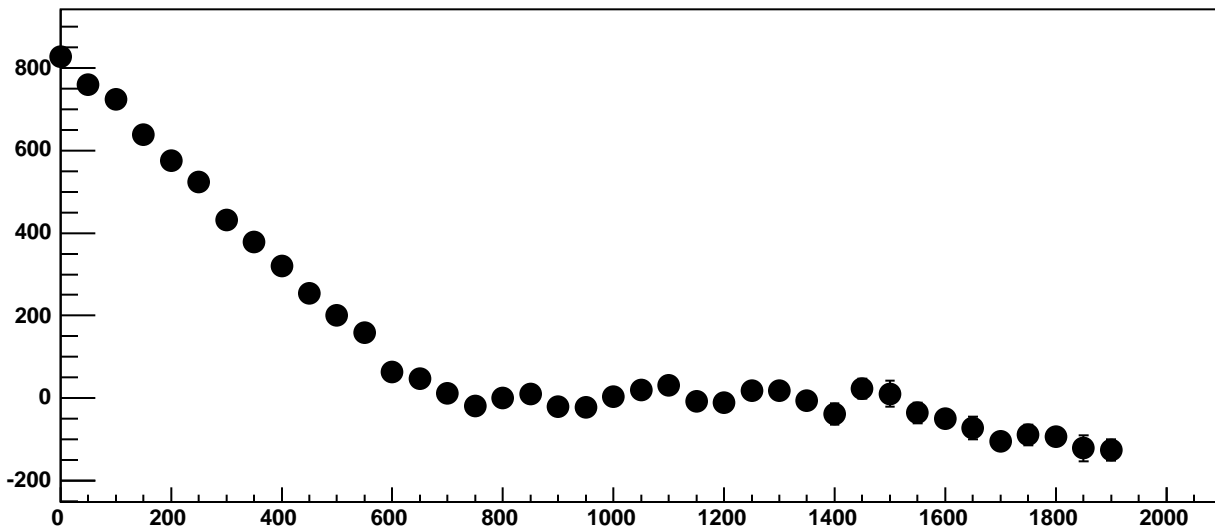
Chip 4, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC



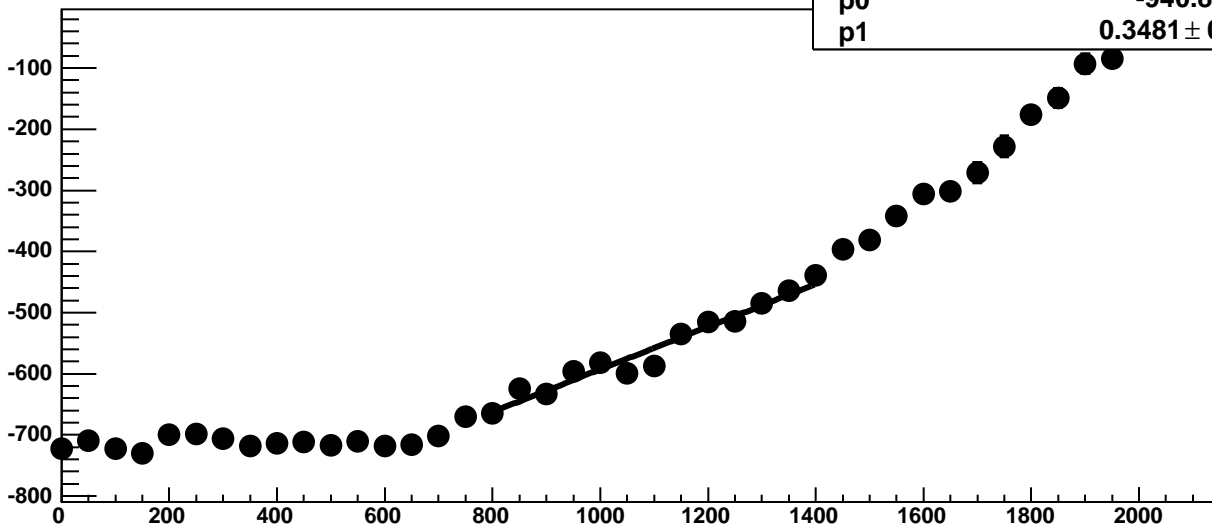
Chip 4, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC



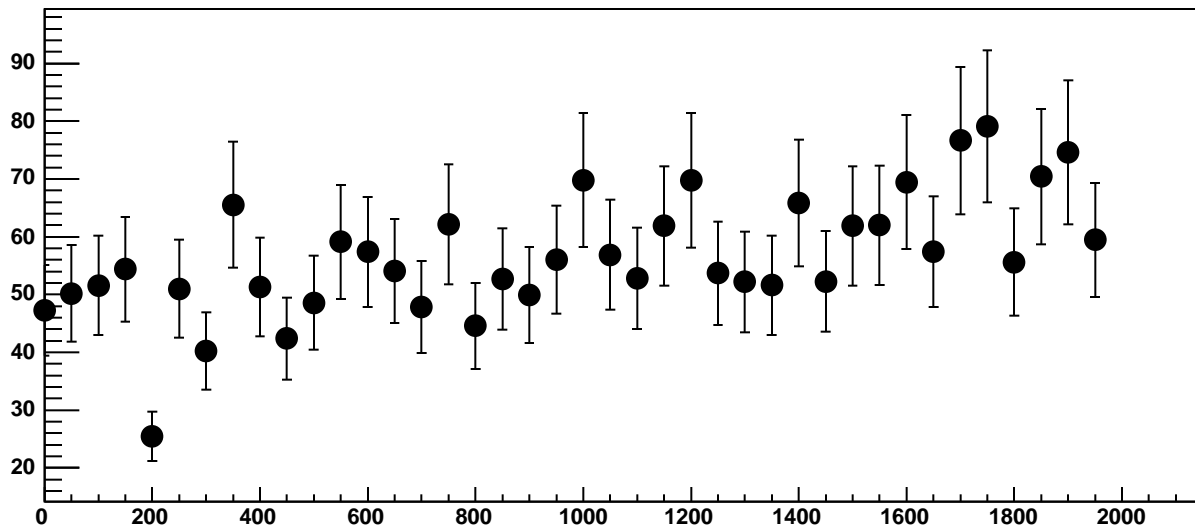
Chip 4, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC



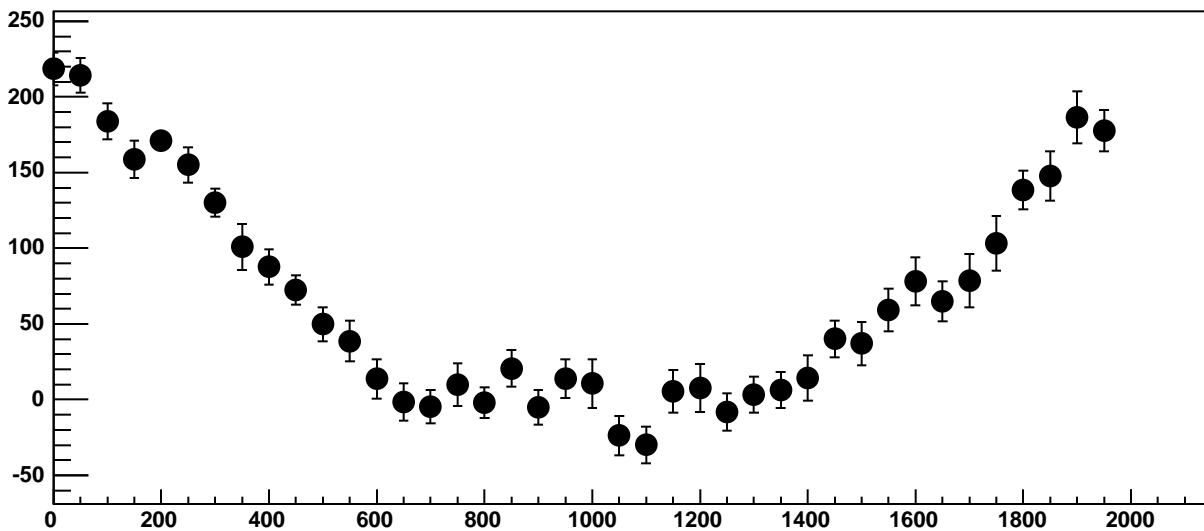
Chip 4, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC



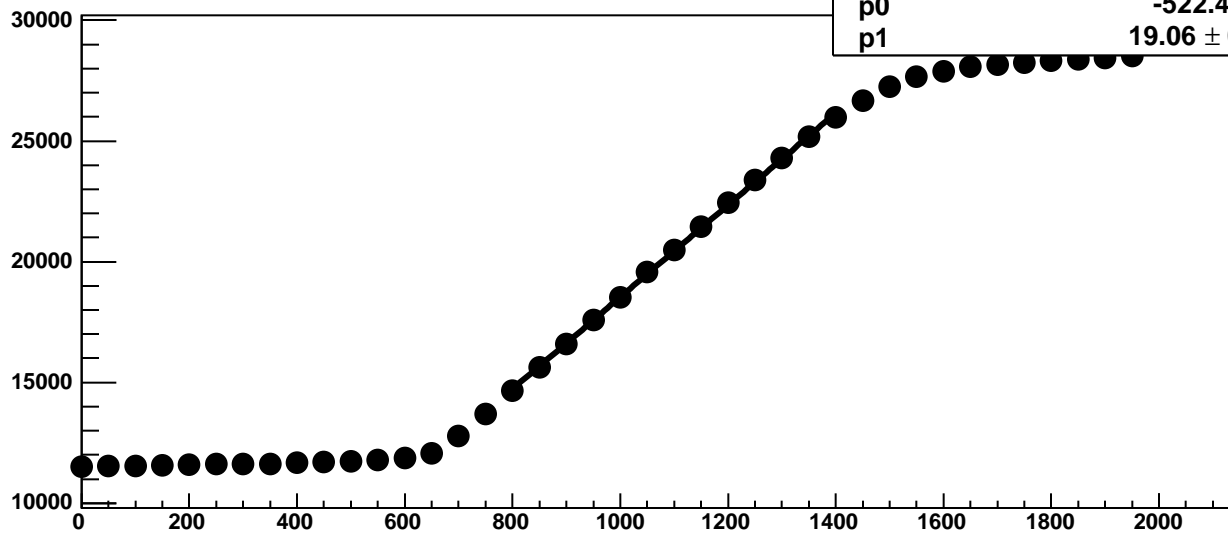
Chip 4, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC



Chip 4, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC

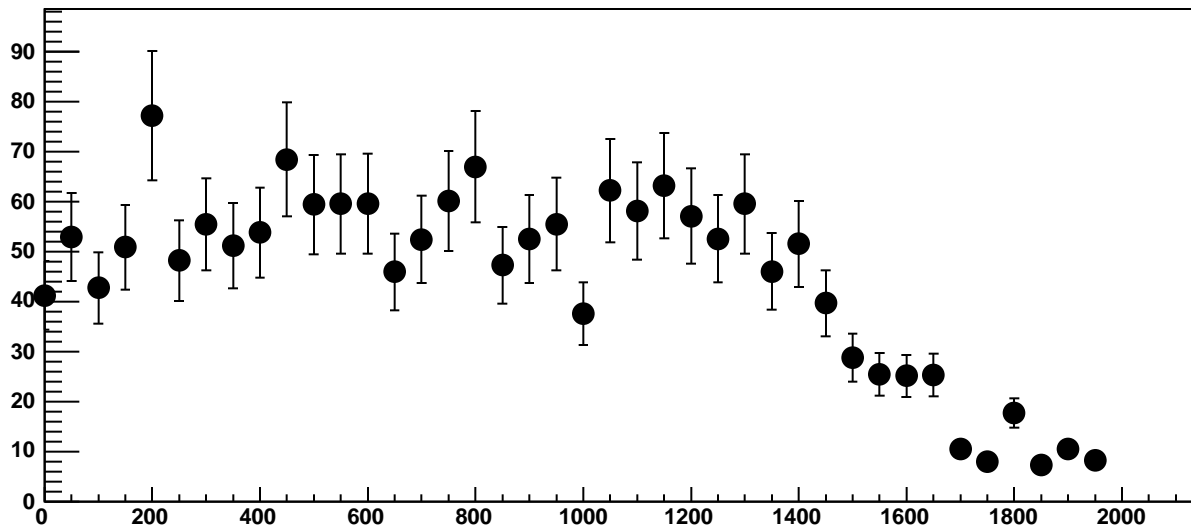


Chip 4, Channel 5, Enable 1!, Hold=30, ADC Mean vs DAC

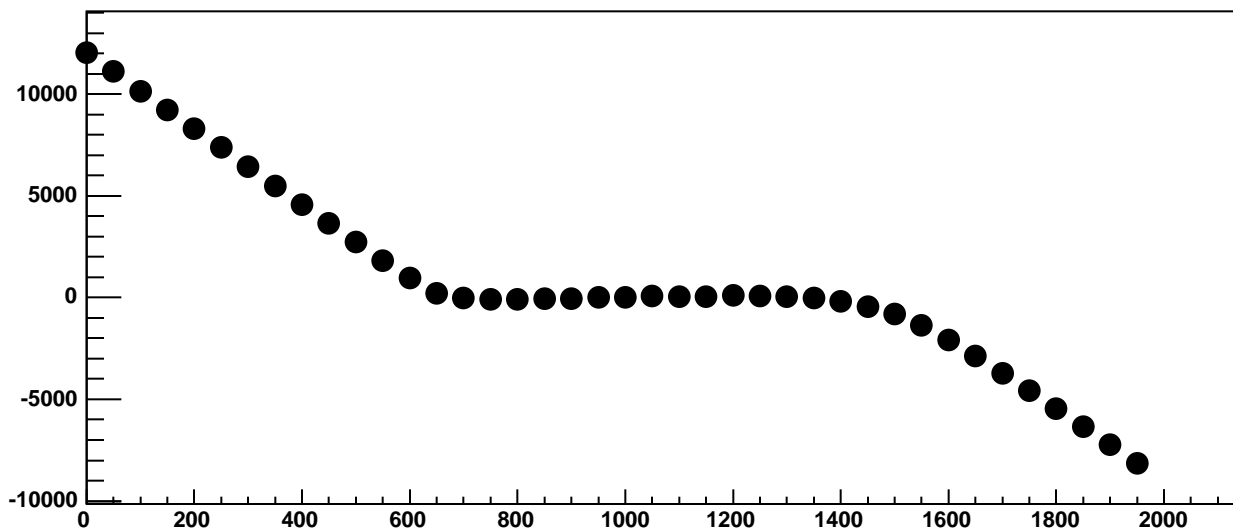


$\chi^2 / \text{ndf}$	445.8 / 11
p0	-522.4 ± 19.91
p1	19.06 ± 0.01786

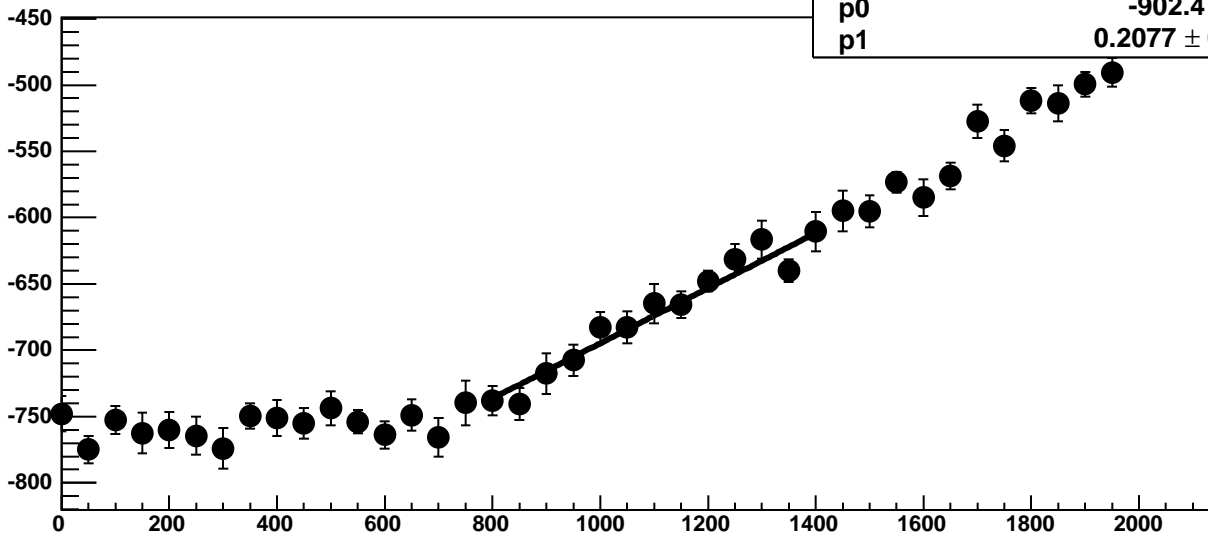
Chip 4, Channel 5, Enable 1!, Hold=30, ADC Noise vs DAC



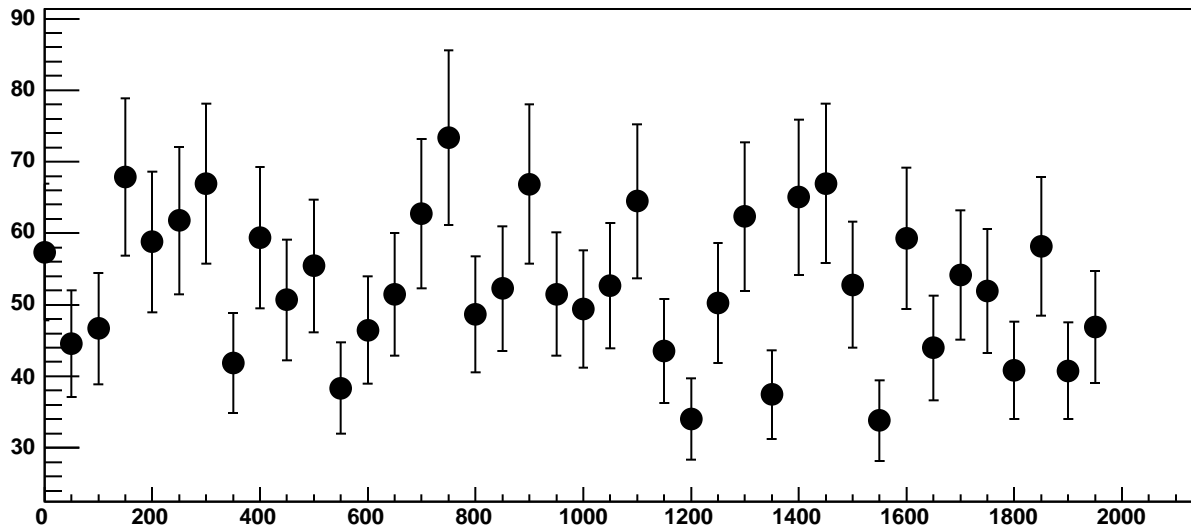
Chip 4, Channel 5, Enable 1!, Hold=30, ADC Residuals vs DAC



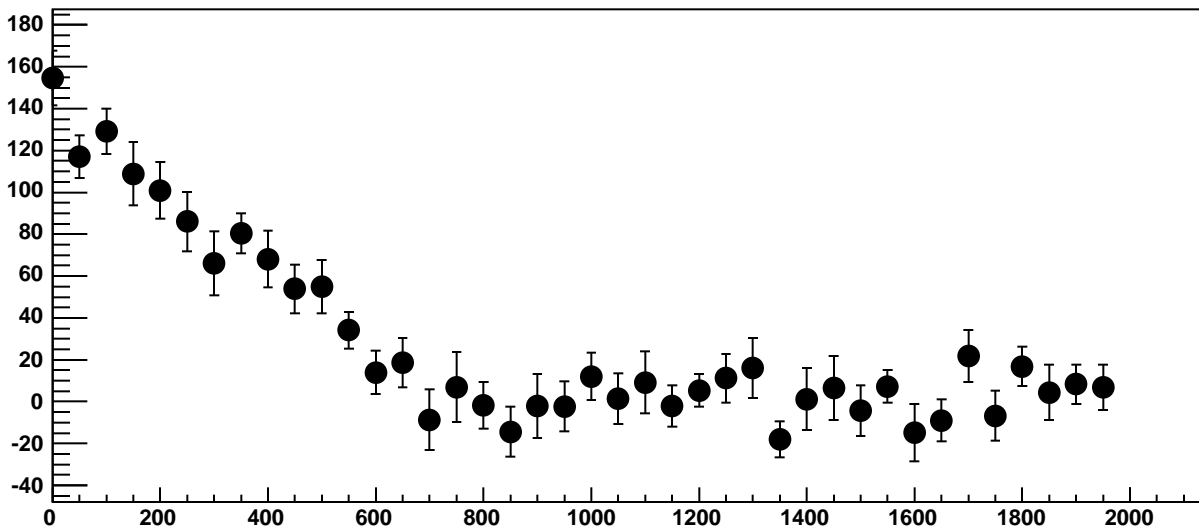
Chip 4, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC



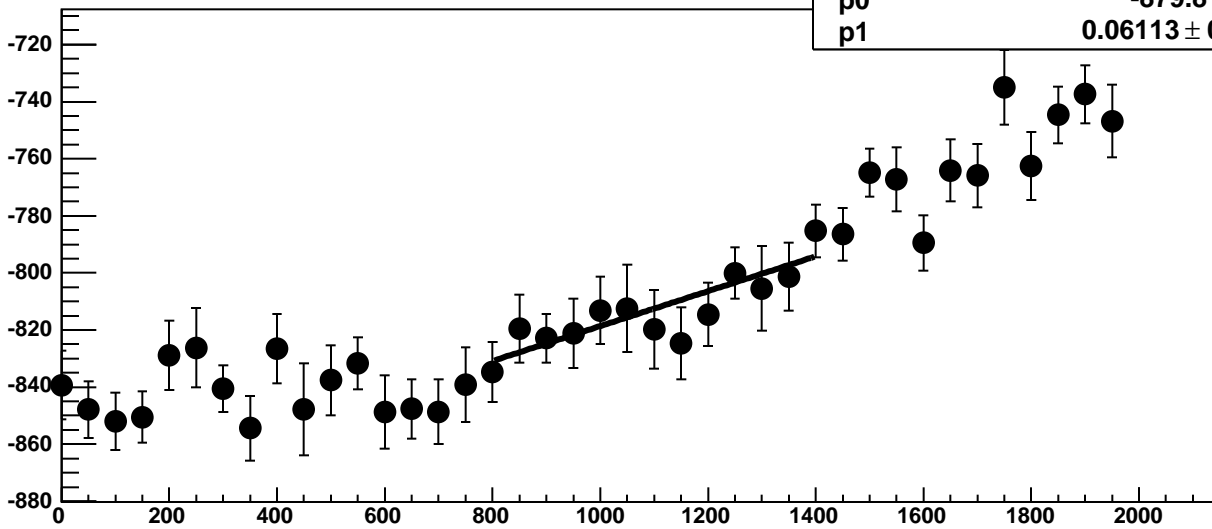
Chip 4, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC



Chip 4, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC

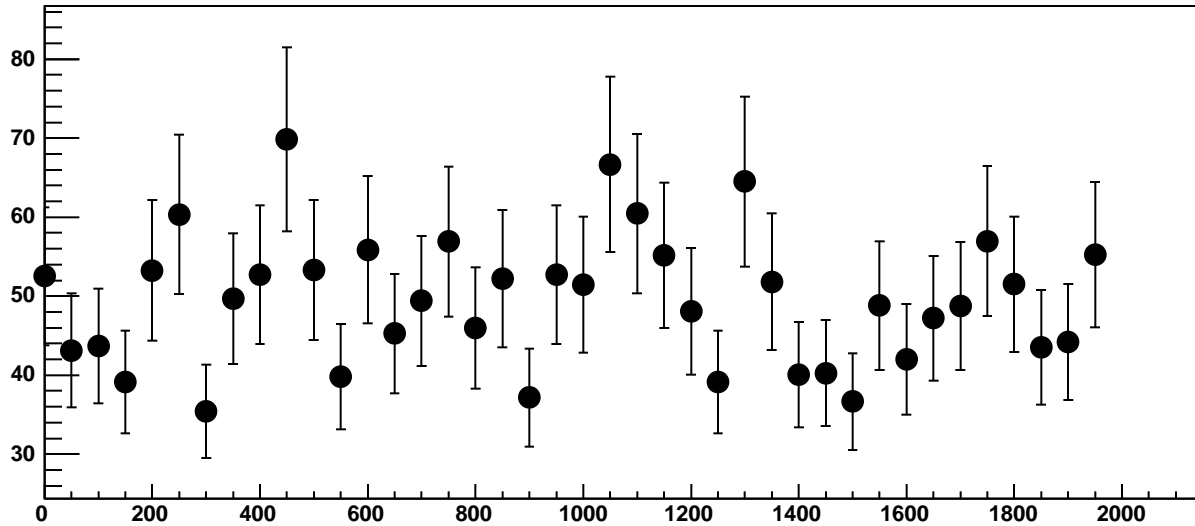


Chip 4, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC

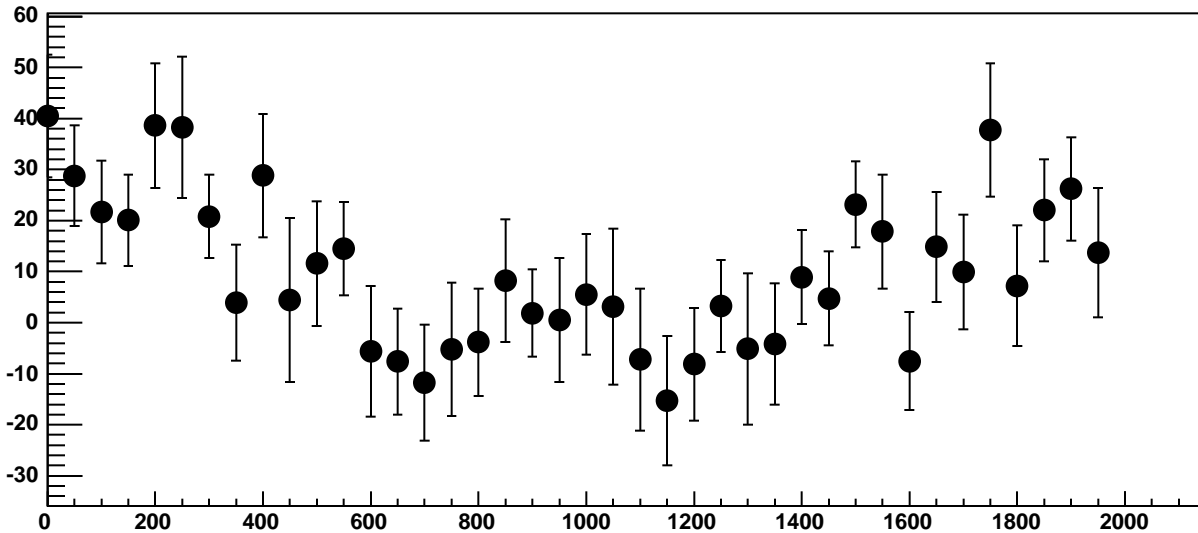


$\chi^2 / \text{ndf}$  4.493 / 11  
p0  $-879.8 \pm 17.47$   
p1  $0.06113 \pm 0.01562$

Chip 4, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC

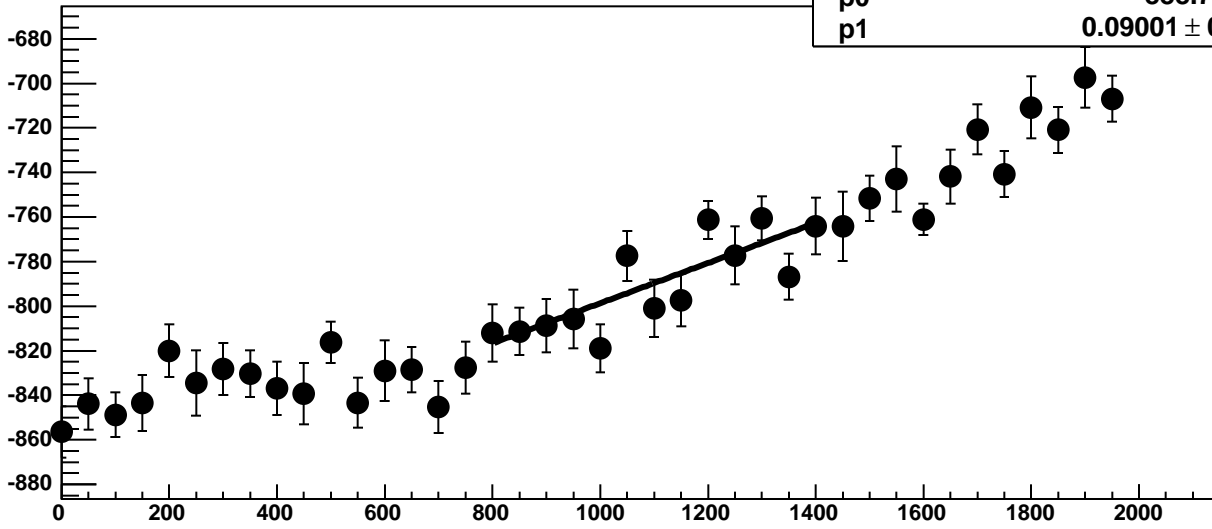


Chip 4, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC





Chip 4, Channel 5, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

18 / 11

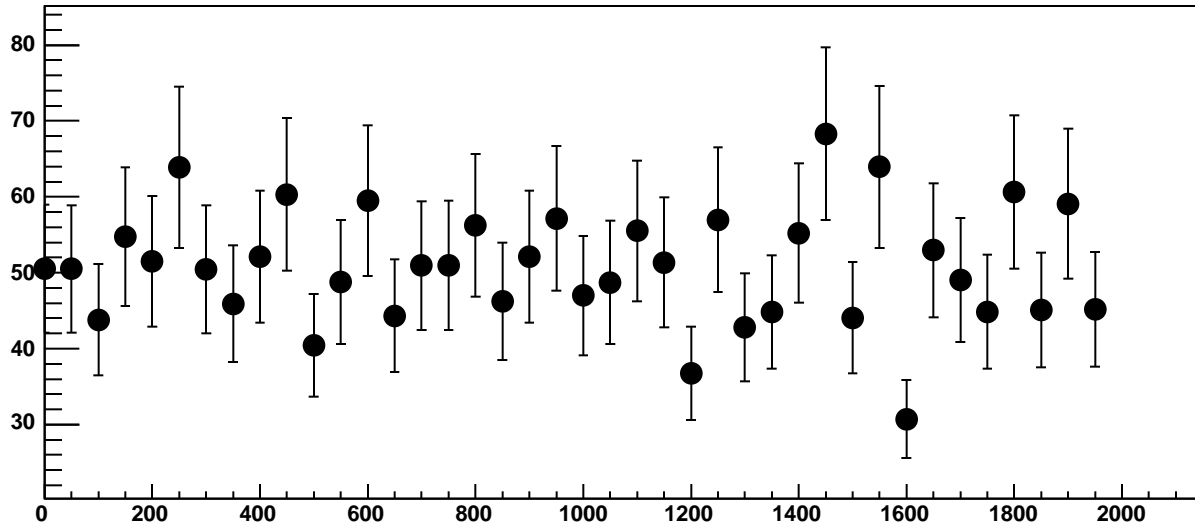
p0

$-888.7 \pm 19.21$

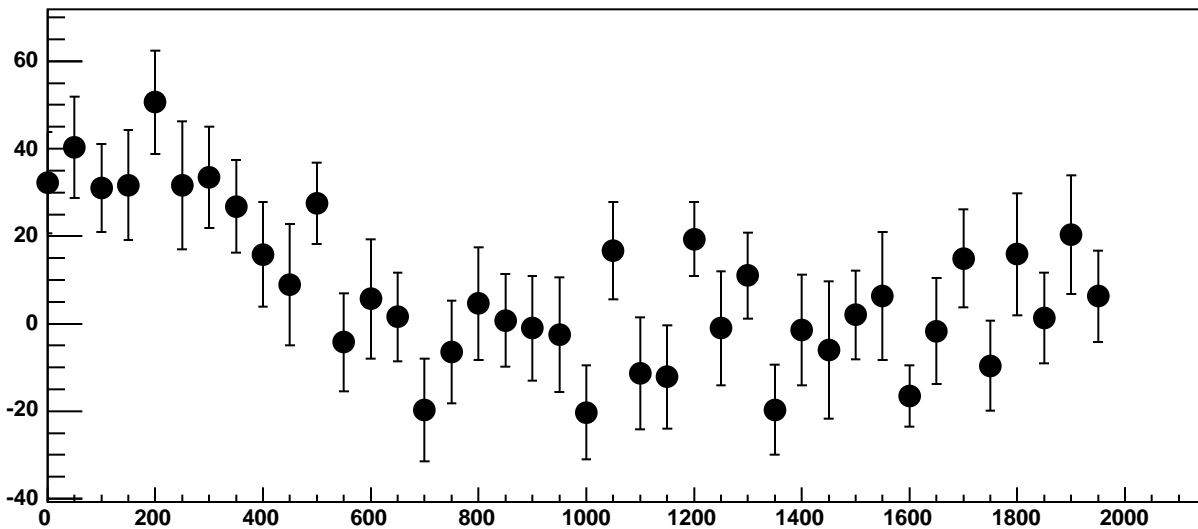
p1

$0.09001 \pm 0.01703$

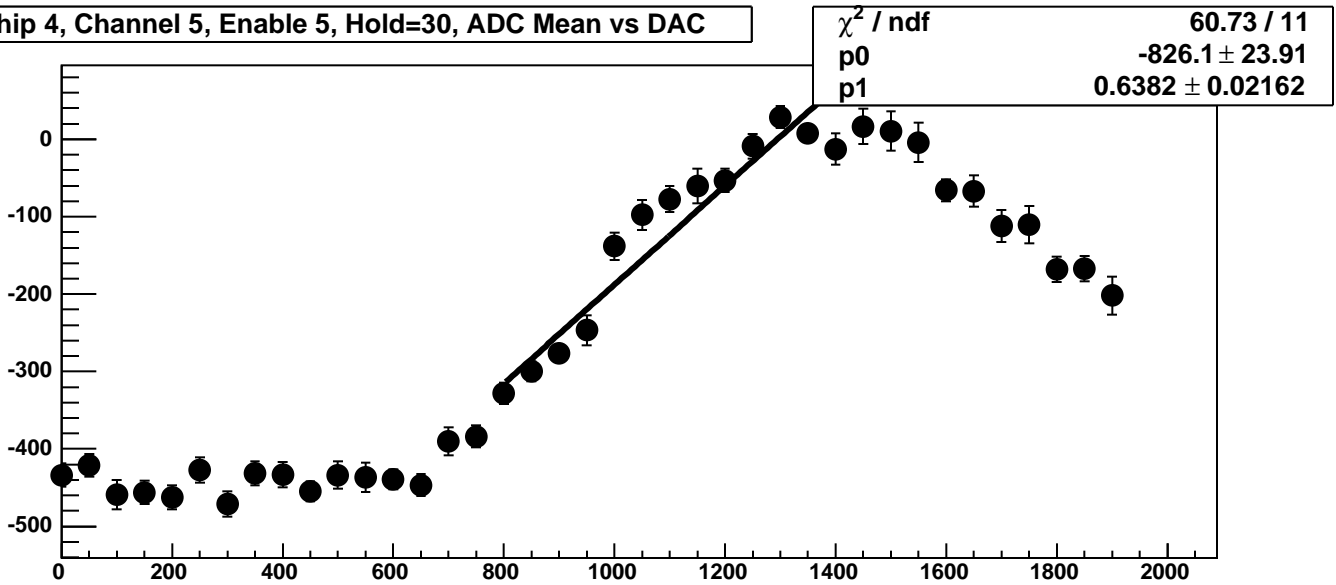
Chip 4, Channel 5, Enable 4, Hold=30, ADC Noise vs DAC



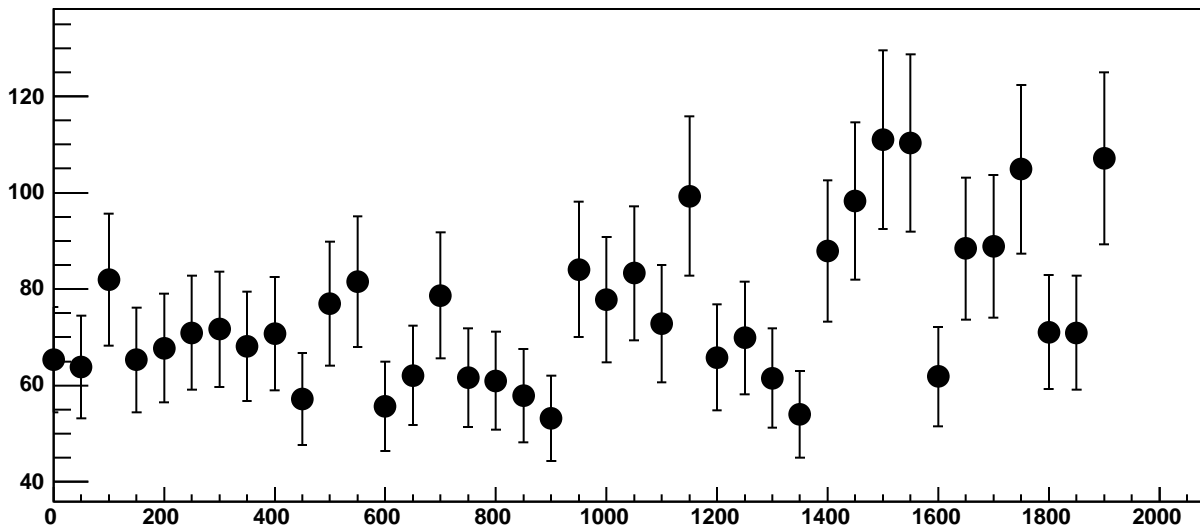
Chip 4, Channel 5, Enable 4, Hold=30, ADC Residuals vs DAC



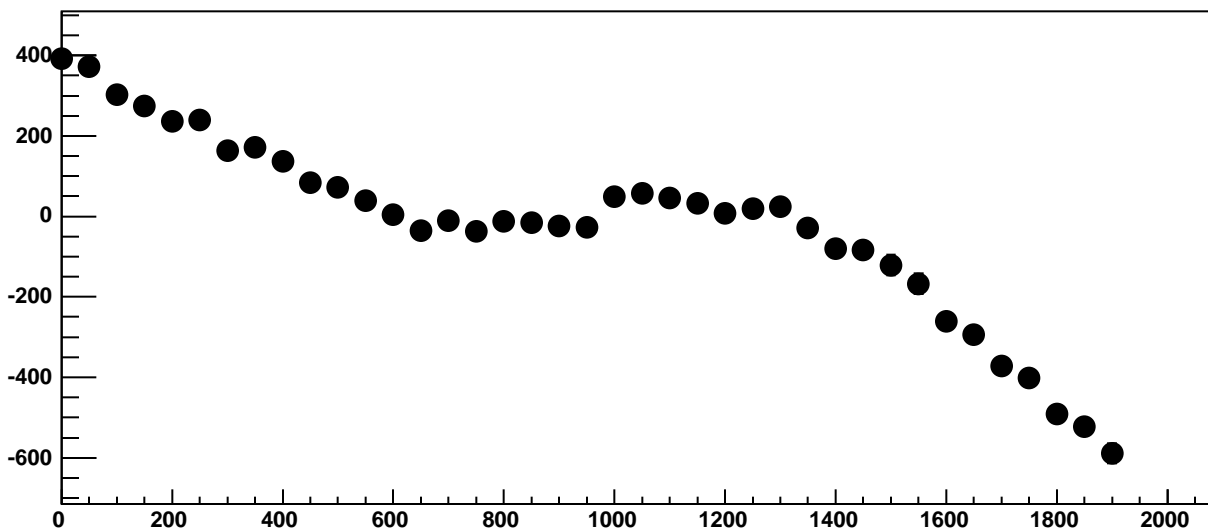
Chip 4, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC



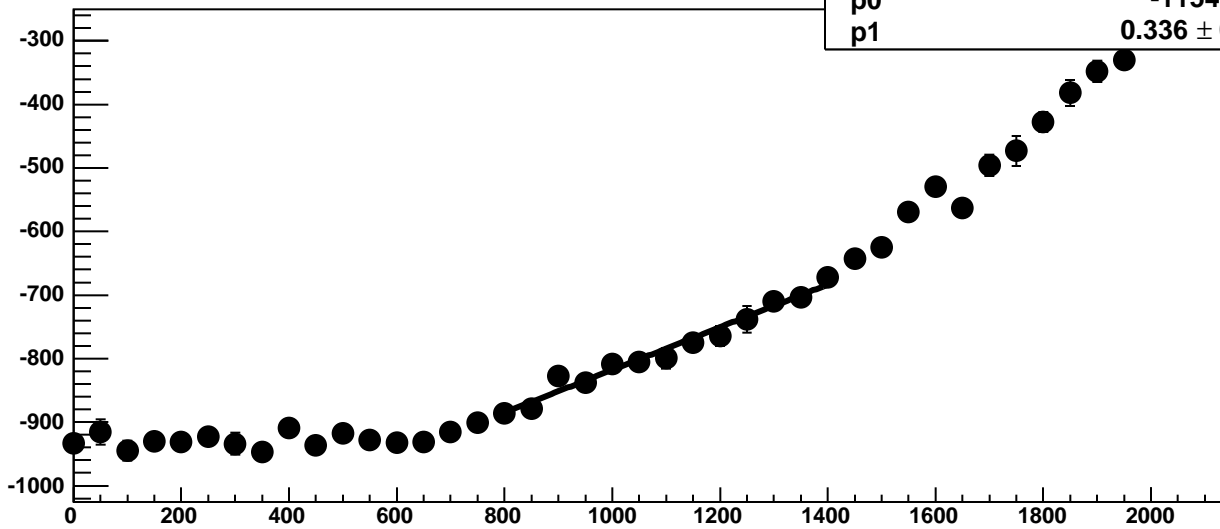
Chip 4, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC



Chip 4, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC

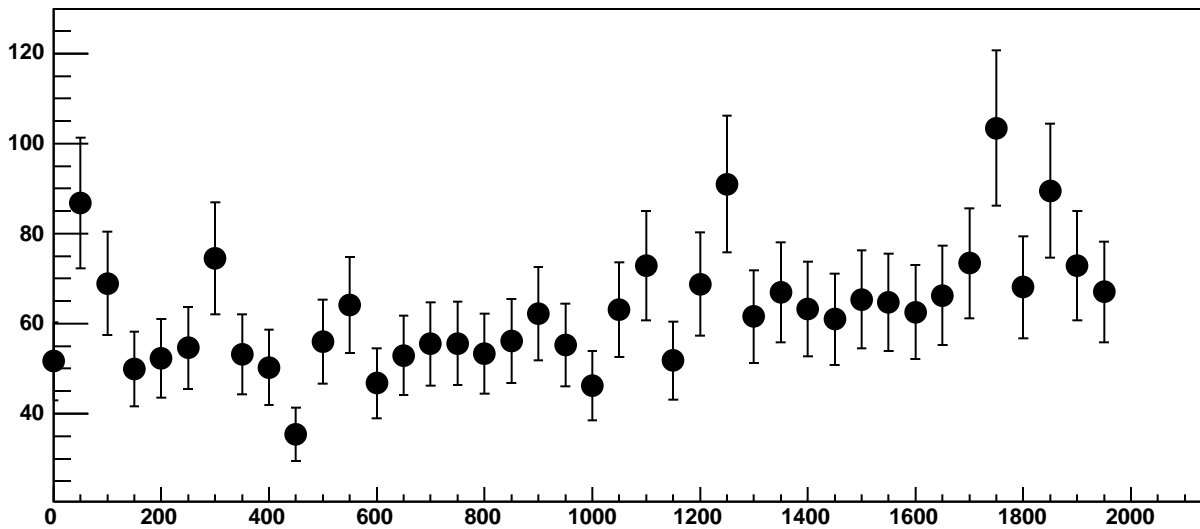


Chip 4, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC

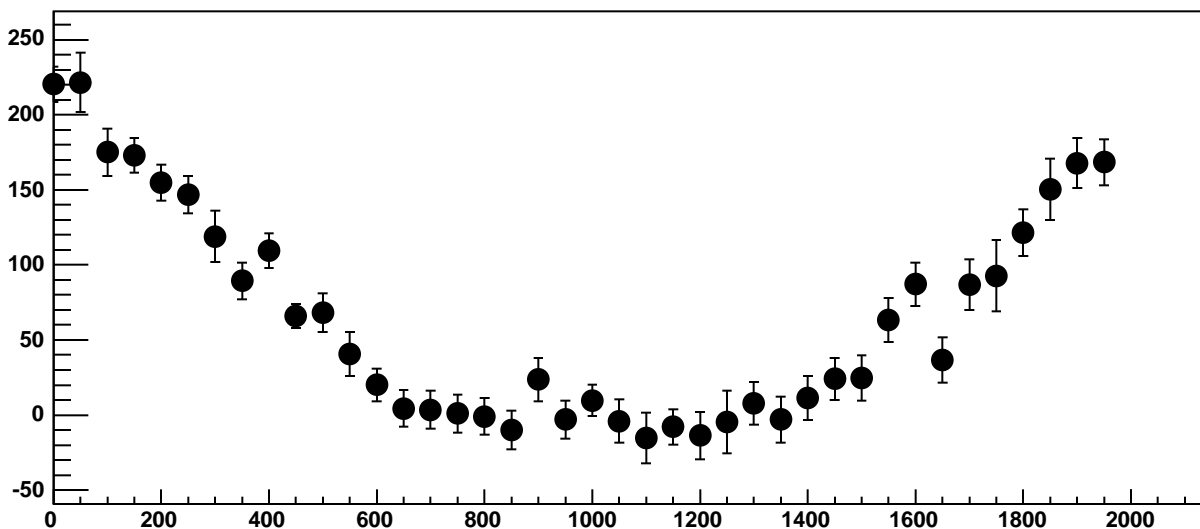


$\chi^2 / \text{ndf}$  7.347 / 11  
p0  $-1154 \pm 22.37$   
p1  $0.336 \pm 0.02057$

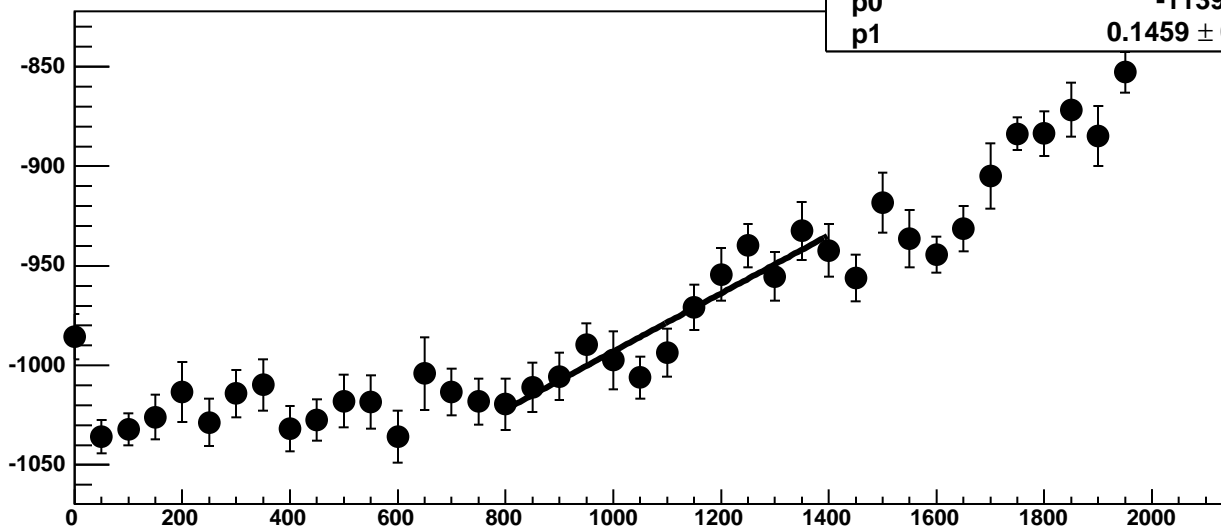
Chip 4, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC



Chip 4, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 4, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

10.5 / 11

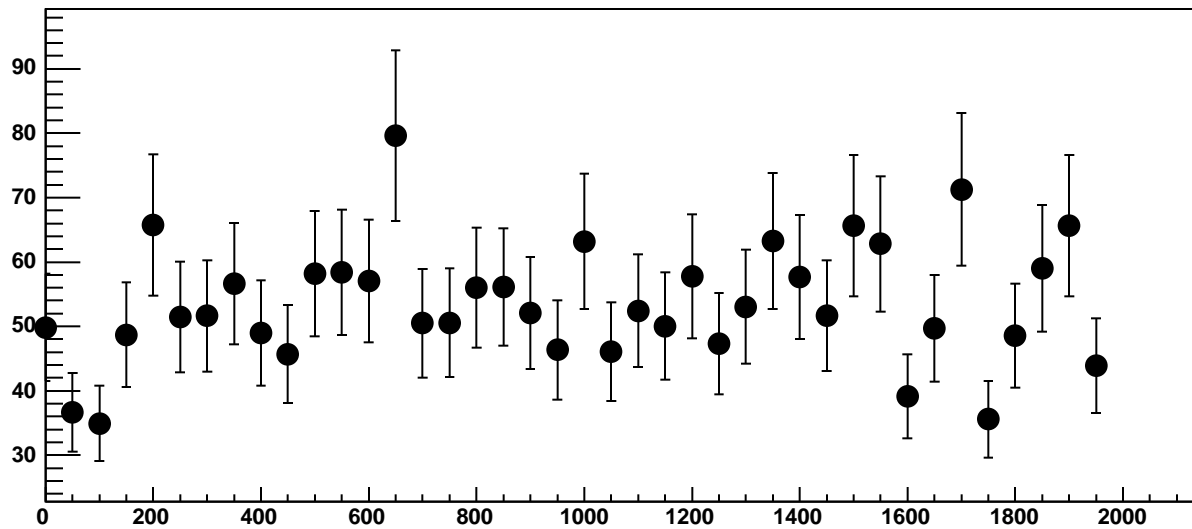
p0

$-1139 \pm 20.81$

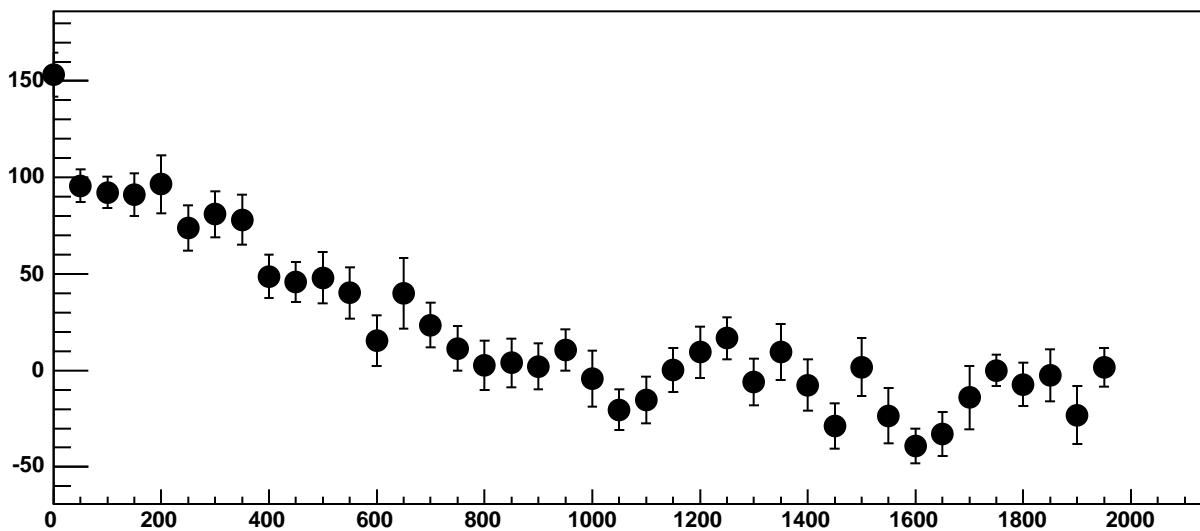
p1

$0.1459 \pm 0.01878$

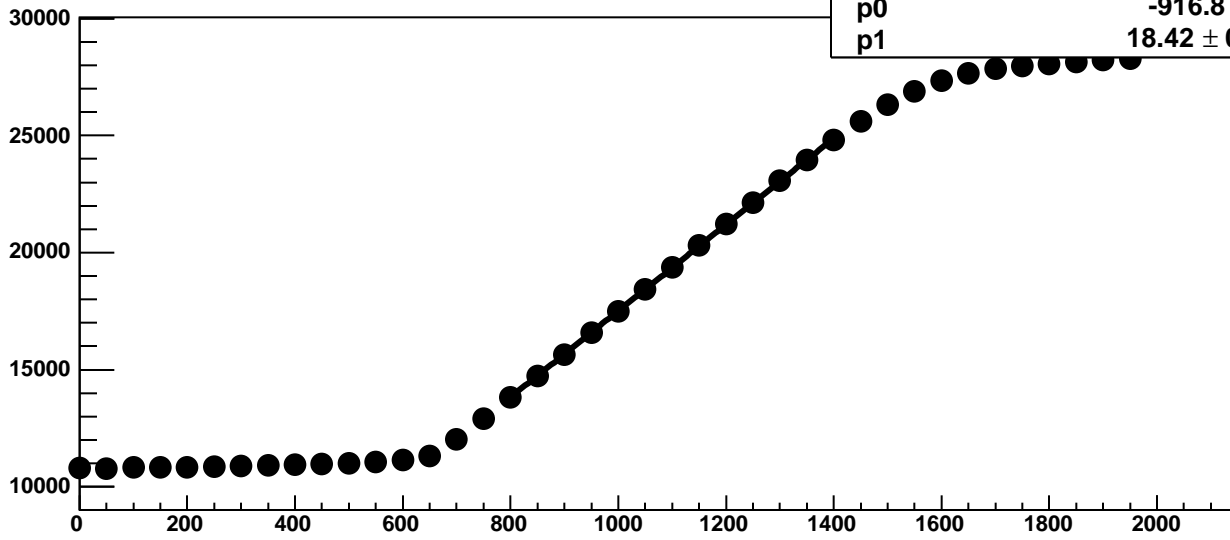
Chip 4, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC



Chip 4, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC

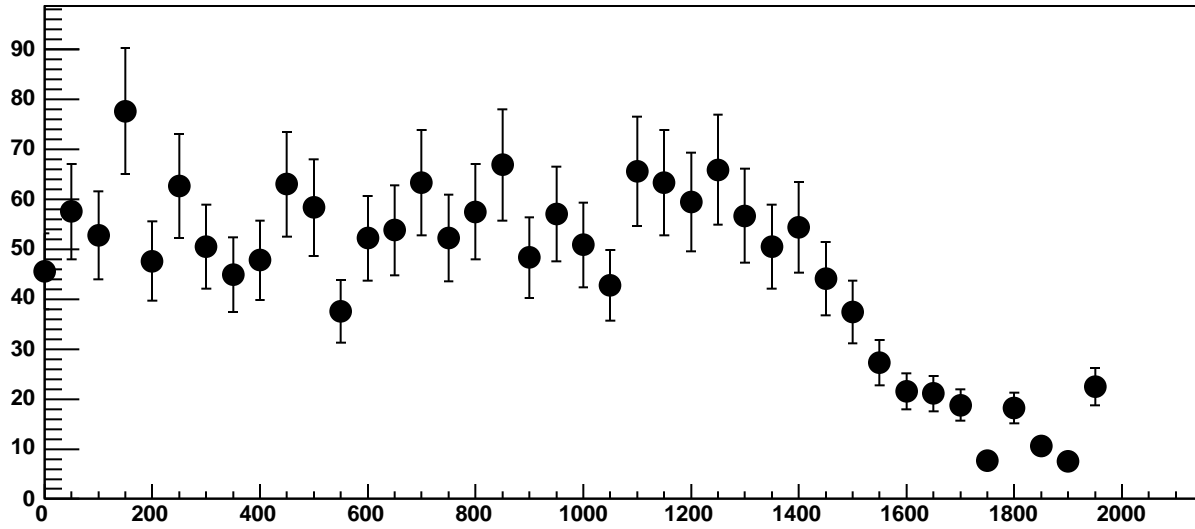


Chip 4, Channel 6, Enable 2!, Hold=30, ADC Mean vs DAC

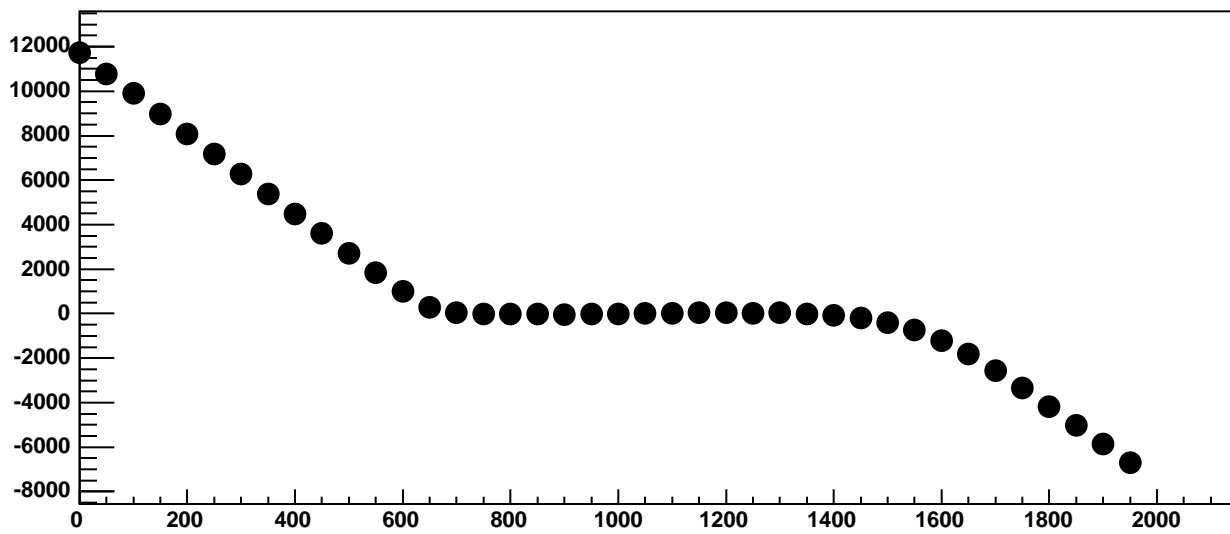


$\chi^2 / \text{ndf}$	71.52 / 11
p0	-916.8 ± 21.13
p1	18.42 ± 0.01897

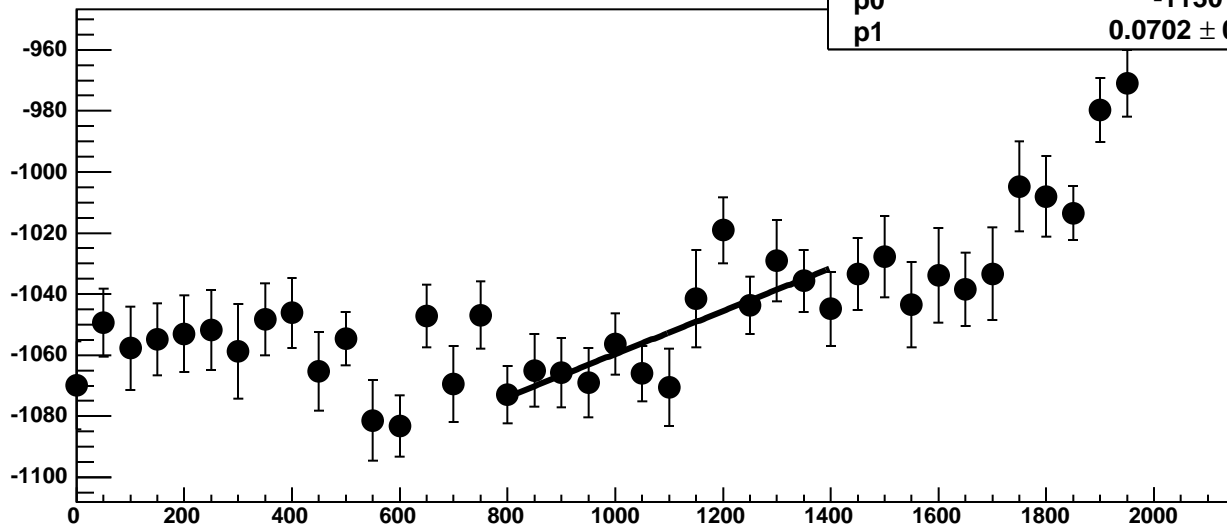
Chip 4, Channel 6, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 4, Channel 6, Enable 2!, Hold=30, ADC Residuals vs DAC

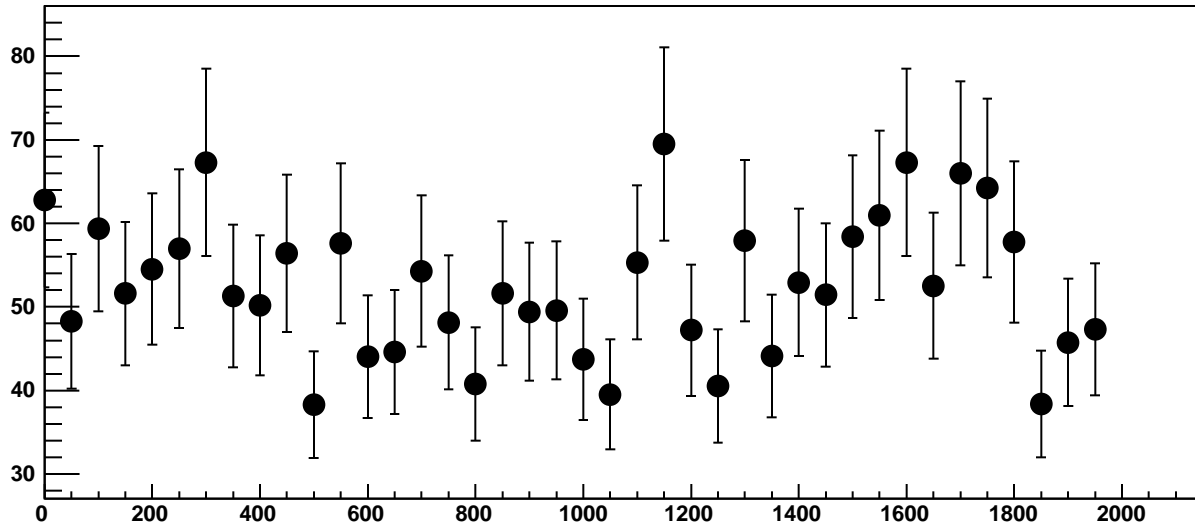


Chip 4, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC

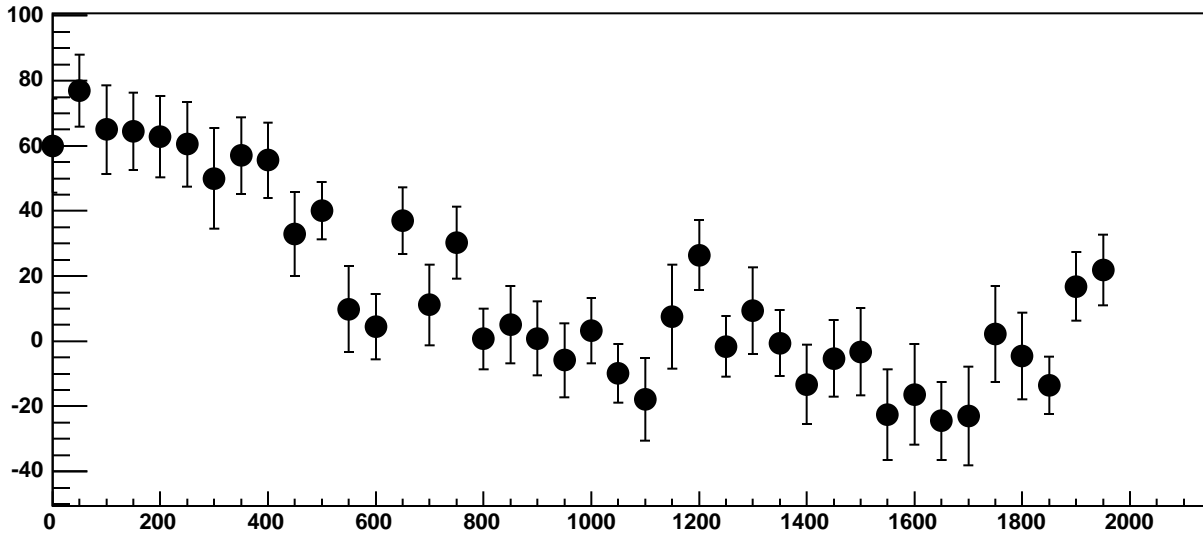


$\chi^2 / \text{ndf}$  11.69 / 11  
p0  $-1130 \pm 17.76$   
p1  $0.0702 \pm 0.01606$

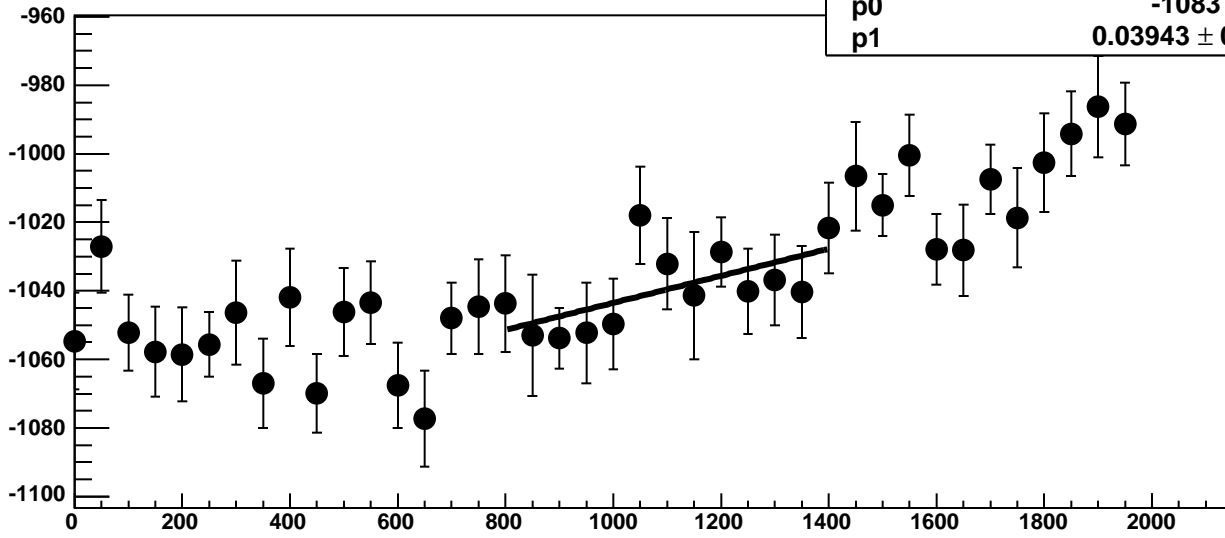
Chip 4, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC



Chip 4, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC

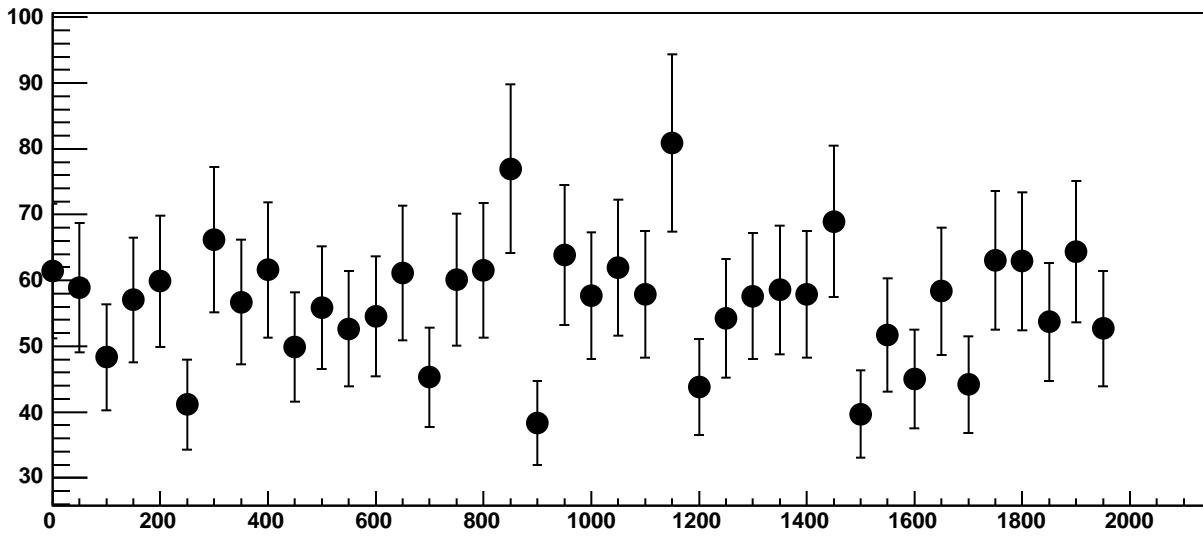


Chip 4, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC

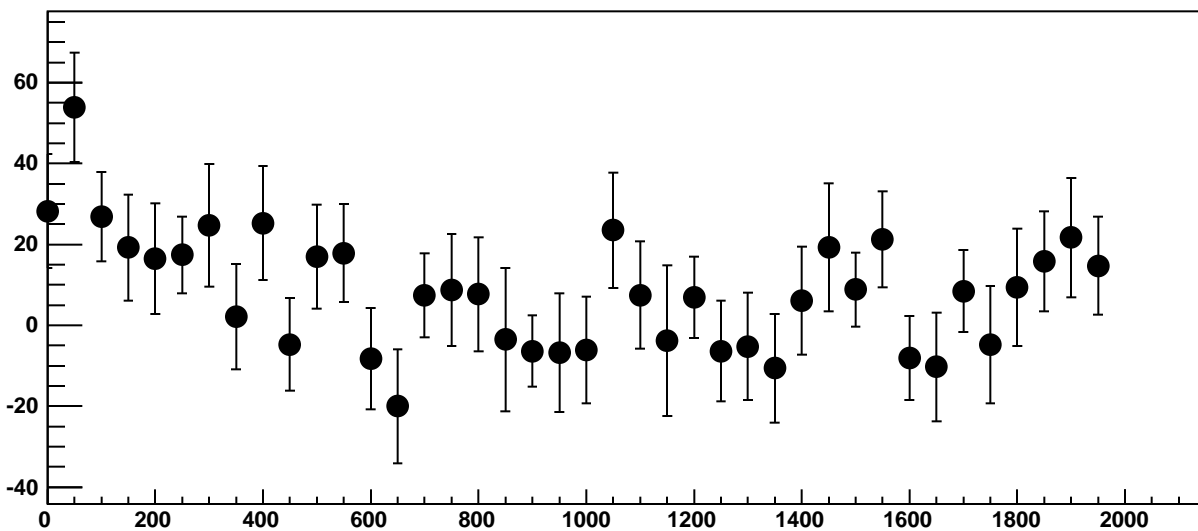


$\chi^2 / \text{ndf}$  6.106 / 11  
p0  $-1083 \pm 21.53$   
p1  $0.03943 \pm 0.01932$

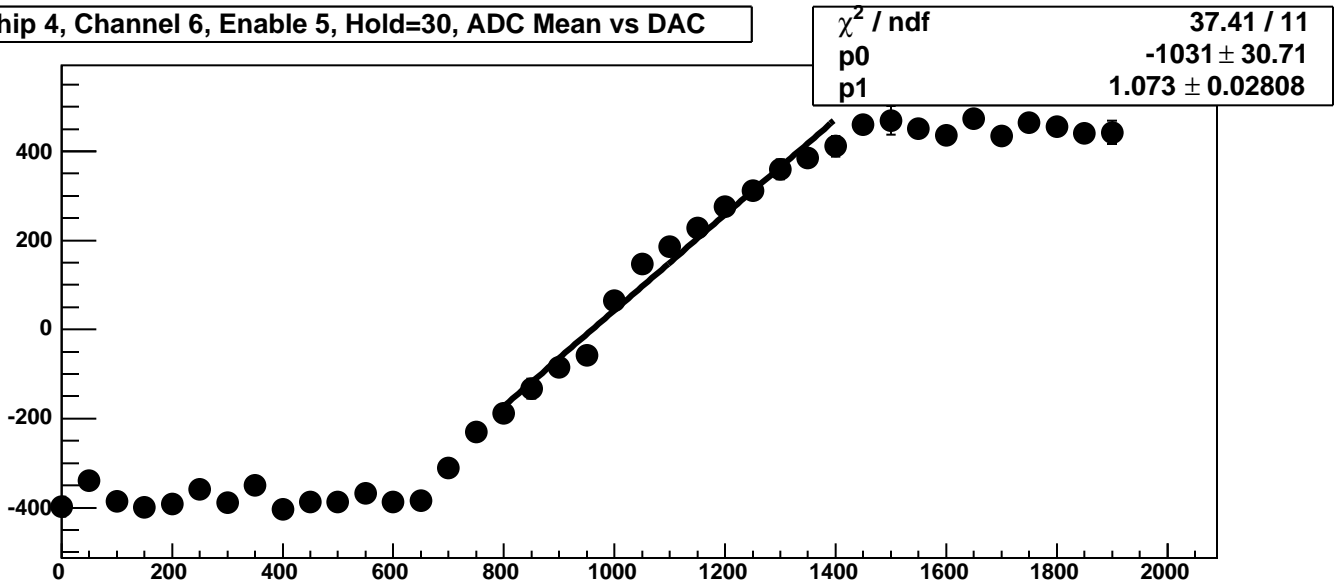
Chip 4, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC



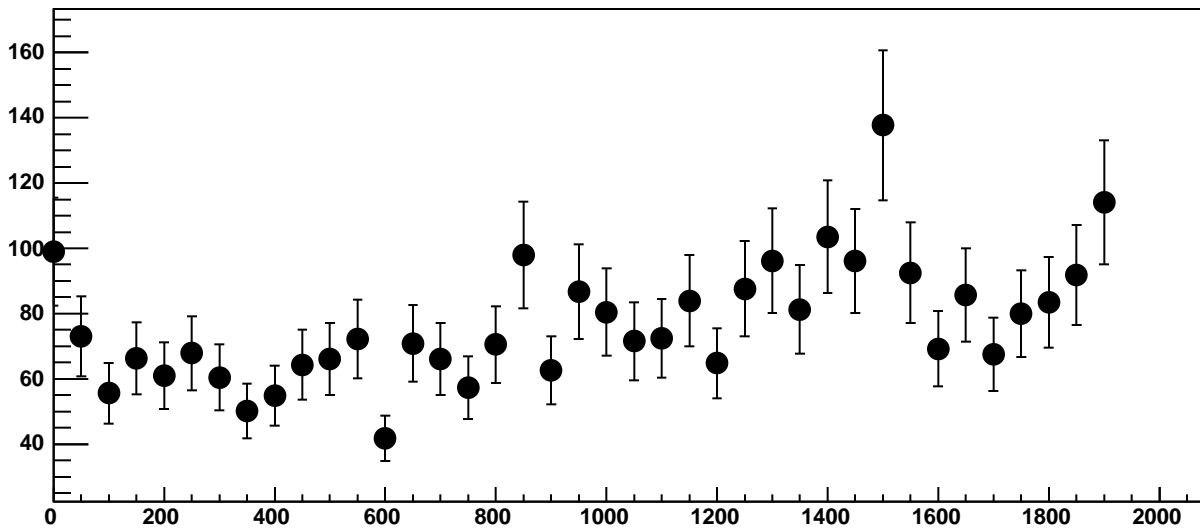
Chip 4, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC



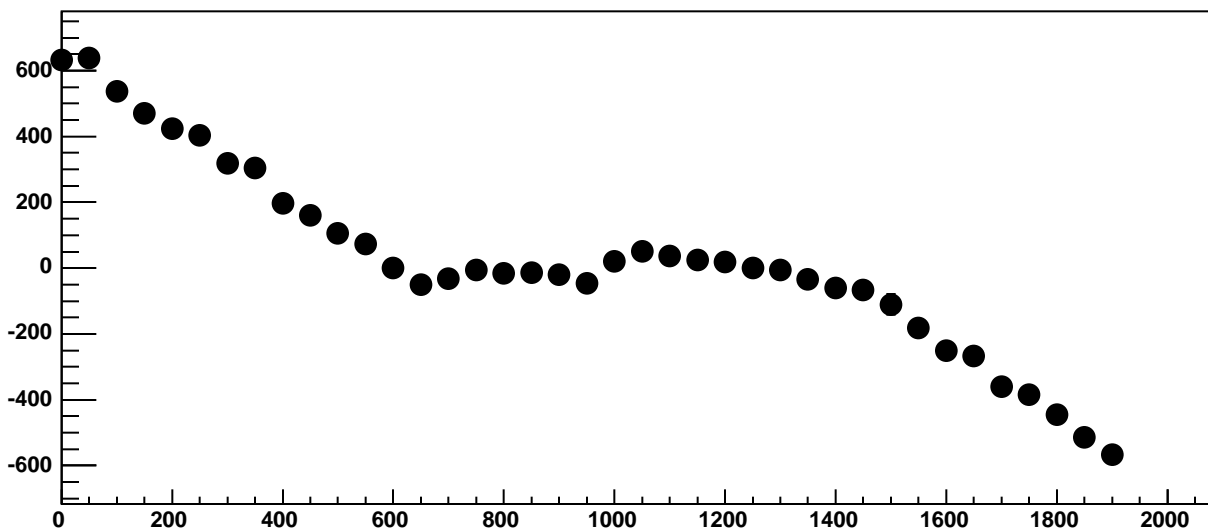
Chip 4, Channel 6, Enable 5, Hold=30, ADC Mean vs DAC



Chip 4, Channel 6, Enable 5, Hold=30, ADC Noise vs DAC

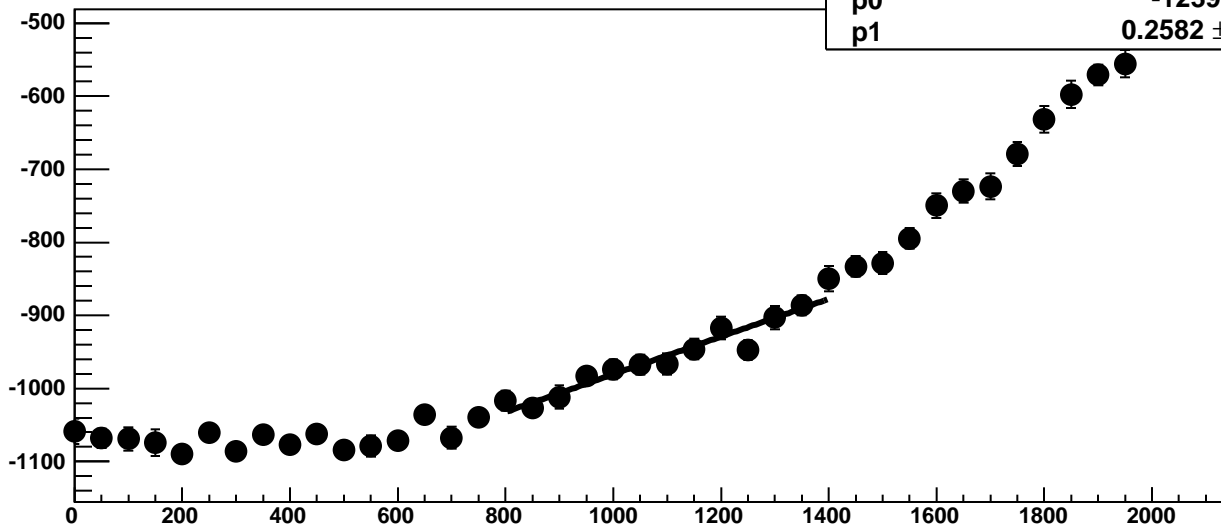


Chip 4, Channel 6, Enable 5, Hold=30, ADC Residuals vs DAC



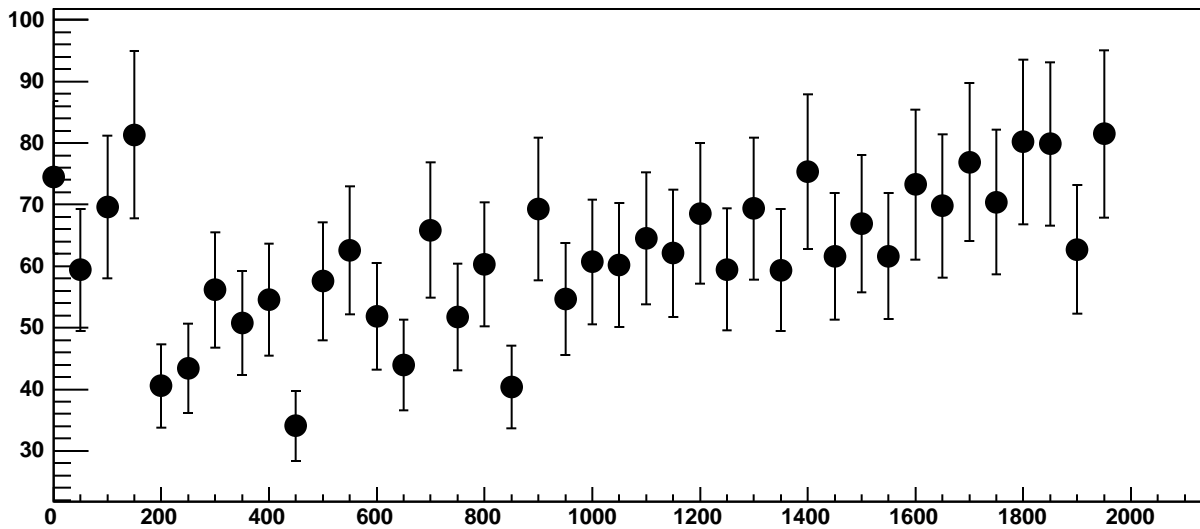


Chip 4, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC

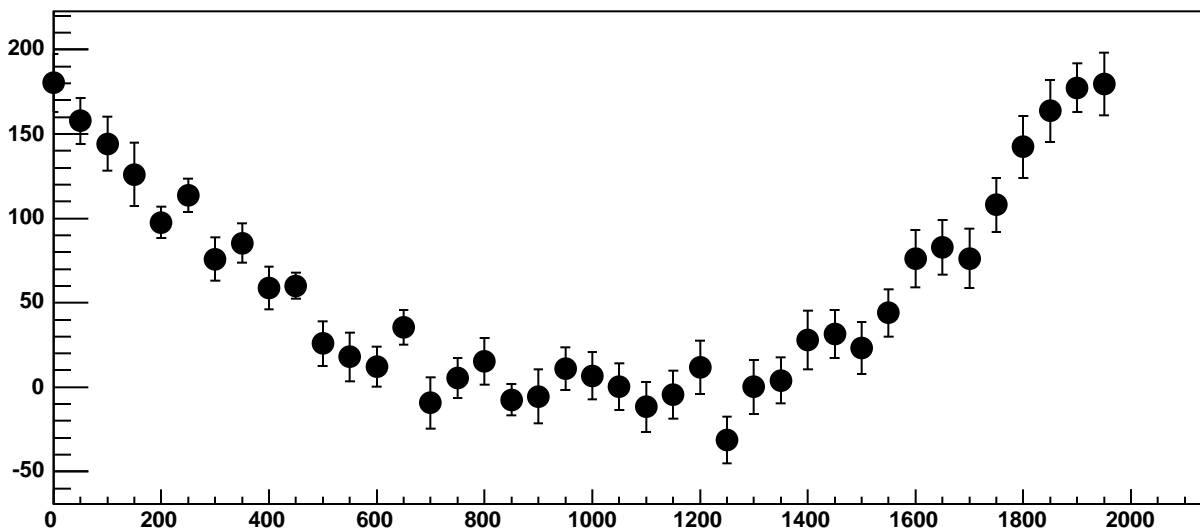


$\chi^2 / \text{ndf}$  12.21 / 11  
p0  $-1239 \pm 21.85$   
p1  $0.2582 \pm 0.0202$

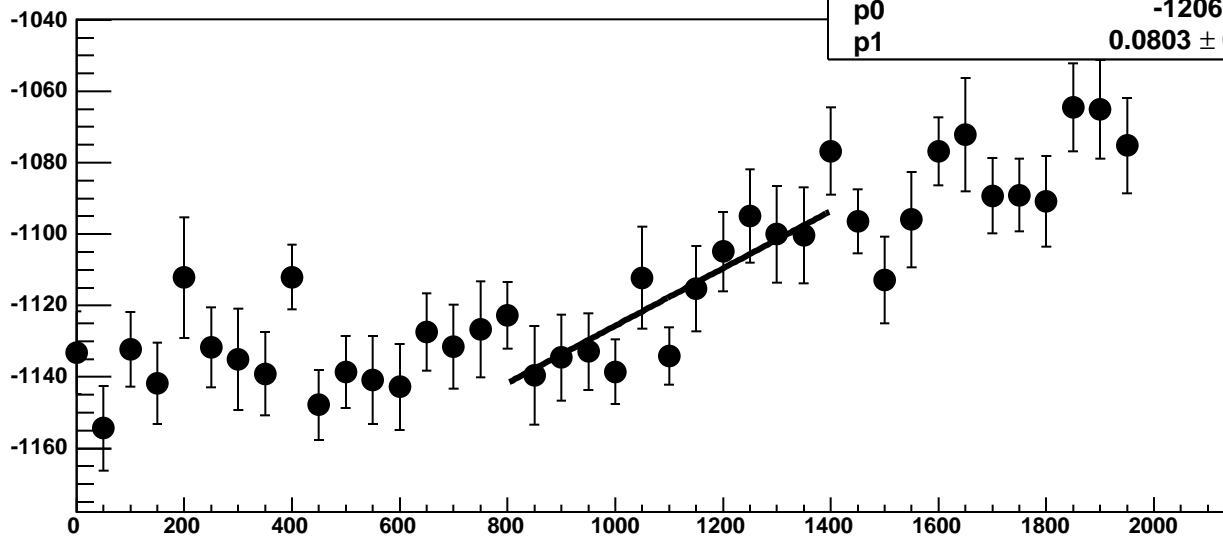
Chip 4, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC



Chip 4, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC

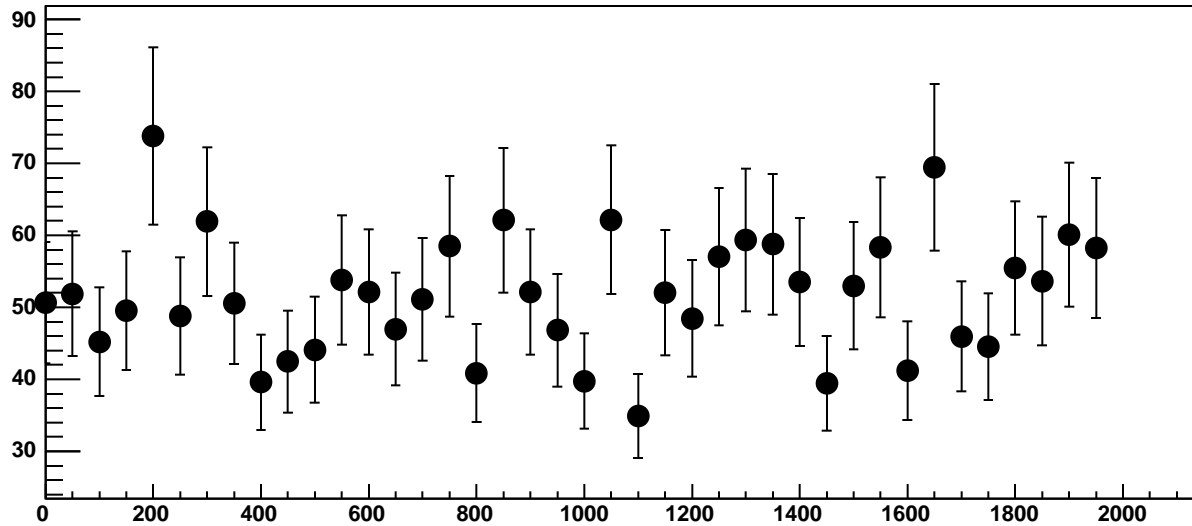


Chip 4, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC

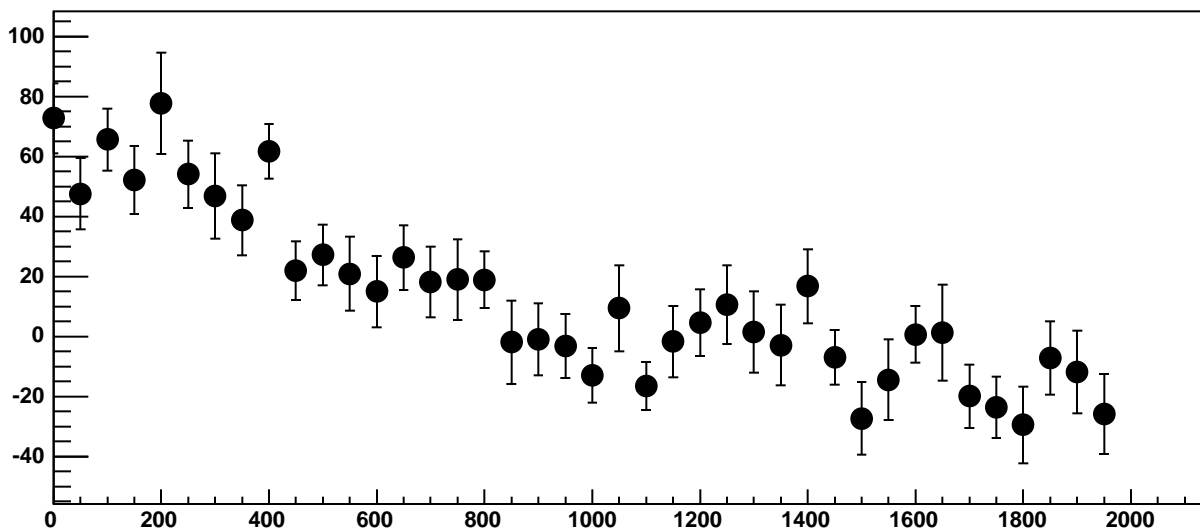


$\chi^2 / \text{ndf}$  13.61 / 11  
p0  $-1206 \pm 19.04$   
p1  $0.0803 \pm 0.01744$

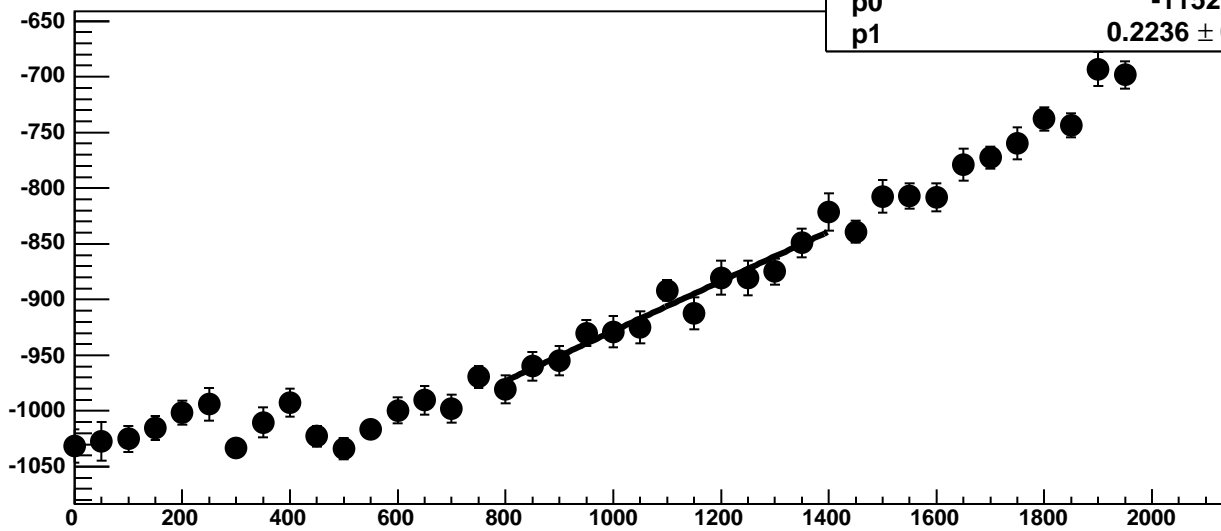
Chip 4, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC



Chip 4, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC

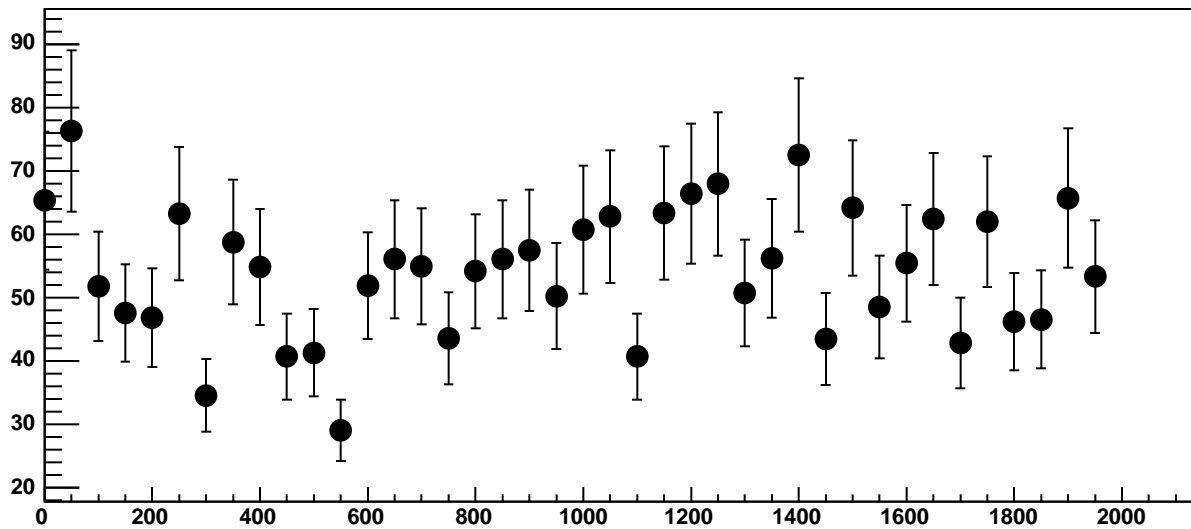


Chip 4, Channel 7, Enable 2, Hold=30, ADC Mean vs DAC

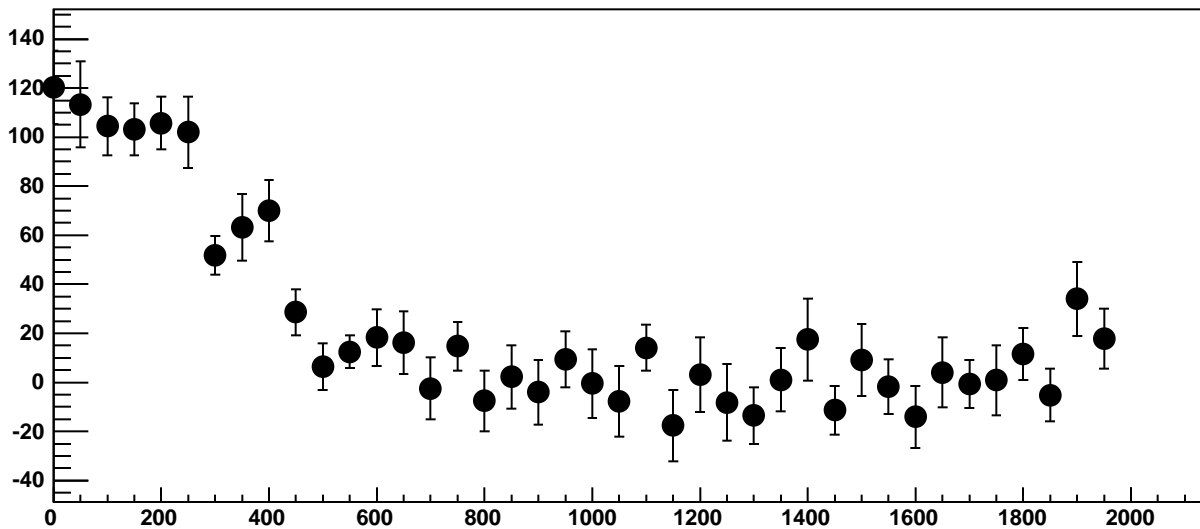


$\chi^2 / \text{ndf}$  7.957 / 11  
p0  $-1152 \pm 21.78$   
p1  $0.2236 \pm 0.01979$

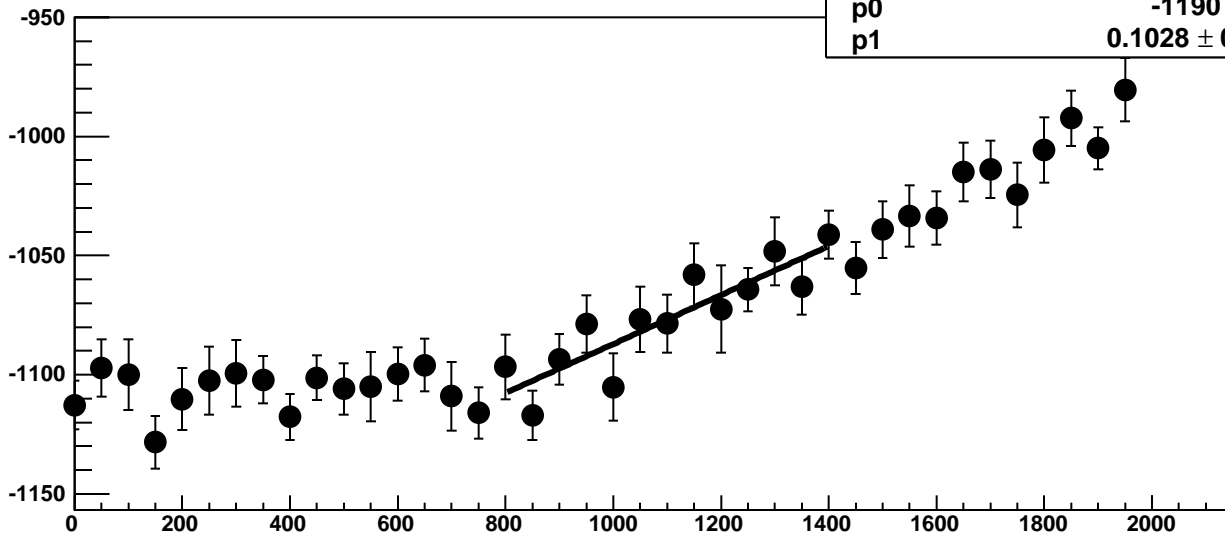
Chip 4, Channel 7, Enable 2, Hold=30, ADC Noise vs DAC



Chip 4, Channel 7, Enable 2, Hold=30, ADC Residuals vs DAC

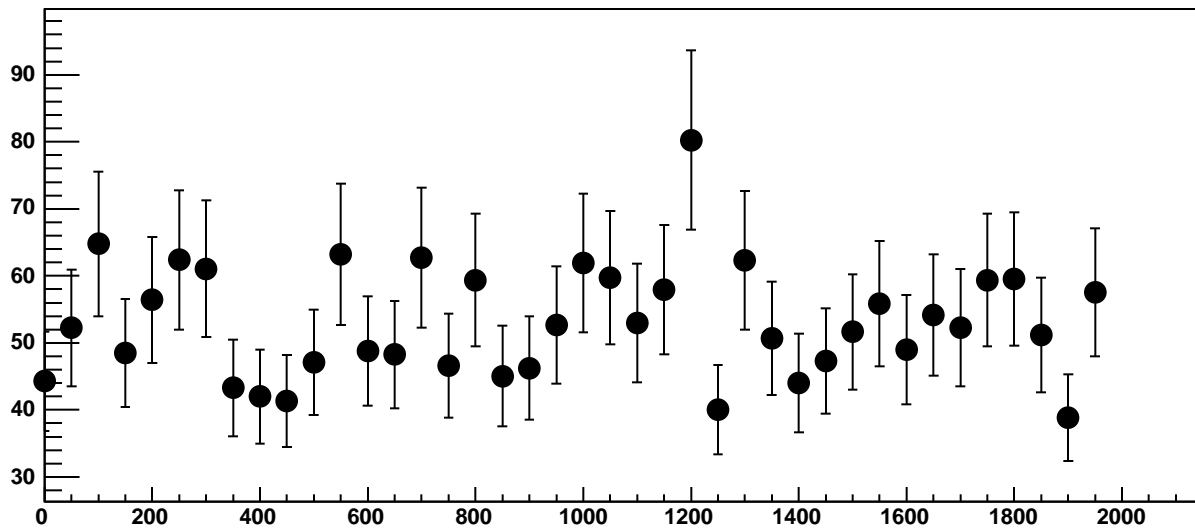


Chip 4, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC

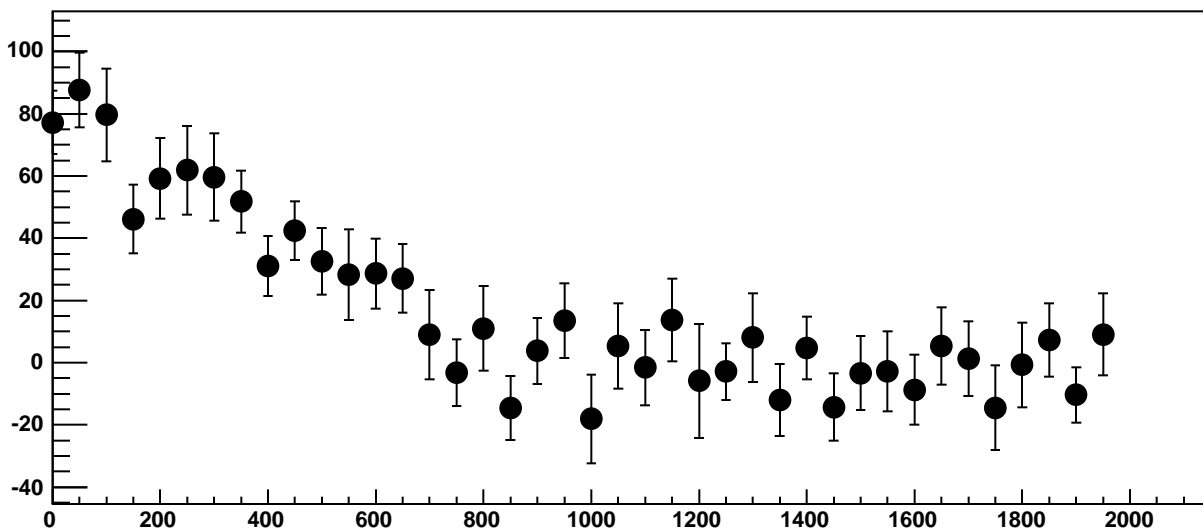


$\chi^2 / \text{ndf}$  8.646 / 11  
p0  $-1190 \pm 19.02$   
p1  $0.1028 \pm 0.01692$

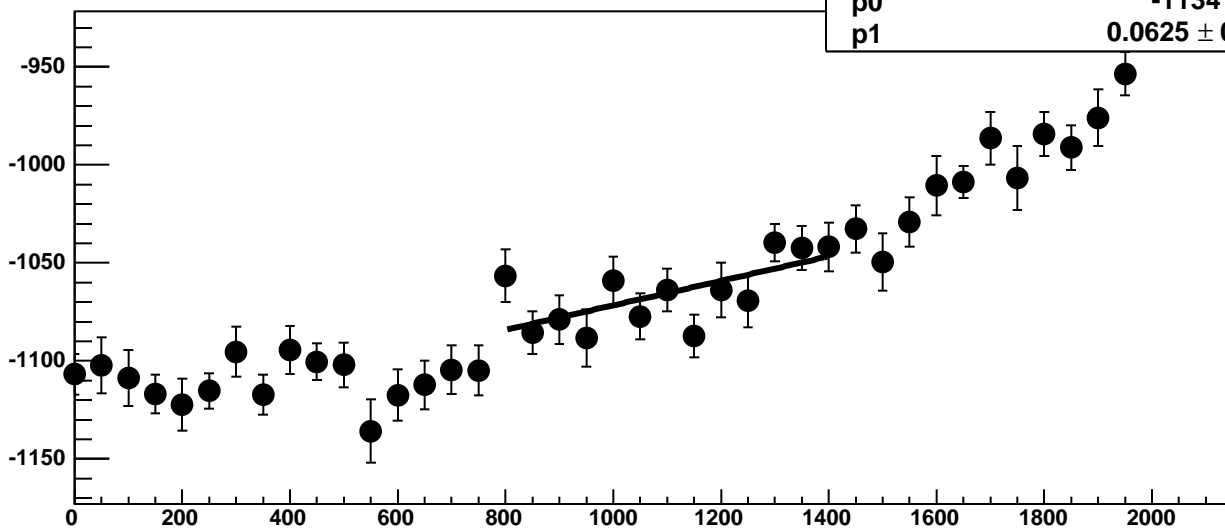
Chip 4, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC



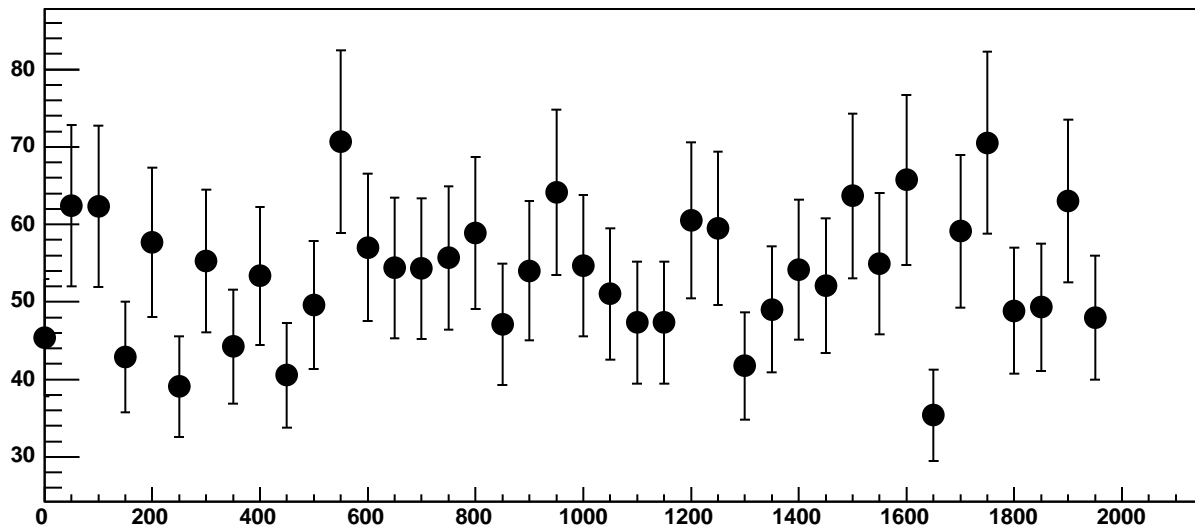
Chip 4, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC



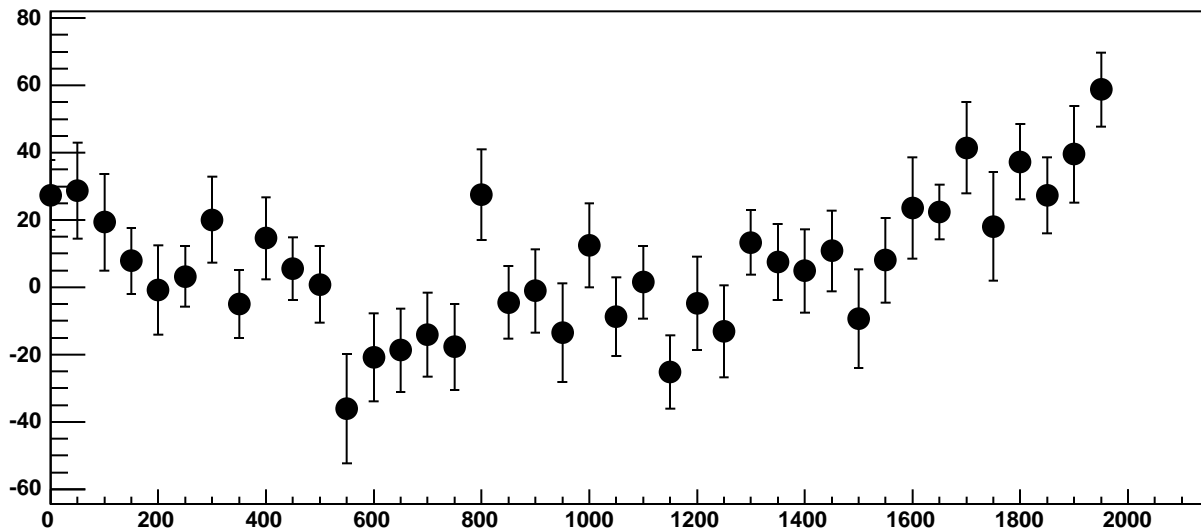
Chip 4, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC



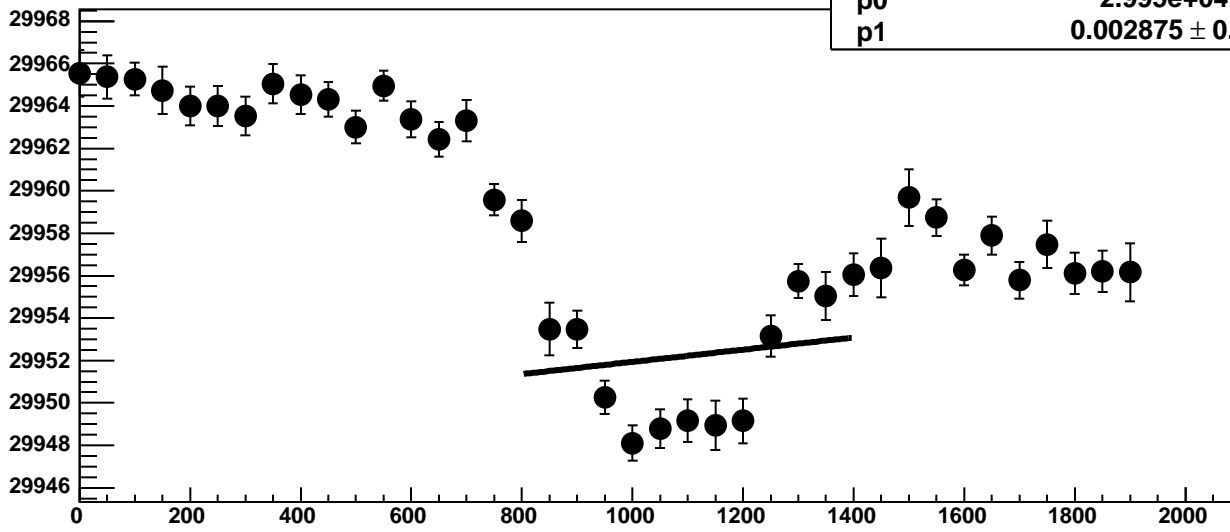
Chip 4, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC



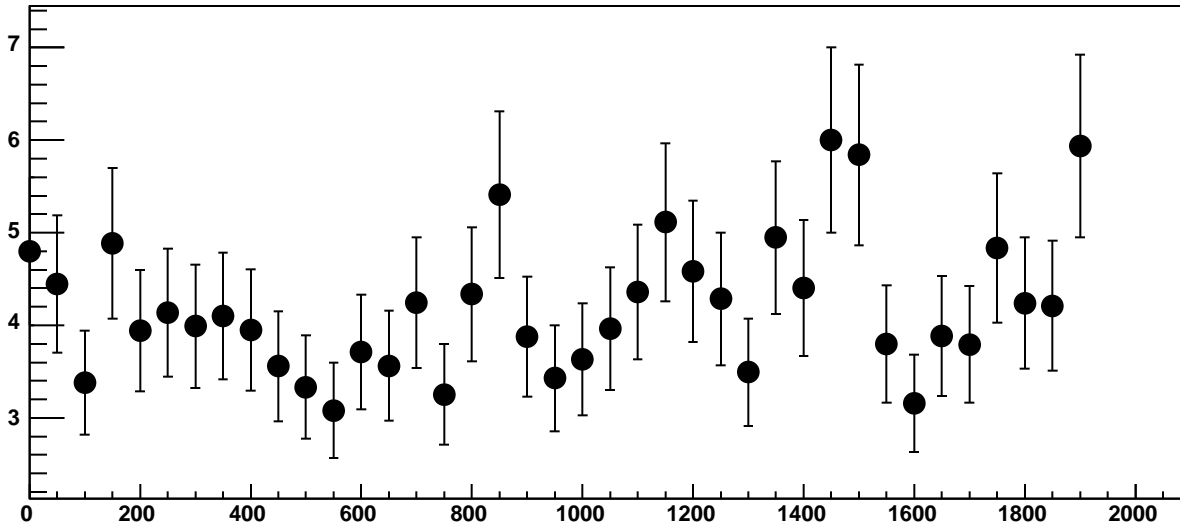
Chip 4, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC



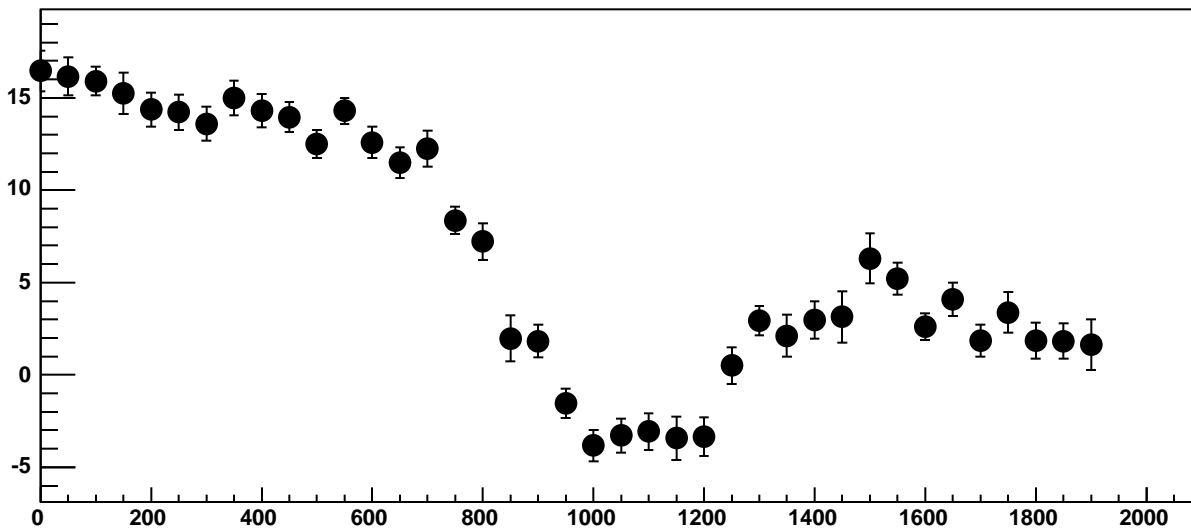
Chip 4, Channel 7, Enable 5!, Hold=30, ADC Mean vs DAC



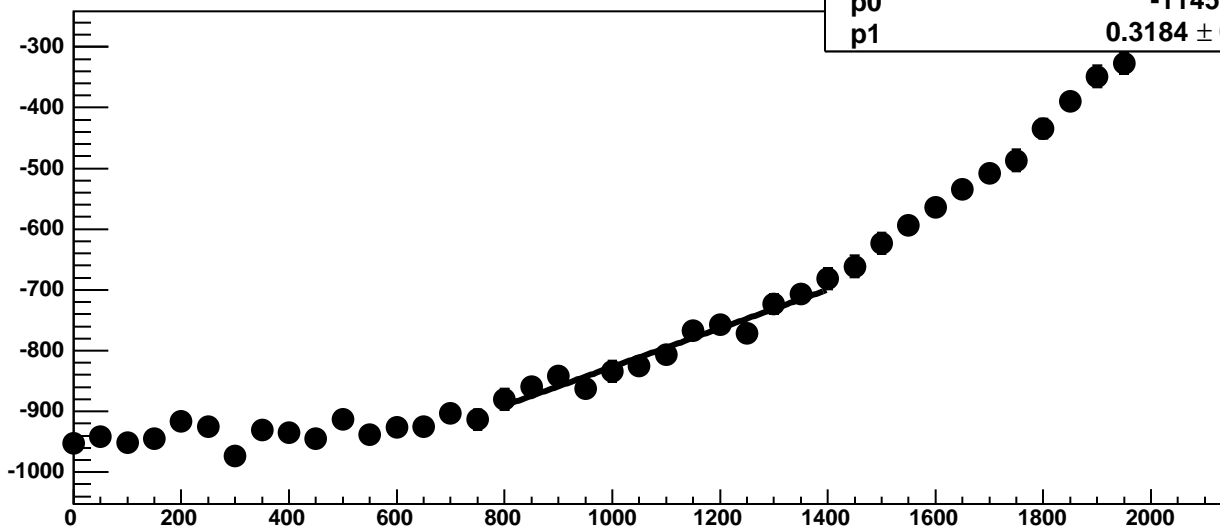
Chip 4, Channel 7, Enable 5!, Hold=30, ADC Noise vs DAC



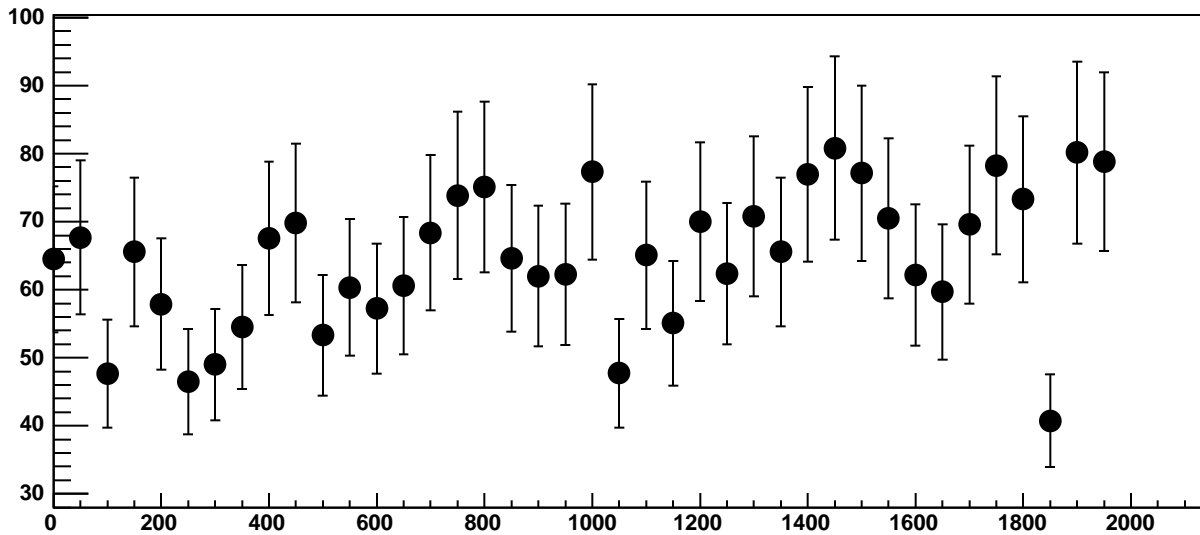
Chip 4, Channel 7, Enable 5!, Hold=30, ADC Residuals vs DAC



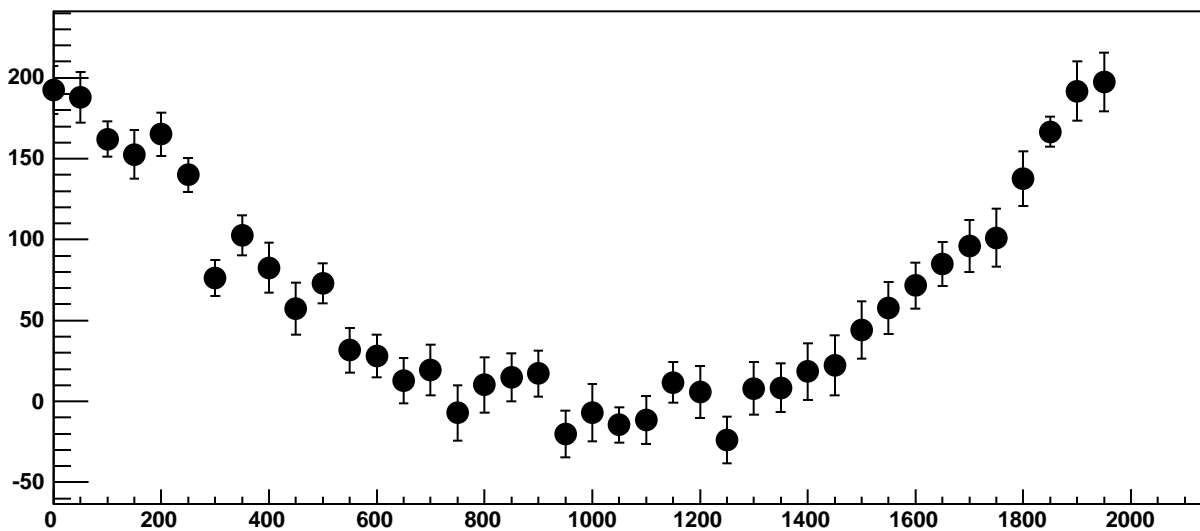
Chip 4, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC



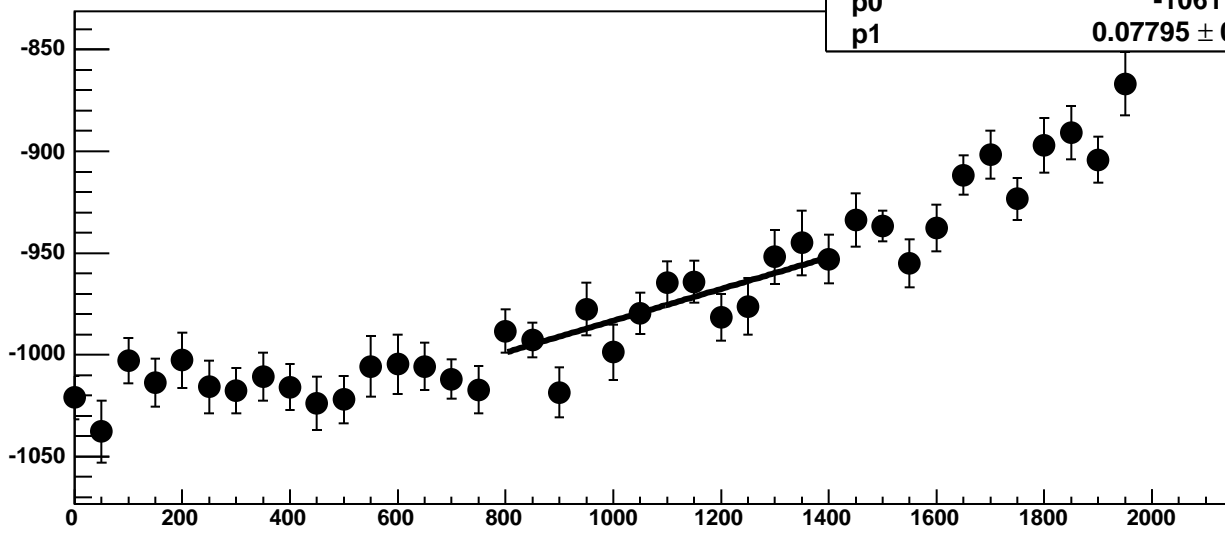
Chip 4, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC



Chip 4, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC

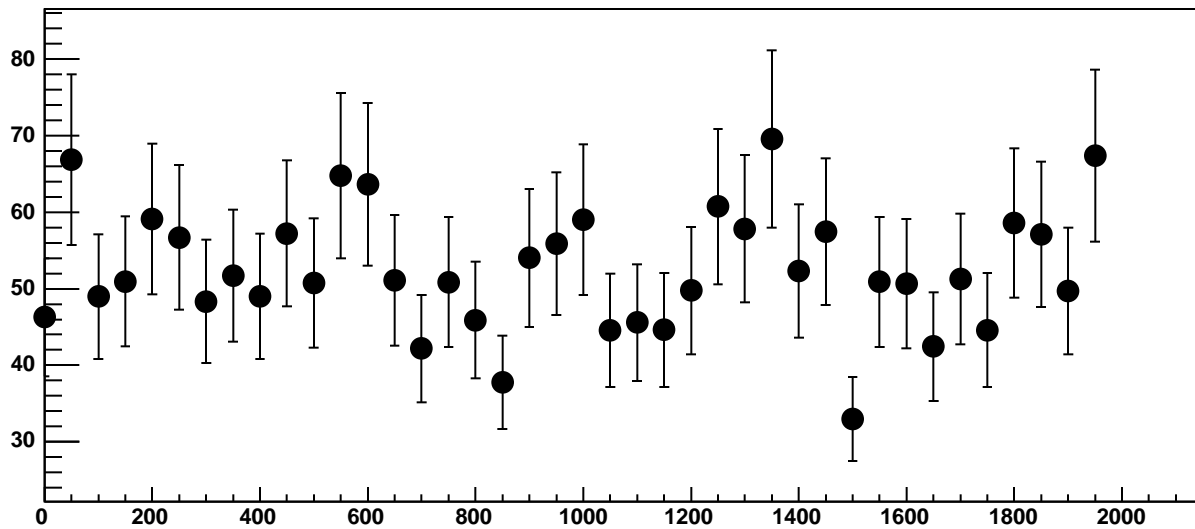


Chip 4, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC

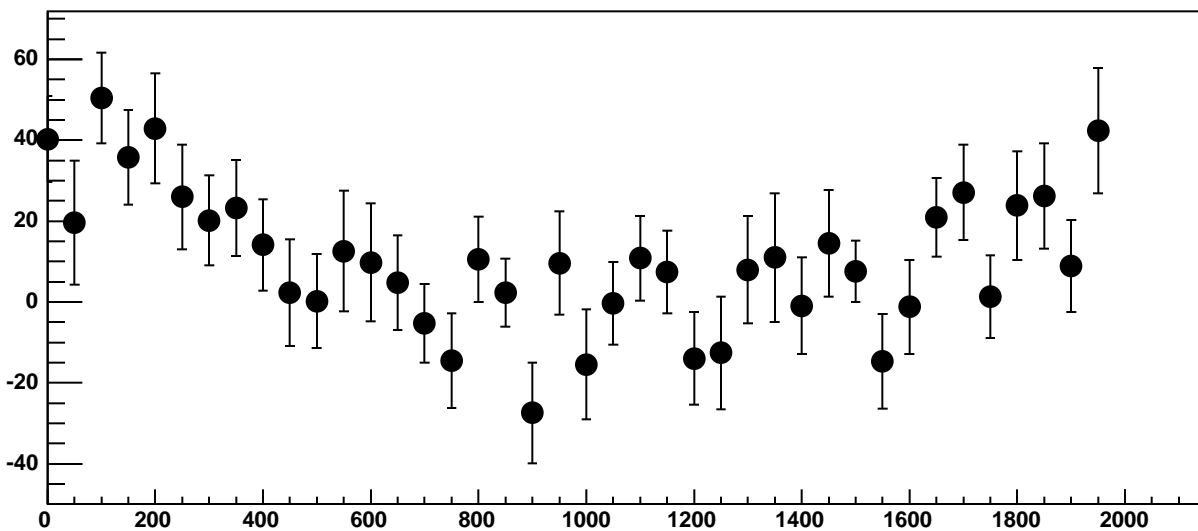


$\chi^2 / \text{ndf}$  12.58 / 11  
p0  $-1061 \pm 18.66$   
p1  $0.07795 \pm 0.01722$

Chip 4, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC

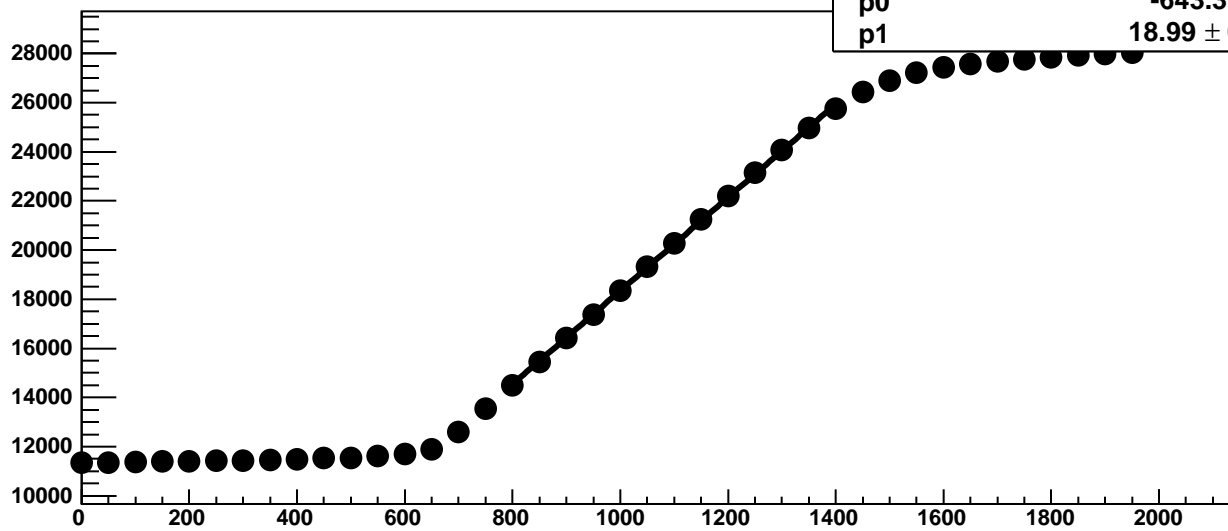


Chip 4, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC

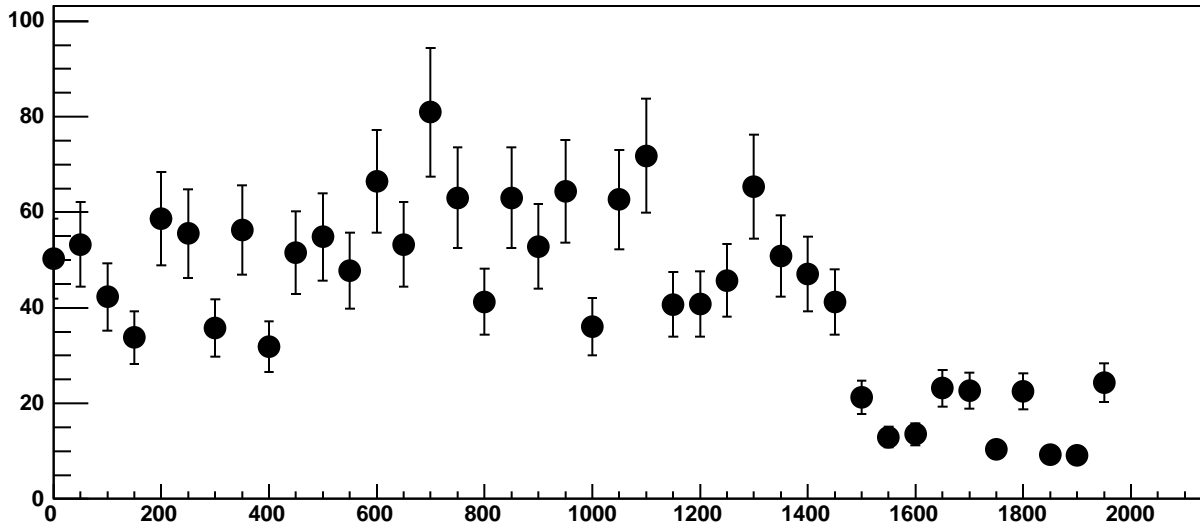




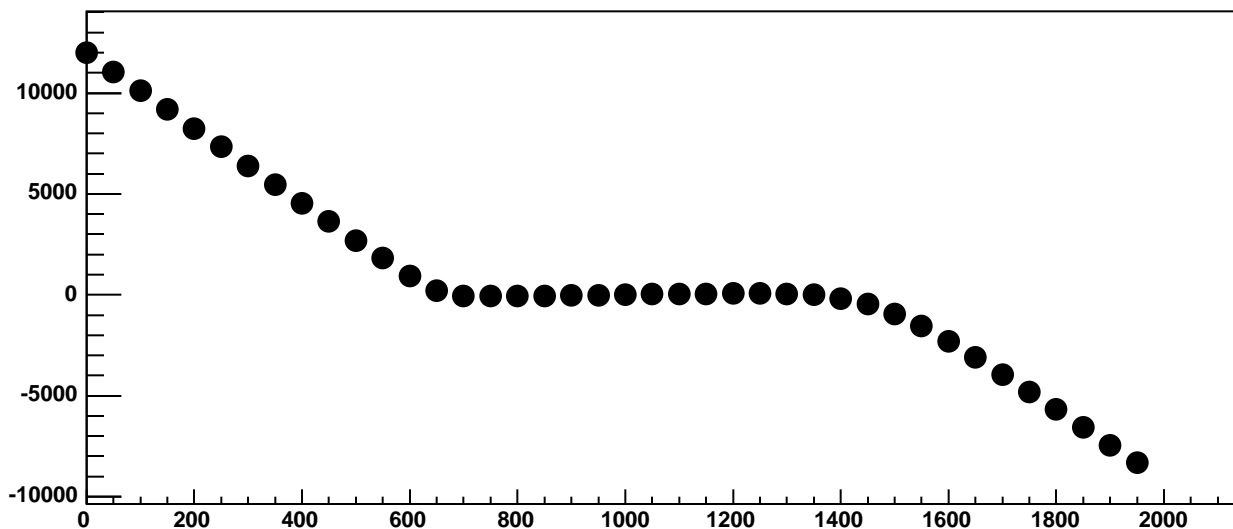
Chip 4, Channel 8, Enable 2!, Hold=30, ADC Mean vs DAC



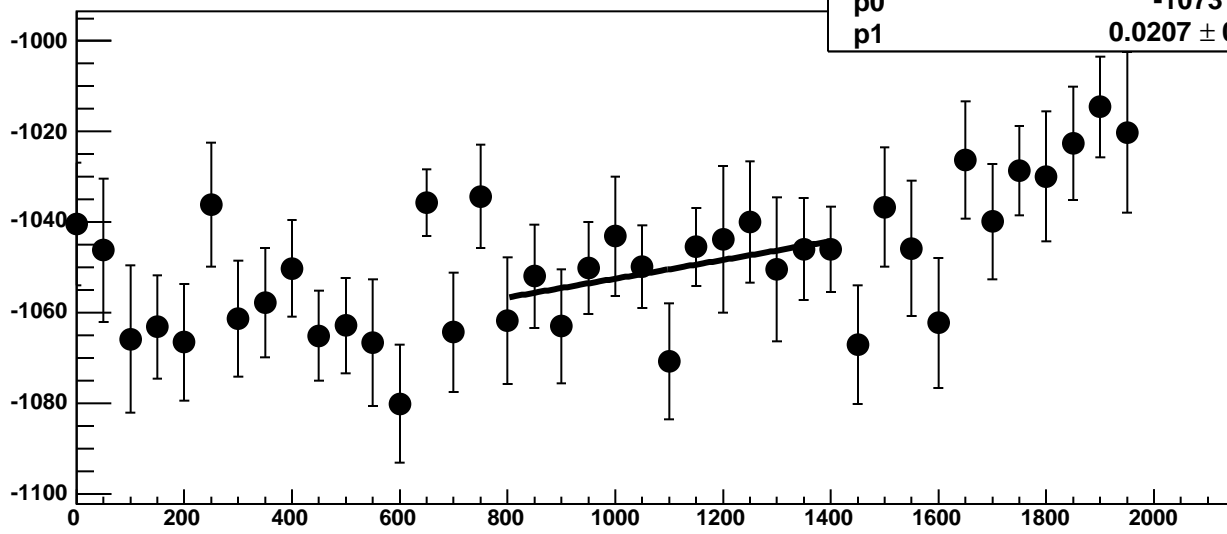
Chip 4, Channel 8, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 4, Channel 8, Enable 2!, Hold=30, ADC Residuals vs DAC

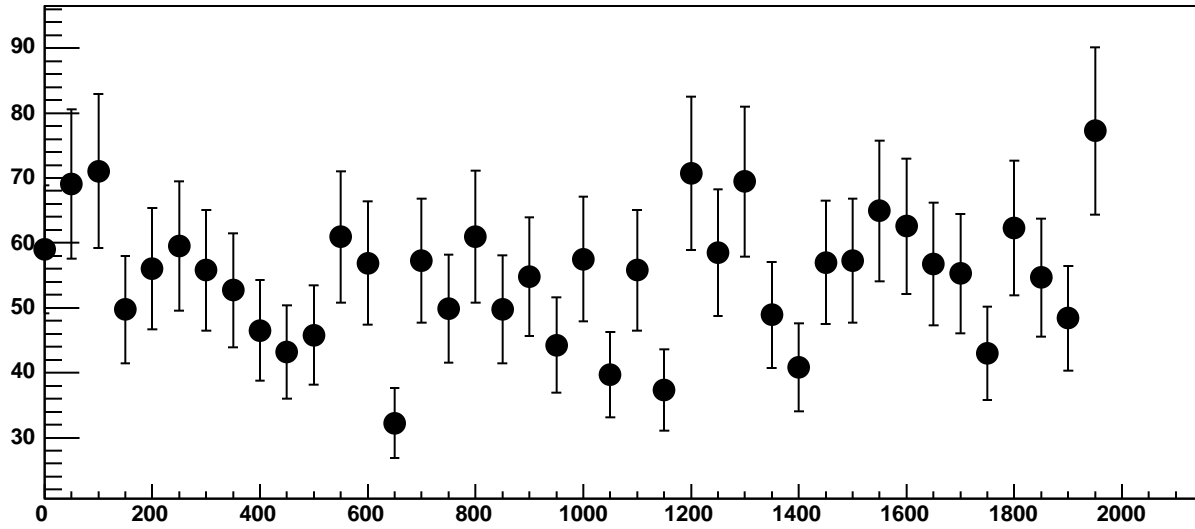


Chip 4, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC

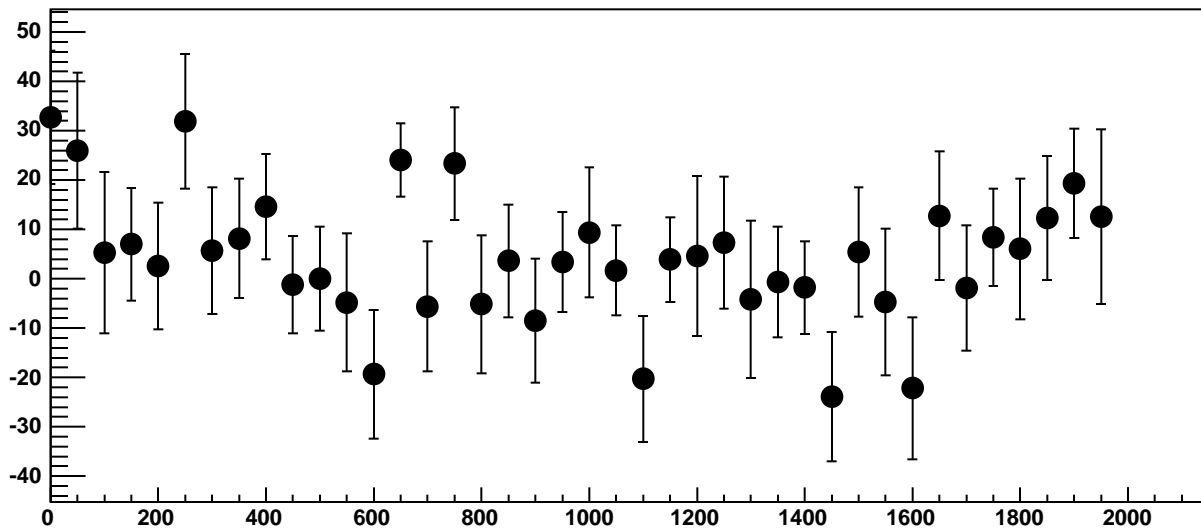


$\chi^2 / \text{ndf}$  4.55 / 11  
p0  $-1073 \pm 19.34$   
p1  $0.0207 \pm 0.01723$

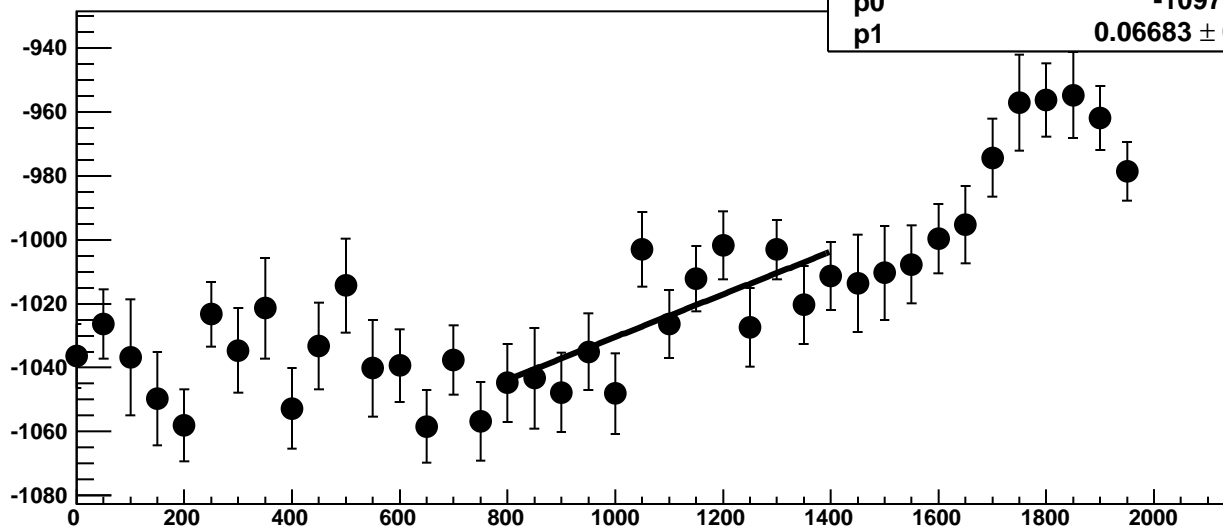
Chip 4, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC



Chip 4, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC

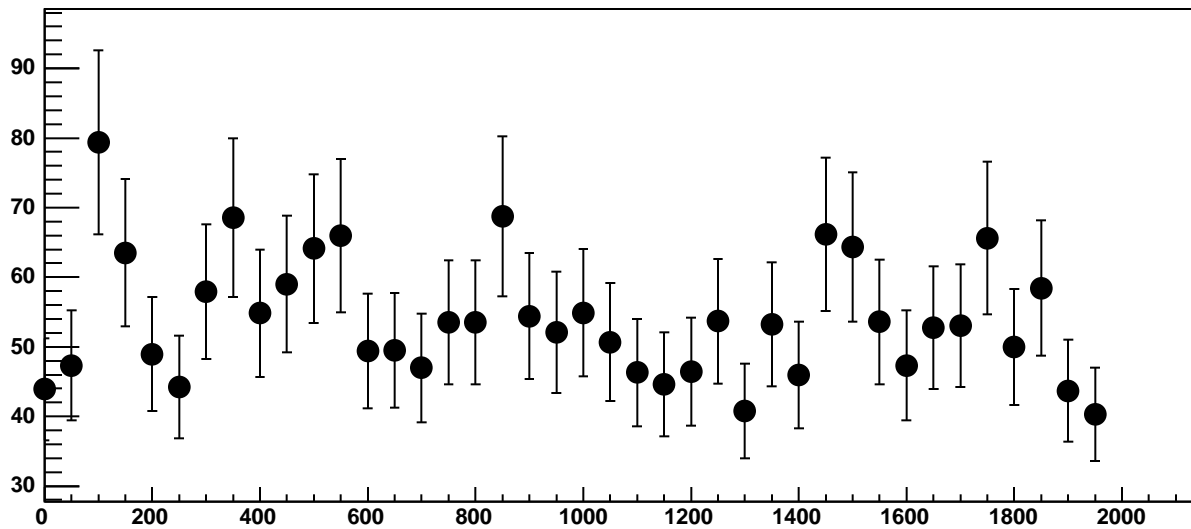


Chip 4, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC

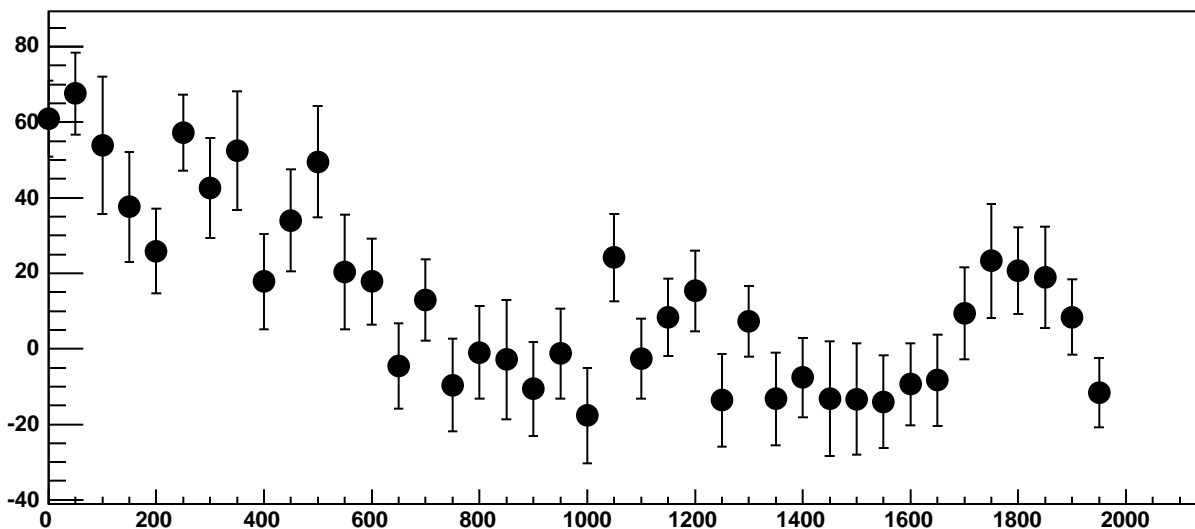


$\chi^2 / \text{ndf}$  13.39 / 11  
p0  $-1097 \pm 20.12$   
p1  $0.06683 \pm 0.01763$

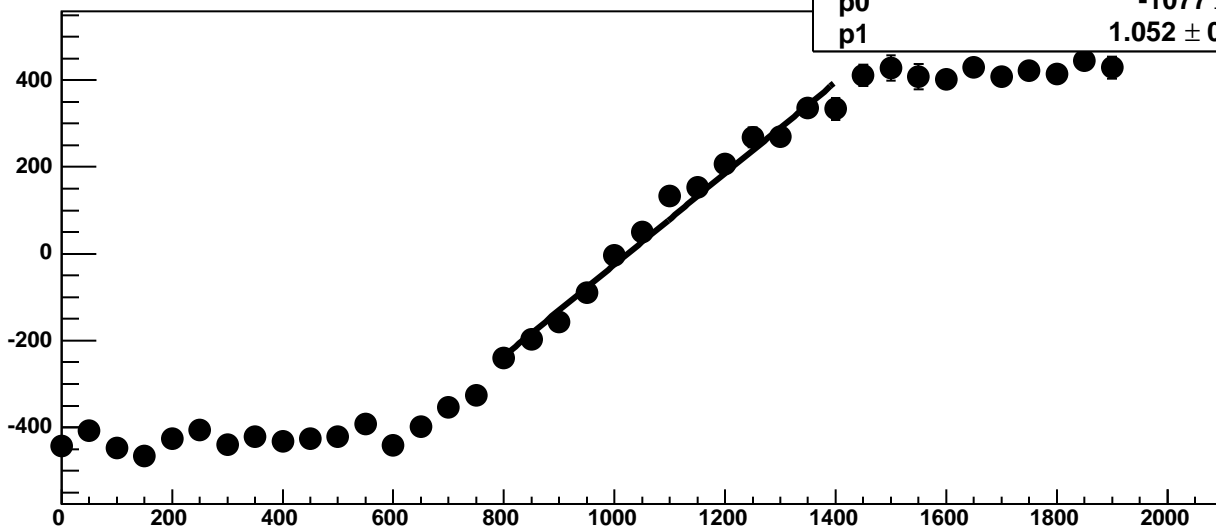
Chip 4, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC



Chip 4, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC

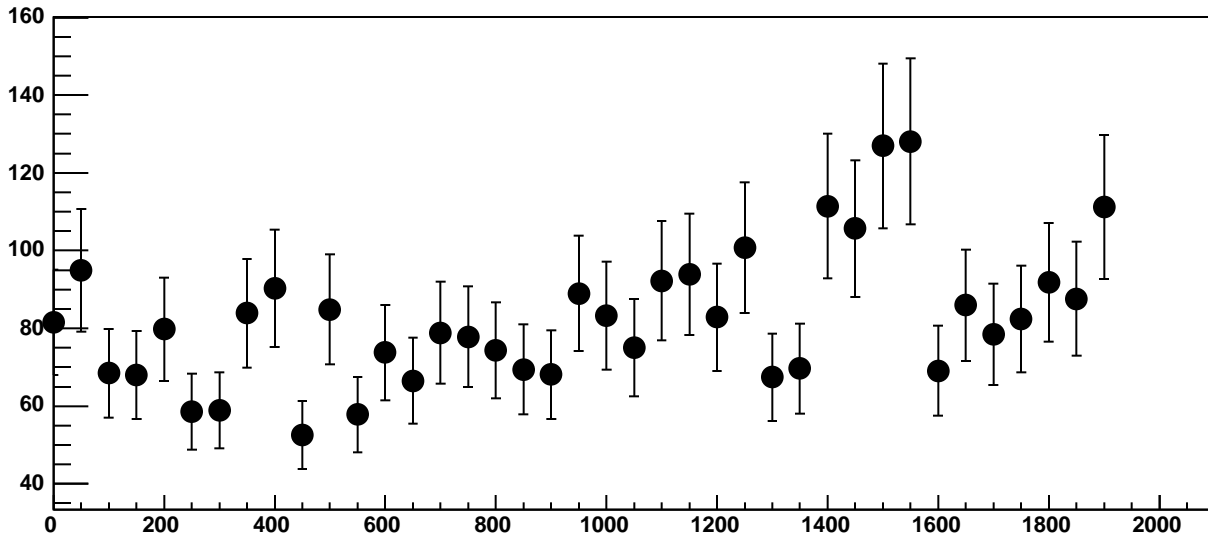


Chip 4, Channel 8, Enable 5, Hold=30, ADC Mean vs DAC

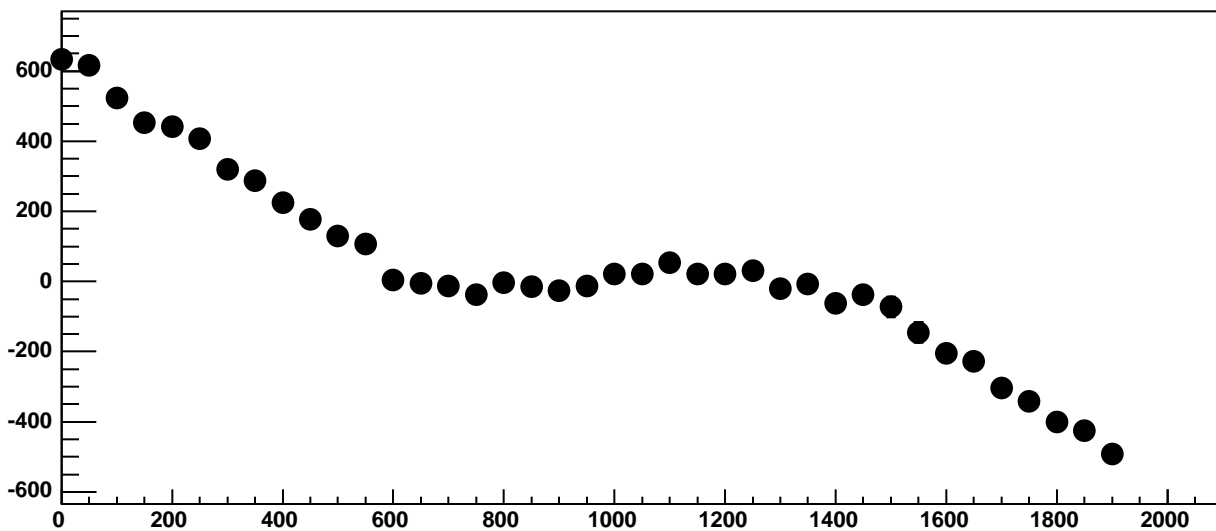


$\chi^2 / \text{ndf}$  25.12 / 11  
p0  $-1077 \pm 29.14$   
p1  $1.052 \pm 0.02652$

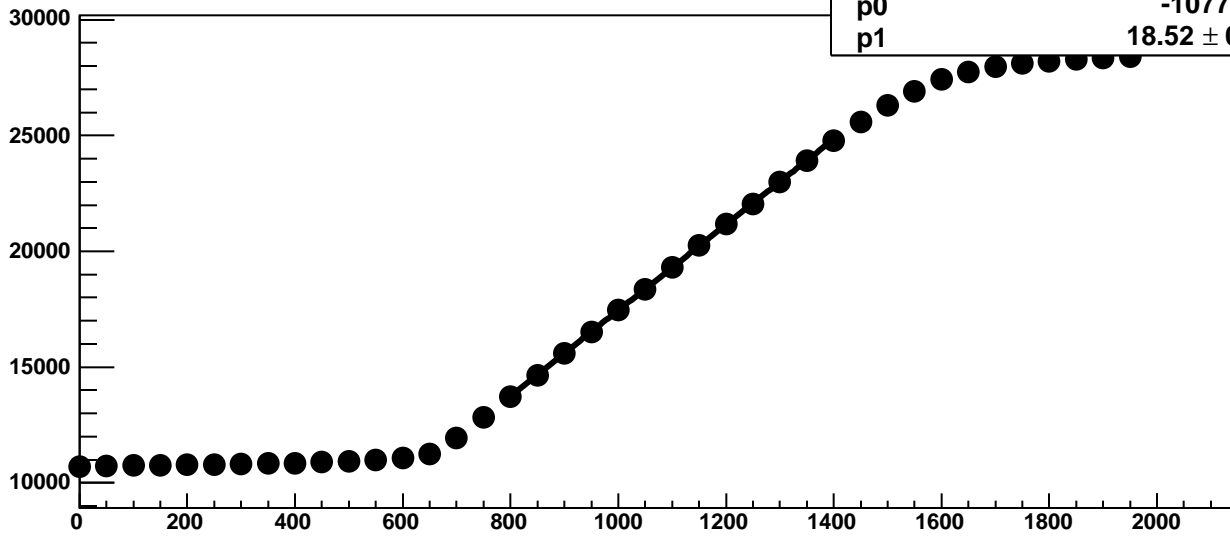
Chip 4, Channel 8, Enable 5, Hold=30, ADC Noise vs DAC



Chip 4, Channel 8, Enable 5, Hold=30, ADC Residuals vs DAC

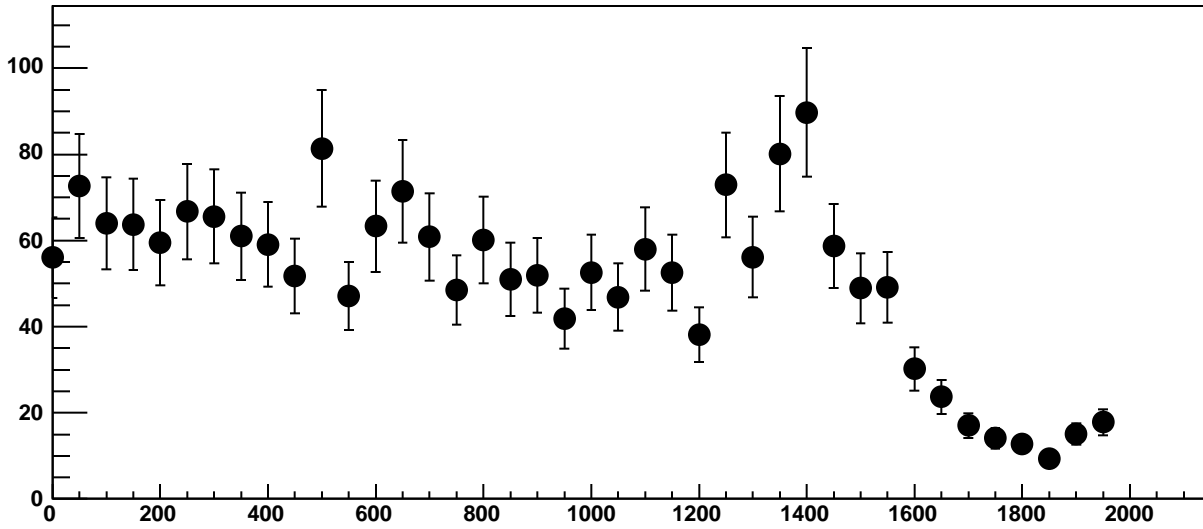


Chip 4, Channel 9, Enable 0!, Hold=30, ADC Mean vs DAC

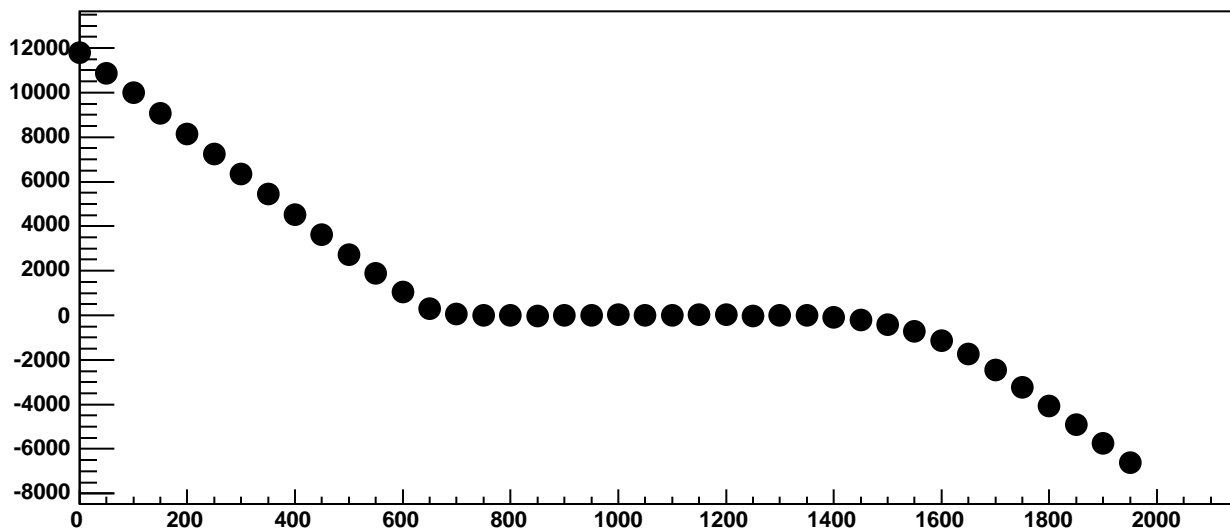


$\chi^2 / \text{ndf}$  39.32 / 11  
p0  $-1077 \pm 22.41$   
p1  $18.52 \pm 0.02073$

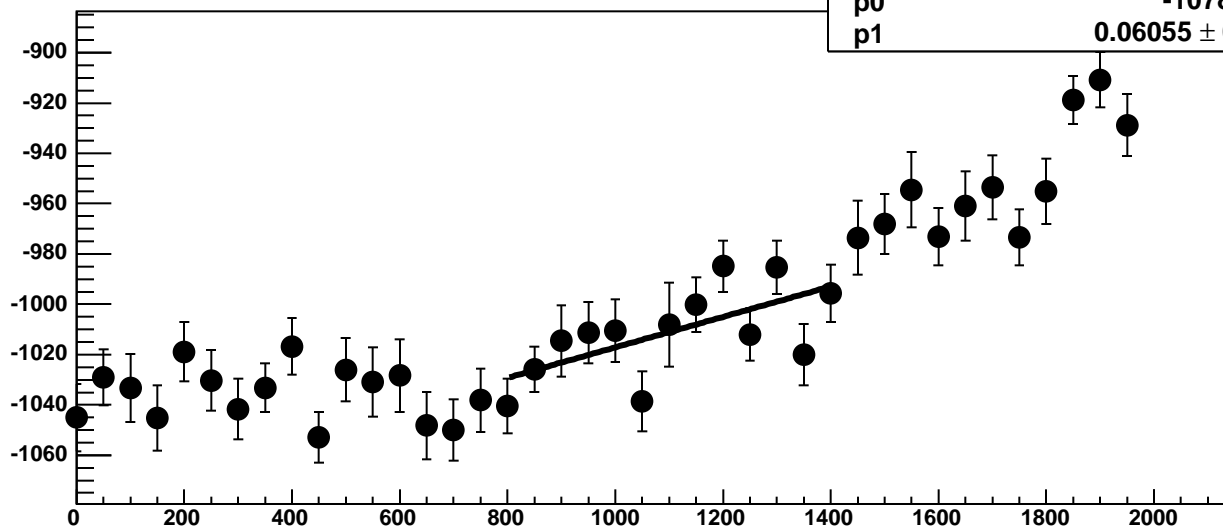
Chip 4, Channel 9, Enable 0!, Hold=30, ADC Noise vs DAC



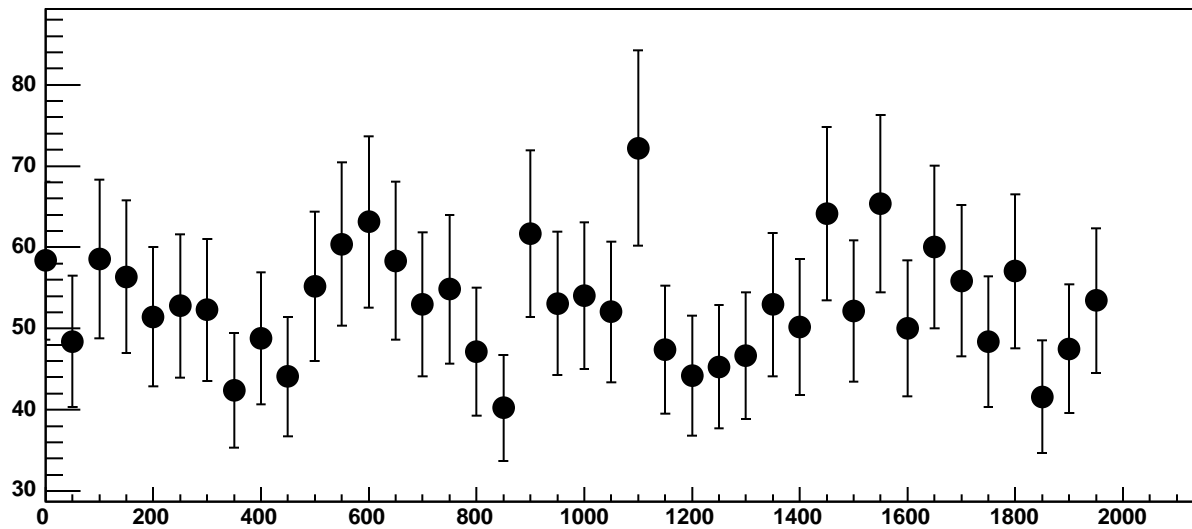
Chip 4, Channel 9, Enable 0!, Hold=30, ADC Residuals vs DAC



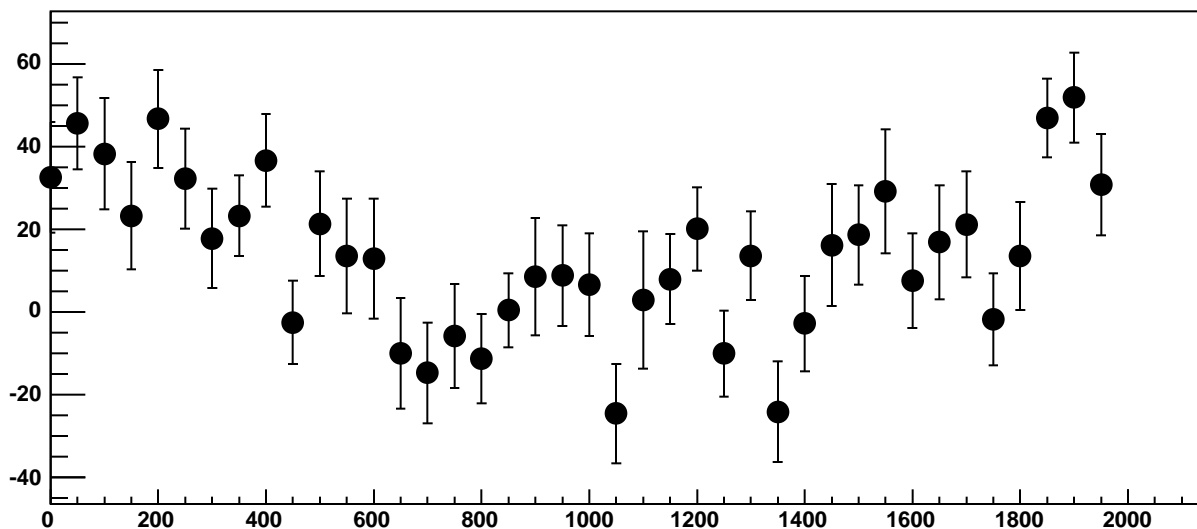
Chip 4, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC



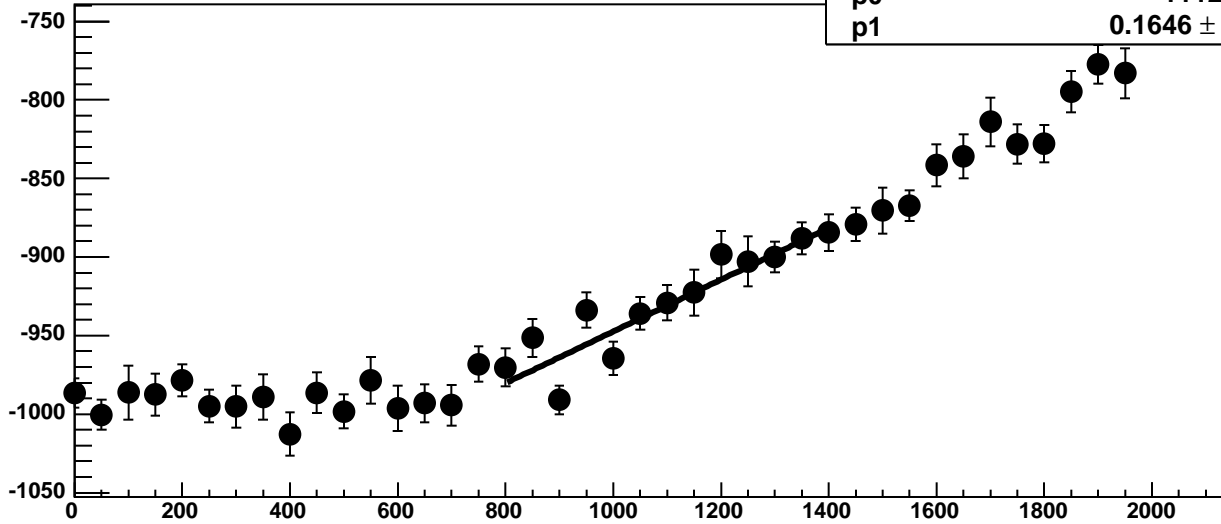
Chip 4, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC



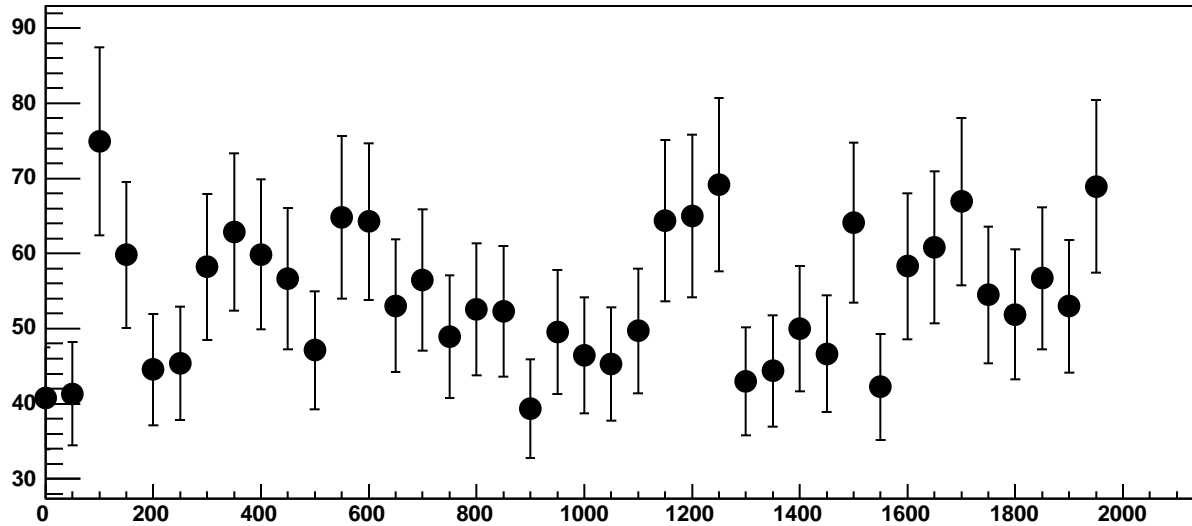
Chip 4, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC



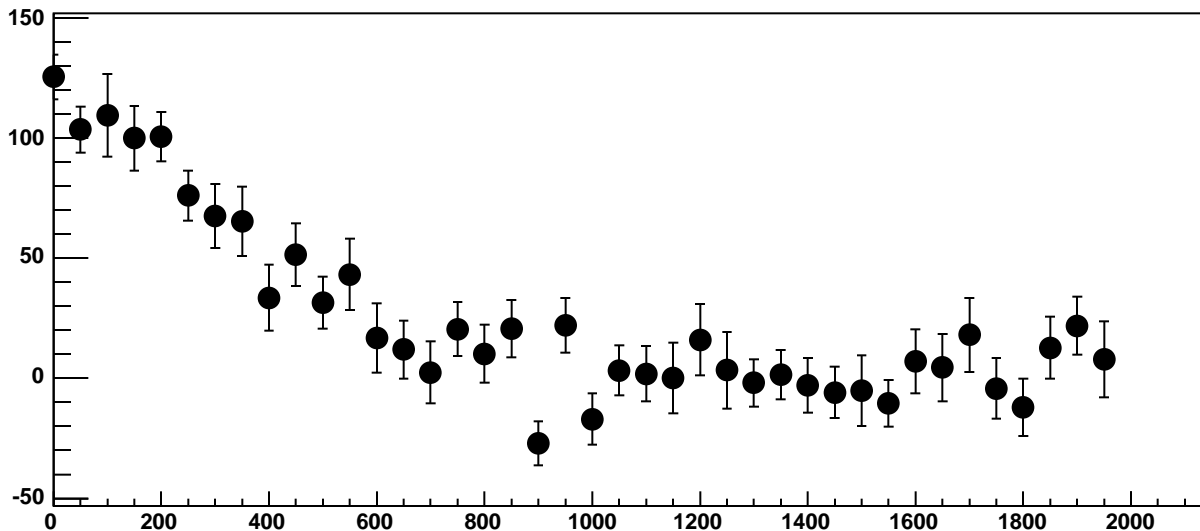
Chip 4, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC



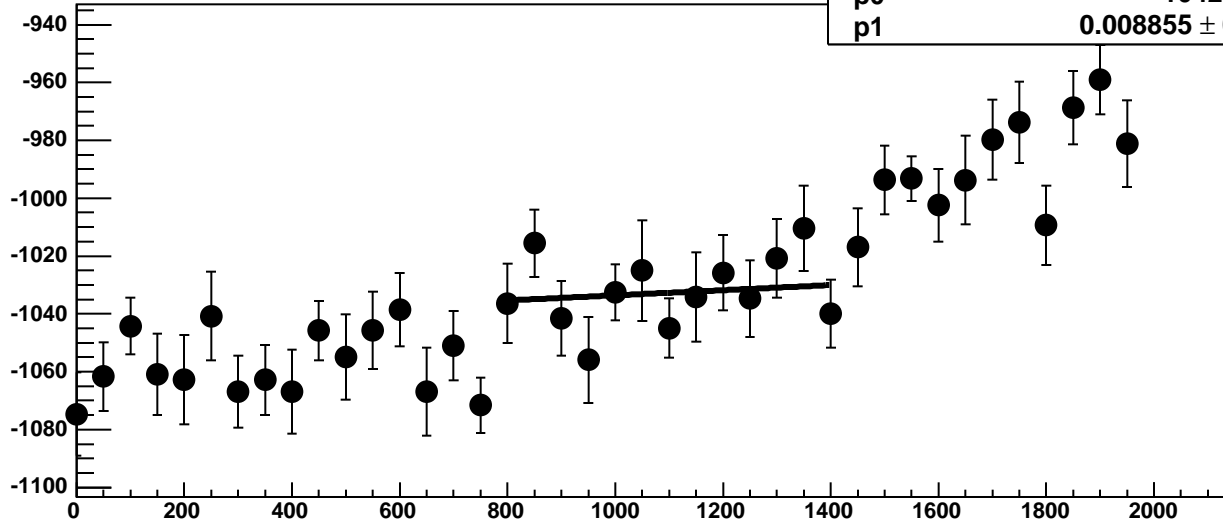
Chip 4, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC



Chip 4, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC

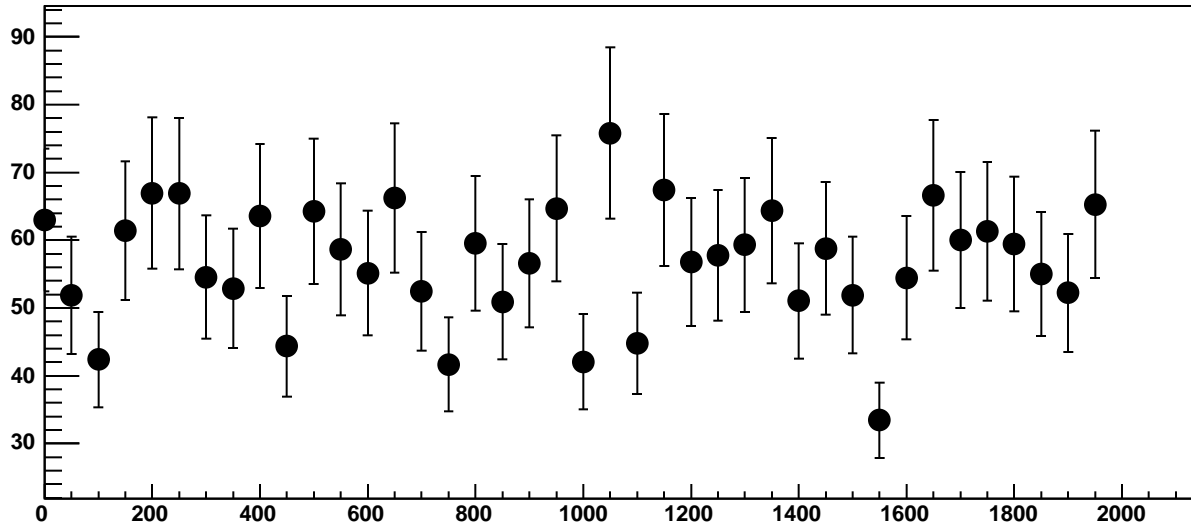


Chip 4, Channel 9, Enable 3, Hold=30, ADC Mean vs DAC

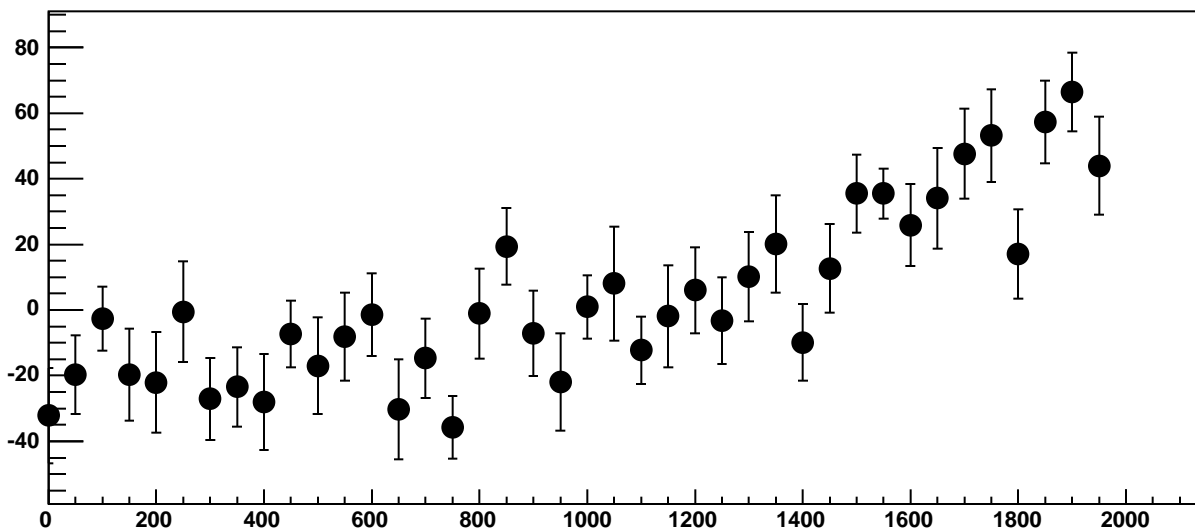


$\chi^2 / \text{ndf}$  10.28 / 11  
p0  $-1042 \pm 21.17$   
p1  $0.008855 \pm 0.01908$

Chip 4, Channel 9, Enable 3, Hold=30, ADC Noise vs DAC

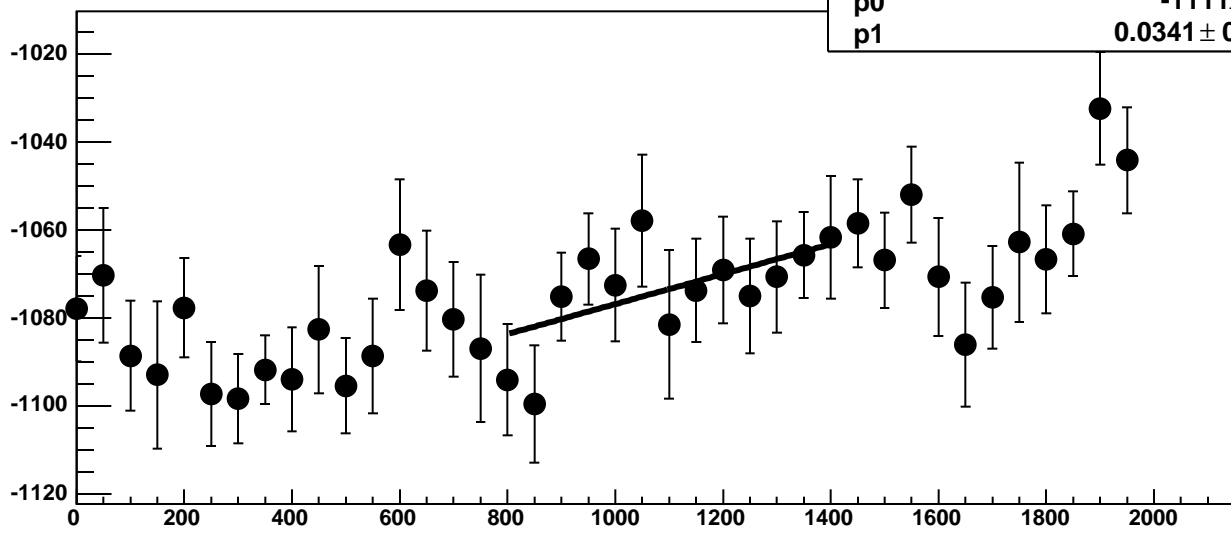


Chip 4, Channel 9, Enable 3, Hold=30, ADC Residuals vs DAC



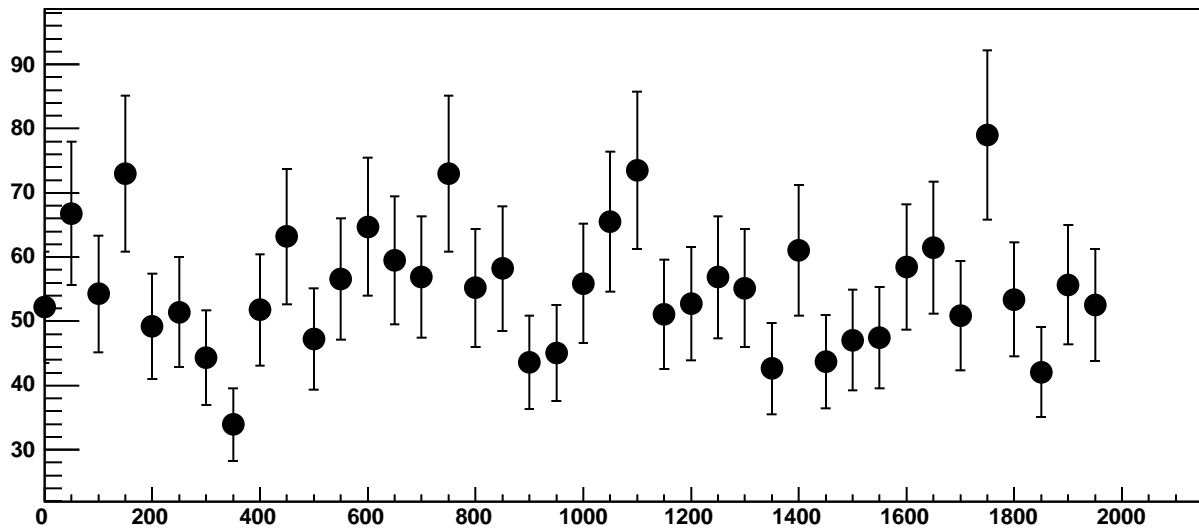


Chip 4, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC

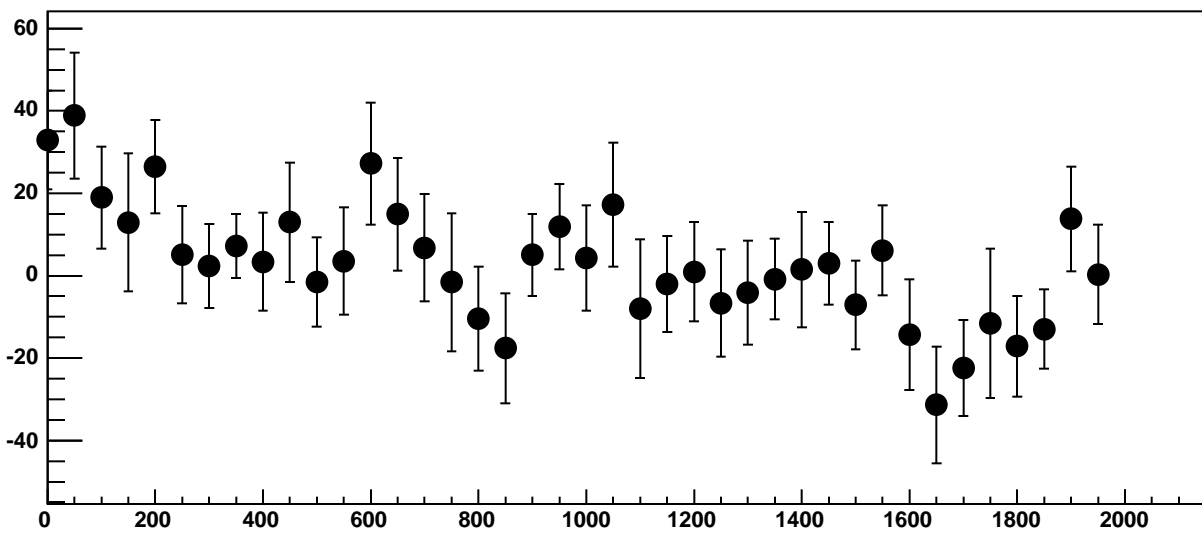


$\chi^2 / \text{ndf}$  6.09 / 11  
p0  $-1111 \pm 19.78$   
p1  $0.0341 \pm 0.01775$

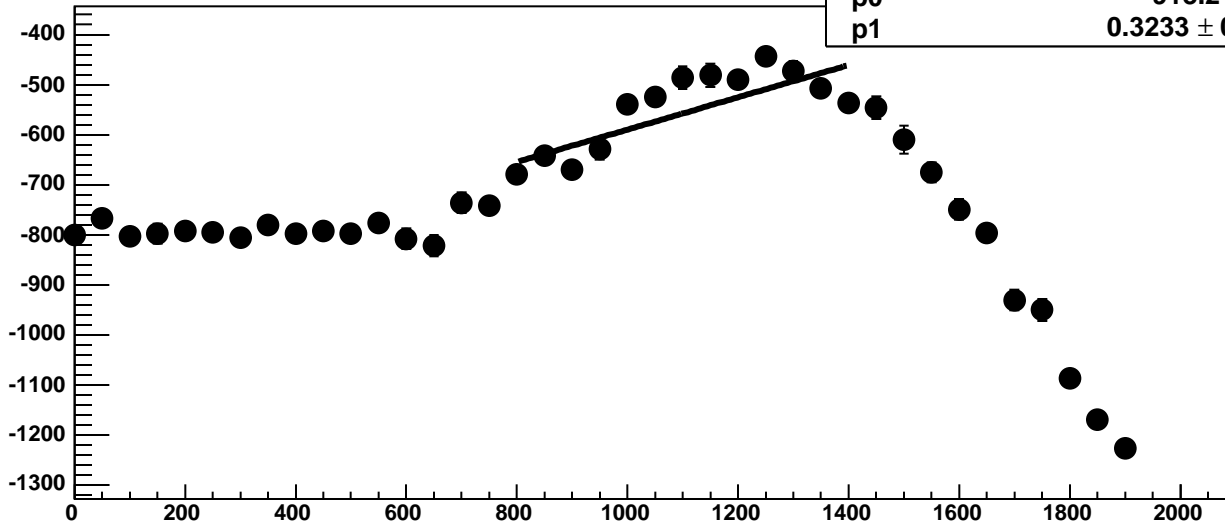
Chip 4, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC



Chip 4, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC

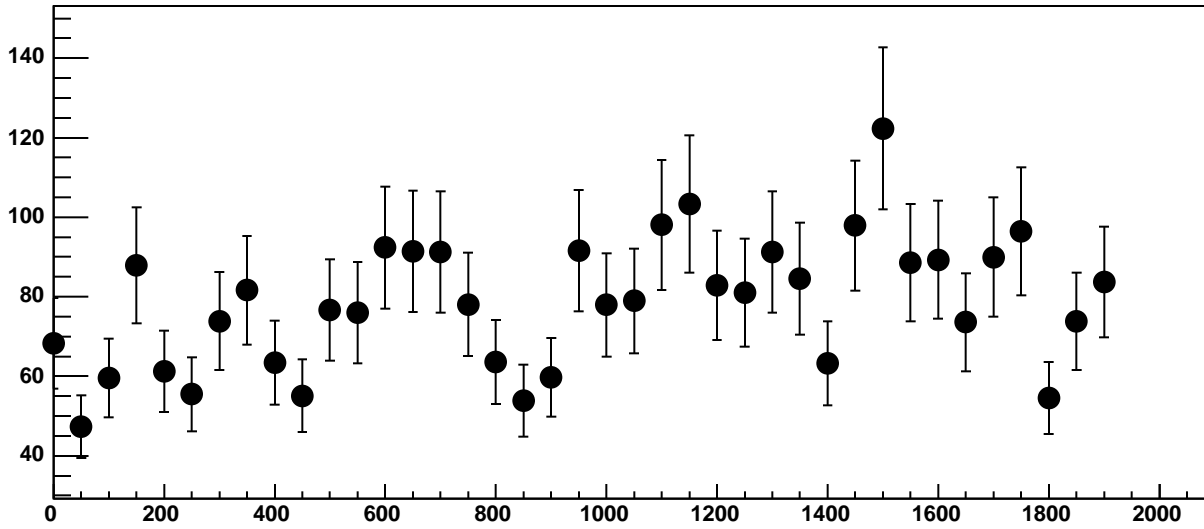


Chip 4, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC

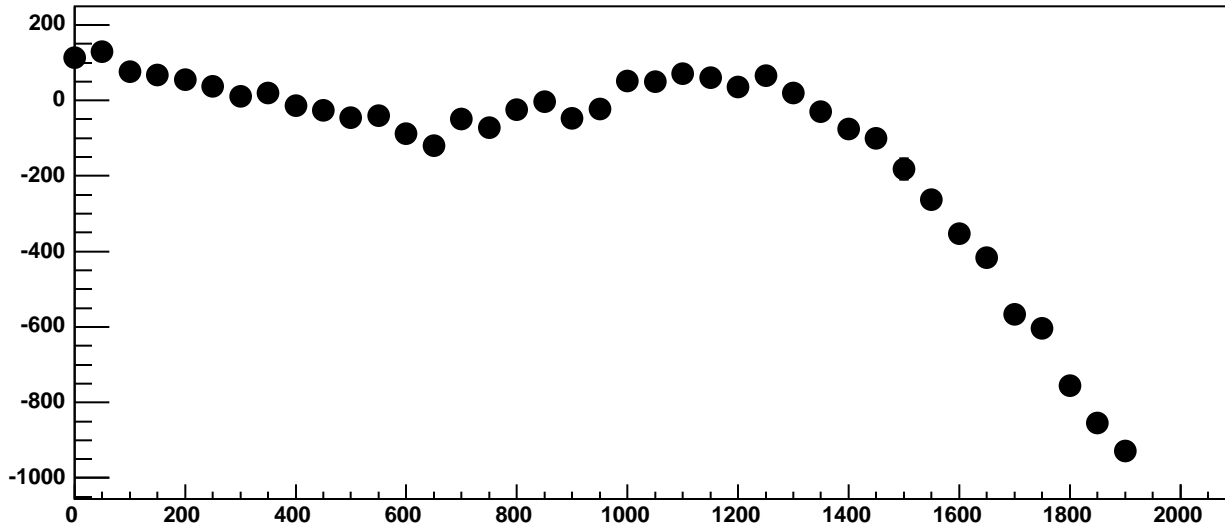


$\chi^2 / \text{ndf}$	95.58 / 11
p0	$-913.2 \pm 25.12$
p1	$0.3233 \pm 0.02318$

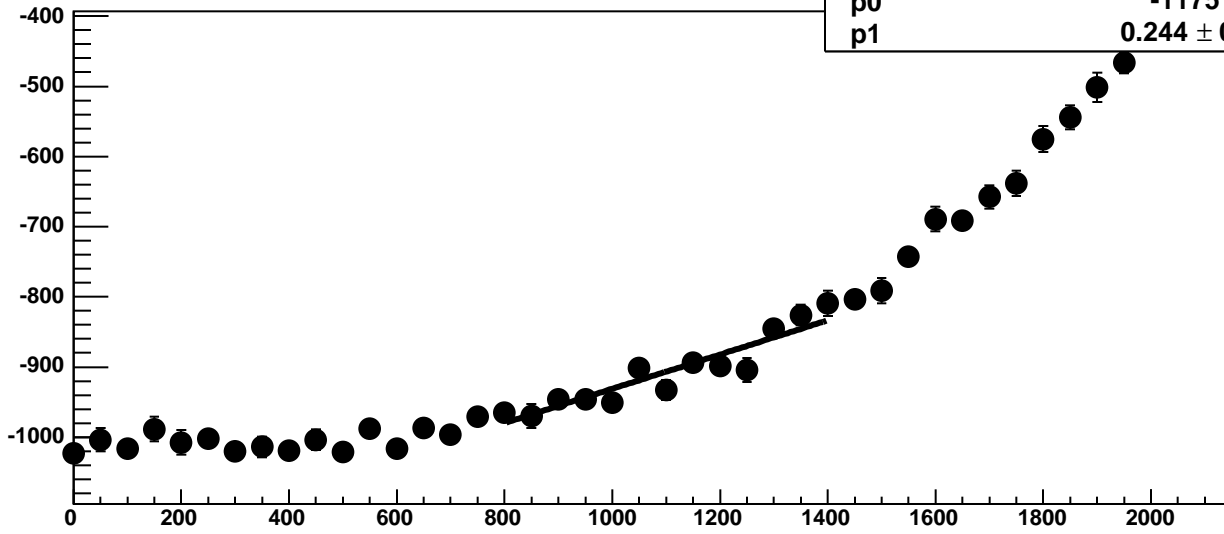
Chip 4, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC



Chip 4, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC

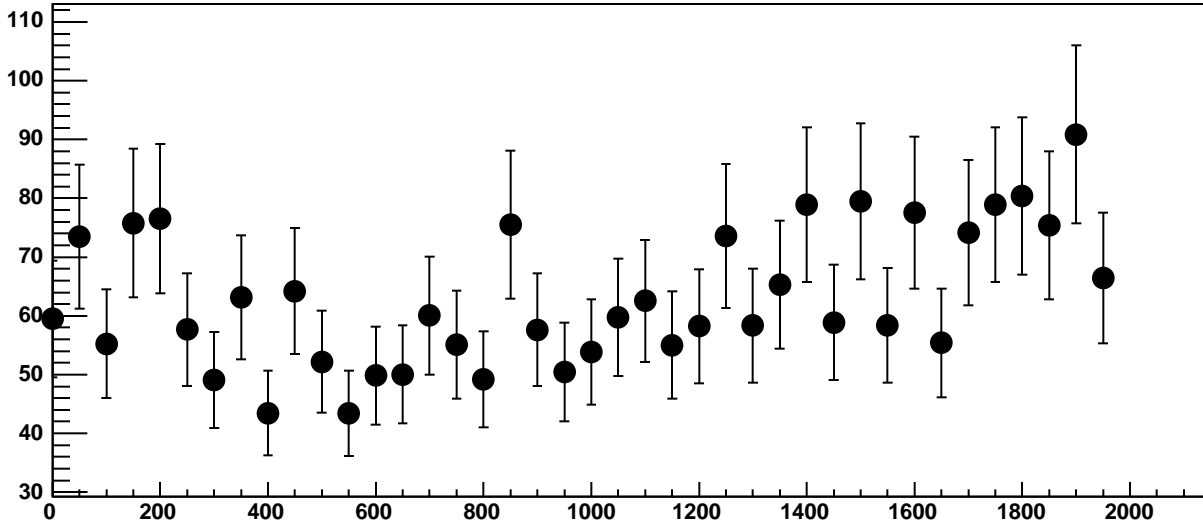


Chip 4, Channel 10, Enable 0, Hold=30, ADC Mean vs DAC

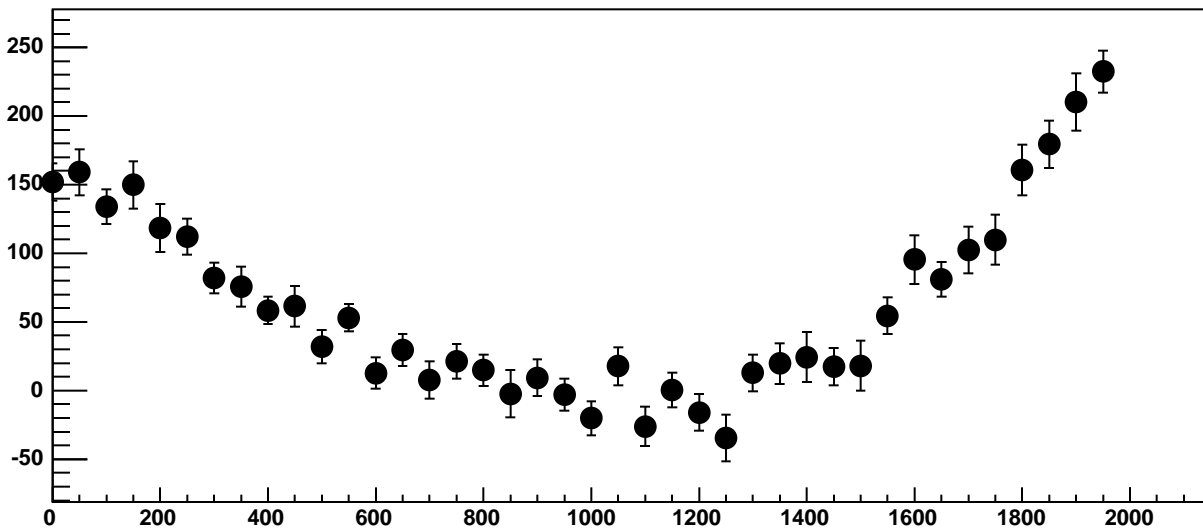


$\chi^2 / \text{ndf}$  20.03 / 11  
p0  $-1175 \pm 22.85$   
p1  $0.244 \pm 0.02099$

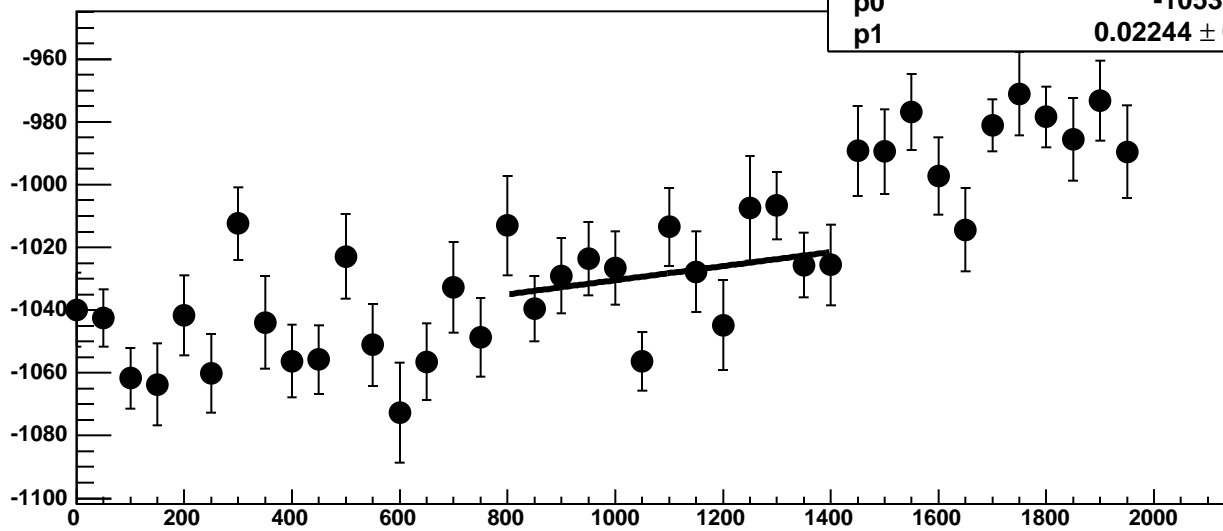
Chip 4, Channel 10, Enable 0, Hold=30, ADC Noise vs DAC



Chip 4, Channel 10, Enable 0, Hold=30, ADC Residuals vs DAC

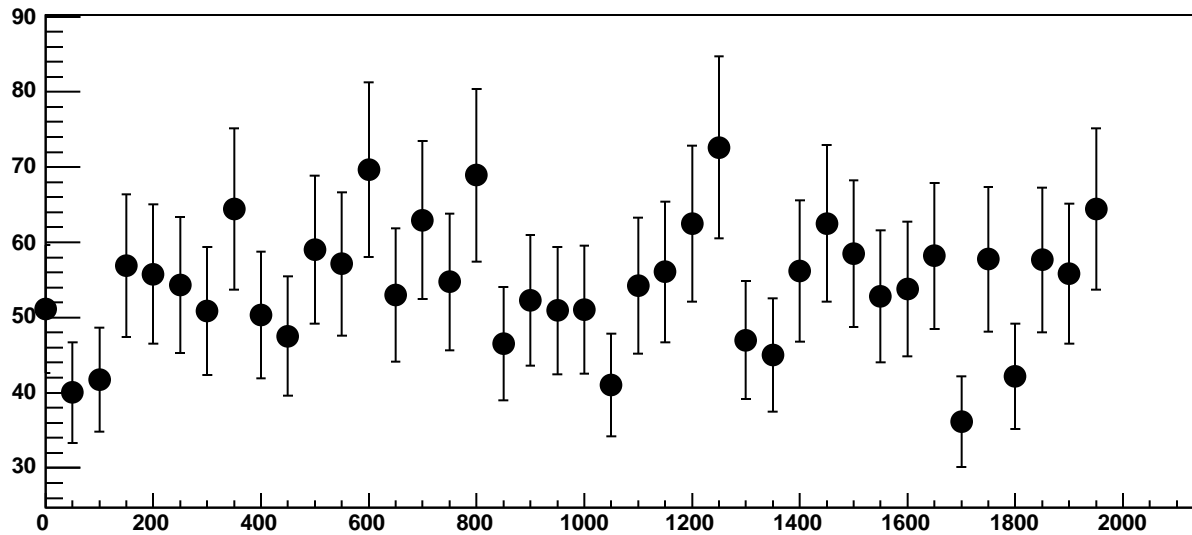


Chip 4, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC

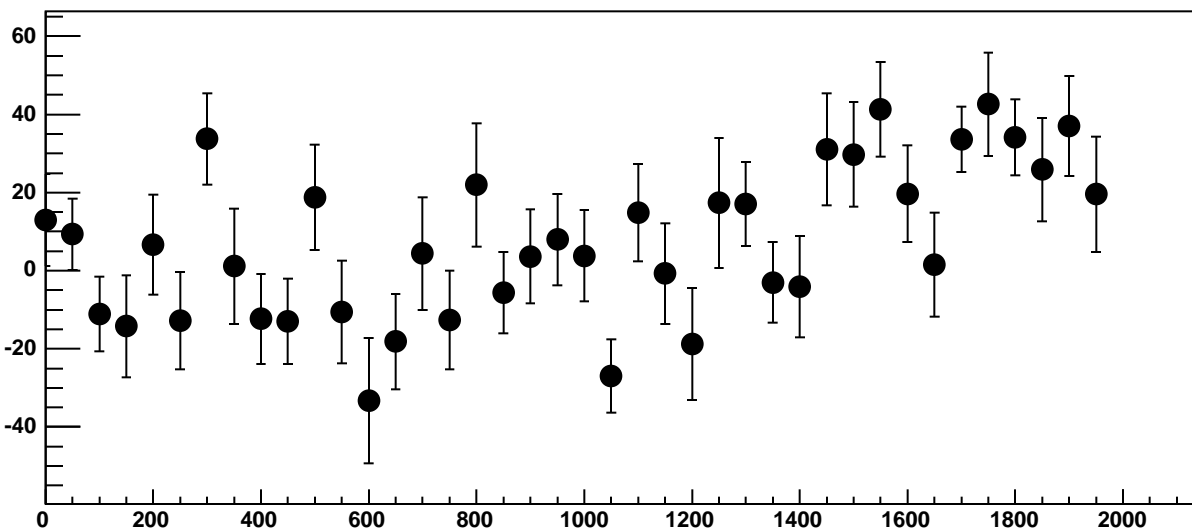


$\chi^2 / \text{ndf}$  18 / 11  
p0  $-1053 \pm 20.02$   
p1  $0.02244 \pm 0.01797$

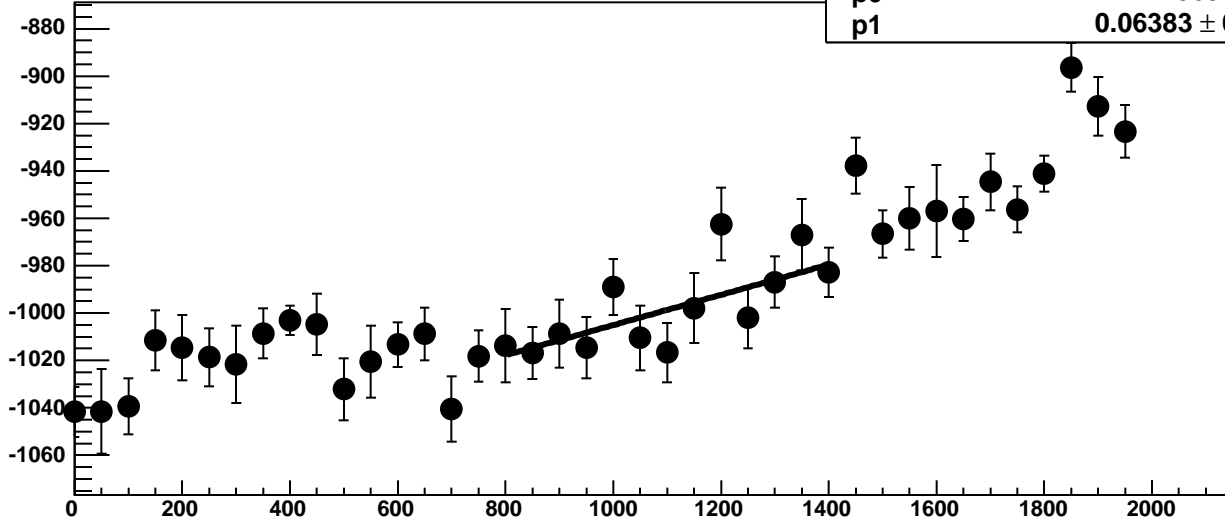
Chip 4, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC



Chip 4, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC

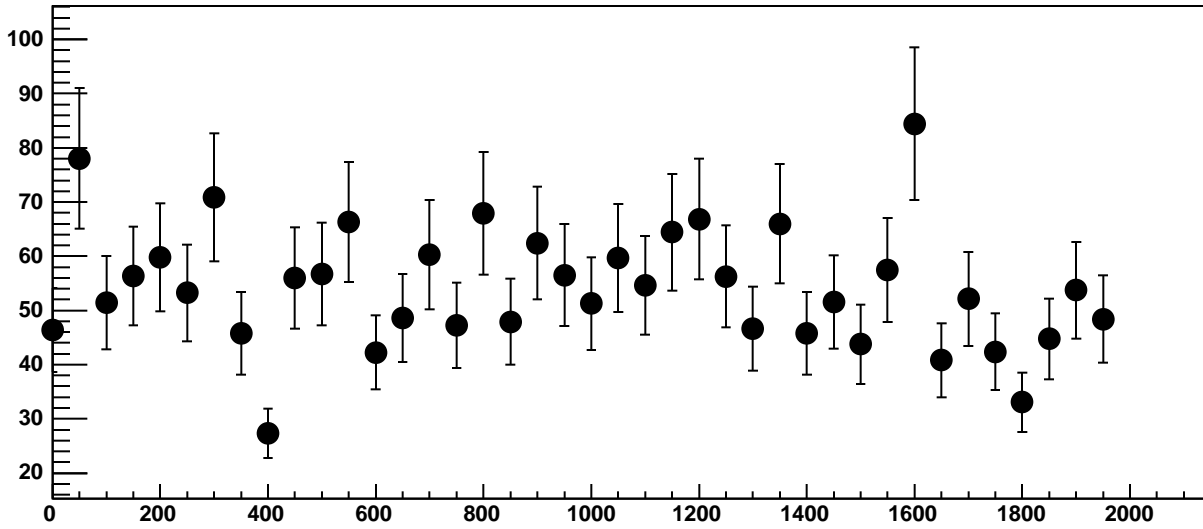


Chip 4, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC

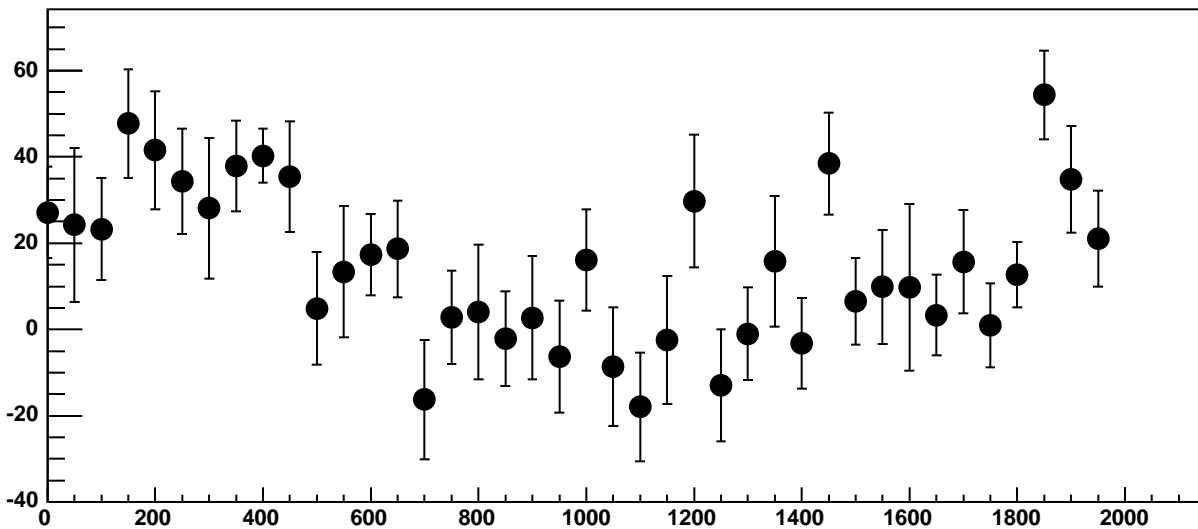


$\chi^2 / \text{ndf}$  10.68 / 11  
p0  $-1069 \pm 20.89$   
p1  $0.06383 \pm 0.01851$

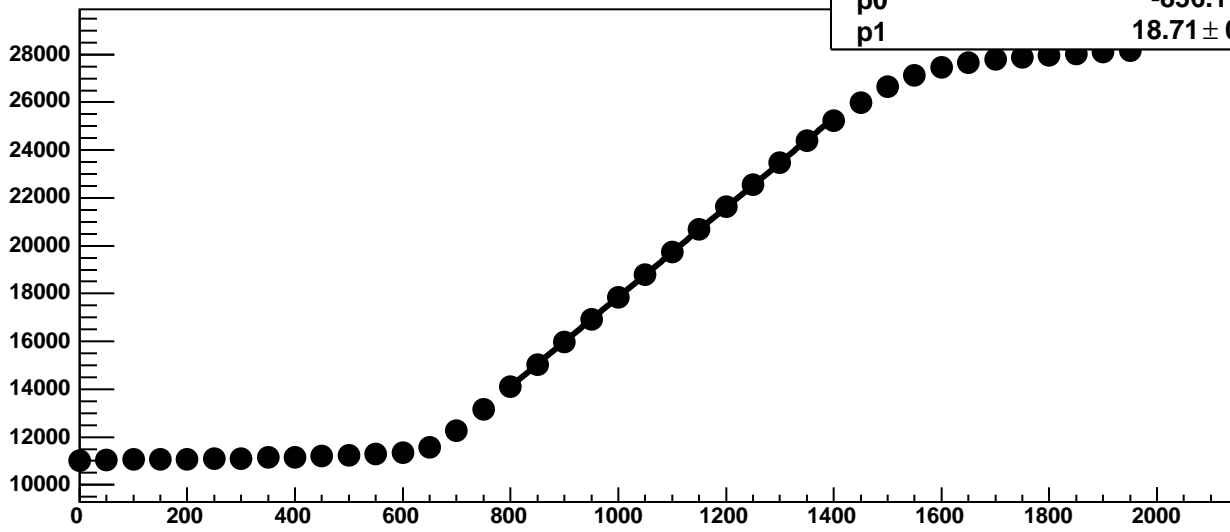
Chip 4, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC



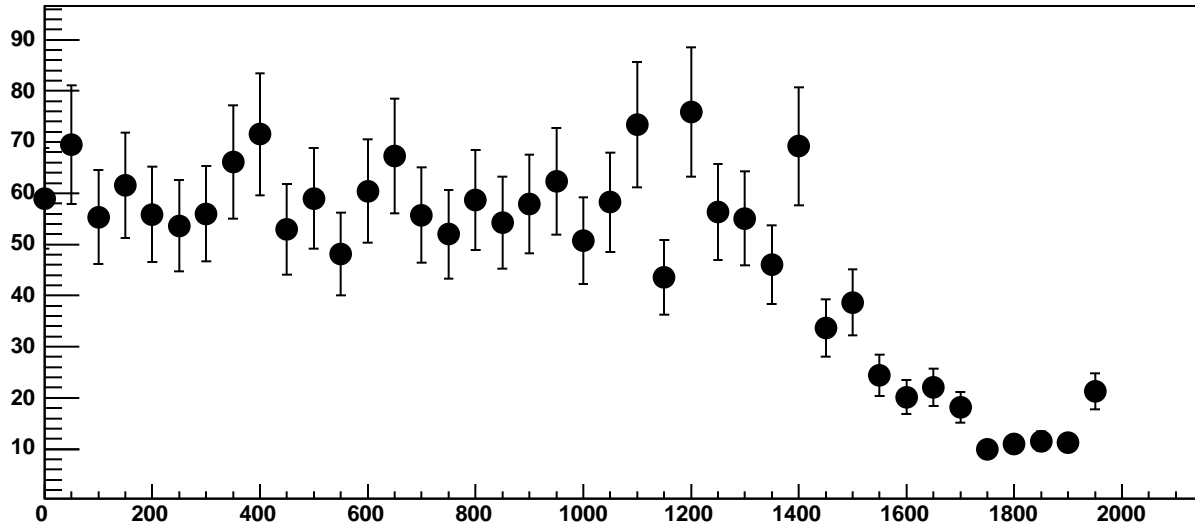
Chip 4, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC



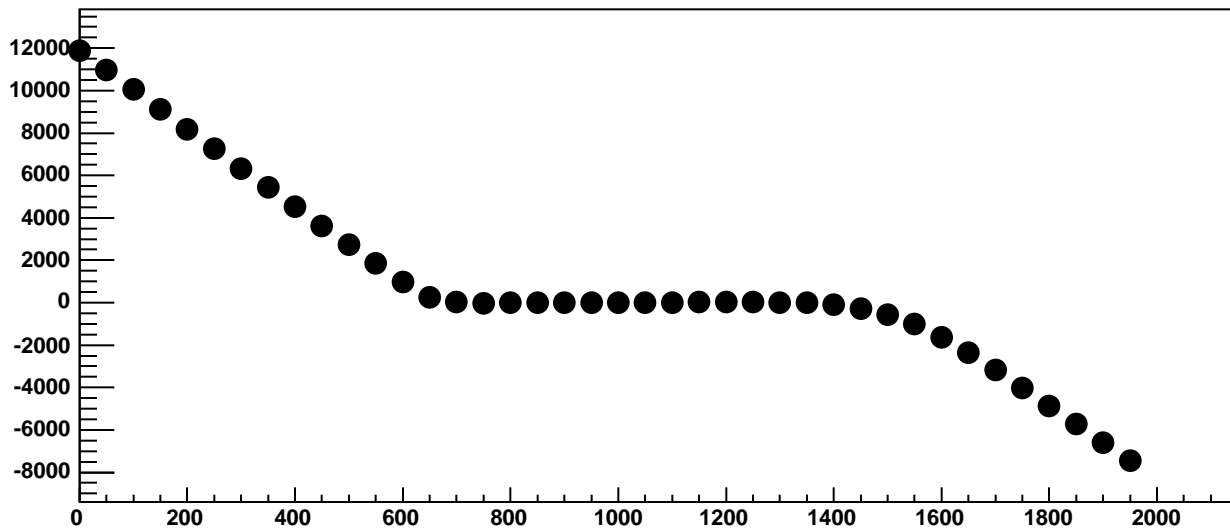
Chip 4, Channel 10, Enable 3!, Hold=30, ADC Mean vs DAC



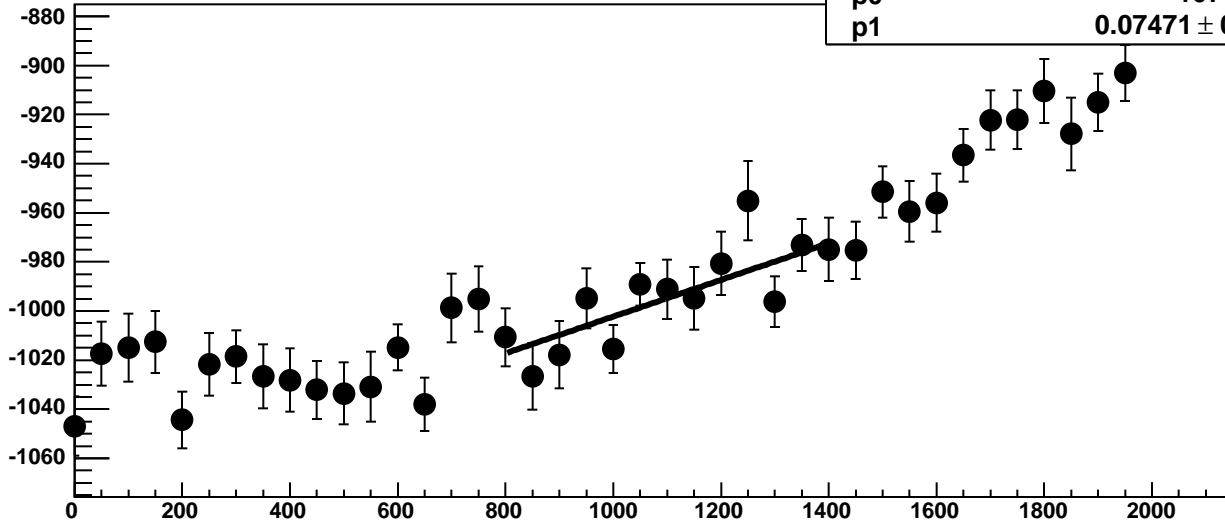
Chip 4, Channel 10, Enable 3!, Hold=30, ADC Noise vs DAC



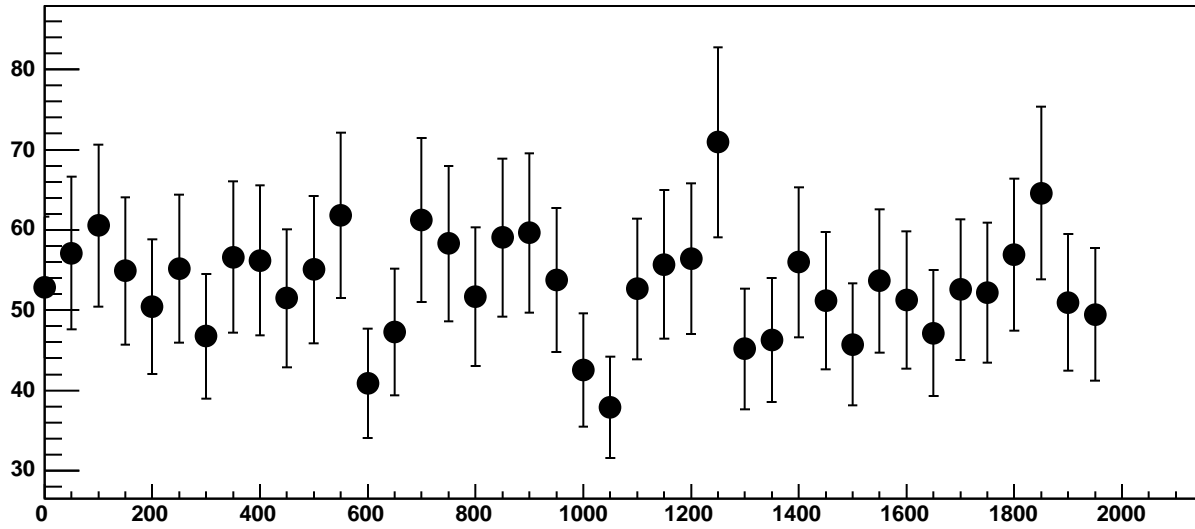
Chip 4, Channel 10, Enable 3!, Hold=30, ADC Residuals vs DAC



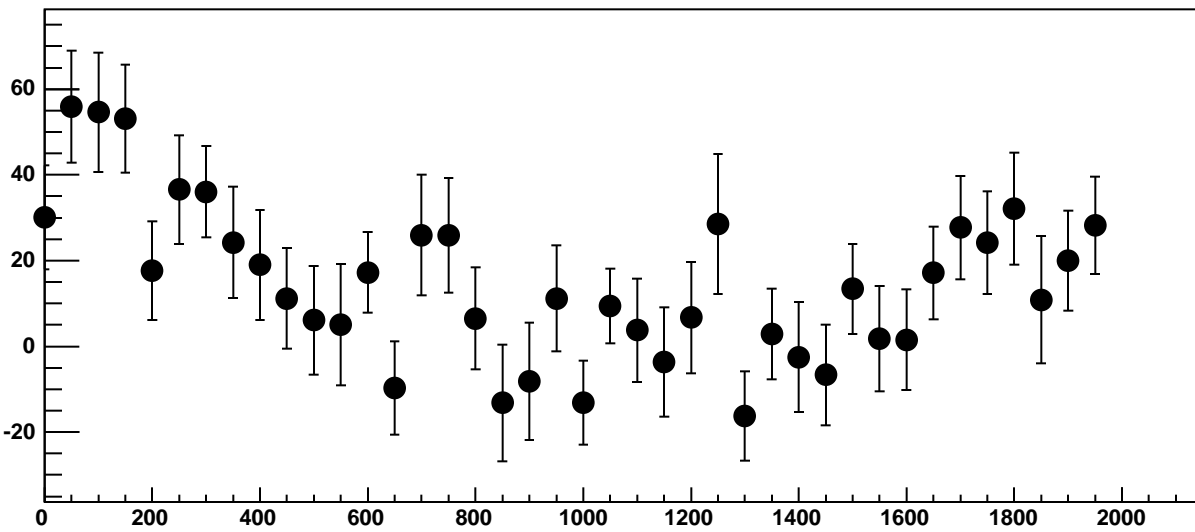
Chip 4, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC



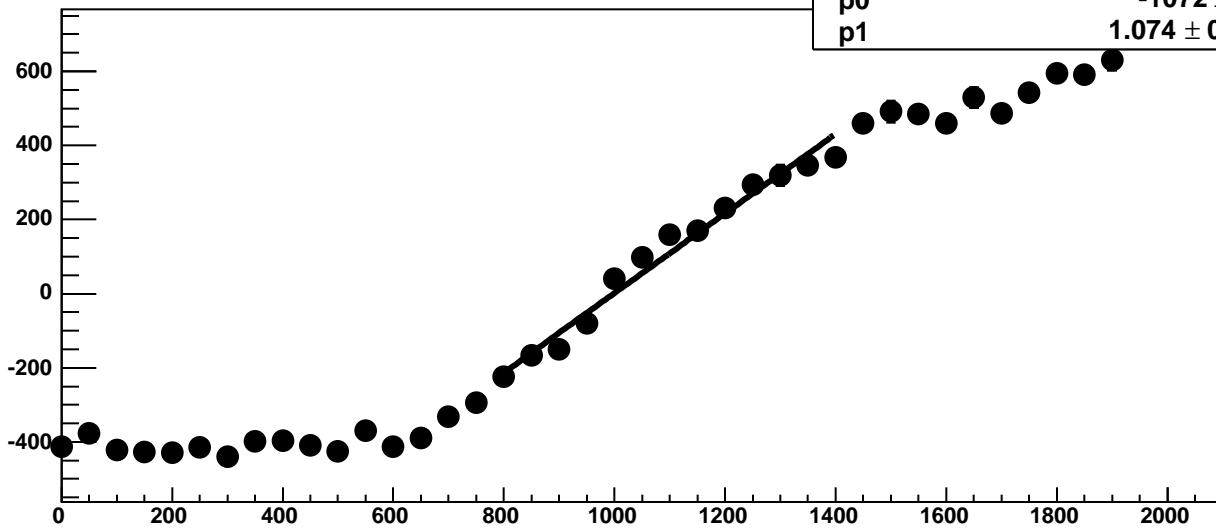
Chip 4, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC



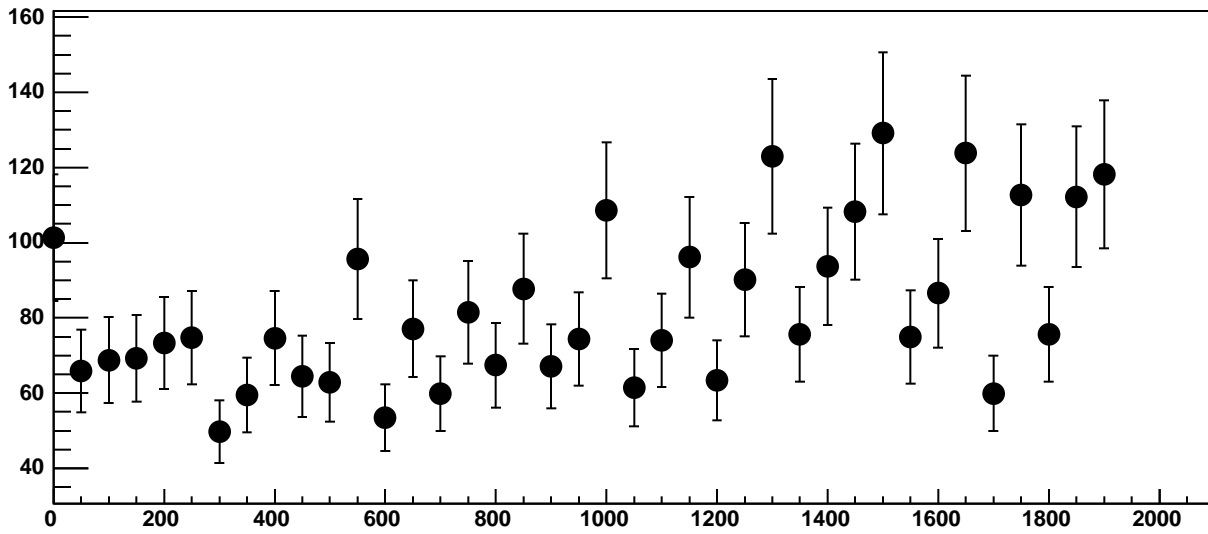
Chip 4, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC



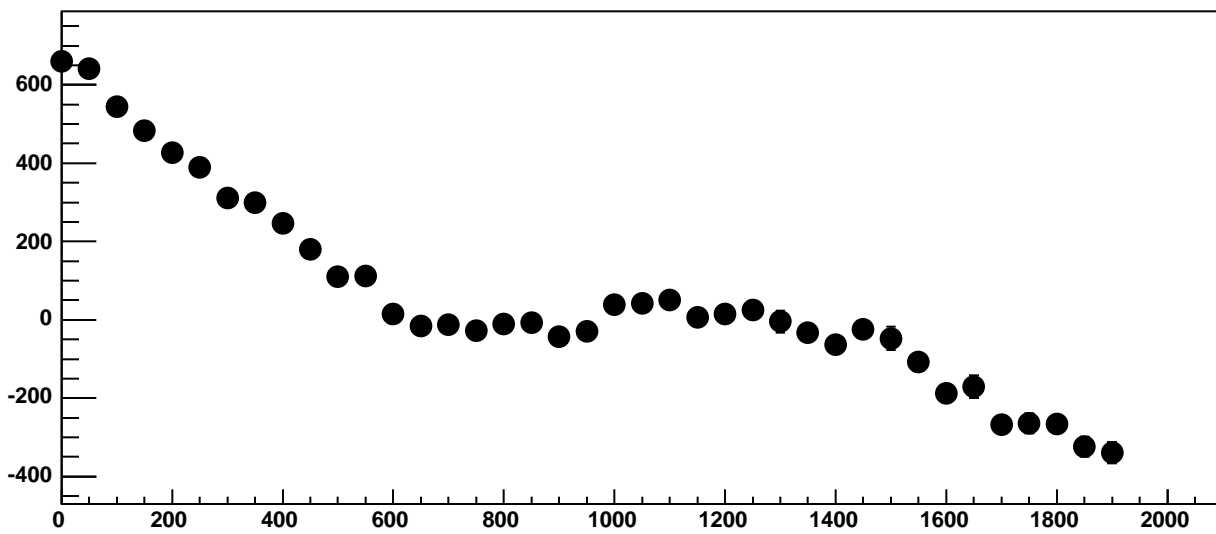
Chip 4, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC



Chip 4, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC

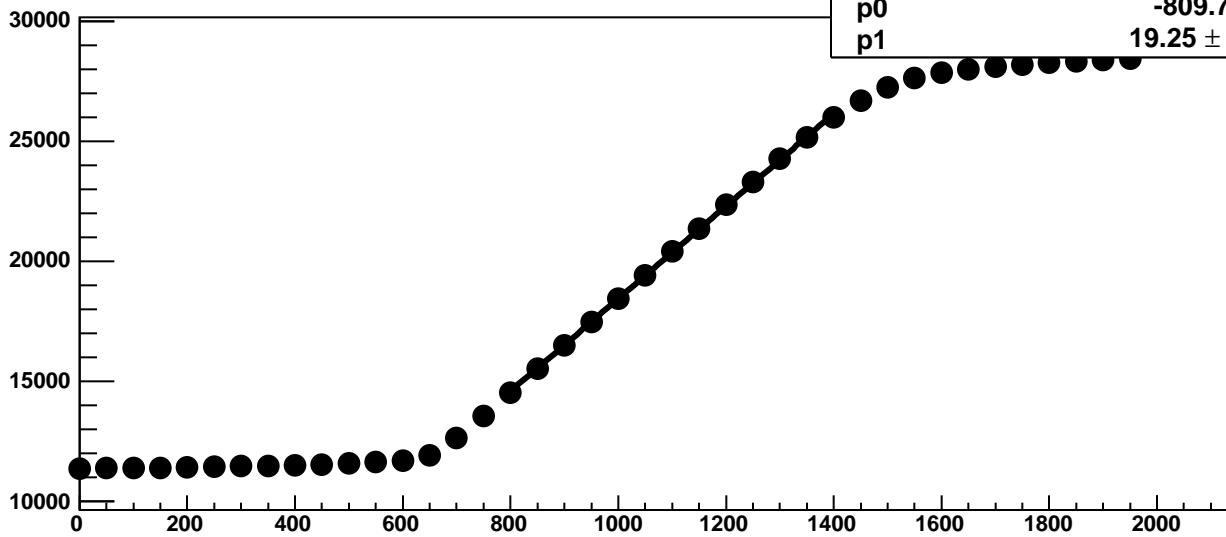


Chip 4, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC





Chip 4, Channel 11, Enable 0!, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

208 / 11

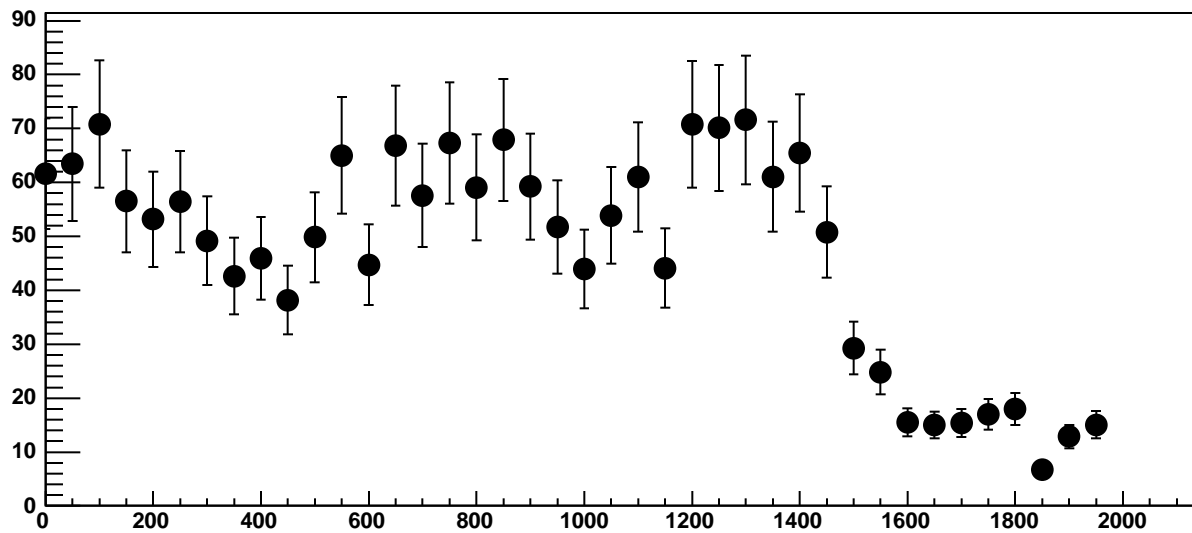
p0

$-809.7 \pm 23.21$

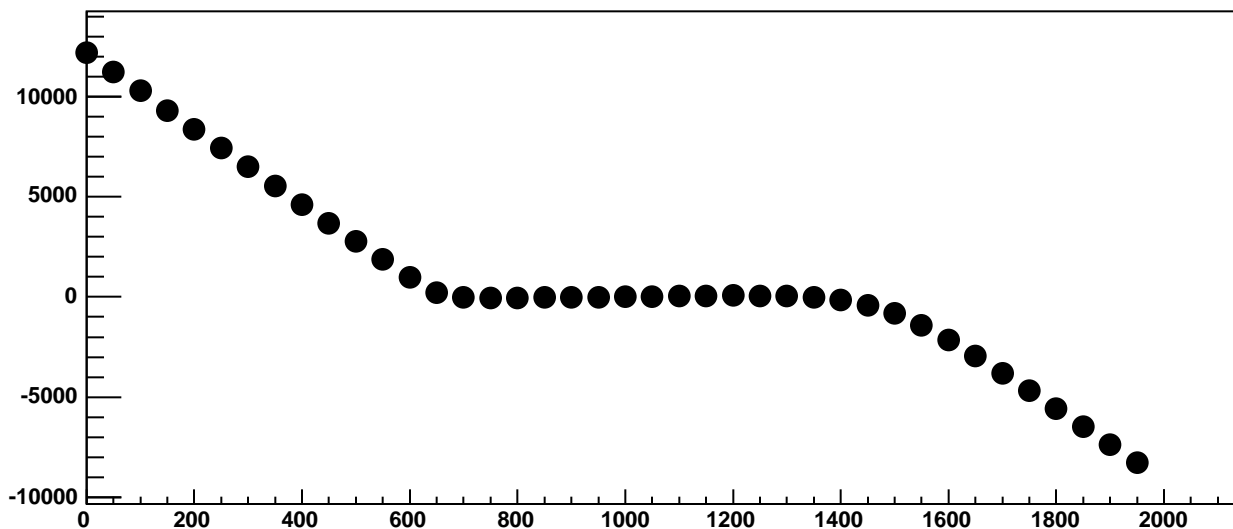
p1

$19.25 \pm 0.02118$

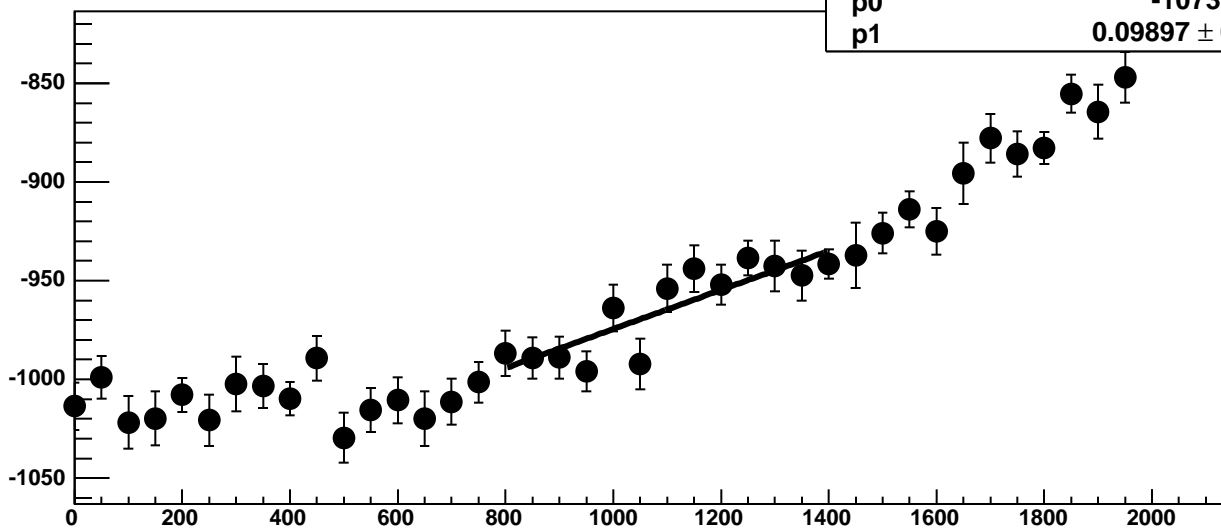
Chip 4, Channel 11, Enable 0!, Hold=30, ADC Noise vs DAC



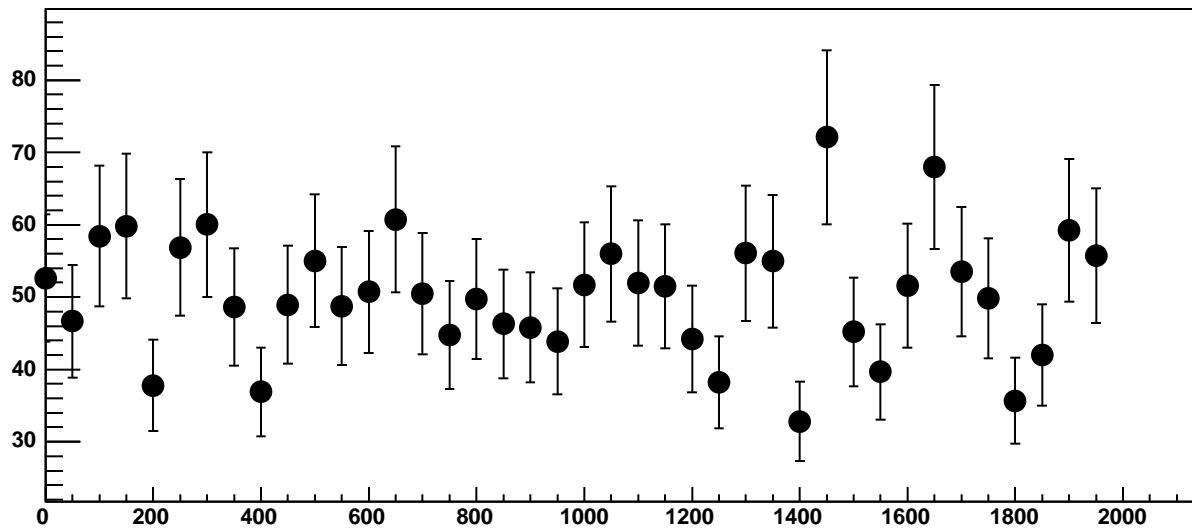
Chip 4, Channel 11, Enable 0!, Hold=30, ADC Residuals vs DAC



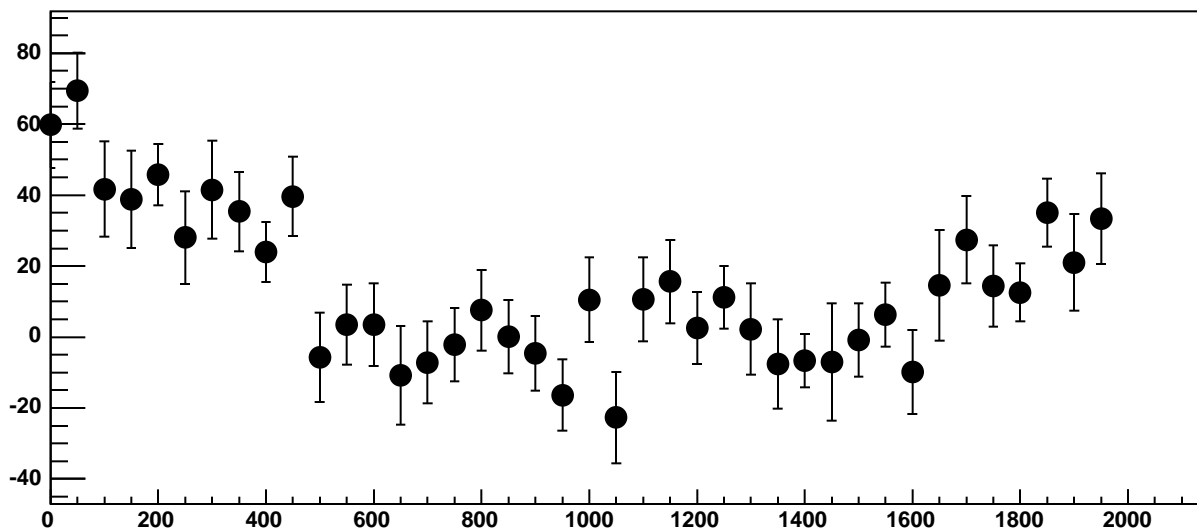
Chip 4, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC



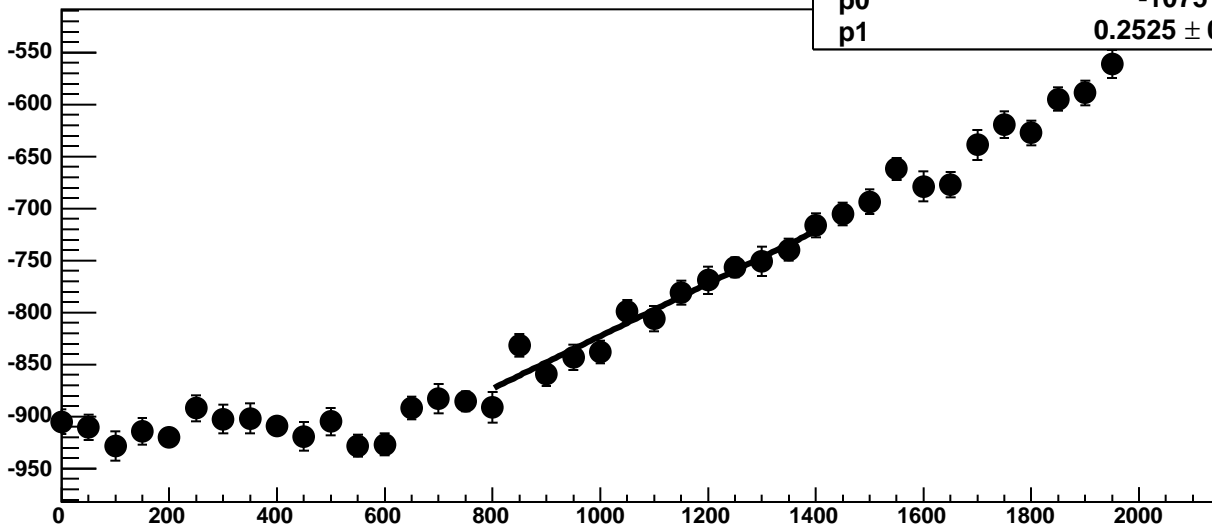
Chip 4, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC



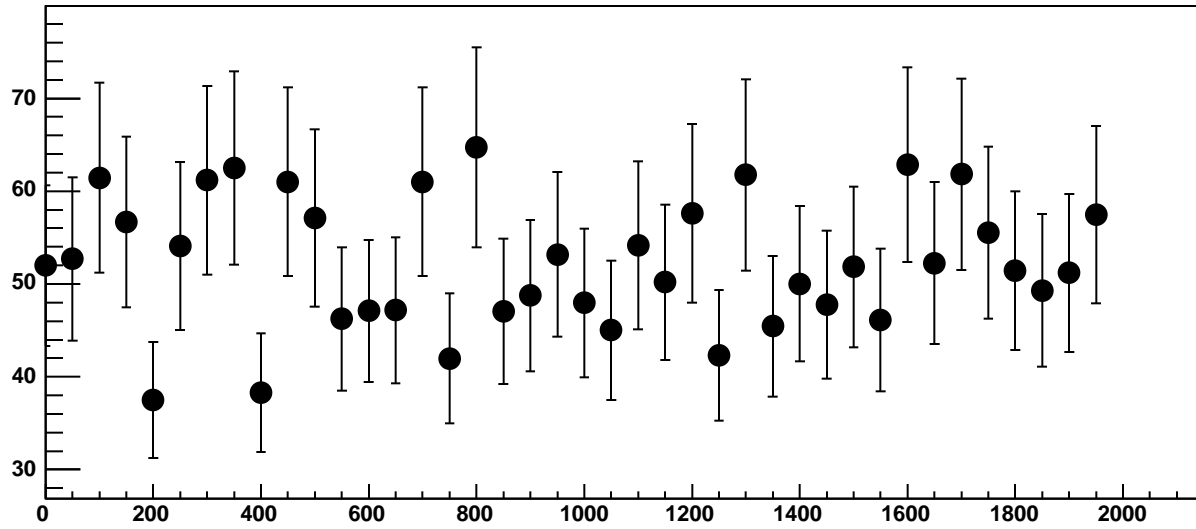
Chip 4, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC



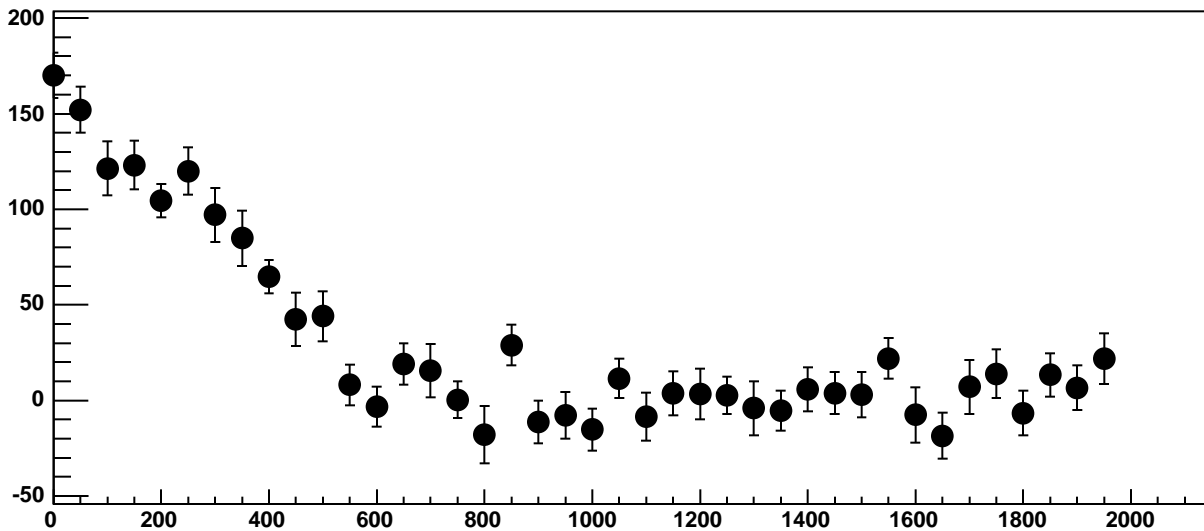
Chip 4, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC



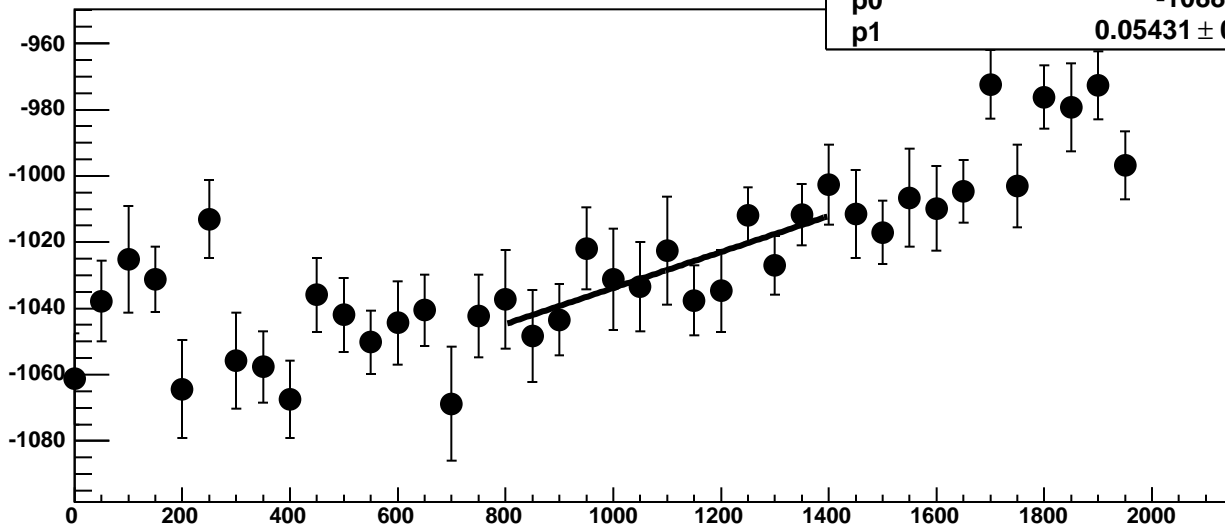
Chip 4, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC



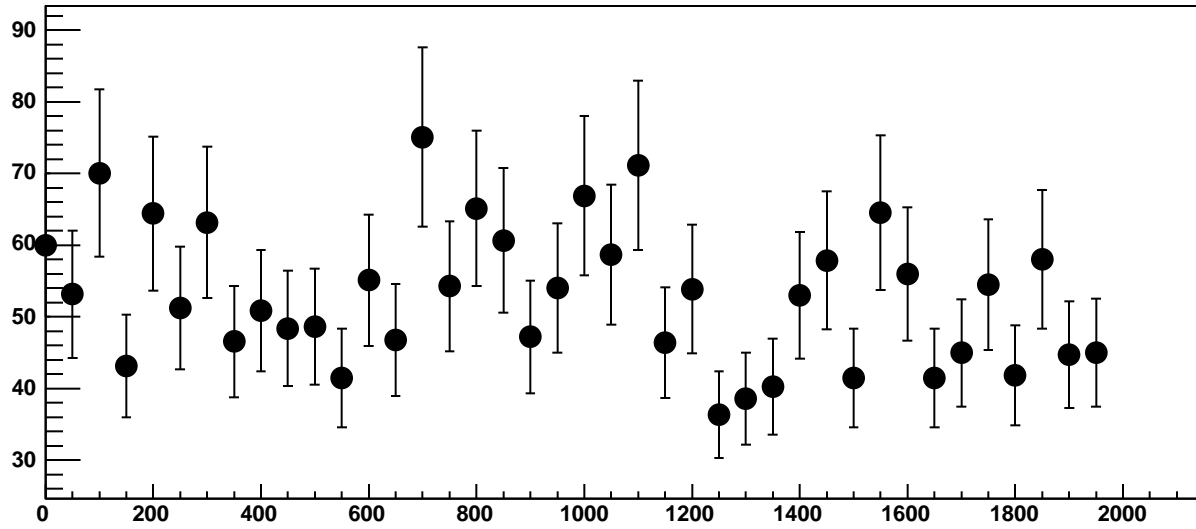
Chip 4, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC



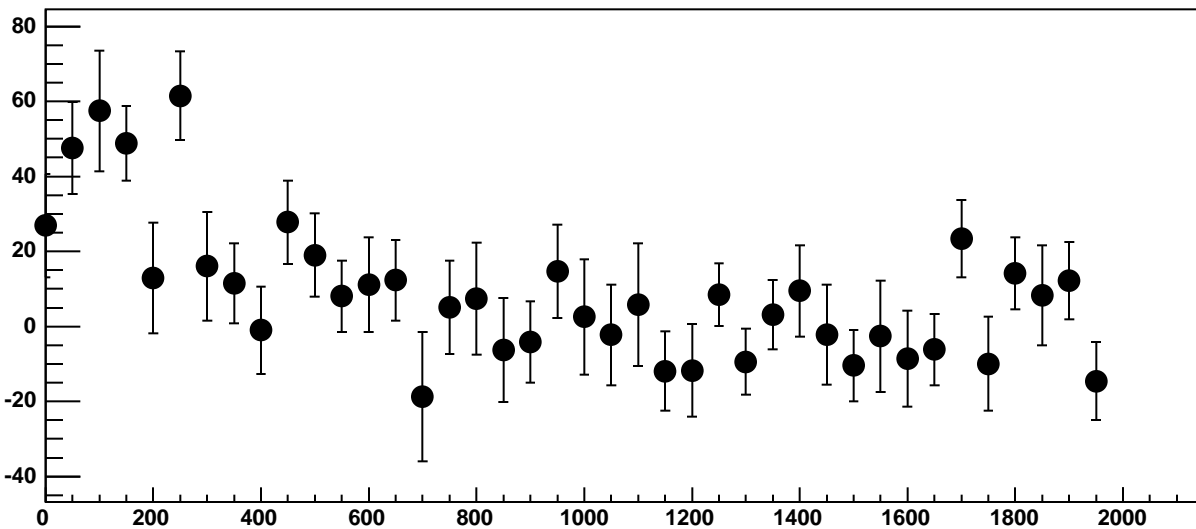
Chip 4, Channel 11, Enable 3, Hold=30, ADC Mean vs DAC



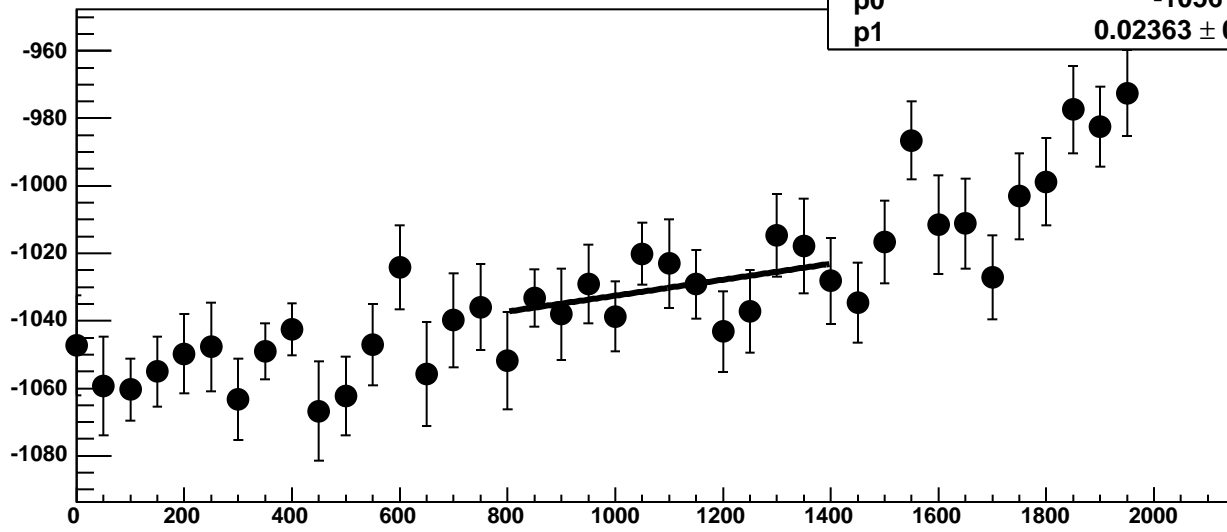
Chip 4, Channel 11, Enable 3, Hold=30, ADC Noise vs DAC



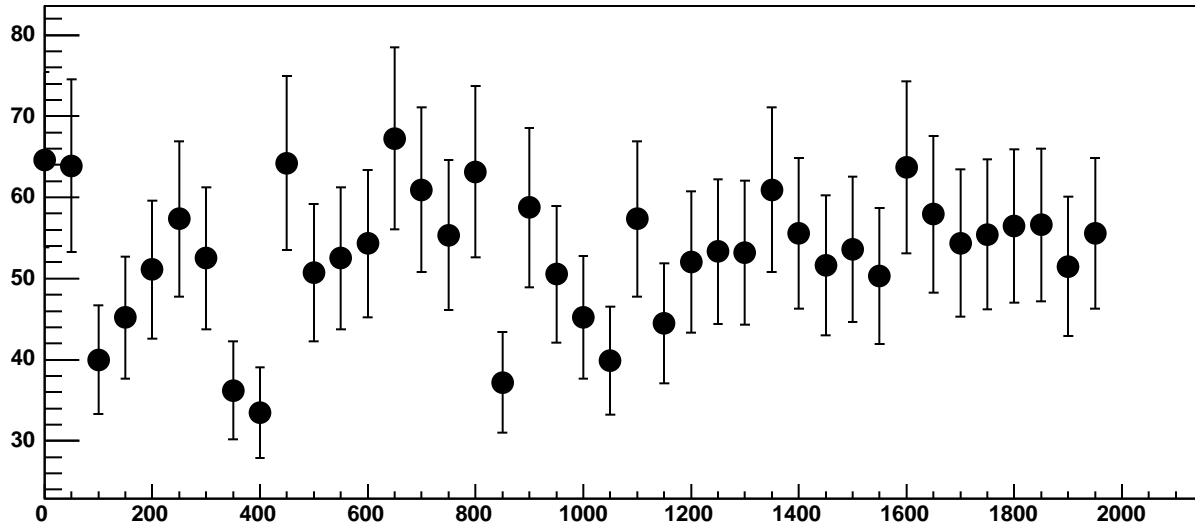
Chip 4, Channel 11, Enable 3, Hold=30, ADC Residuals vs DAC



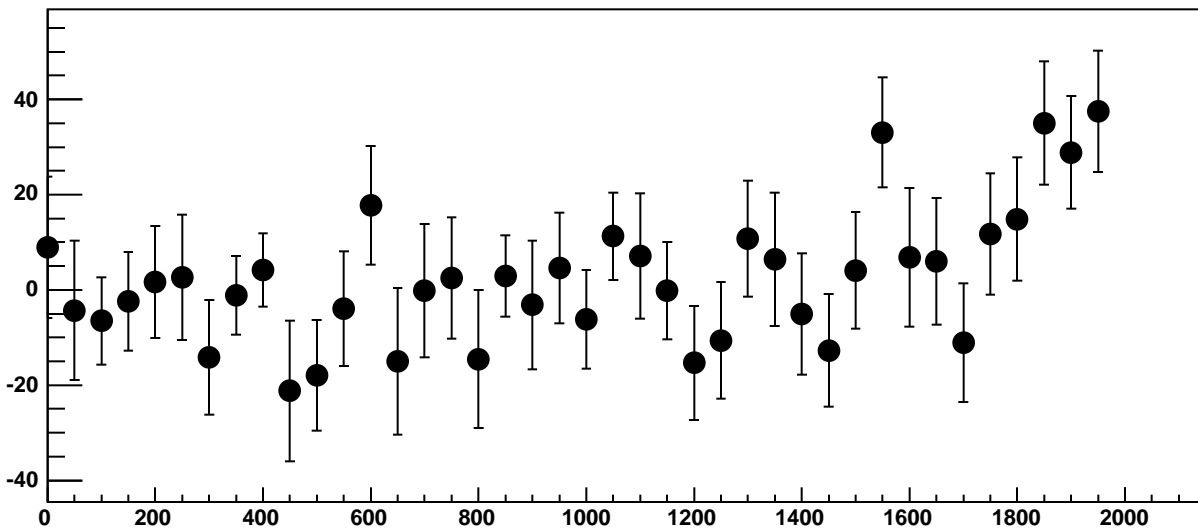
Chip 4, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC



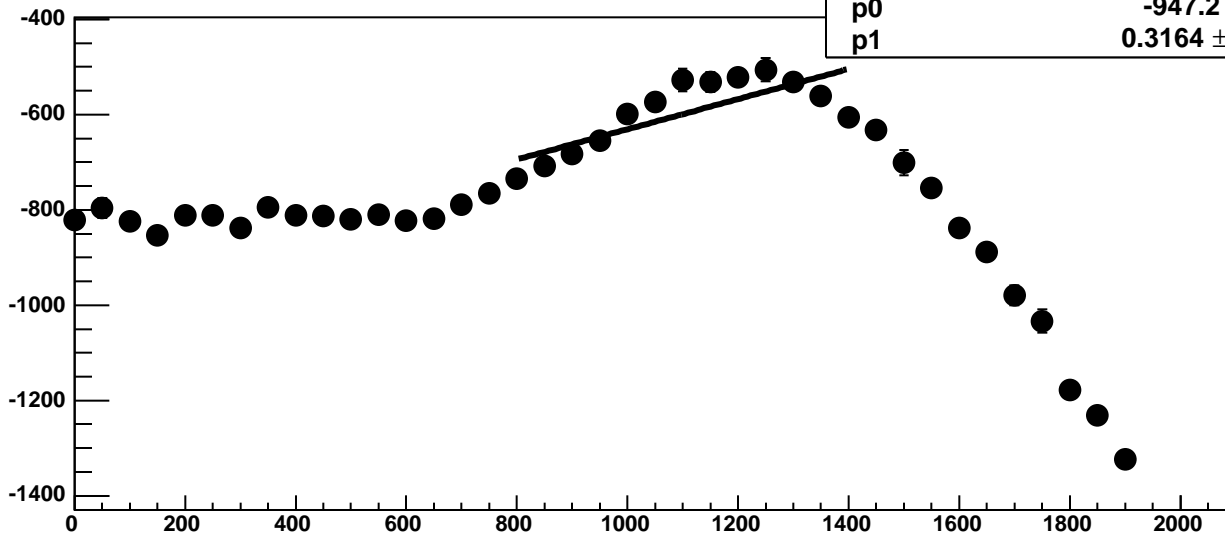
Chip 4, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC



Chip 4, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC



Chip 4, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

86.07 / 11

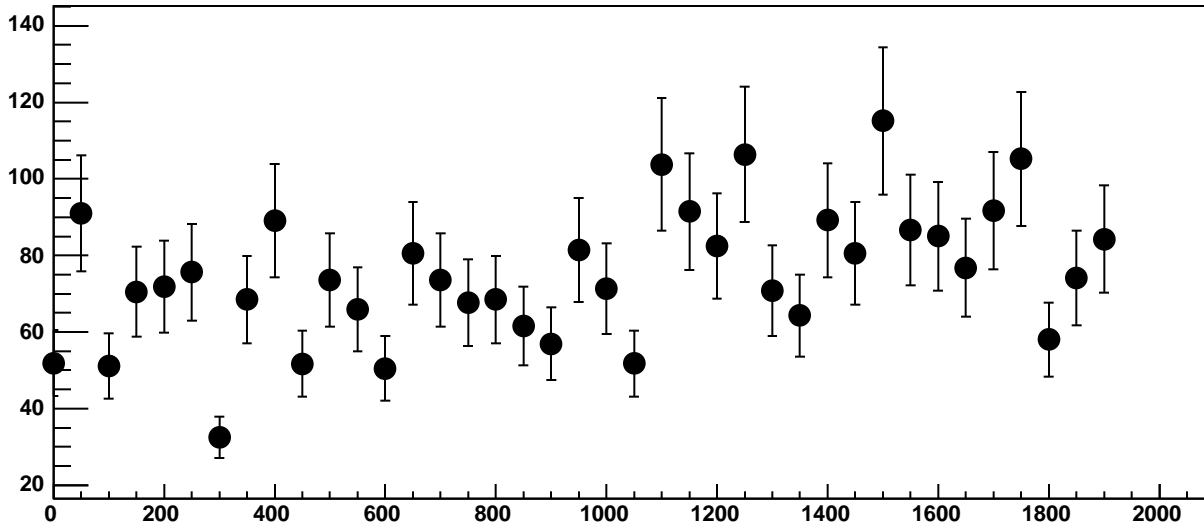
p0

$-947.2 \pm 26.25$

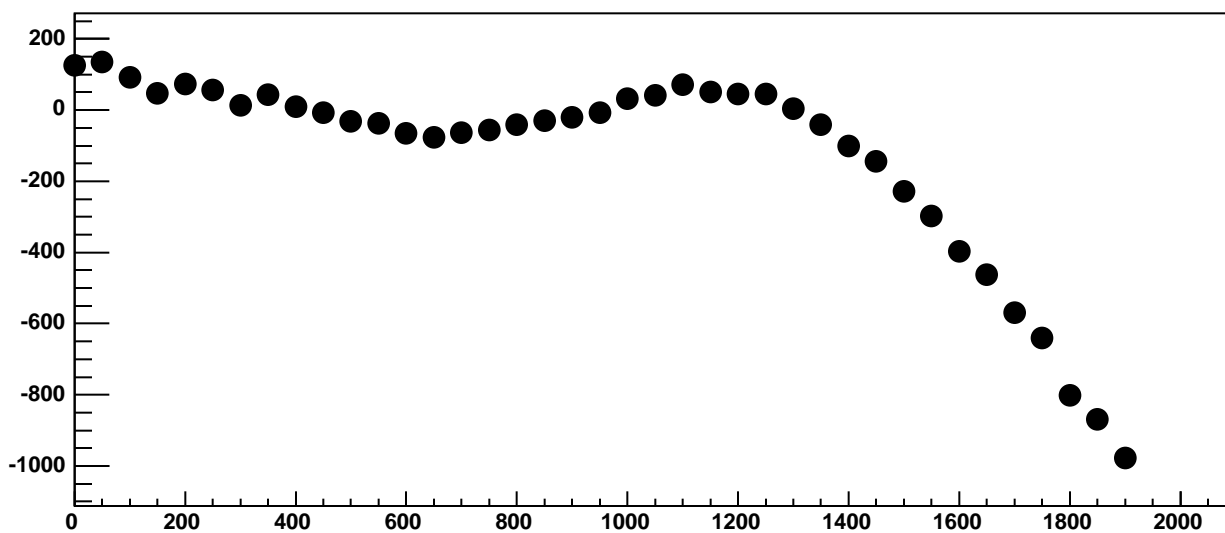
p1

$0.3164 \pm 0.0242$

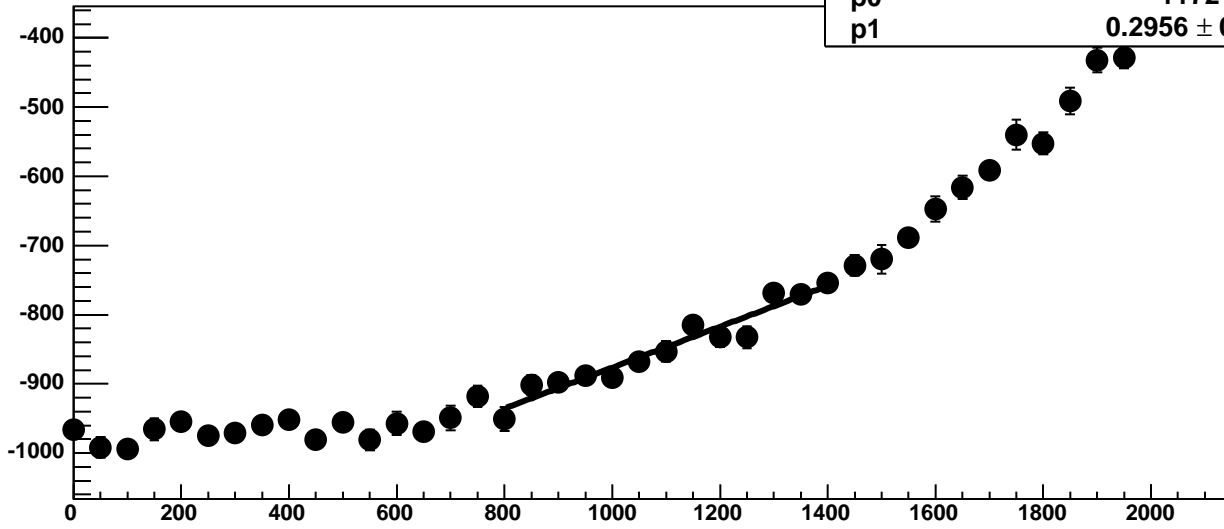
Chip 4, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC



Chip 4, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC

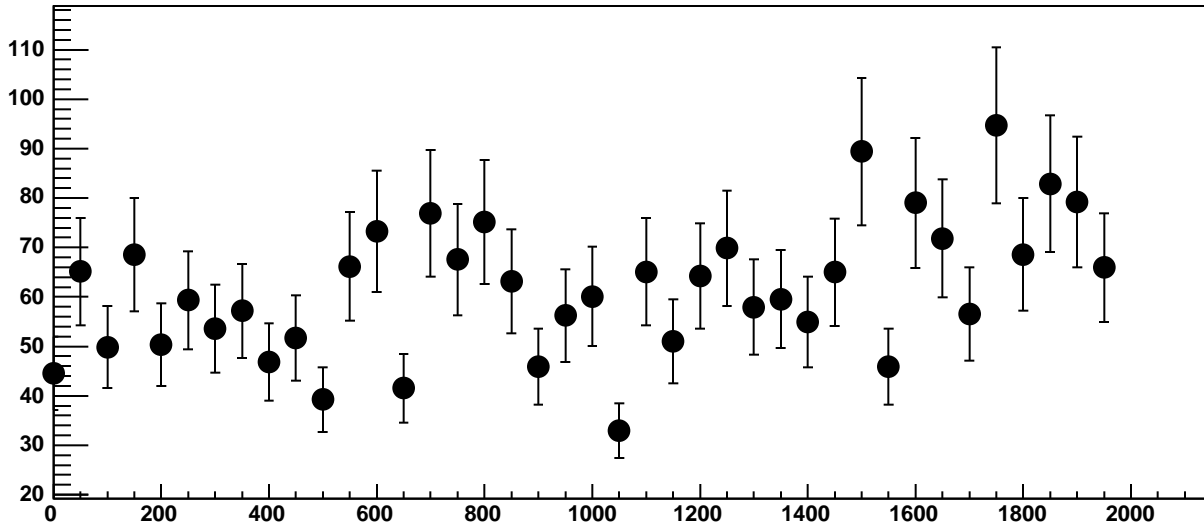


Chip 4, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC

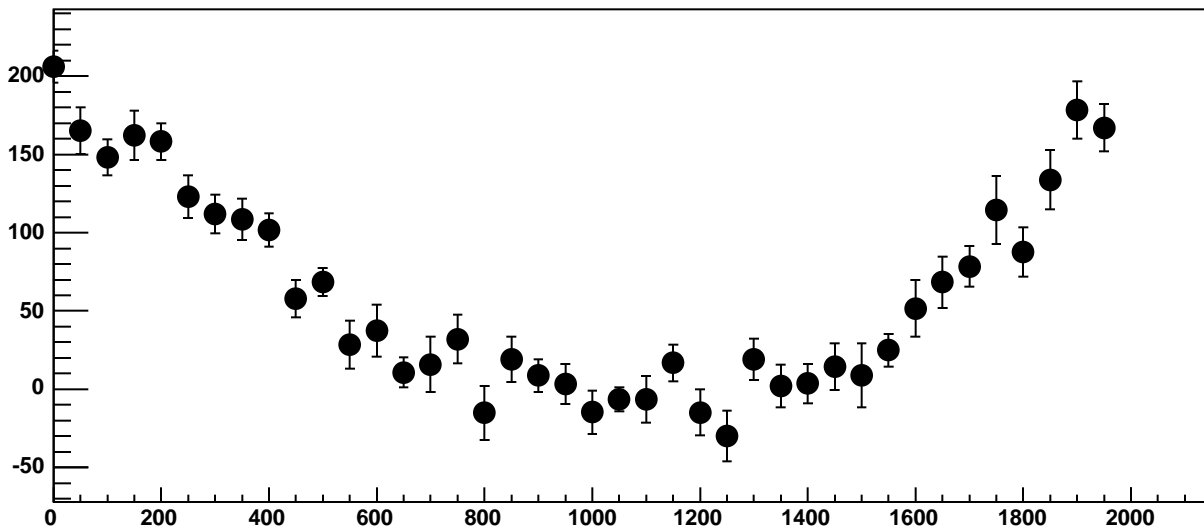


$\chi^2 / \text{ndf}$  14.04 / 11  
p0  $-1172 \pm 22.28$   
p1  $0.2956 \pm 0.02013$

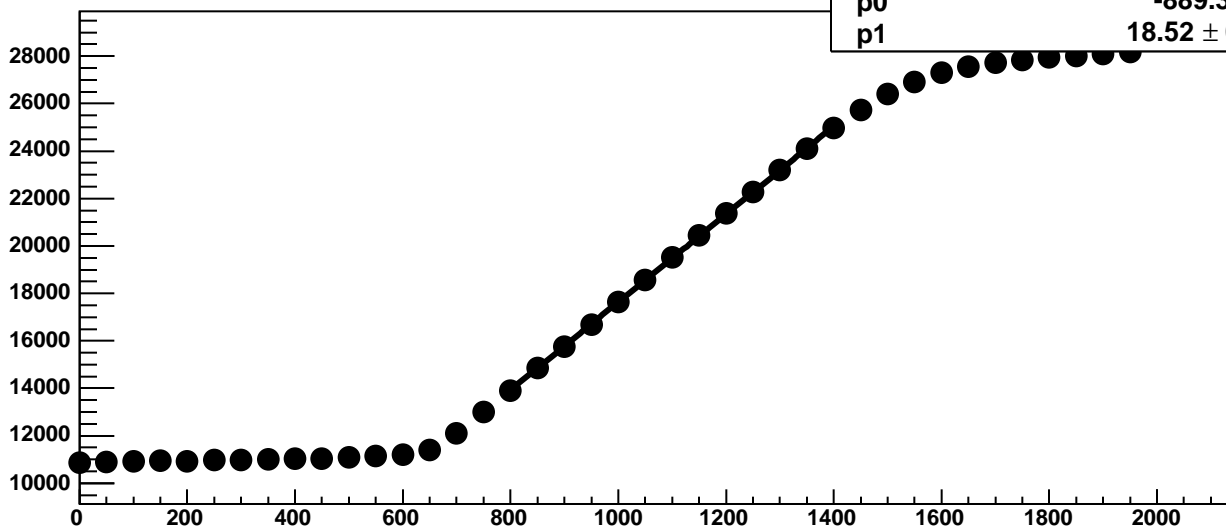
Chip 4, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC



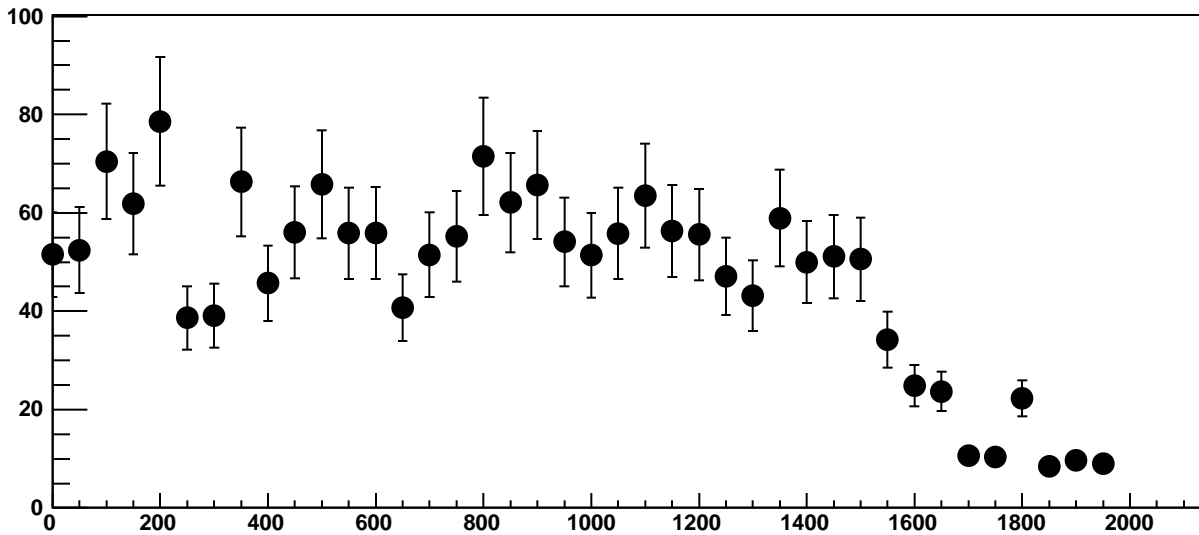
Chip 4, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC



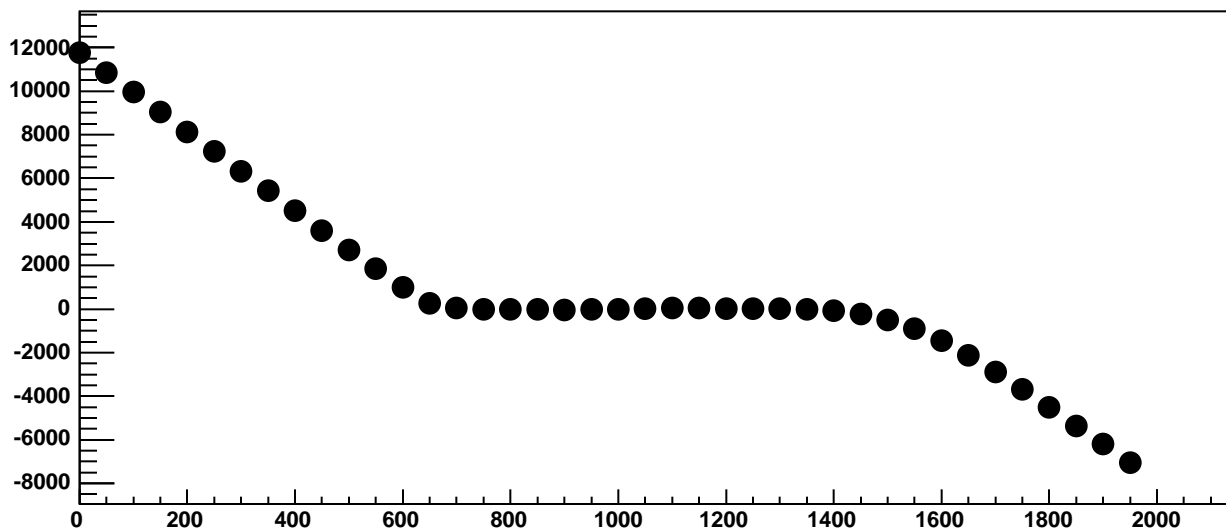
Chip 4, Channel 12, Enable 1!, Hold=30, ADC Mean vs DAC



Chip 4, Channel 12, Enable 1!, Hold=30, ADC Noise vs DAC

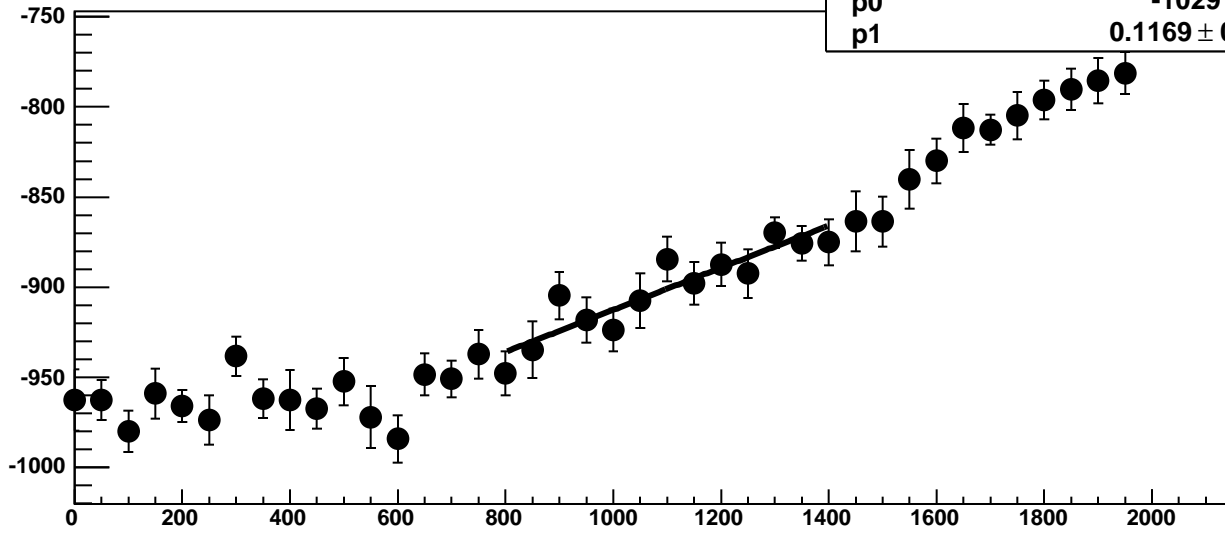


Chip 4, Channel 12, Enable 1!, Hold=30, ADC Residuals vs DAC



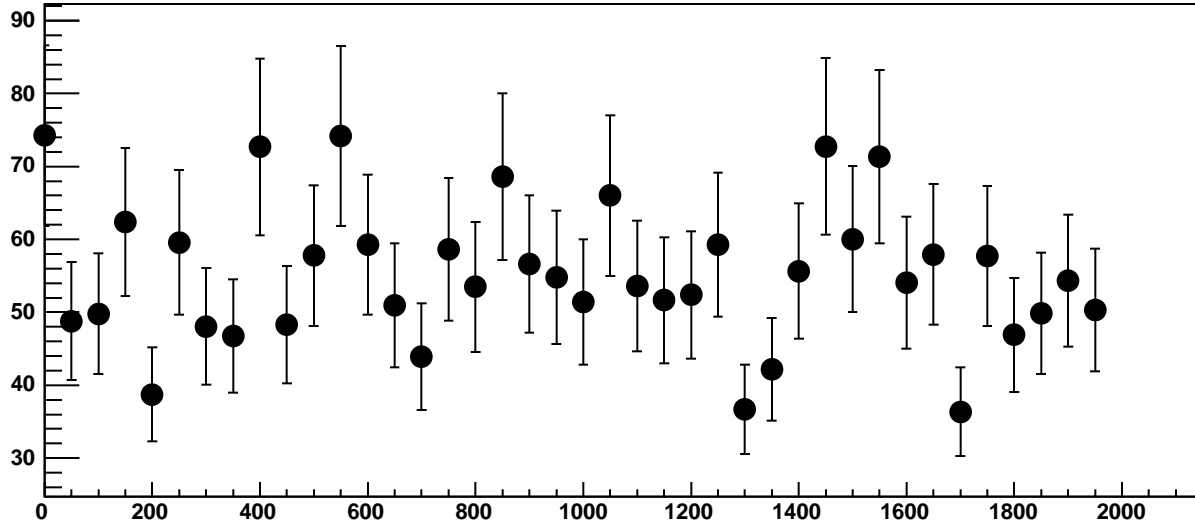


Chip 4, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC

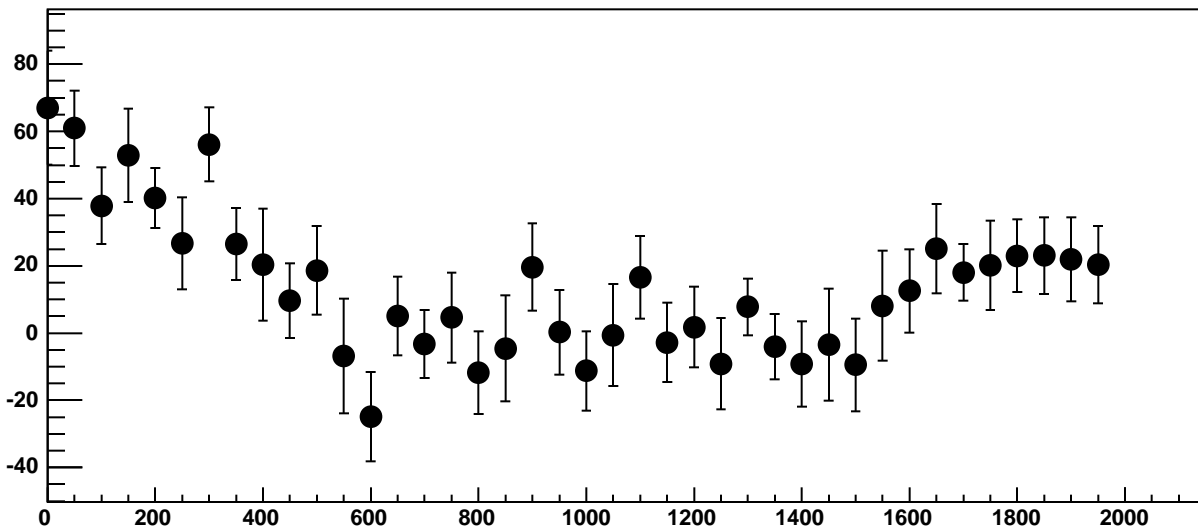


$\chi^2 / \text{ndf}$  8.065 / 11  
p0  $-1029 \pm 20.33$   
p1  $0.1169 \pm 0.01768$

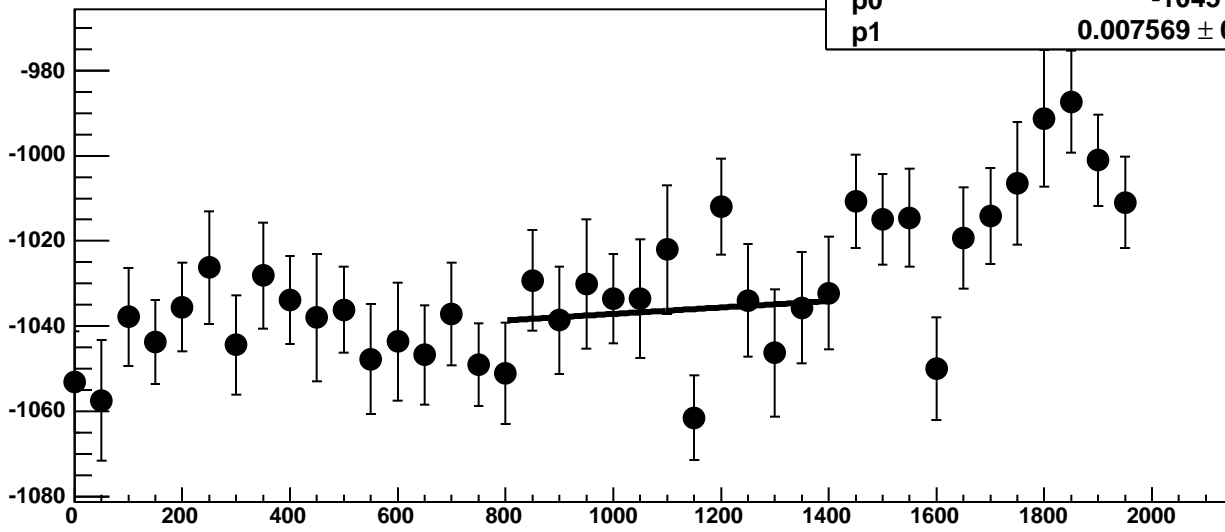
Chip 4, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC



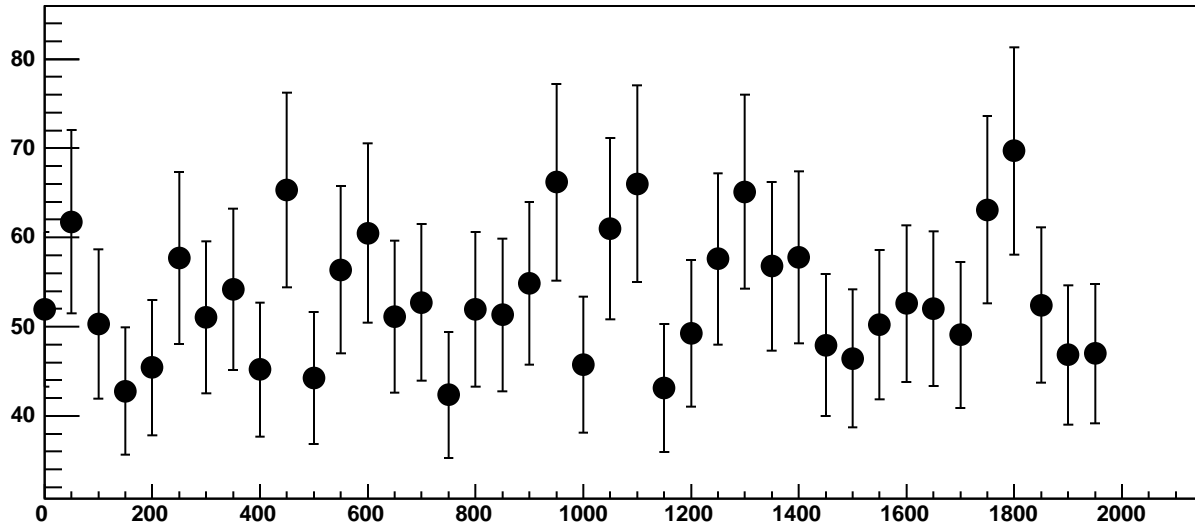
Chip 4, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC



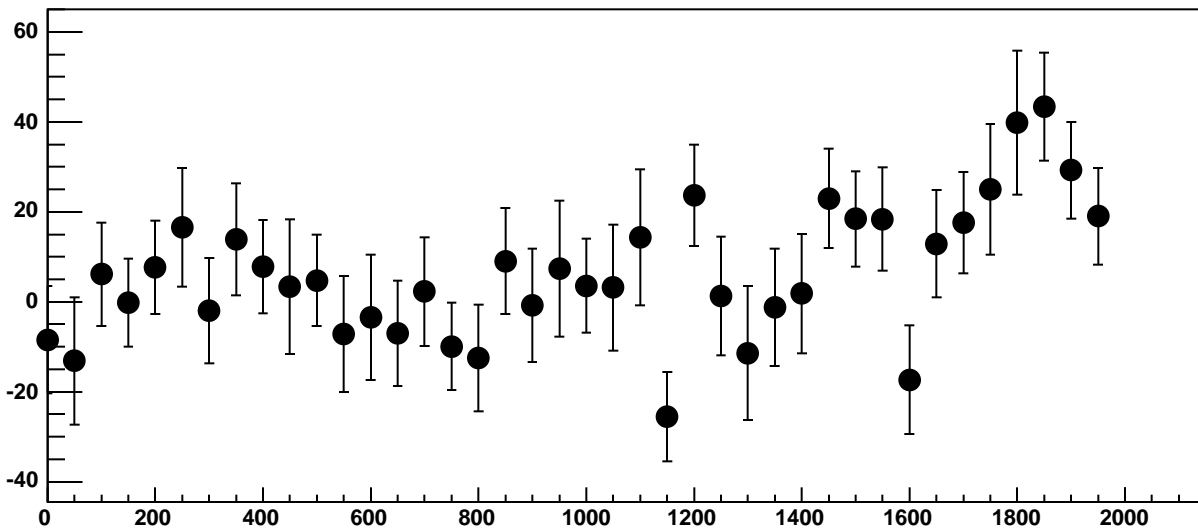
Chip 4, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC



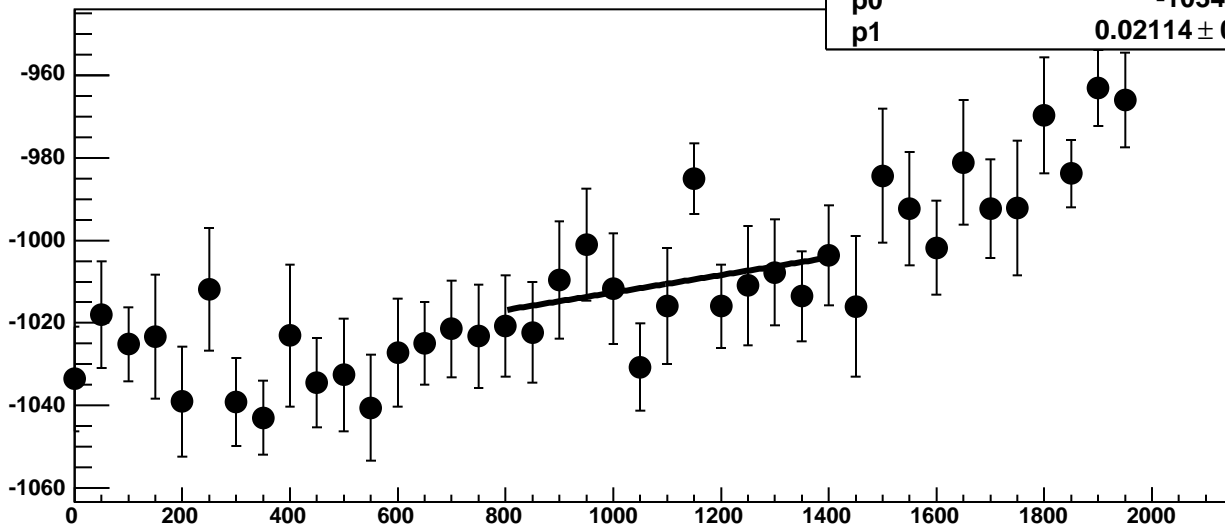
Chip 4, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC



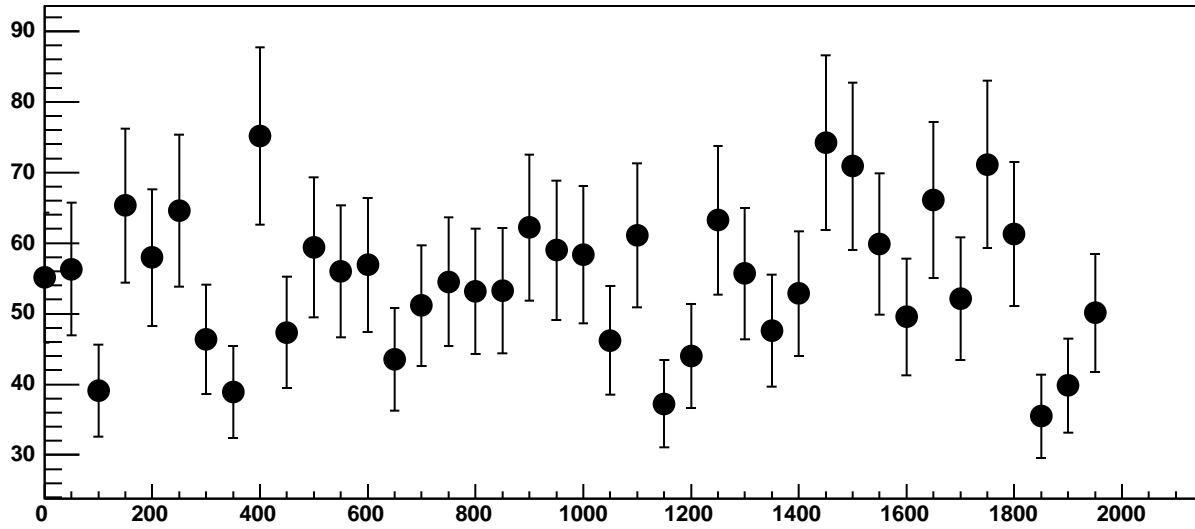
Chip 4, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC



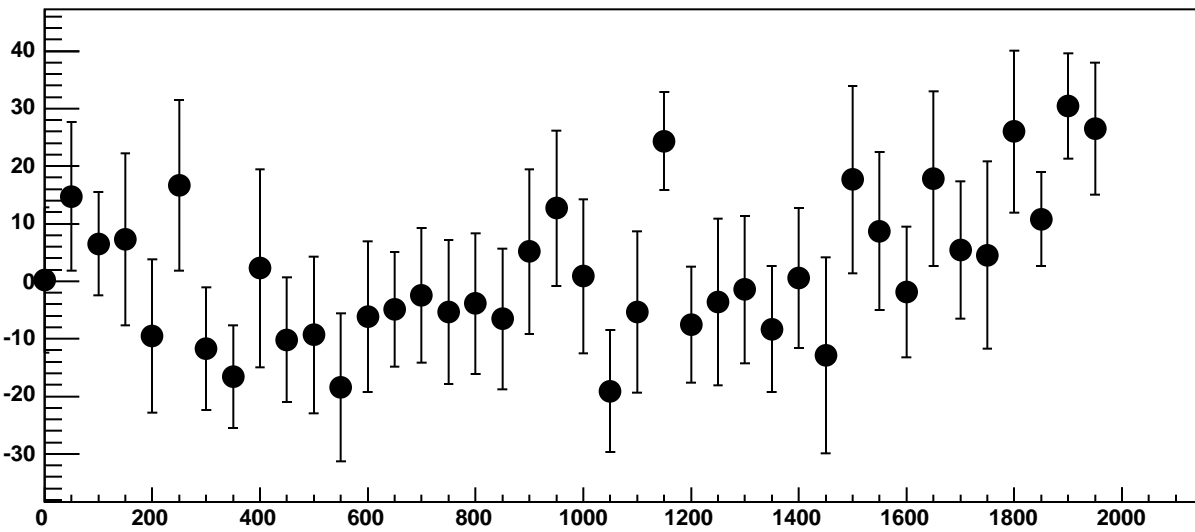
Chip 4, Channel 12, Enable 4, Hold=30, ADC Mean vs DAC



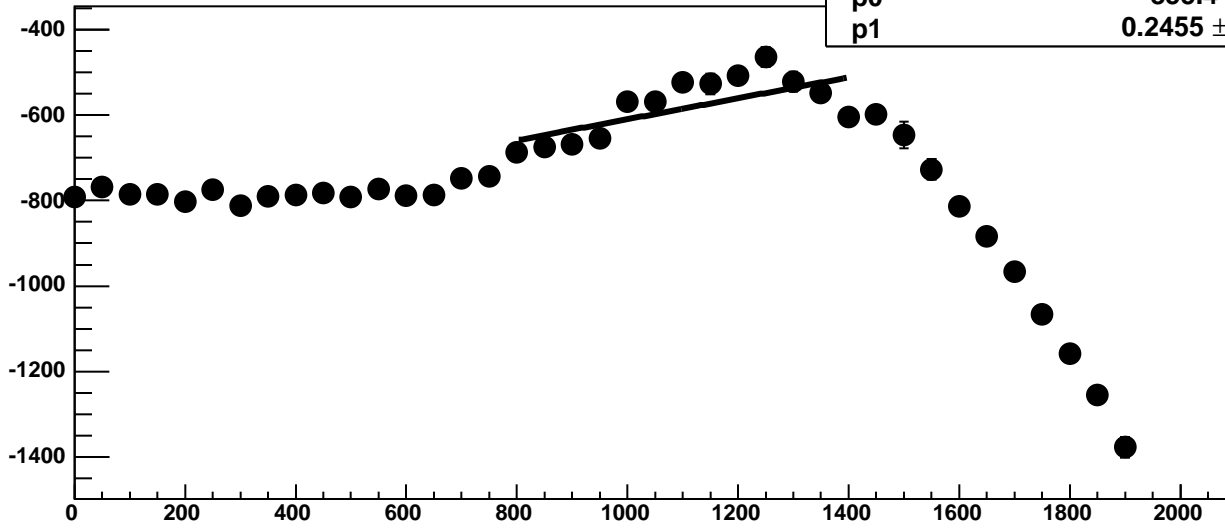
Chip 4, Channel 12, Enable 4, Hold=30, ADC Noise vs DAC



Chip 4, Channel 12, Enable 4, Hold=30, ADC Residuals vs DAC

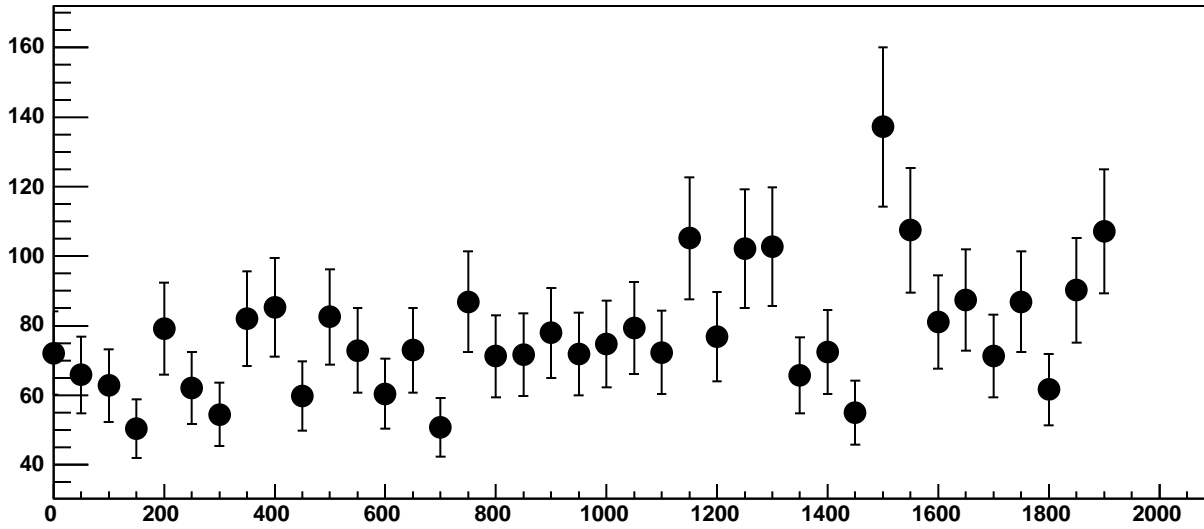


Chip 4, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC

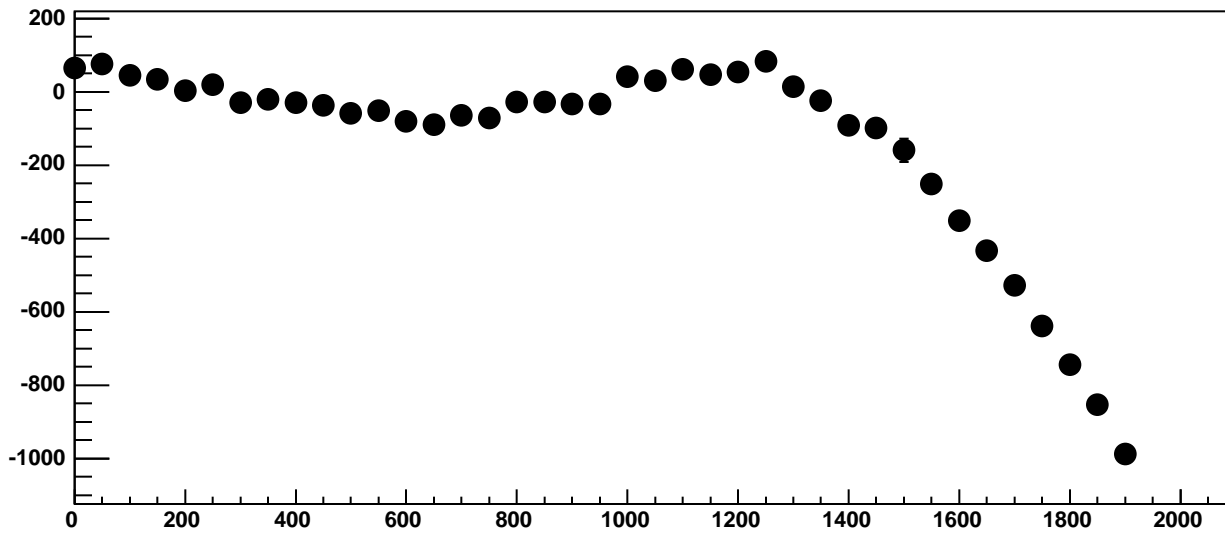


$\chi^2 / \text{ndf}$  94.81 / 11  
p0  $-855.4 \pm 27.96$   
p1  $0.2455 \pm 0.0253$

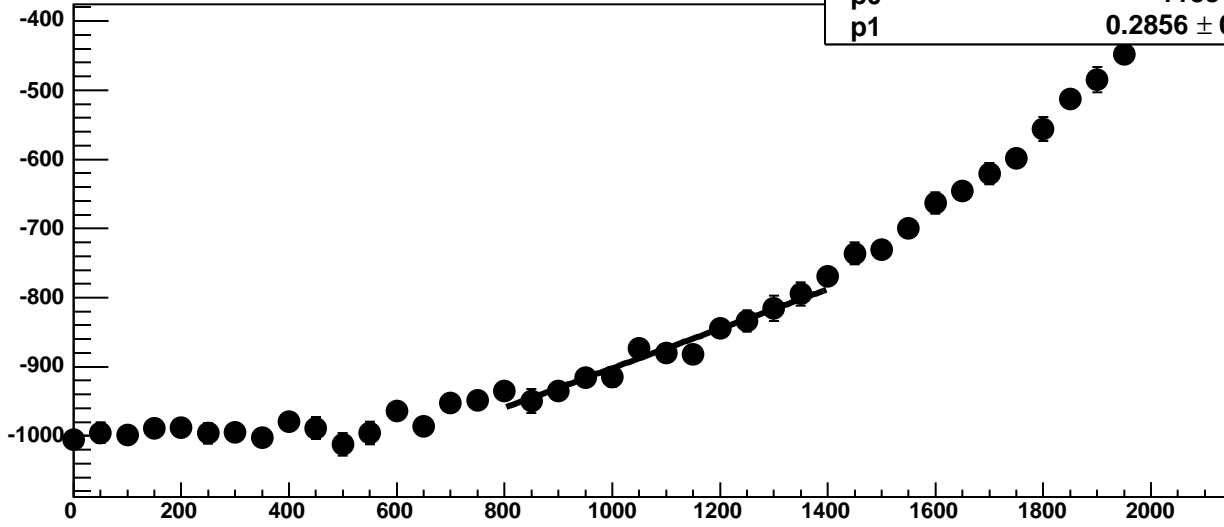
Chip 4, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC



Chip 4, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC

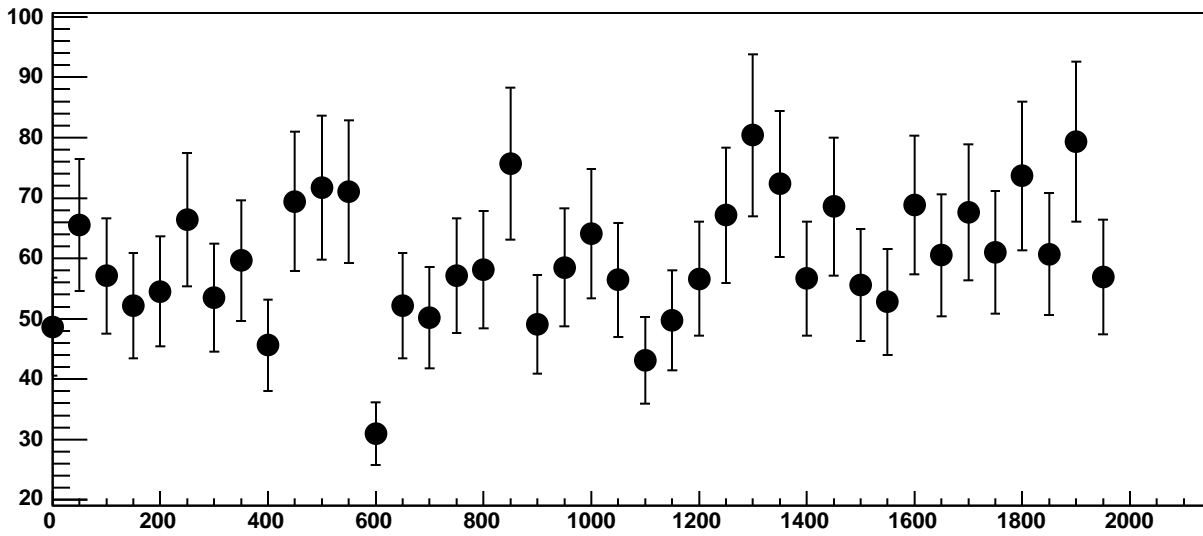


Chip 4, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC

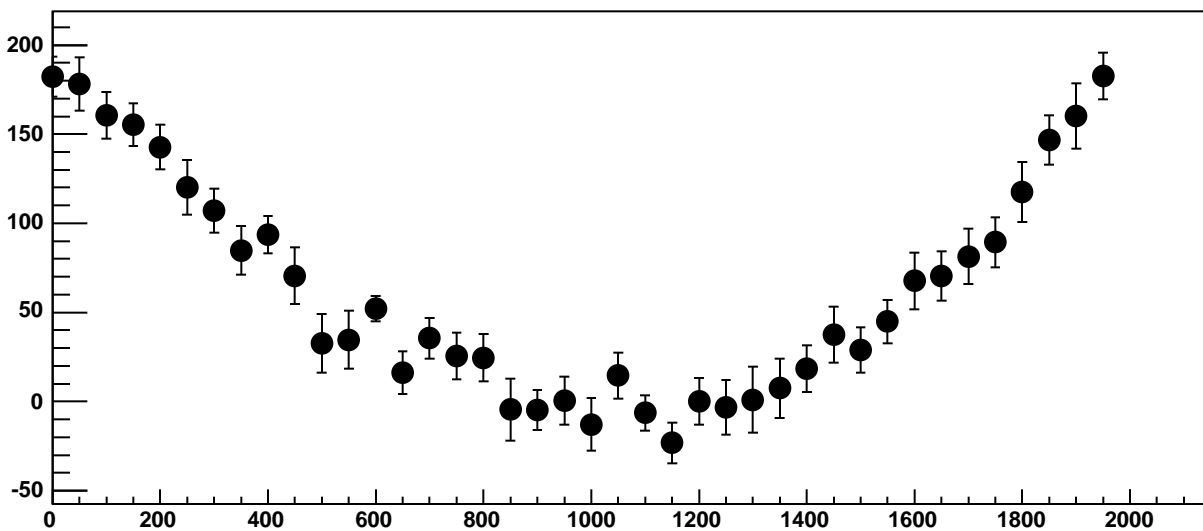


$\chi^2 / \text{ndf}$  12.42 / 11  
p0  $-1188 \pm 23.22$   
p1  $0.2856 \pm 0.02105$

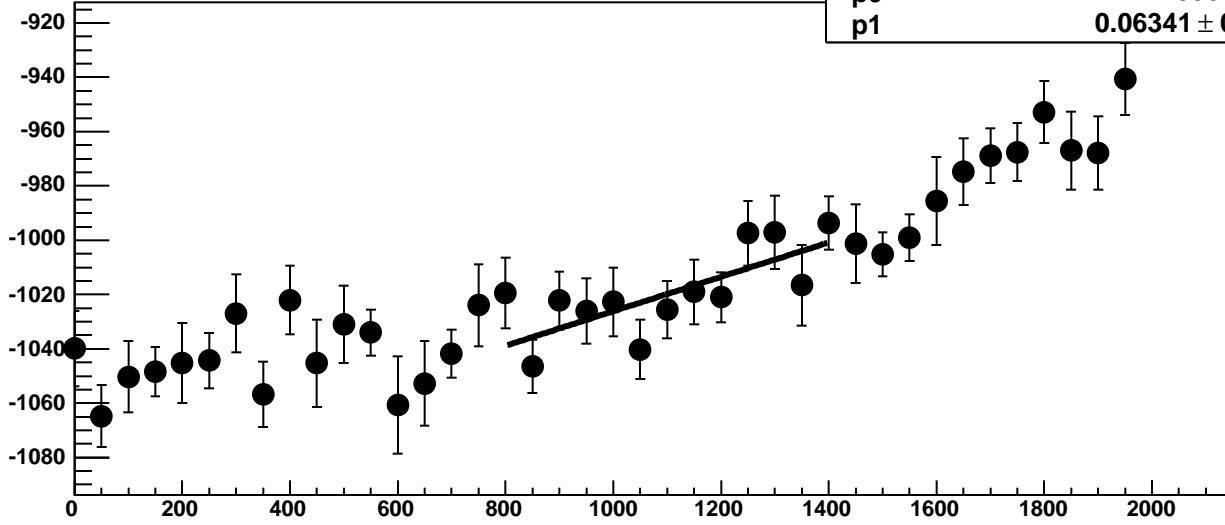
Chip 4, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC



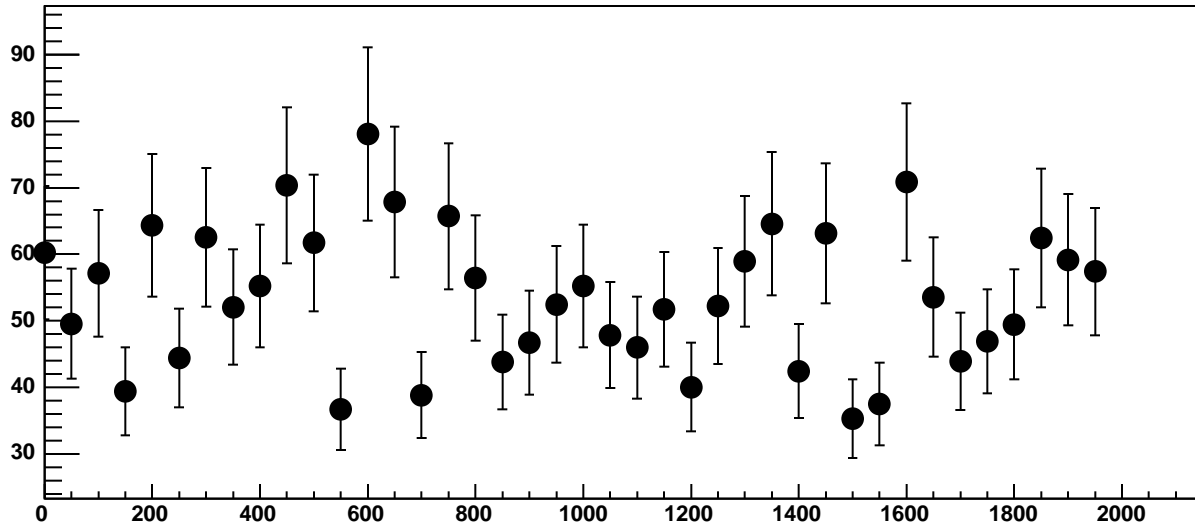
Chip 4, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC



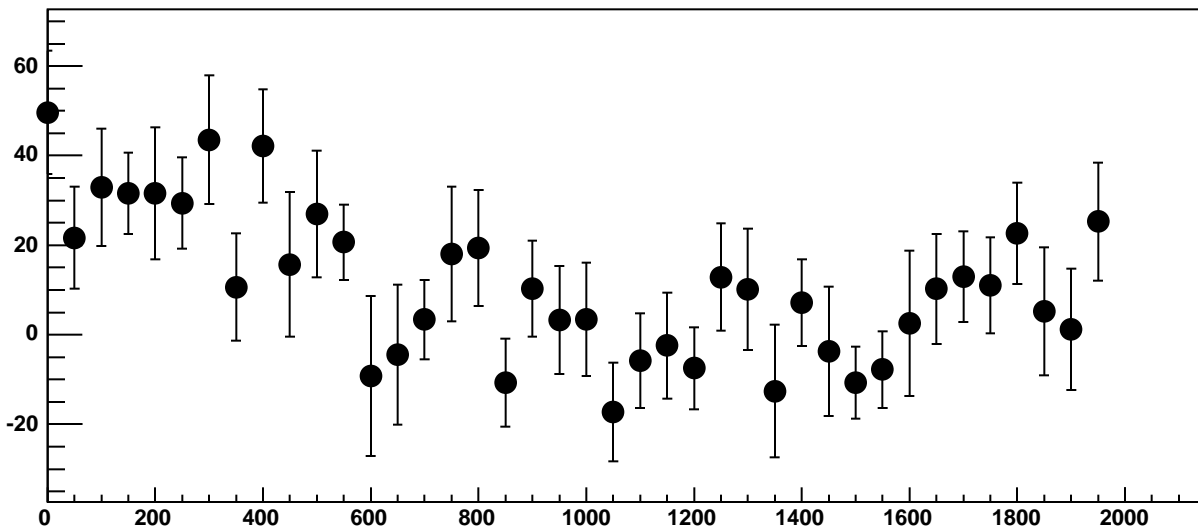
Chip 4, Channel 13, Enable 1, Hold=30, ADC Mean vs DAC



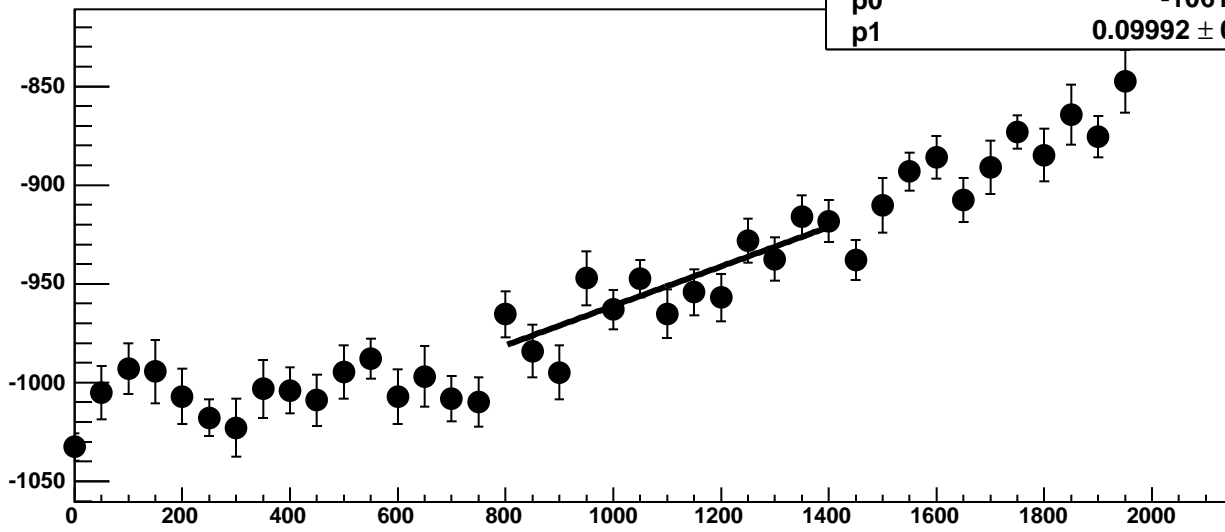
Chip 4, Channel 13, Enable 1, Hold=30, ADC Noise vs DAC



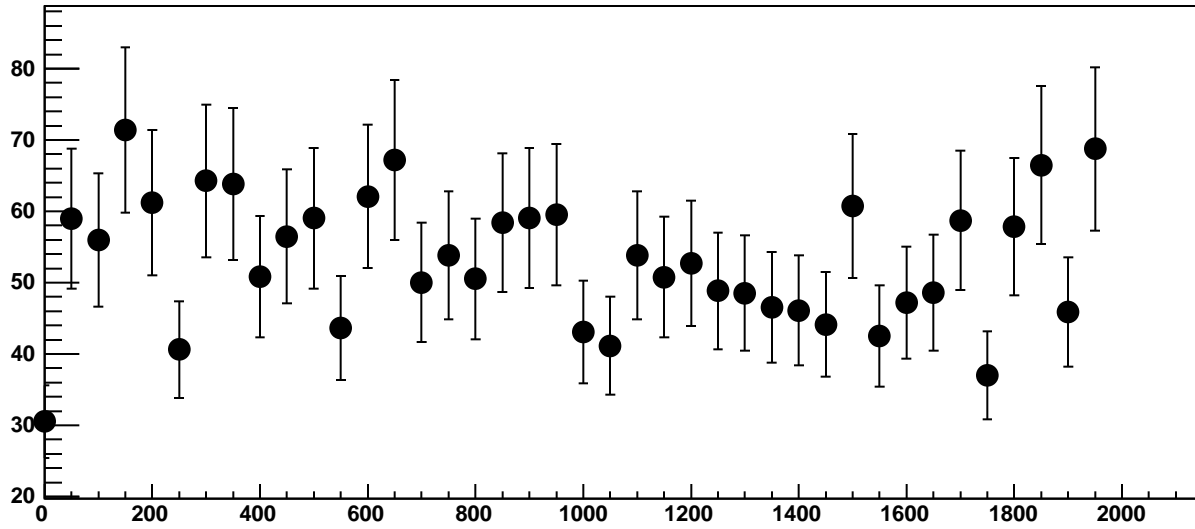
Chip 4, Channel 13, Enable 1, Hold=30, ADC Residuals vs DAC



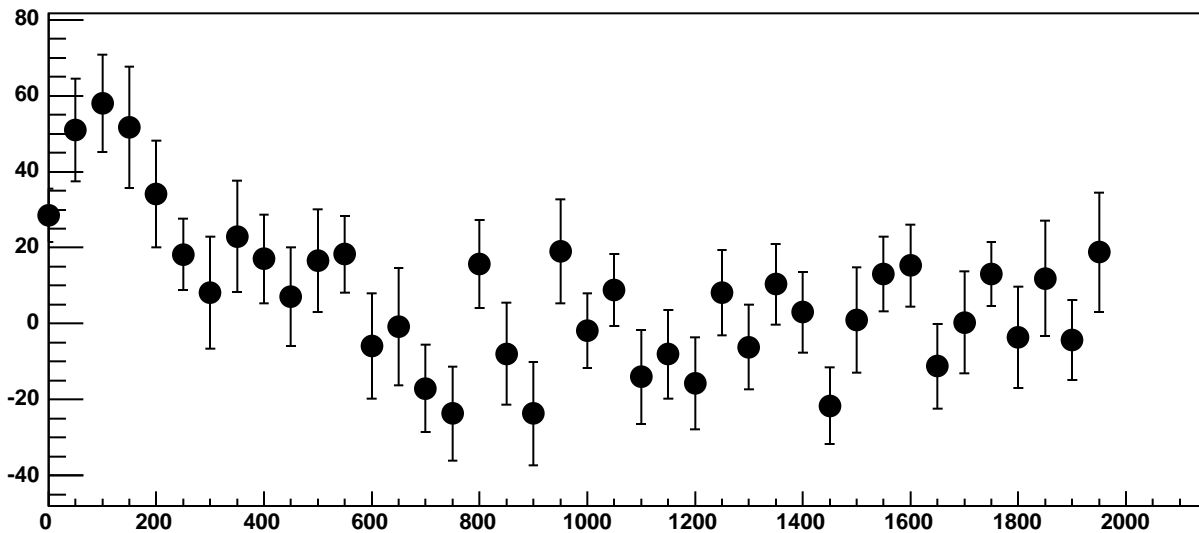
Chip 4, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC



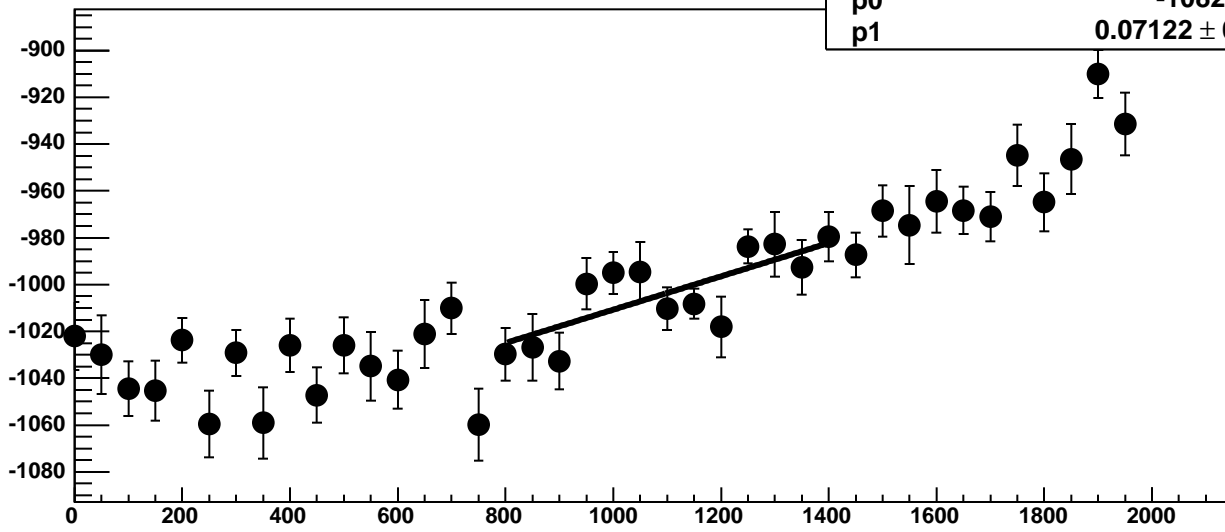
Chip 4, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC



Chip 4, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC

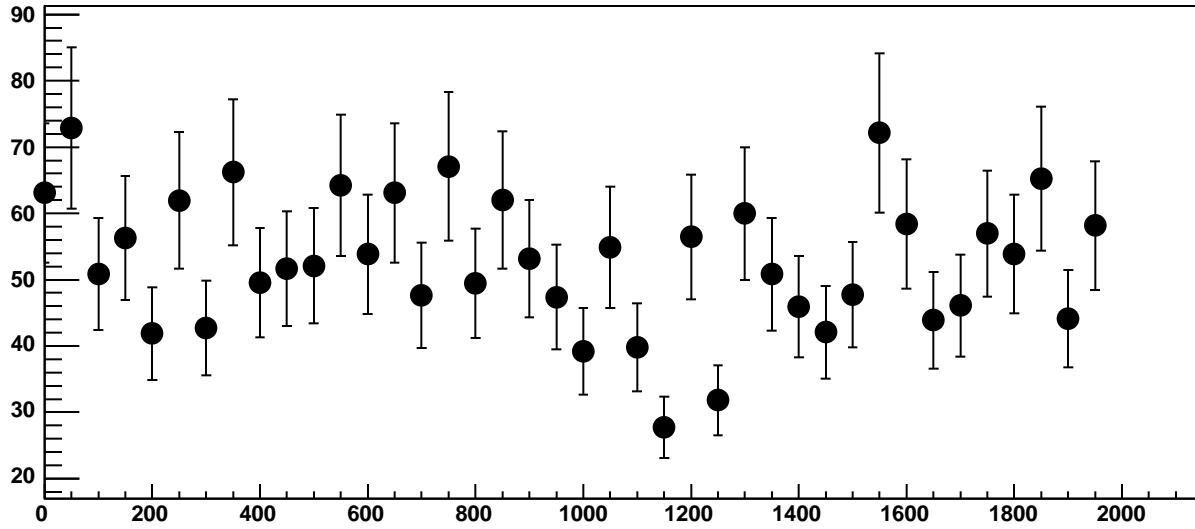


Chip 4, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC

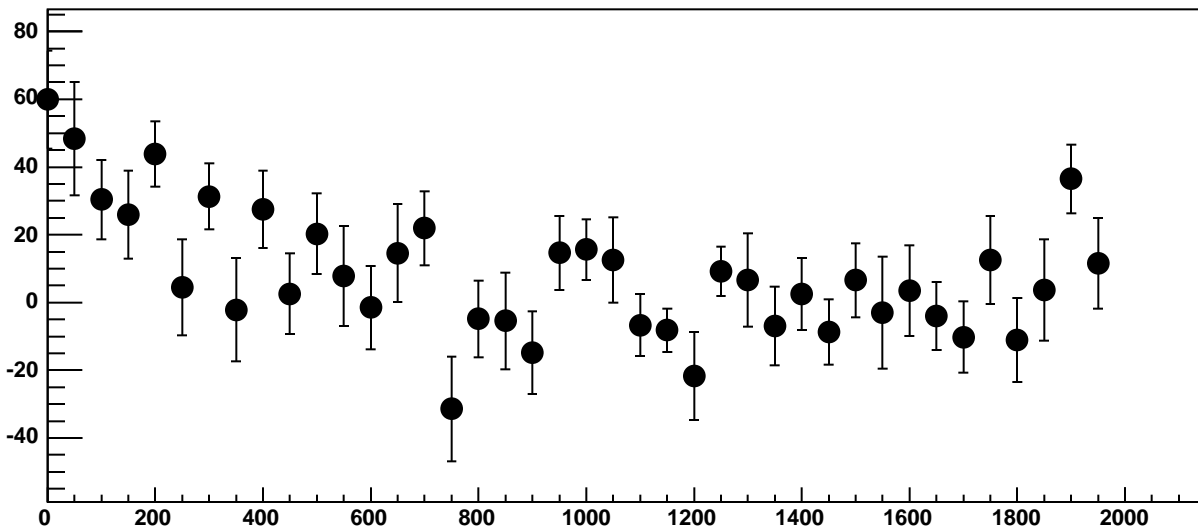


$\chi^2 / \text{ndf}$  14.86 / 11  
p0  $-1082 \pm 18.91$   
p1  $0.07122 \pm 0.01671$

Chip 4, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC

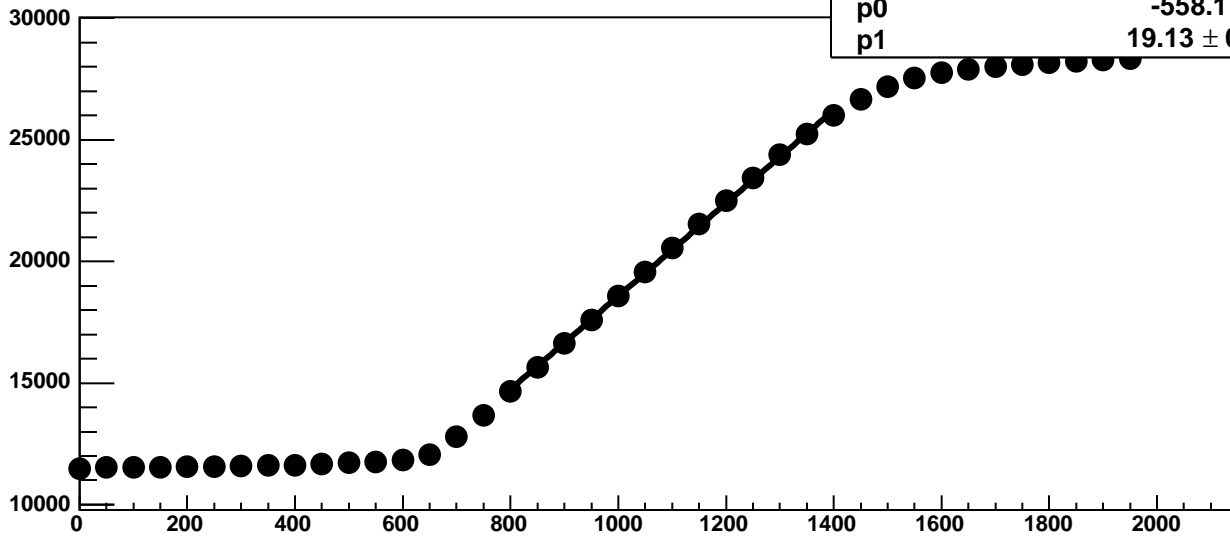


Chip 4, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC



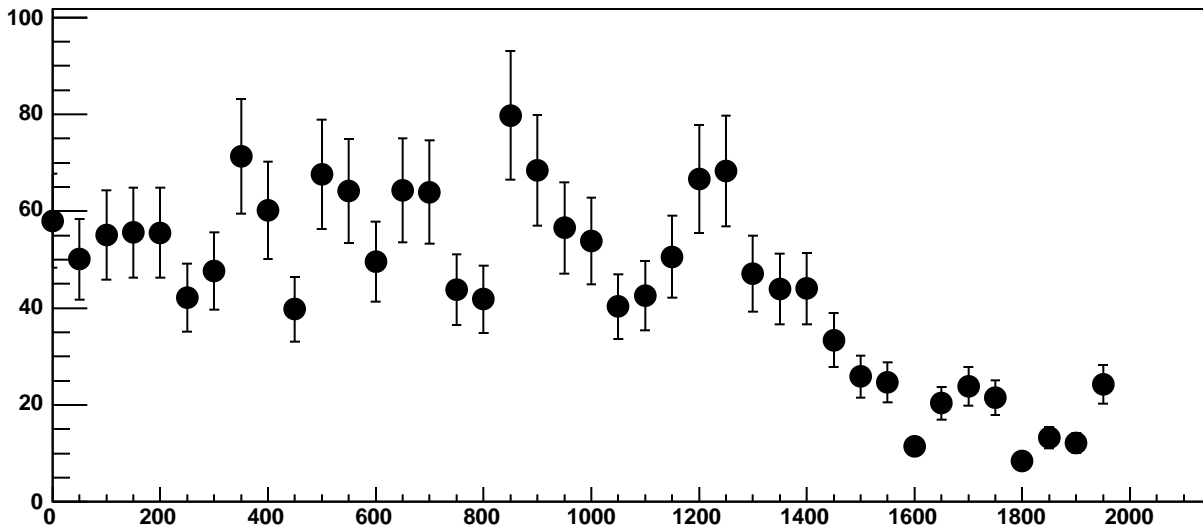


Chip 4, Channel 13, Enable 4!, Hold=30, ADC Mean vs DAC

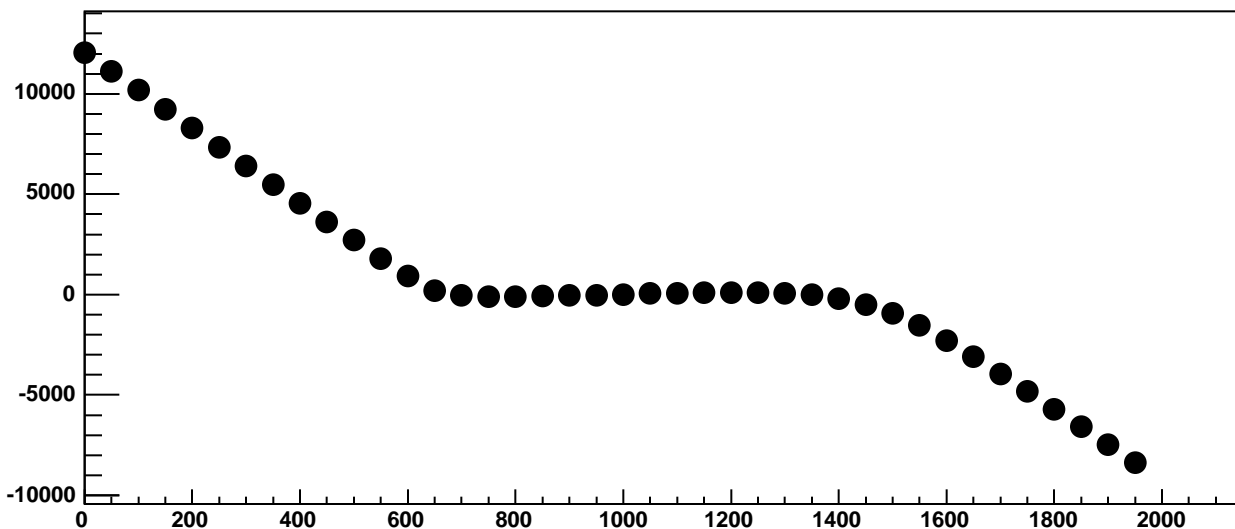


$\chi^2 / \text{ndf}$  774.6 / 11  
p0  $-558.1 \pm 19.09$   
p1  $19.13 \pm 0.01685$

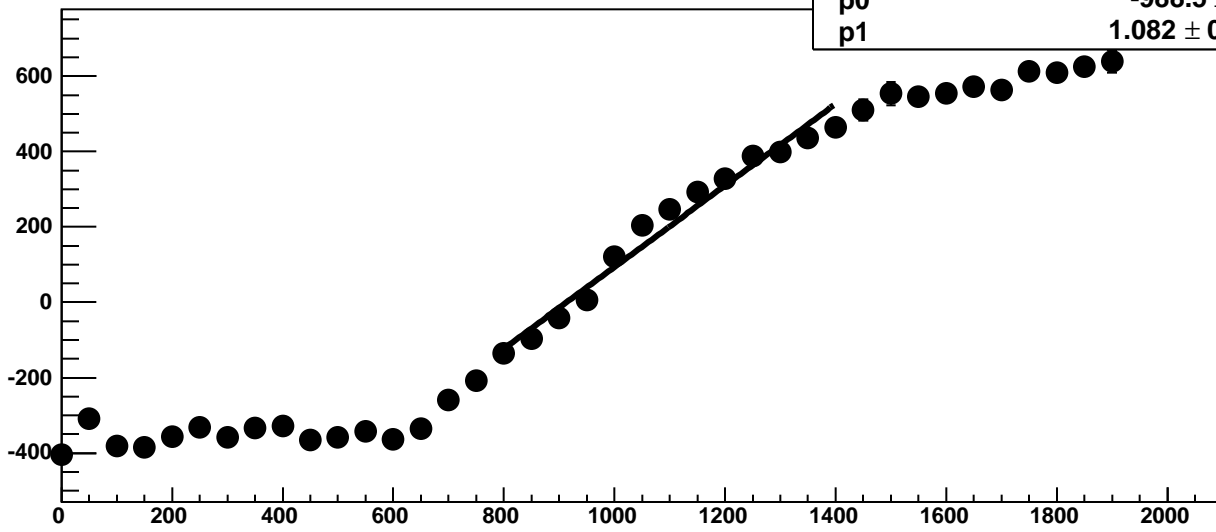
Chip 4, Channel 13, Enable 4!, Hold=30, ADC Noise vs DAC



Chip 4, Channel 13, Enable 4!, Hold=30, ADC Residuals vs DAC

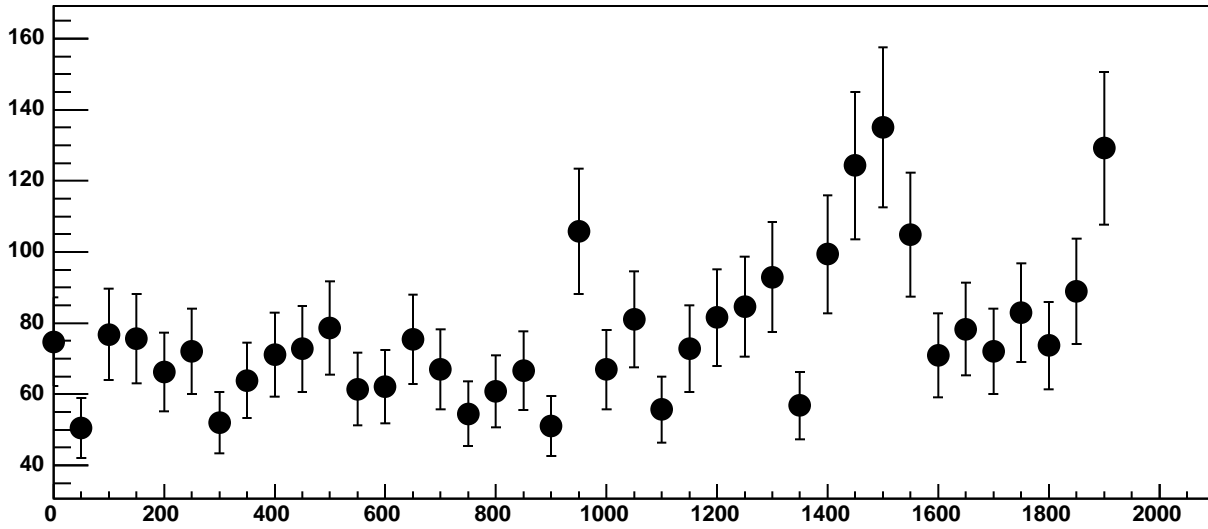


Chip 4, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC

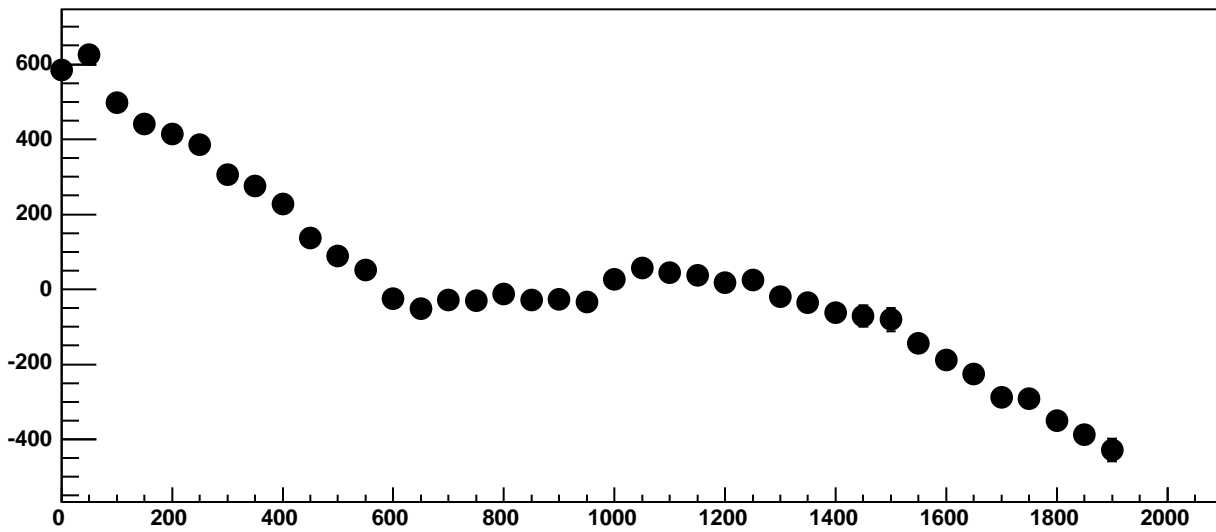


$\chi^2 / \text{ndf}$  59.54 / 11  
p0  $-988.5 \pm 25.66$   
p1  $1.082 \pm 0.02363$

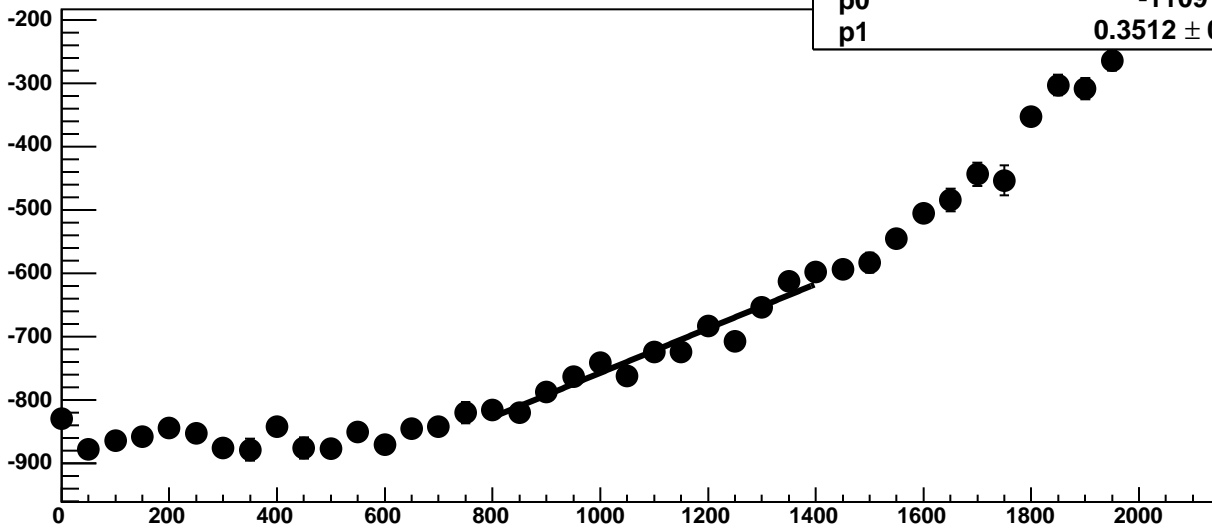
Chip 4, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC



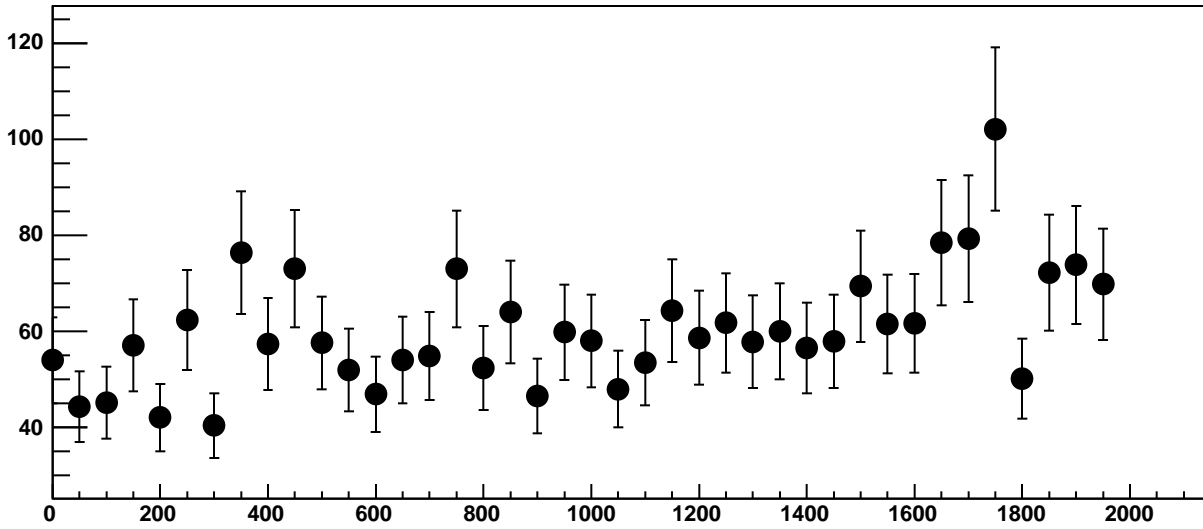
Chip 4, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC



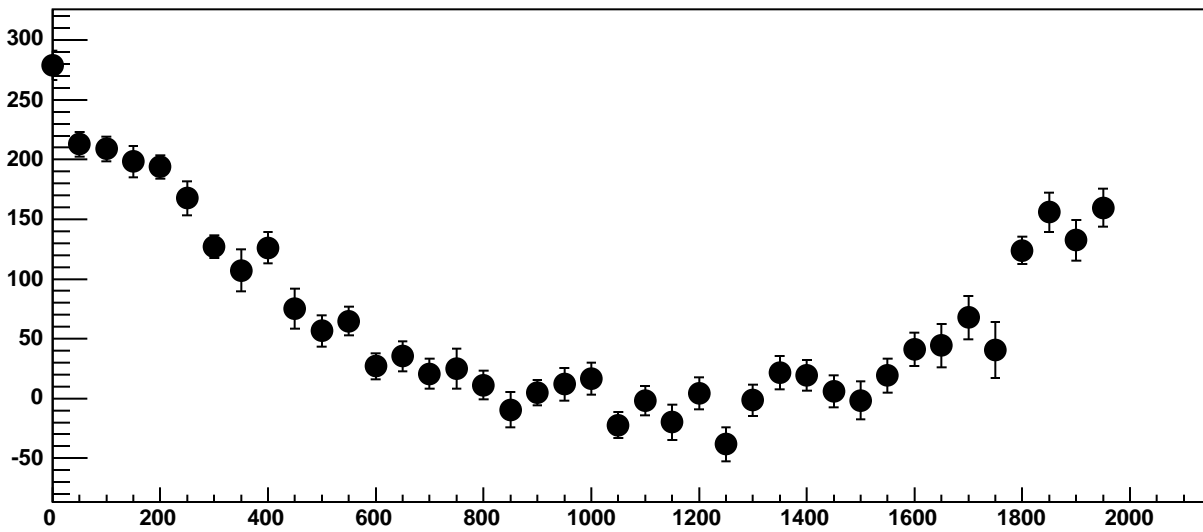
Chip 4, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC



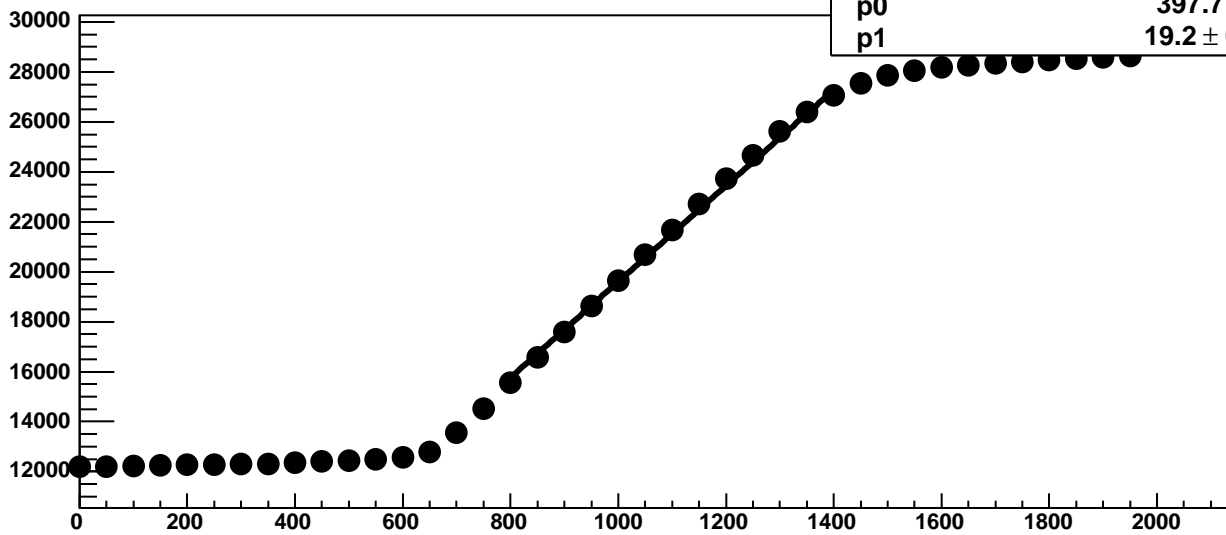
Chip 4, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC



Chip 4, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC

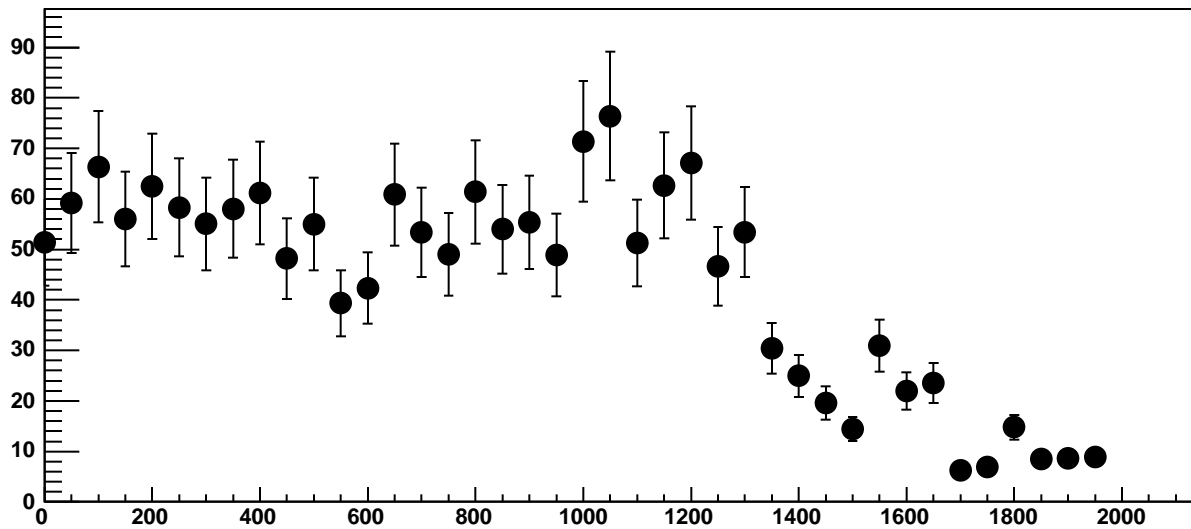


Chip 4, Channel 14, Enable 1!, Hold=30, ADC Mean vs DAC

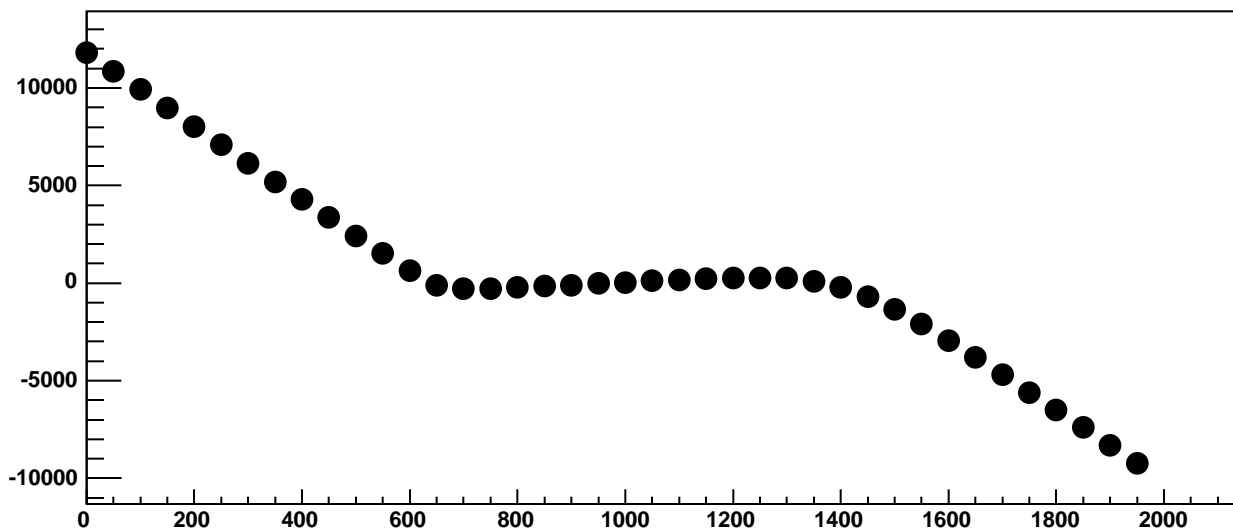


$\chi^2 / \text{ndf}$	3863 / 11
p0	$397.7 \pm 17.59$
p1	$19.2 \pm 0.01447$

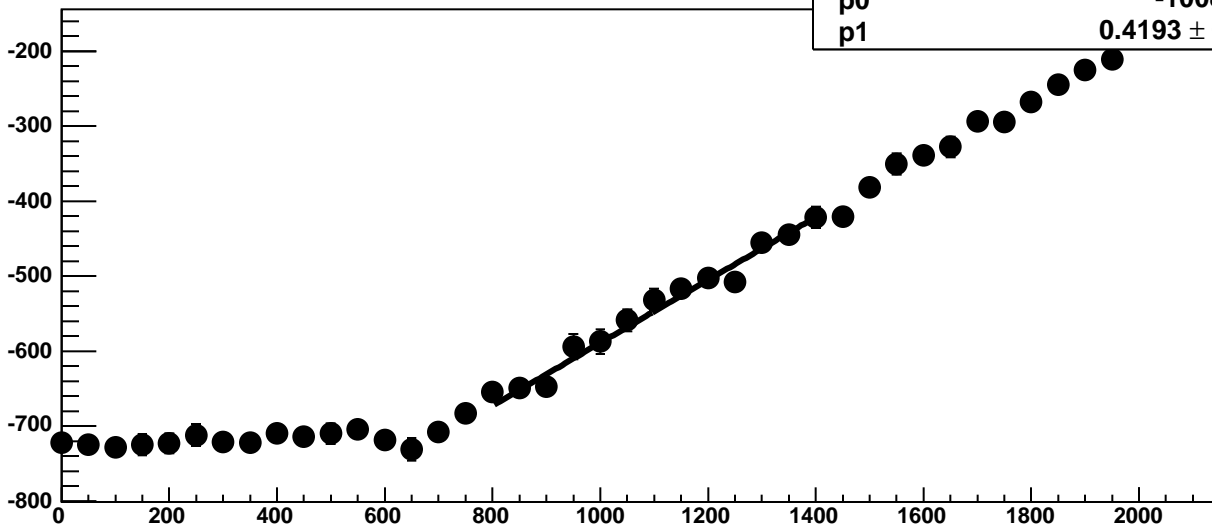
Chip 4, Channel 14, Enable 1!, Hold=30, ADC Noise vs DAC



Chip 4, Channel 14, Enable 1!, Hold=30, ADC Residuals vs DAC

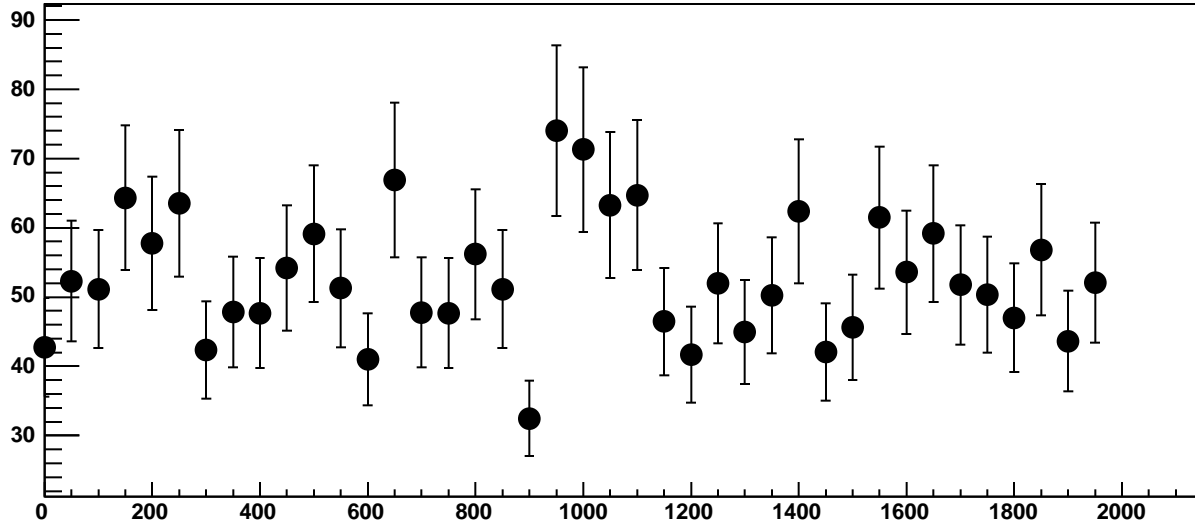


Chip 4, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC

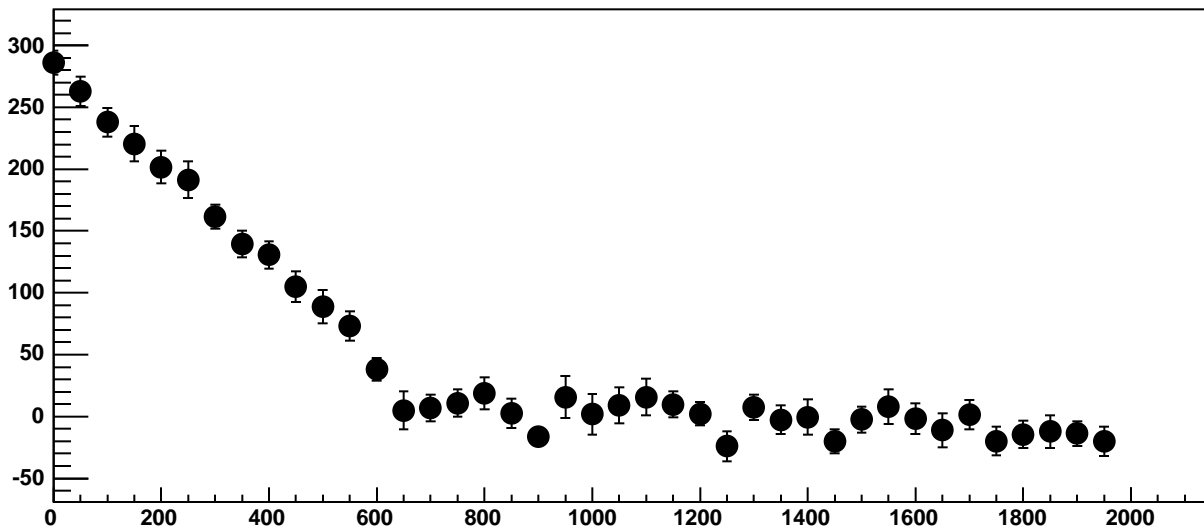


$\chi^2 / \text{ndf}$  14.98 / 11  
p0  $-1008 \pm 19$   
p1  $0.4193 \pm 0.01711$

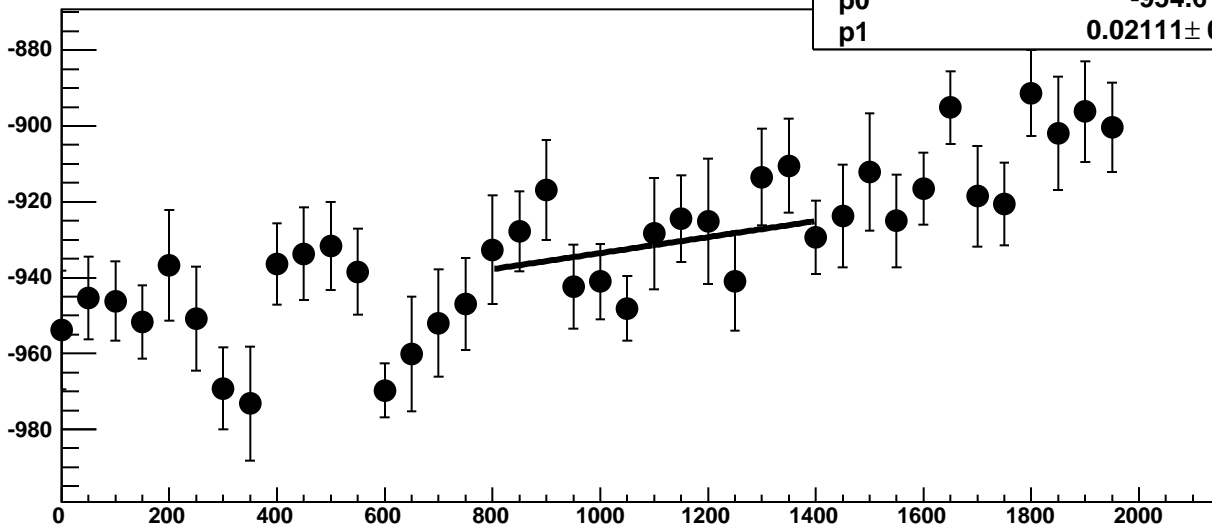
Chip 4, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC



Chip 4, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC

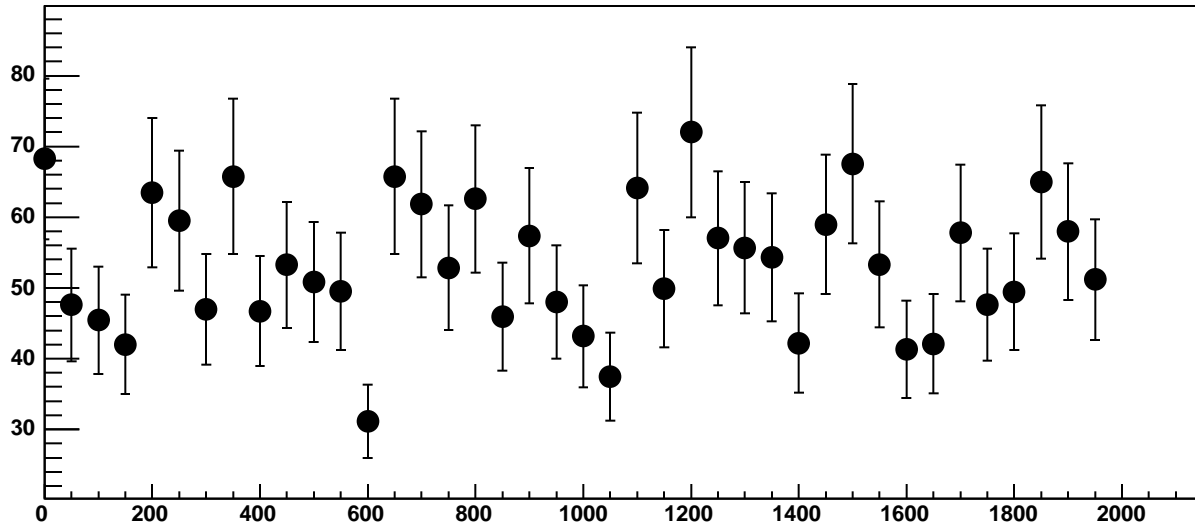


Chip 4, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC

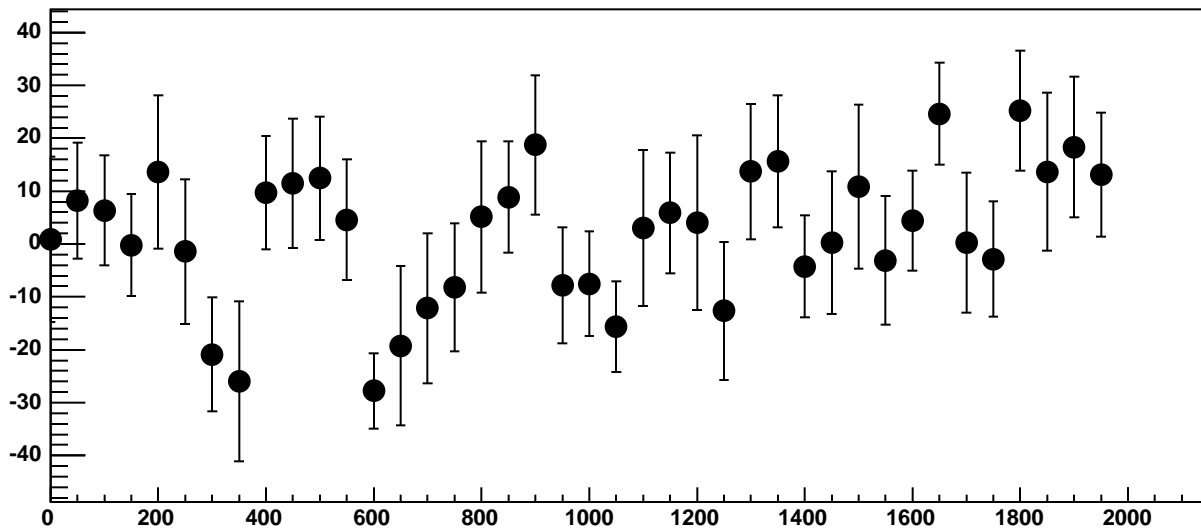


$\chi^2 / \text{ndf}$  11.48 / 11  
p0  $-954.6 \pm 19.26$   
p1  $0.02111 \pm 0.01728$

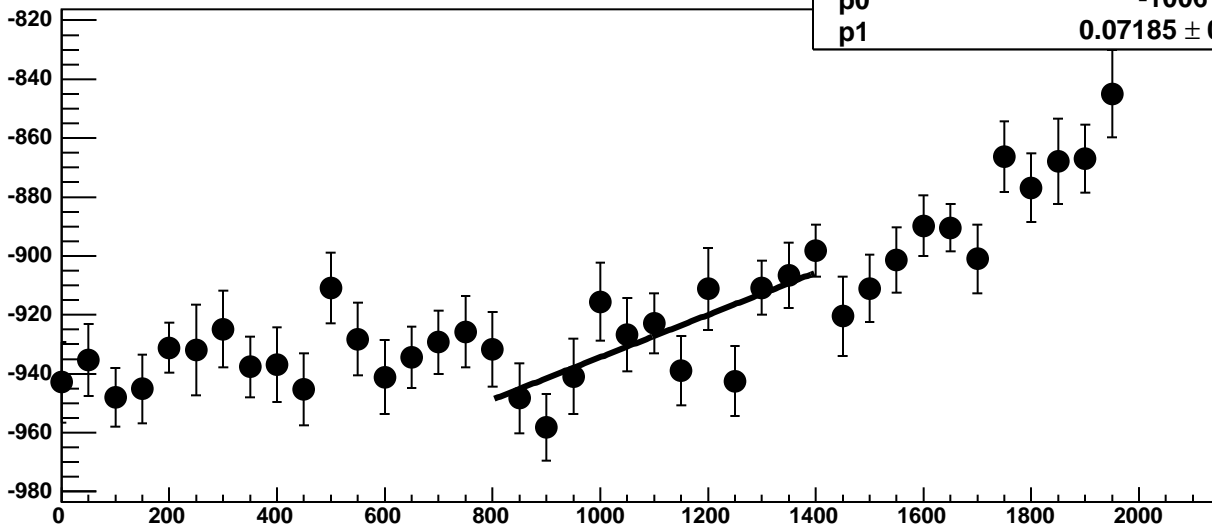
Chip 4, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC



Chip 4, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC

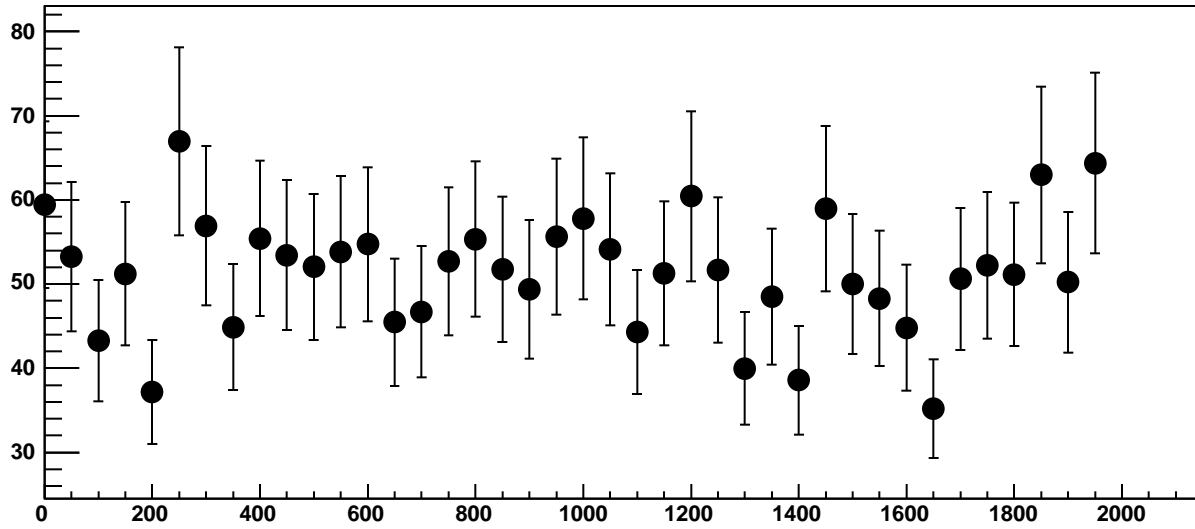


Chip 4, Channel 14, Enable 4, Hold=30, ADC Mean vs DAC

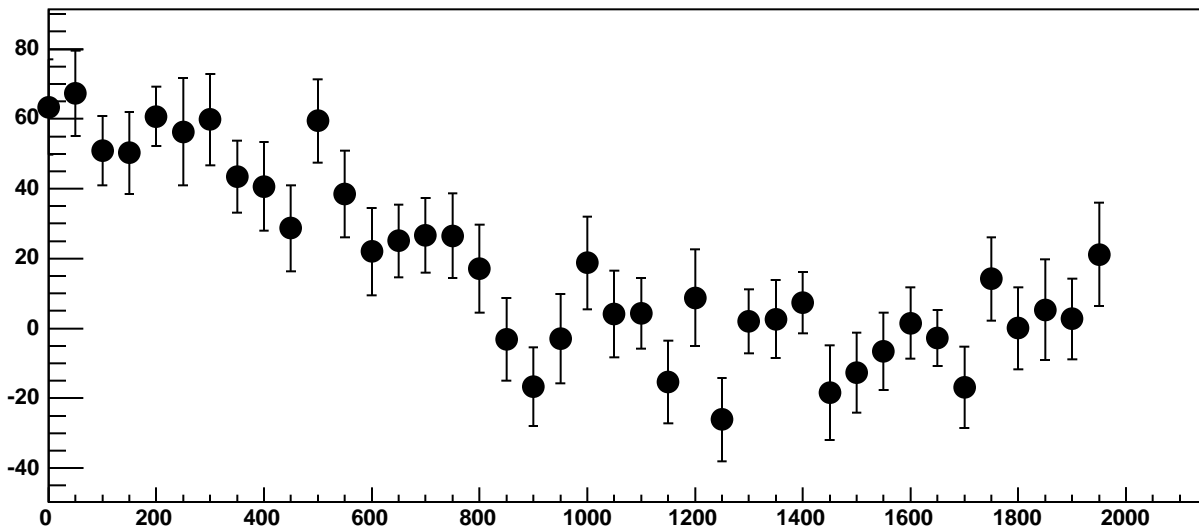


$\chi^2 / \text{ndf}$  14.15 / 11  
p0  $-1006 \pm 18.65$   
p1  $0.07185 \pm 0.01625$

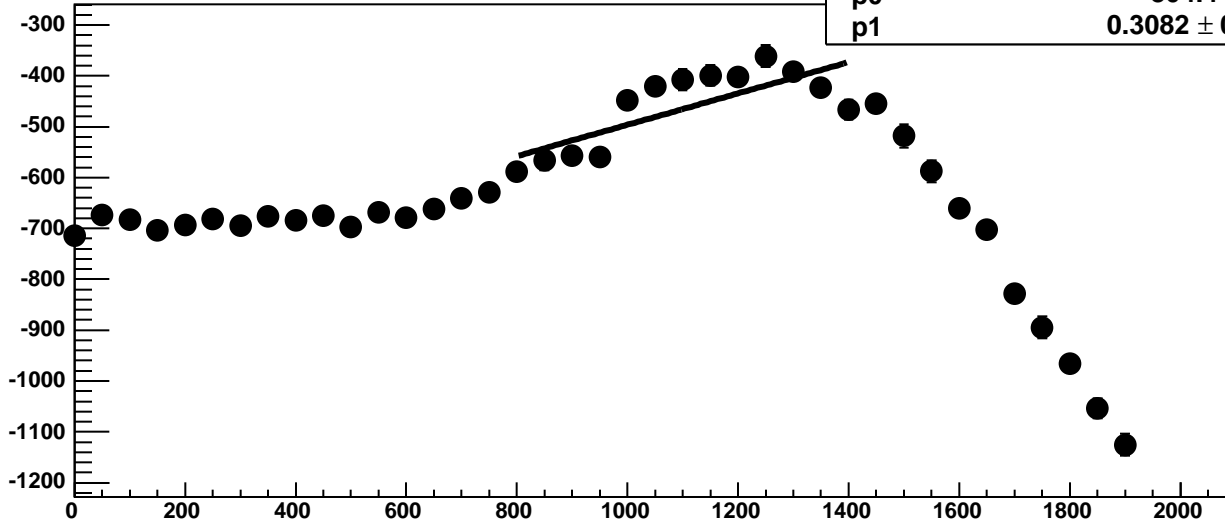
Chip 4, Channel 14, Enable 4, Hold=30, ADC Noise vs DAC



Chip 4, Channel 14, Enable 4, Hold=30, ADC Residuals vs DAC



Chip 4, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

90.72 / 11

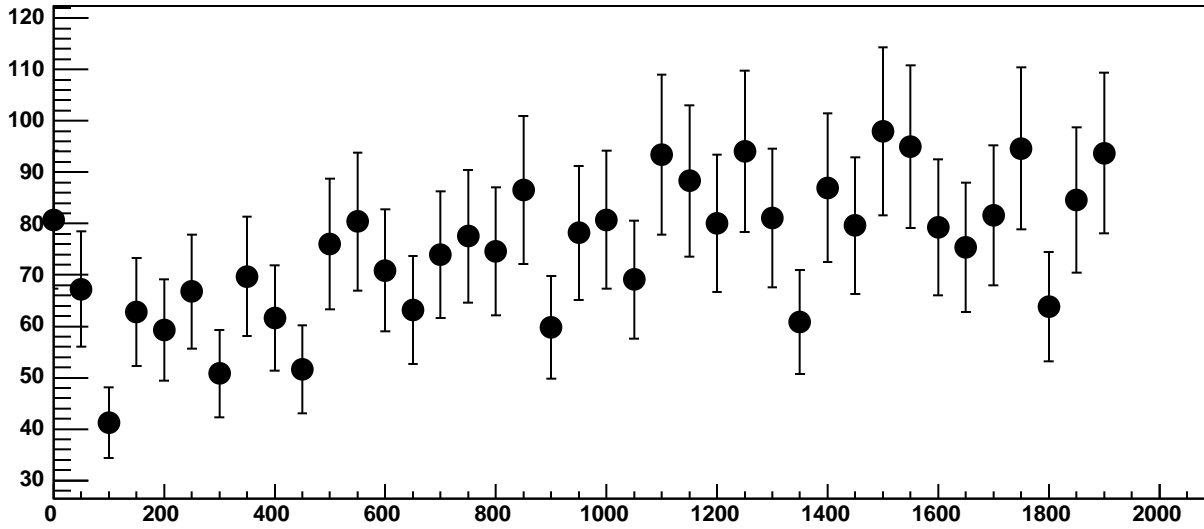
p0

$-804.4 \pm 28.34$

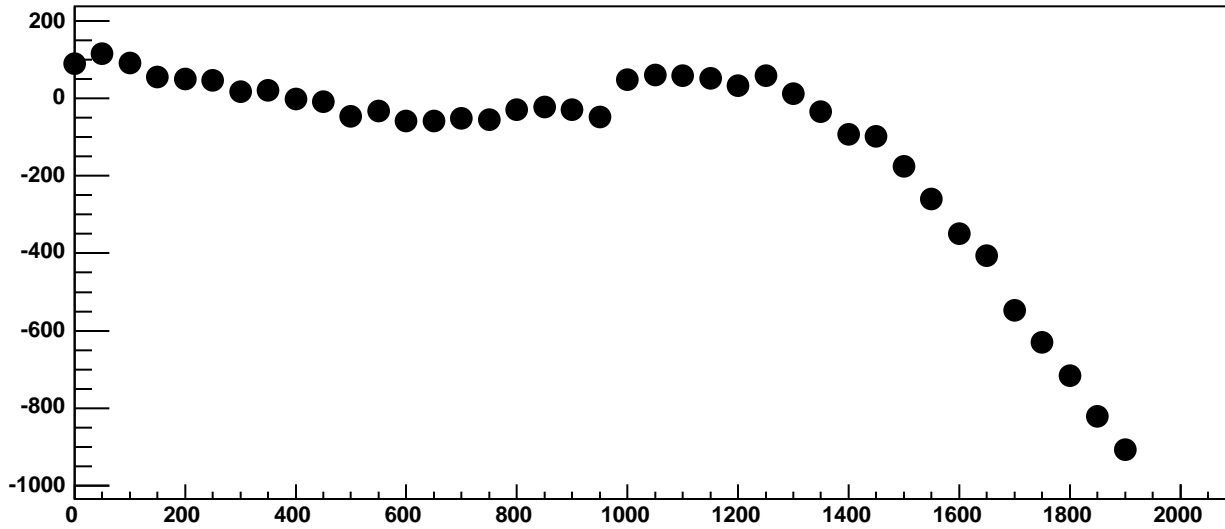
p1

$0.3082 \pm 0.02556$

Chip 4, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC

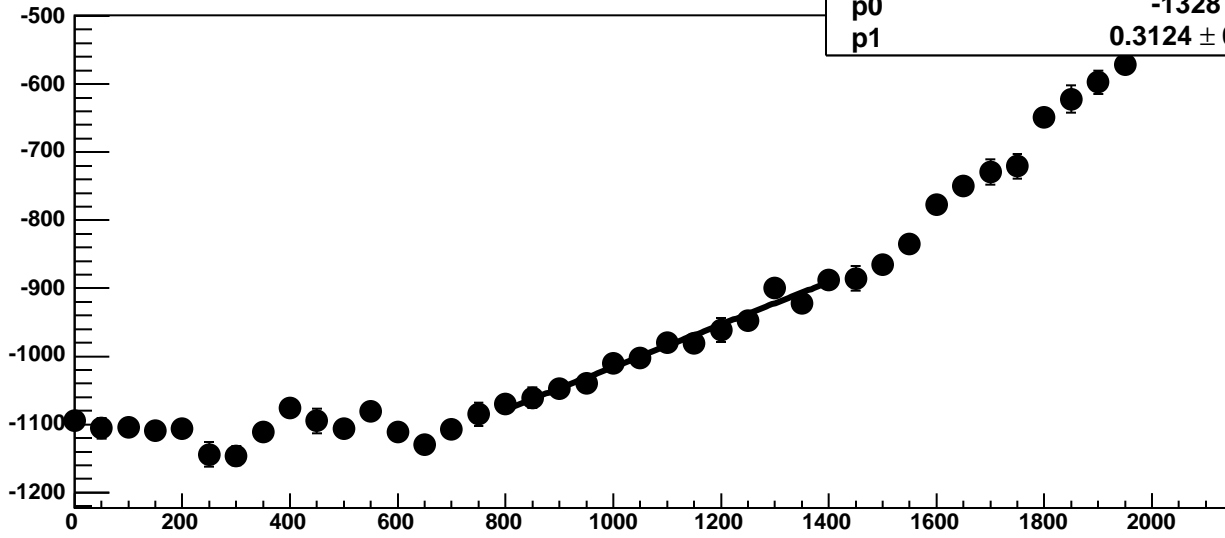


Chip 4, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC



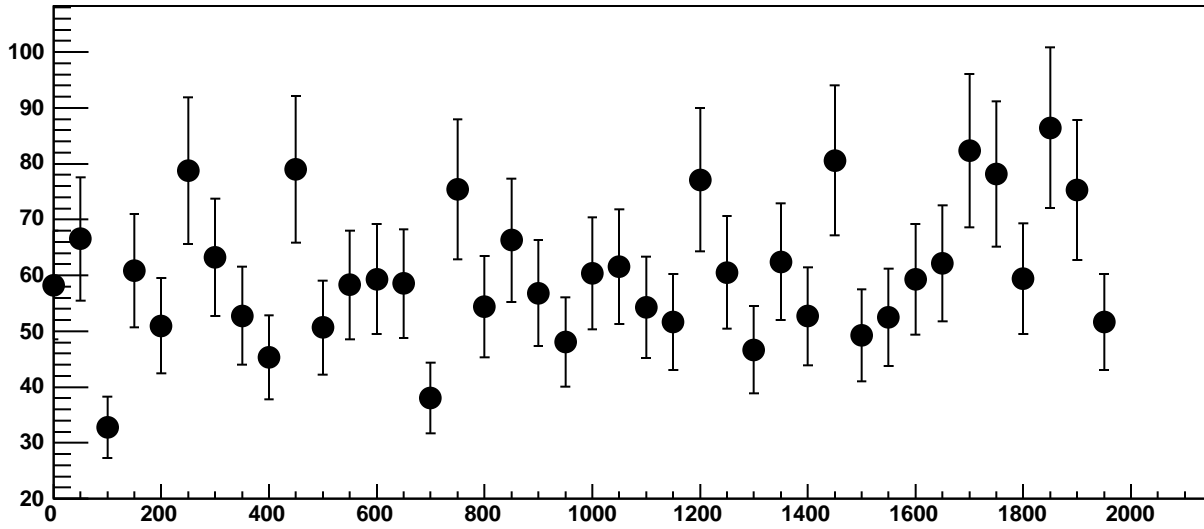


Chip 4, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC

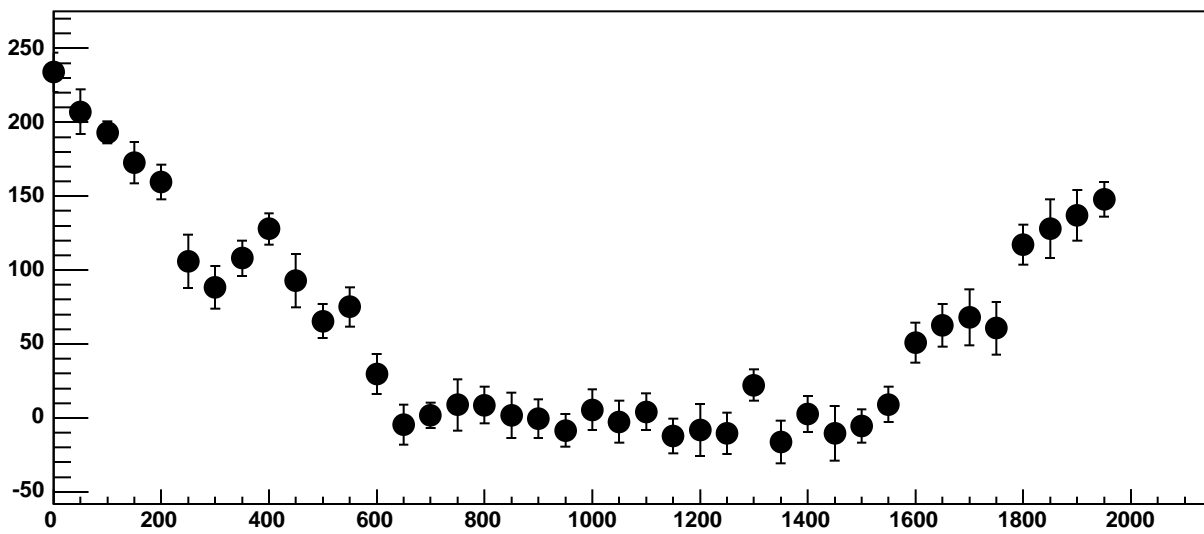


$\chi^2 / \text{ndf}$  8.847 / 11  
p0  $-1328 \pm 21.29$   
p1  $0.3124 \pm 0.01901$

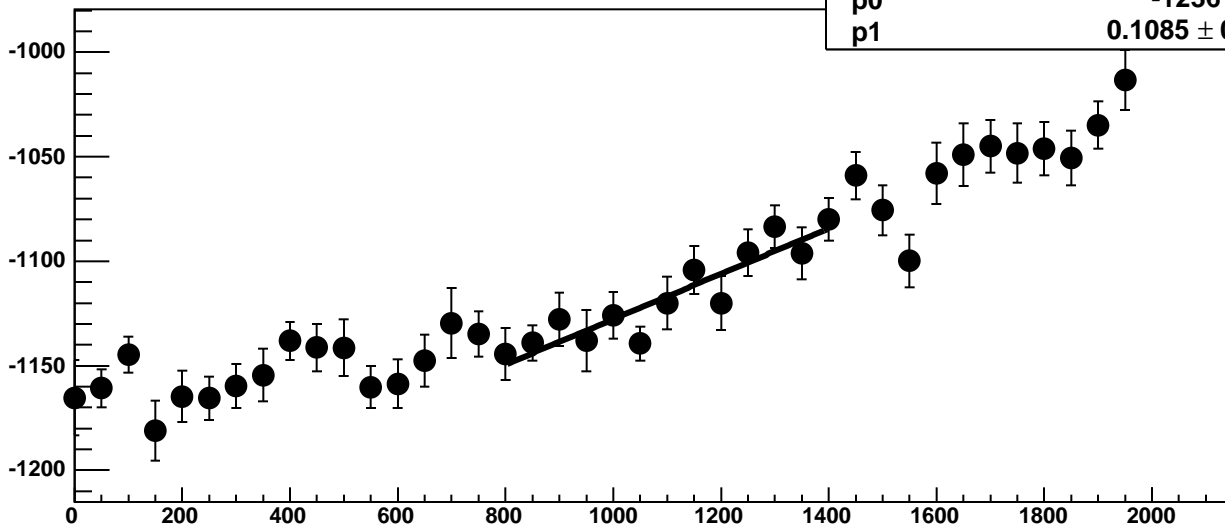
Chip 4, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC



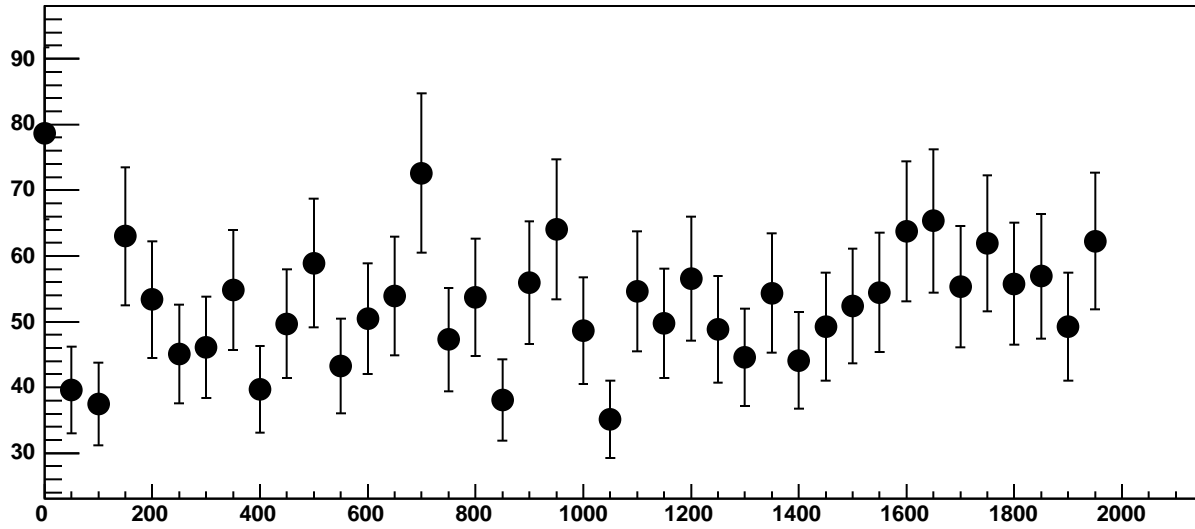
Chip 4, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC



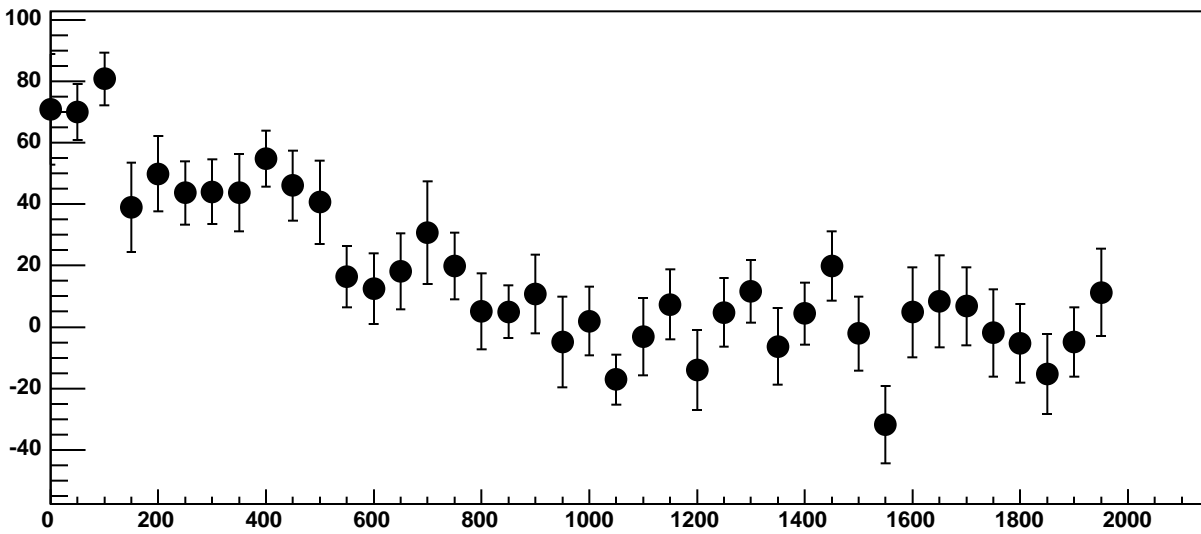
Chip 4, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC



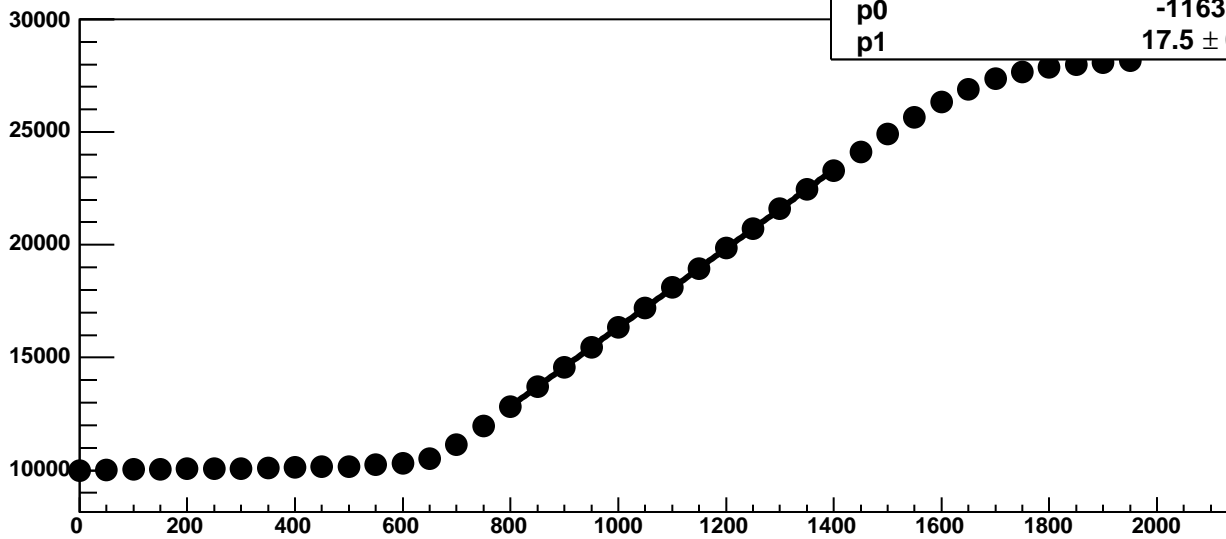
Chip 4, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC



Chip 4, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC

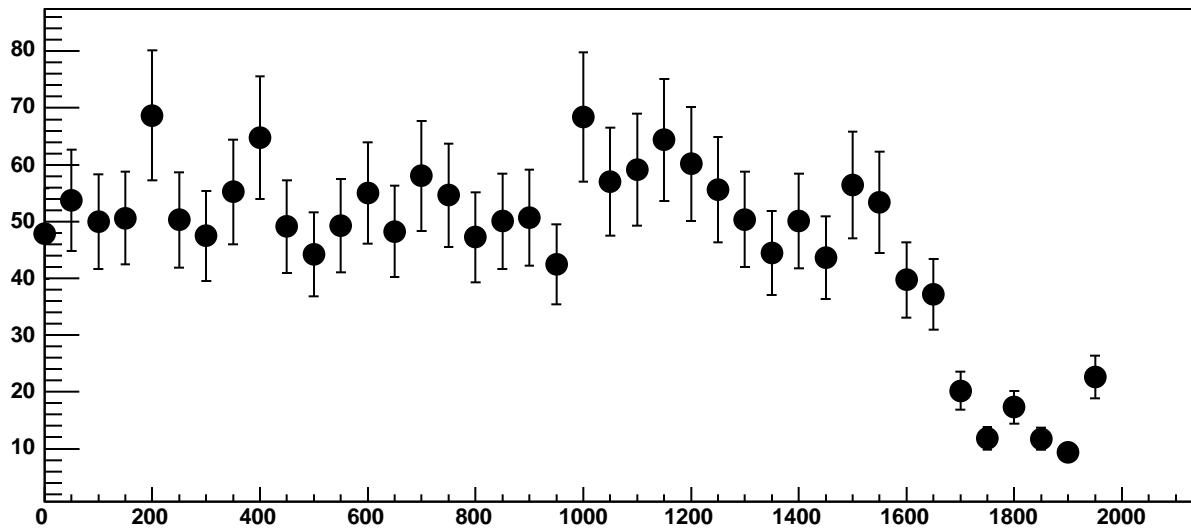


Chip 4, Channel 15, Enable 2!, Hold=30, ADC Mean vs DAC

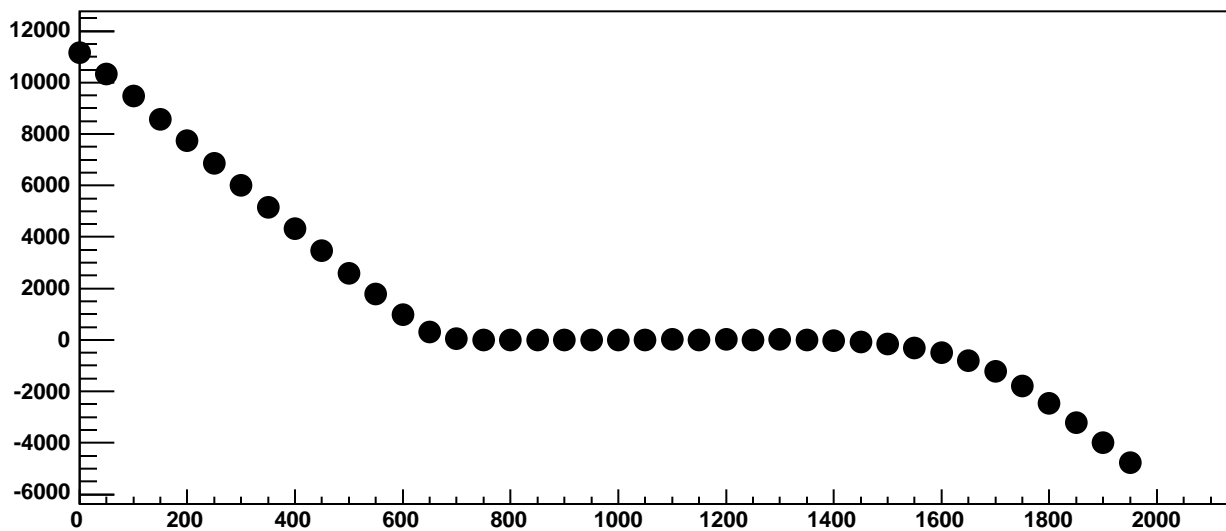


$\chi^2 / \text{ndf}$  21.49 / 11  
p0  $-1163 \pm 18.54$   
p1  $17.5 \pm 0.01664$

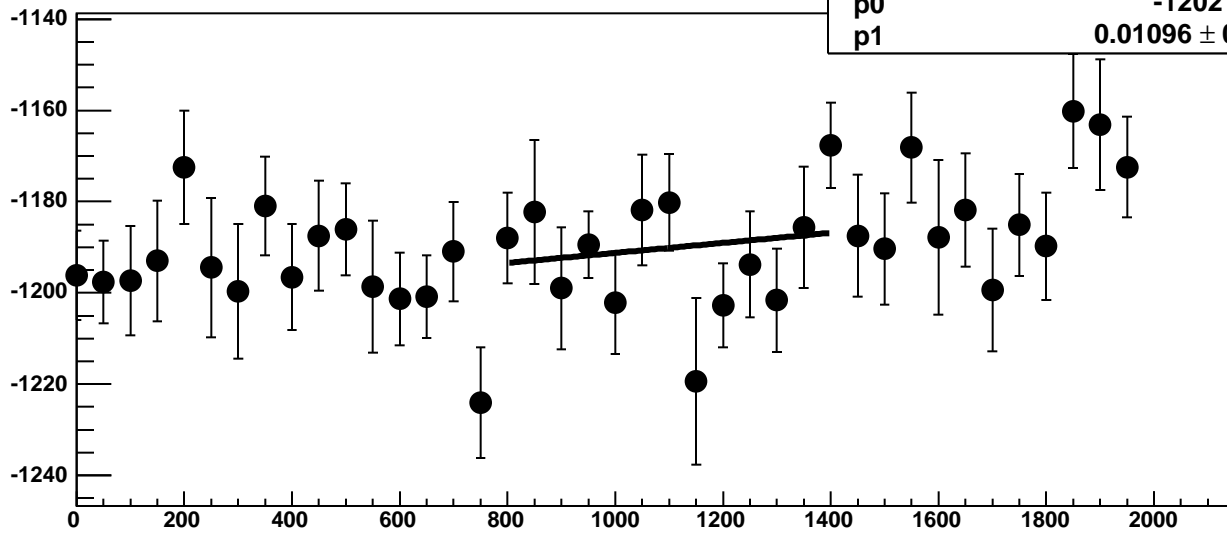
Chip 4, Channel 15, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 4, Channel 15, Enable 2!, Hold=30, ADC Residuals vs DAC

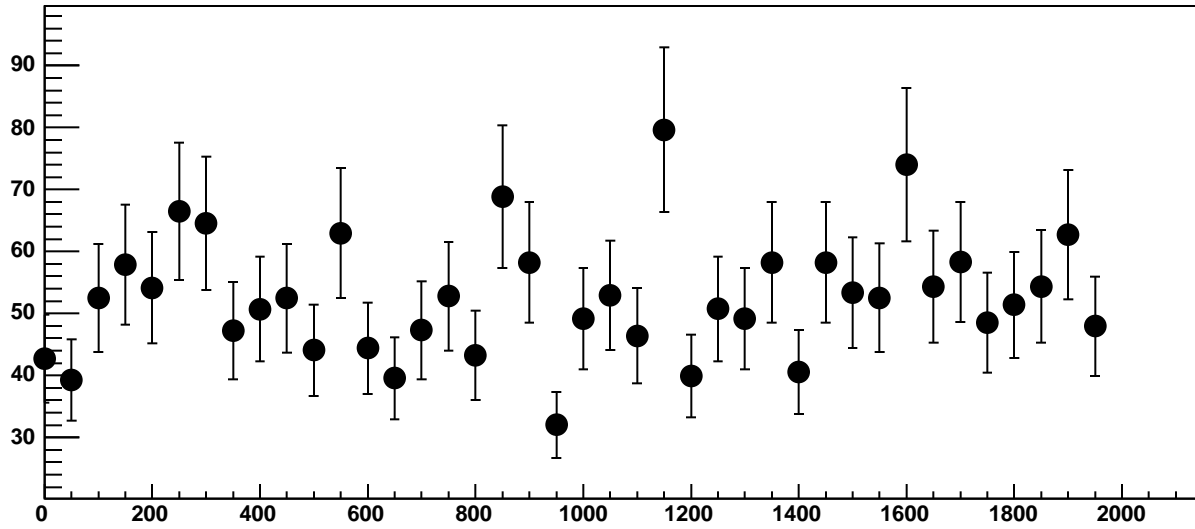


Chip 4, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC

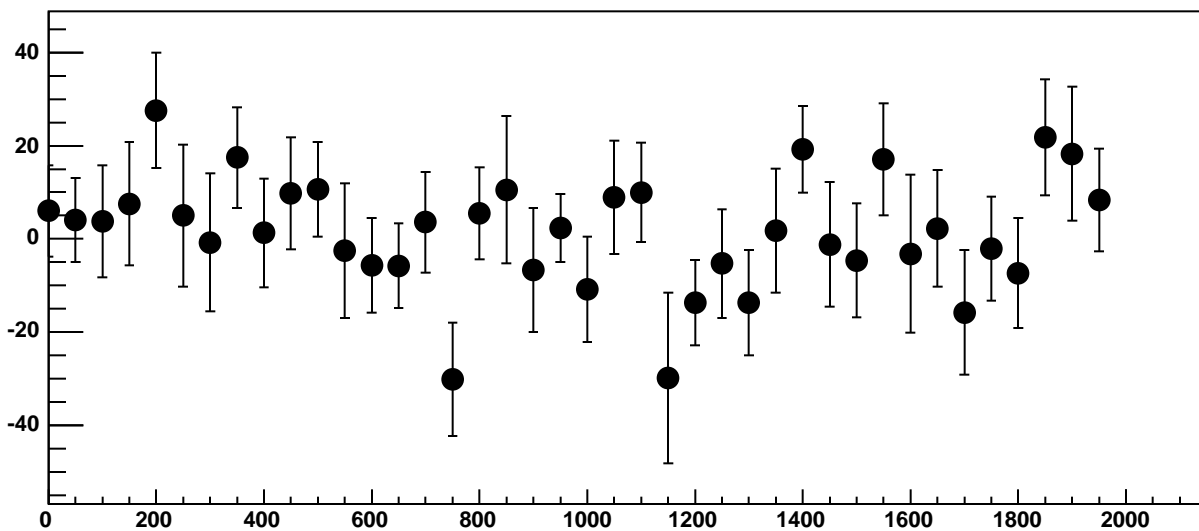


$\chi^2 / \text{ndf}$  14.31 / 11  
p0  $-1202 \pm 17.85$   
p1  $0.01096 \pm 0.01602$

Chip 4, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC



Chip 4, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC



Chip 4, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

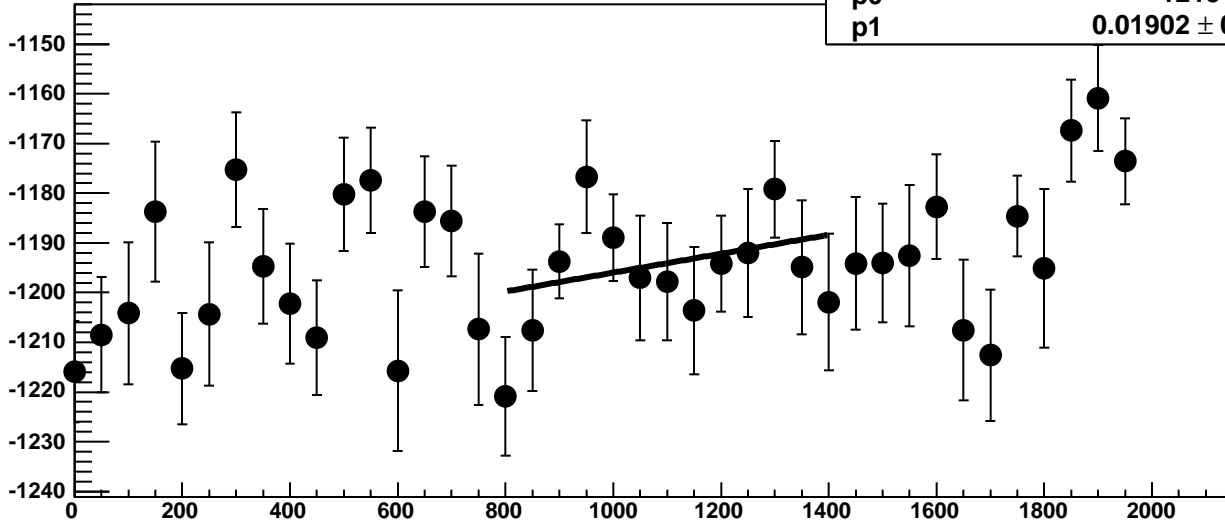
11.05 / 11

p0

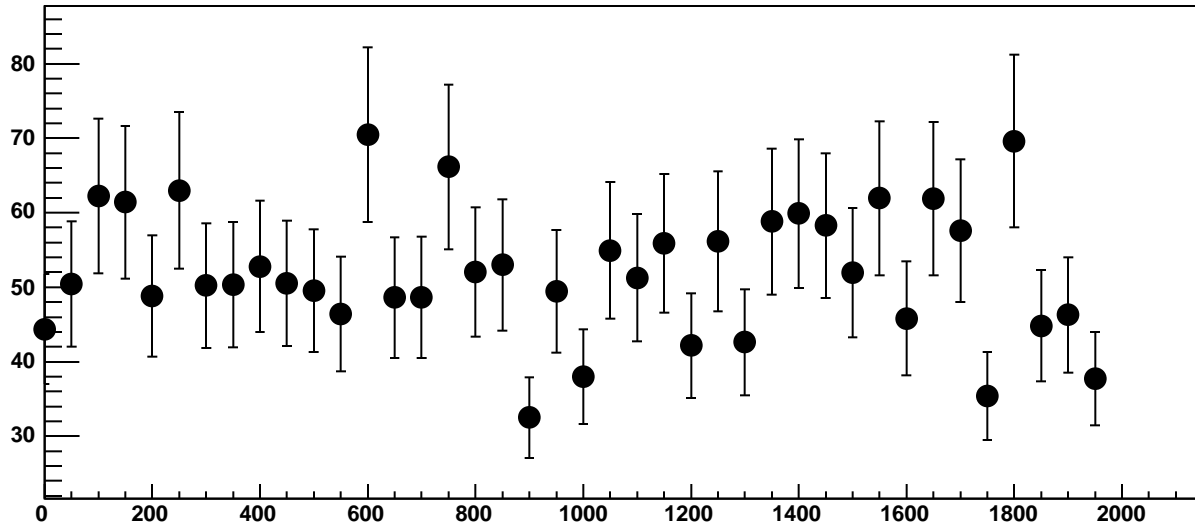
$-1215 \pm 18.36$

p1

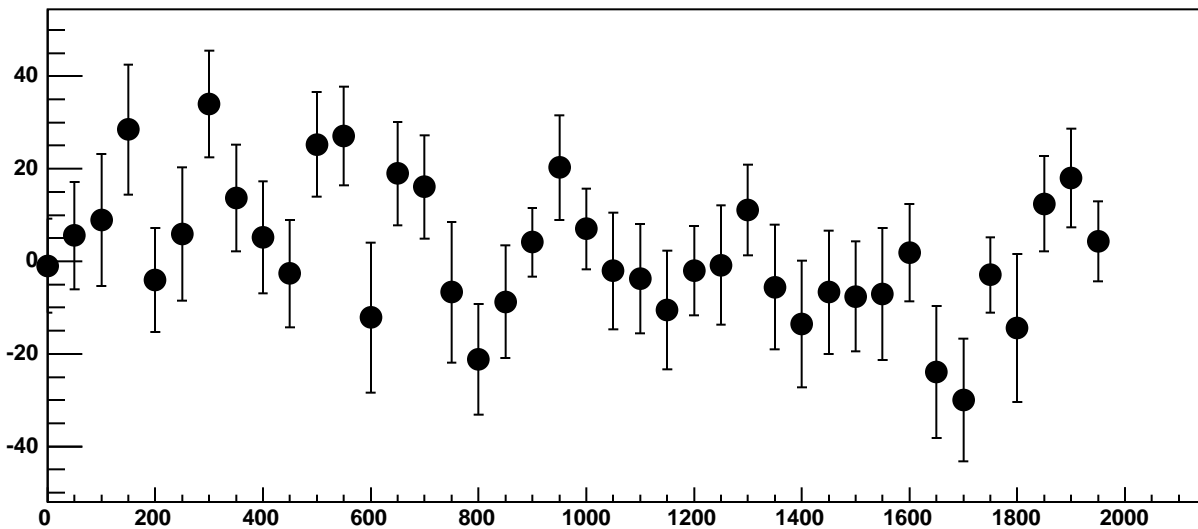
$0.01902 \pm 0.01686$



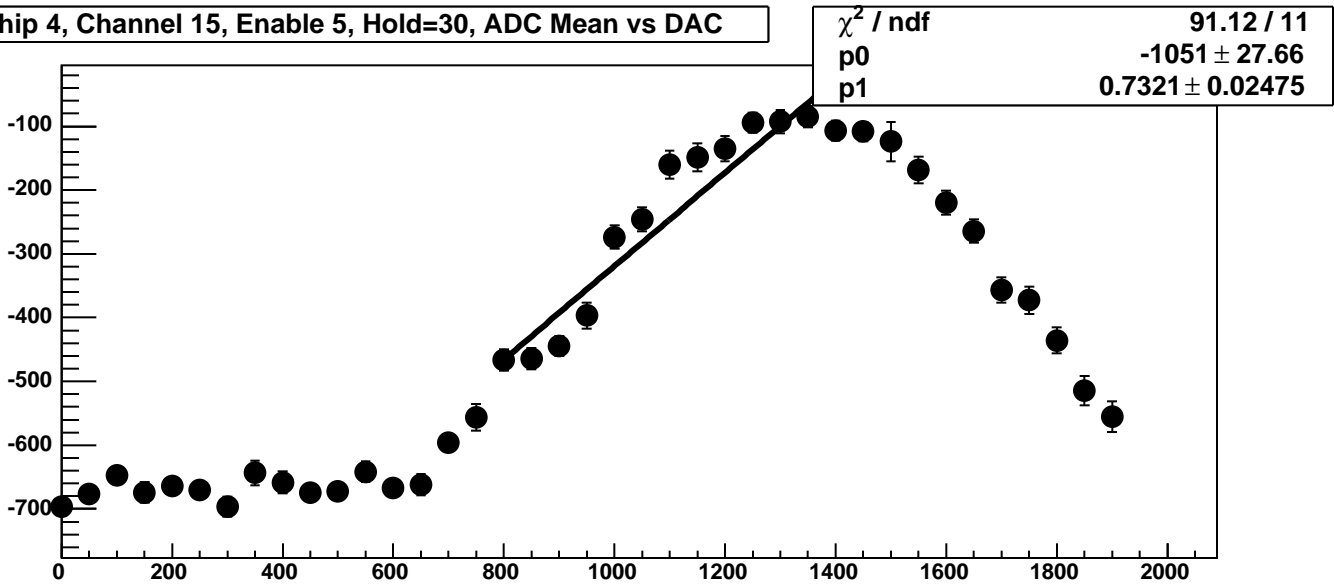
Chip 4, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC



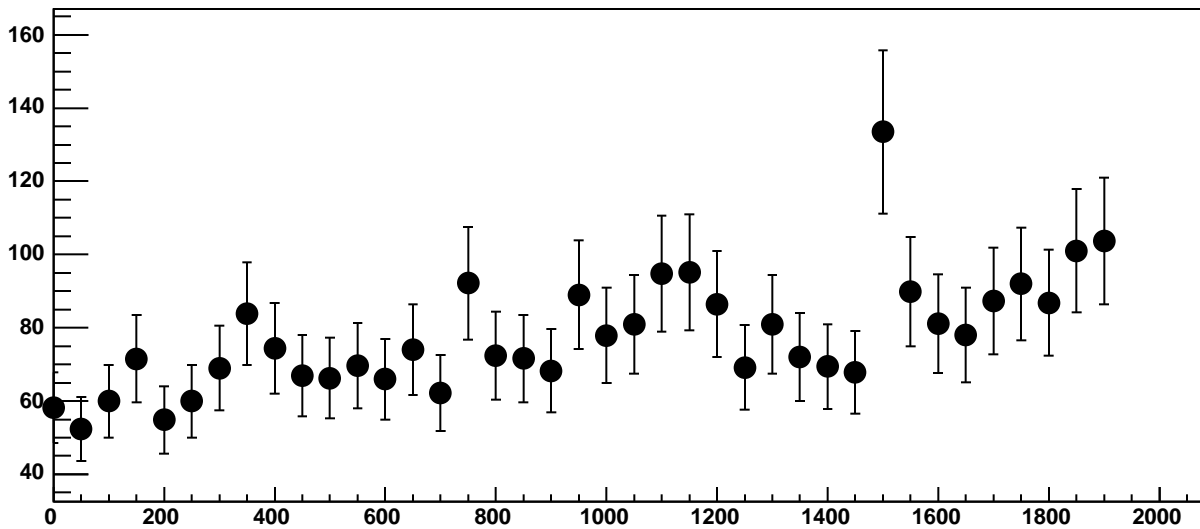
Chip 4, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC



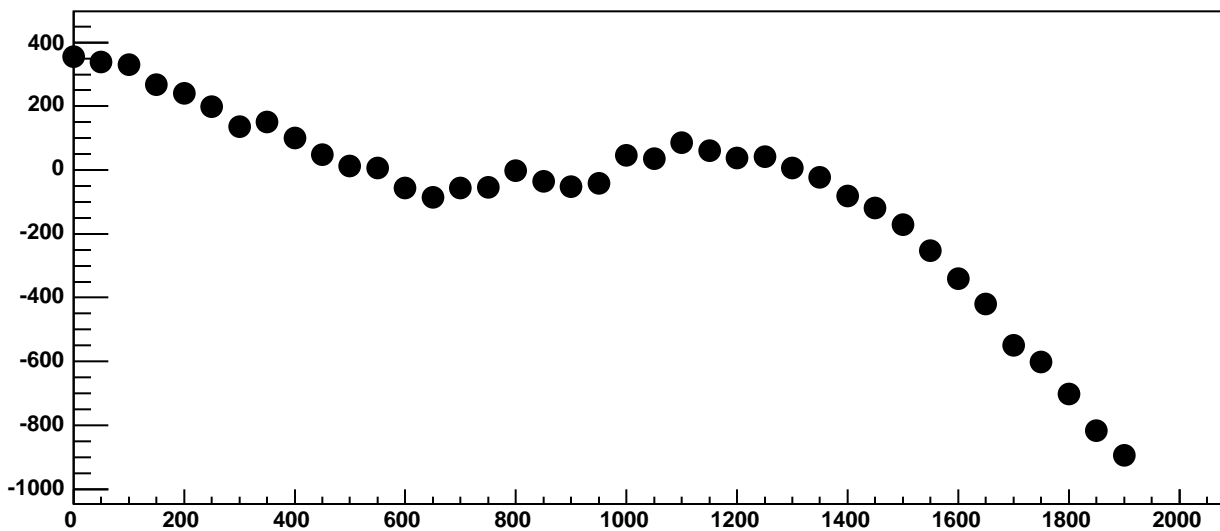
Chip 4, Channel 15, Enable 5, Hold=30, ADC Mean vs DAC



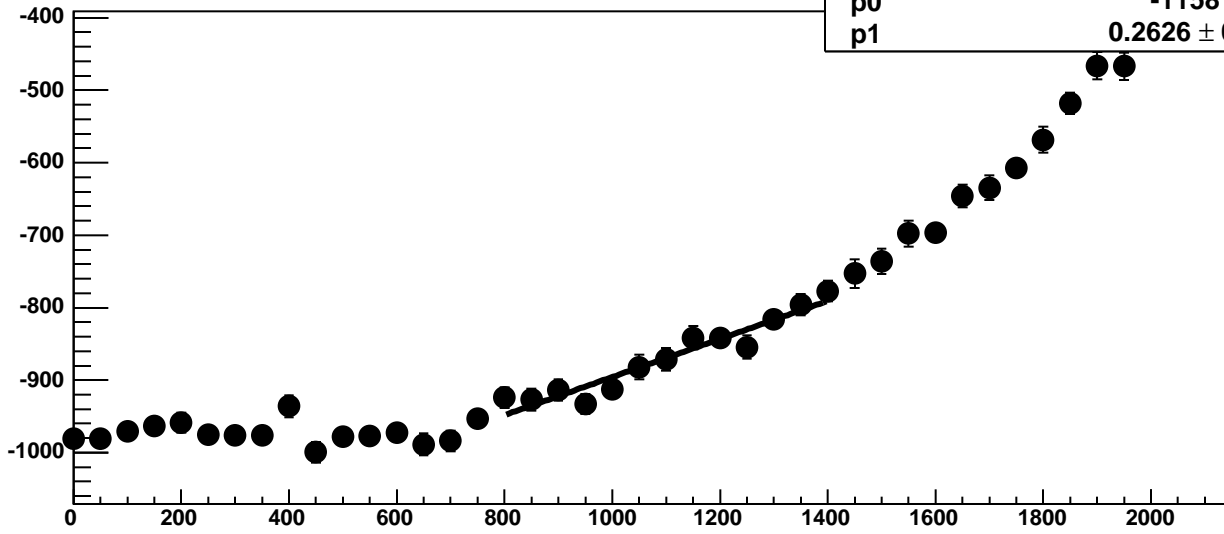
Chip 4, Channel 15, Enable 5, Hold=30, ADC Noise vs DAC



Chip 4, Channel 15, Enable 5, Hold=30, ADC Residuals vs DAC

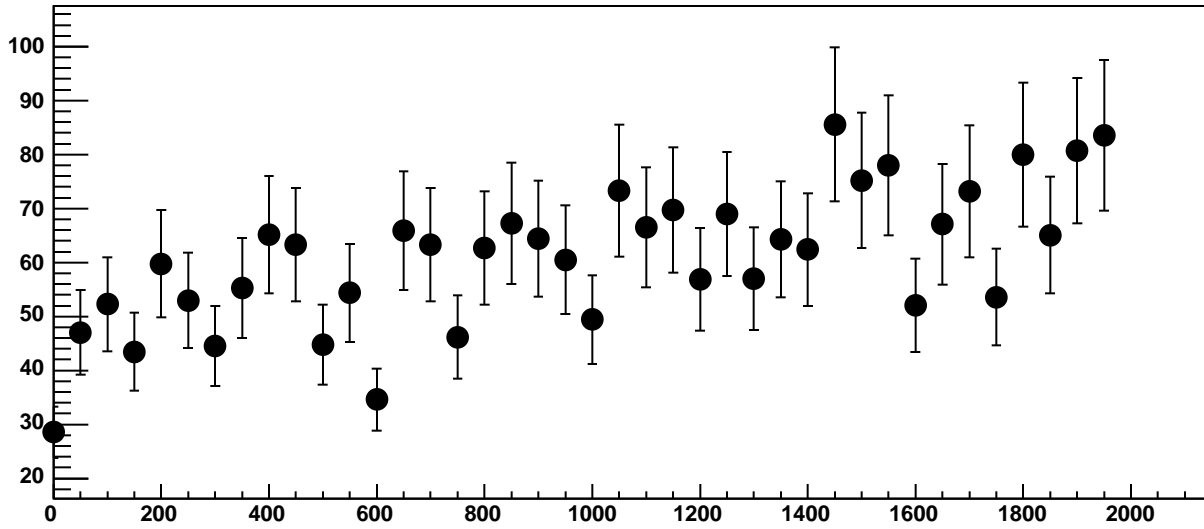


Chip 4, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC

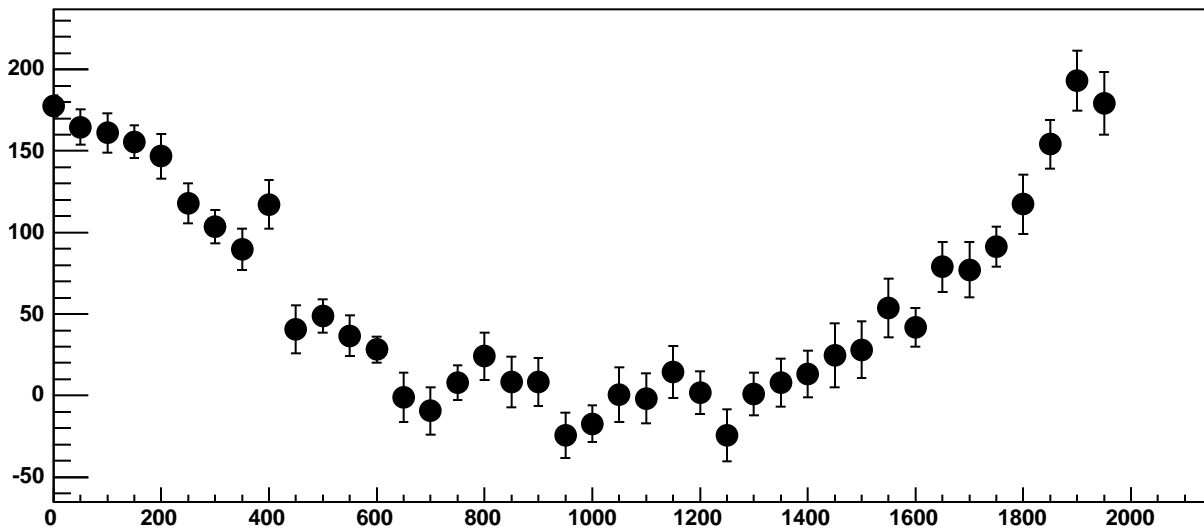


$\chi^2 / \text{ndf}$  13.21 / 11  
p0  $-1158 \pm 23.78$   
p1  $0.2626 \pm 0.02131$

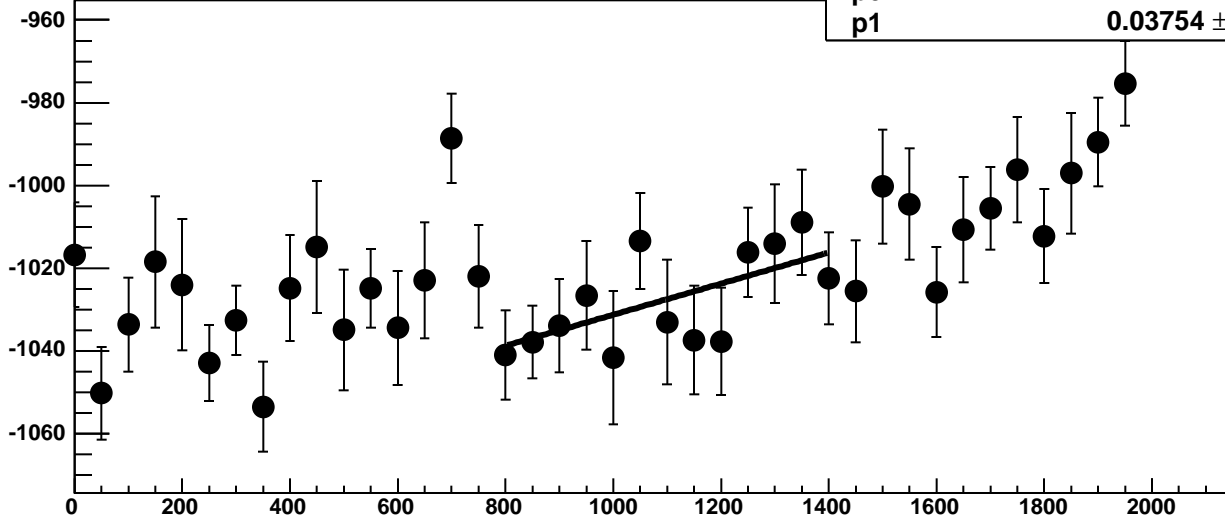
Chip 4, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC



Chip 4, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 4, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

5.998 / 11

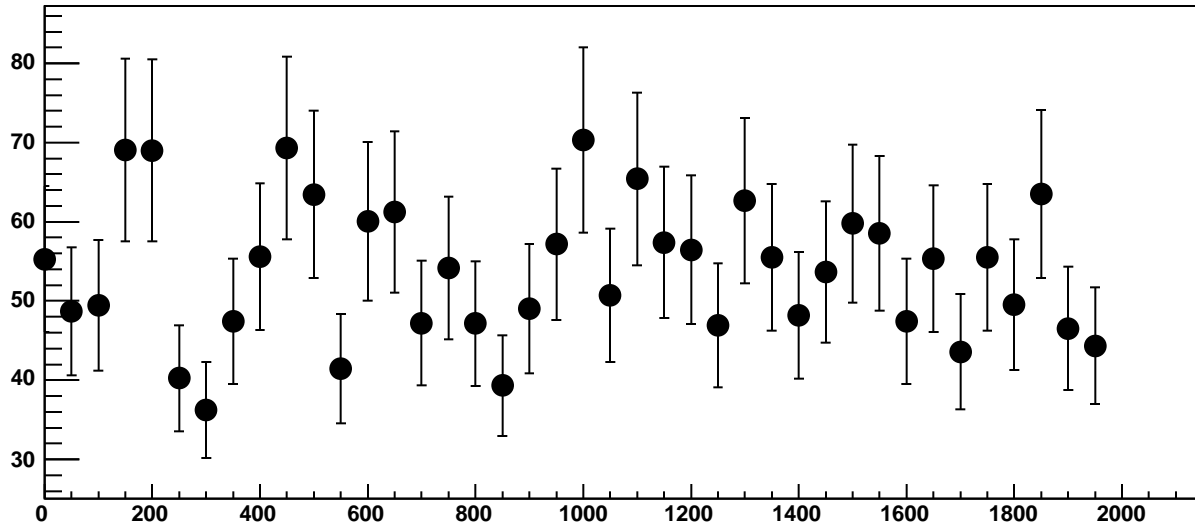
p0

$-1069 \pm 18.23$

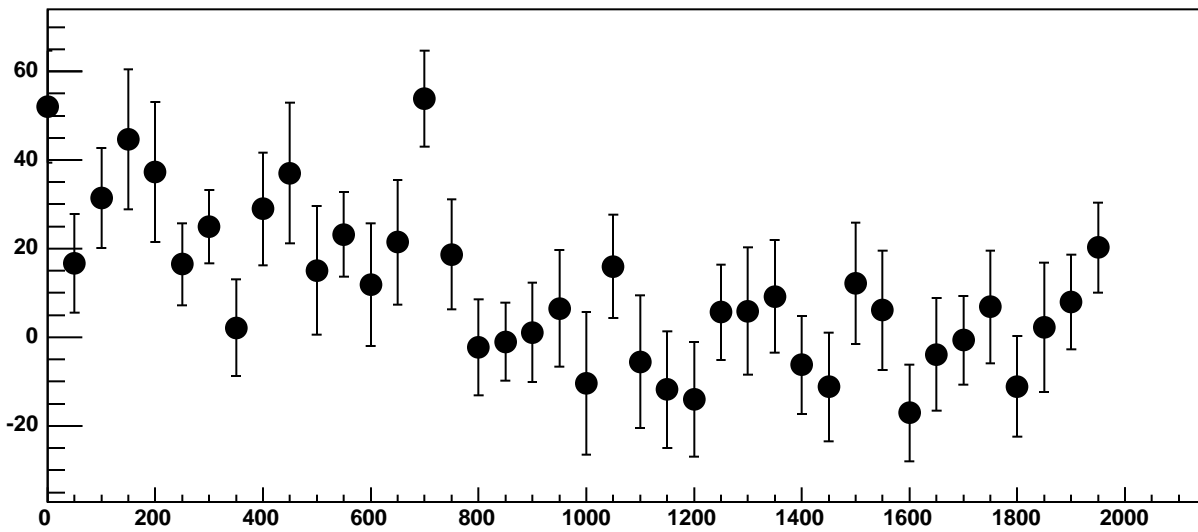
p1

$0.03754 \pm 0.0166$

Chip 4, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC

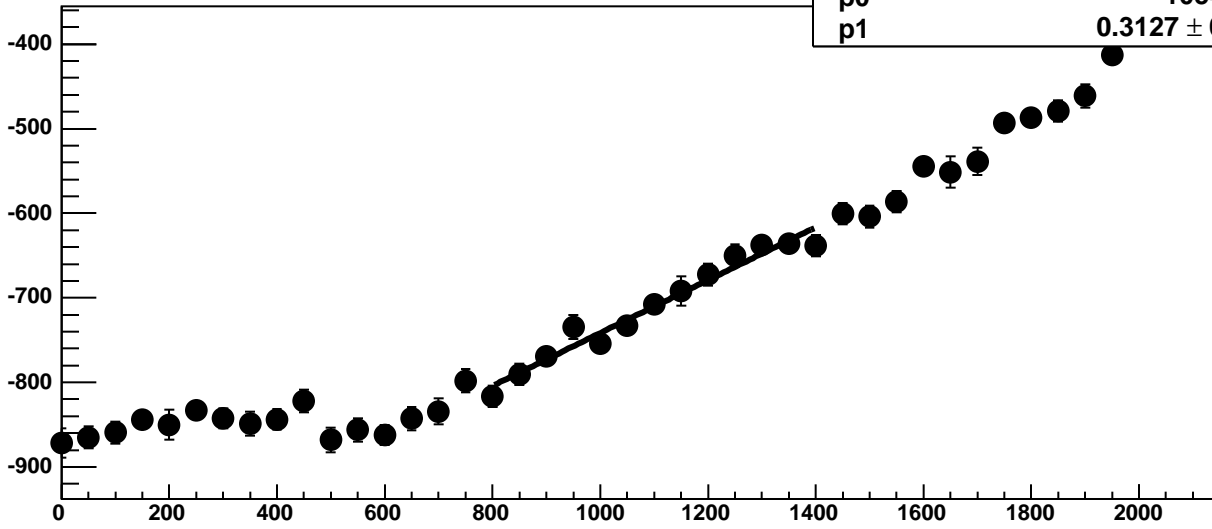


Chip 4, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC

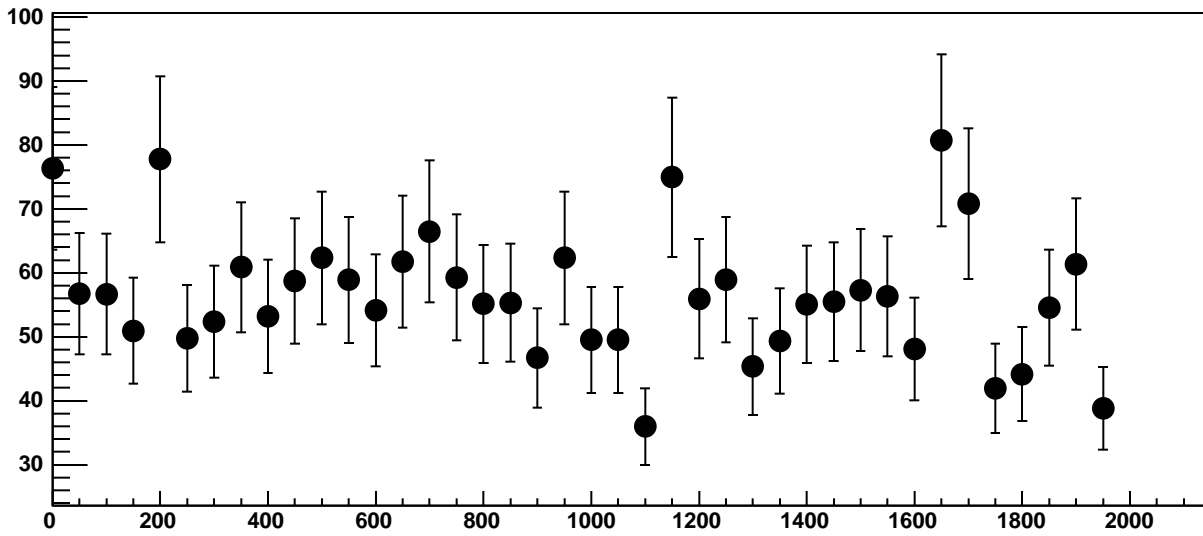




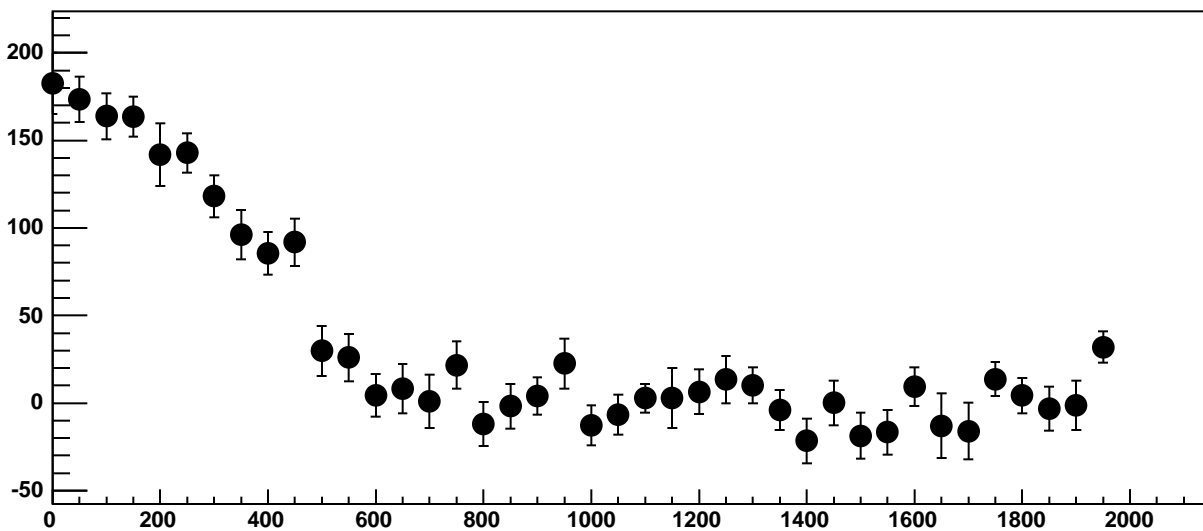
Chip 4, Channel 16, Enable 2, Hold=30, ADC Mean vs DAC



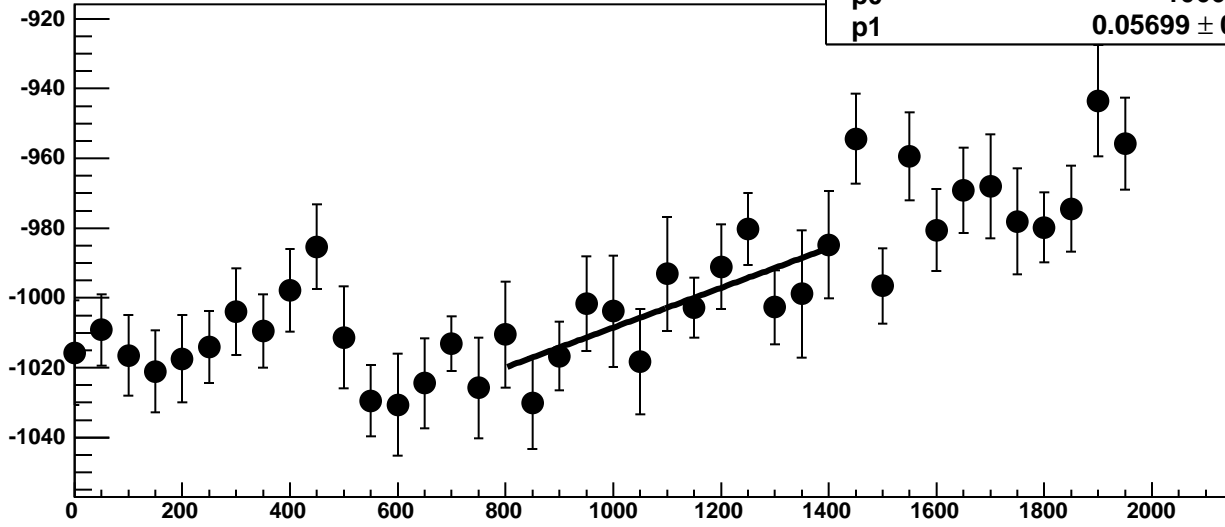
Chip 4, Channel 16, Enable 2, Hold=30, ADC Noise vs DAC



Chip 4, Channel 16, Enable 2, Hold=30, ADC Residuals vs DAC

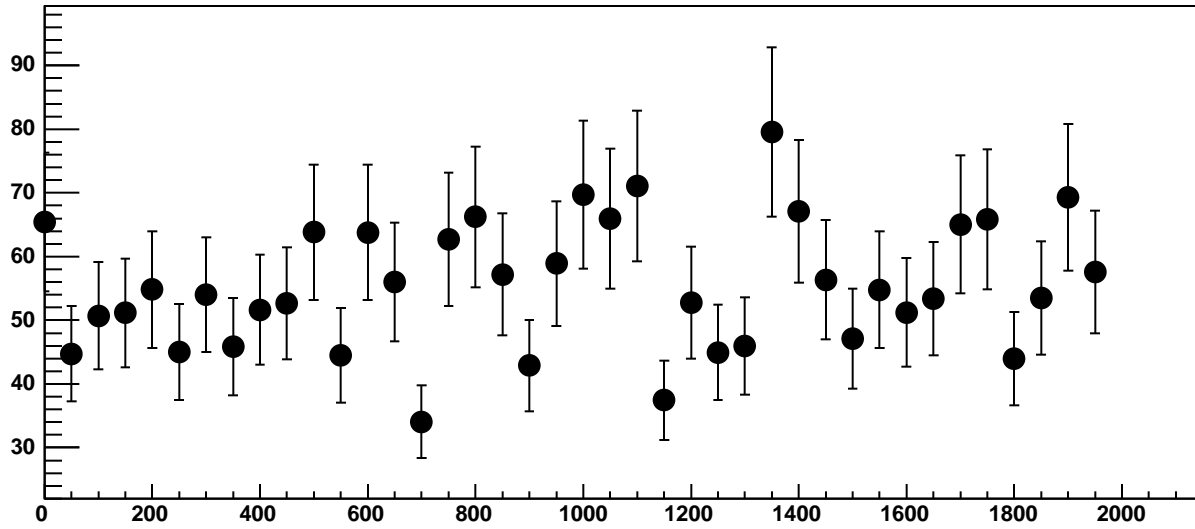


Chip 4, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC

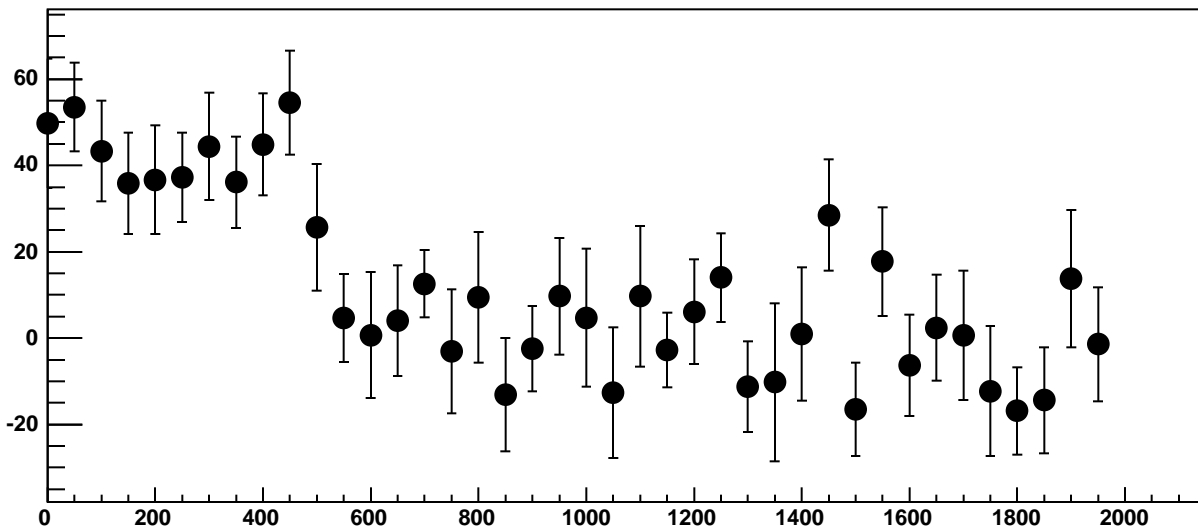


$\chi^2 / \text{ndf}$  6.74 / 11  
p0  $-1066 \pm 22.01$   
p1  $0.05699 \pm 0.01969$

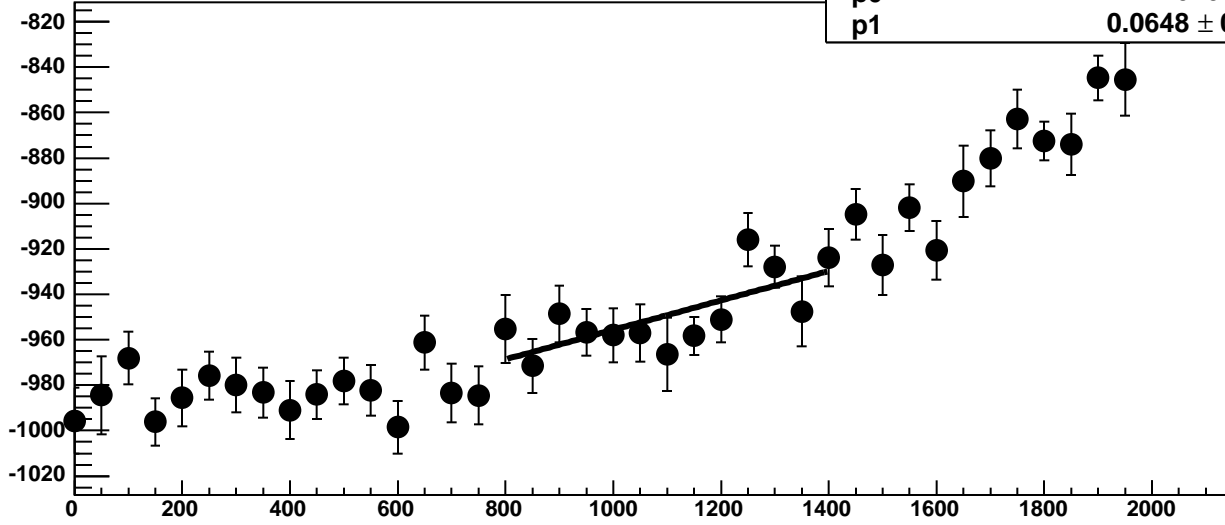
Chip 4, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC



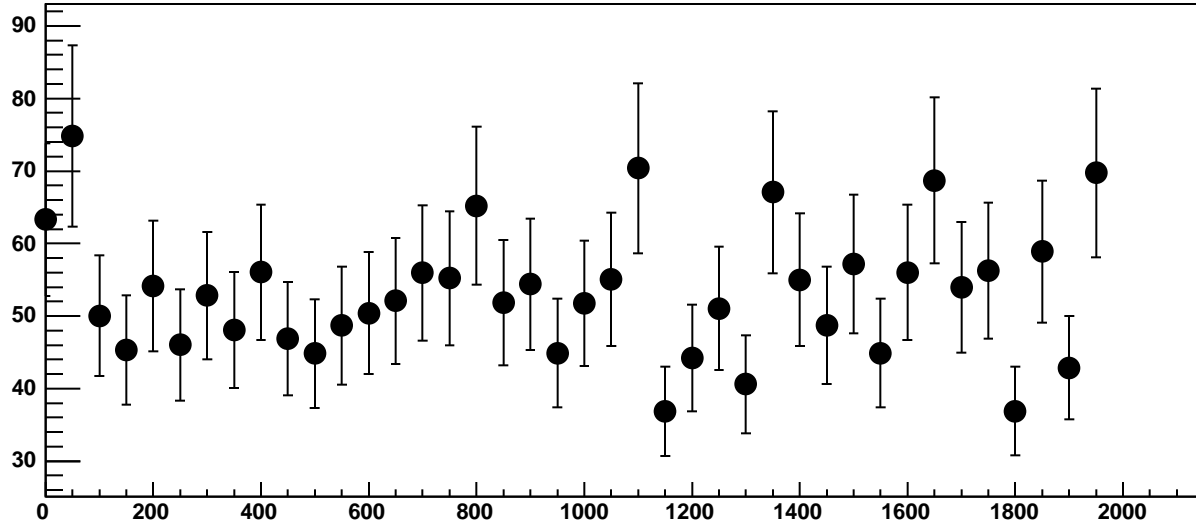
Chip 4, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC



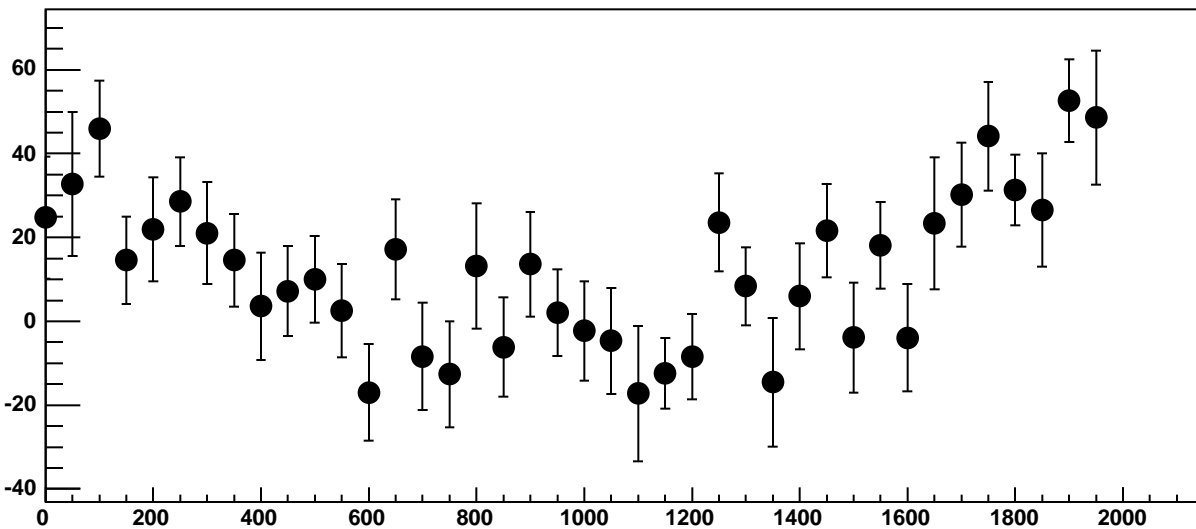
Chip 4, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC



Chip 4, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC

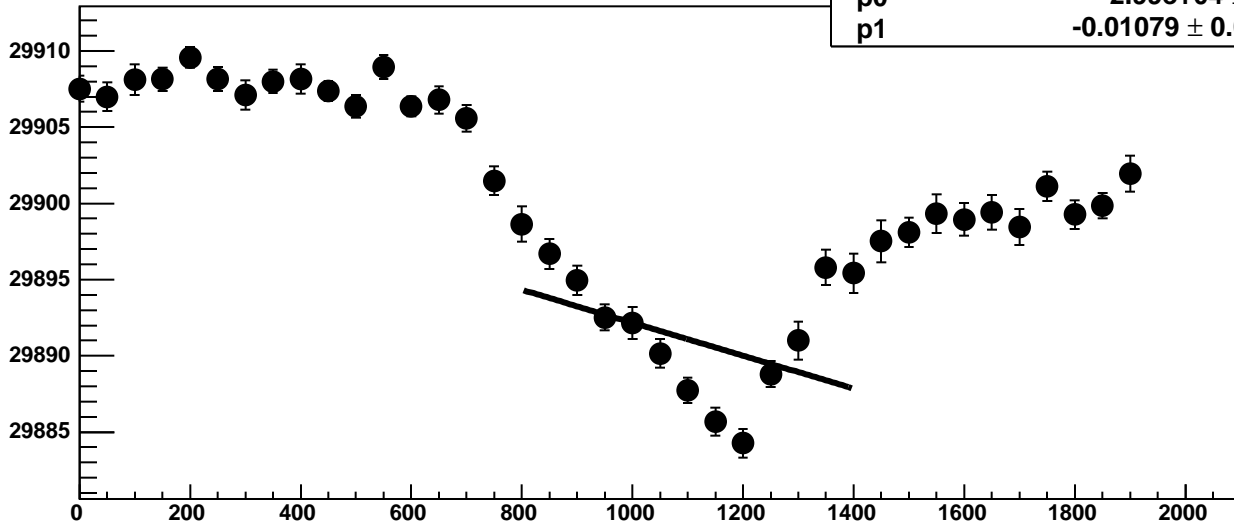


Chip 4, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC

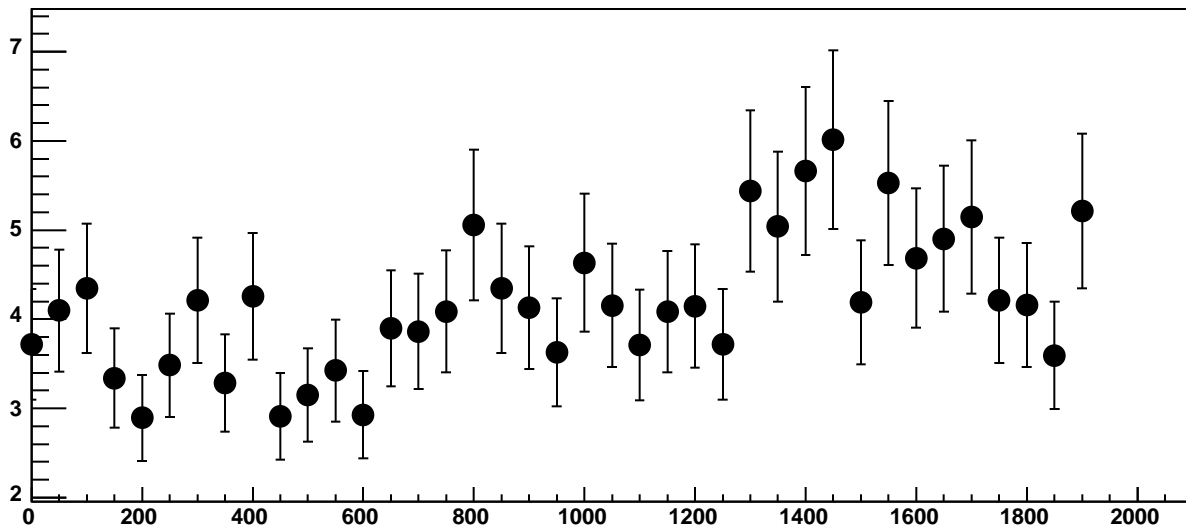


Chip 4, Channel 16, Enable 5!, Hold=30, ADC Mean vs DAC

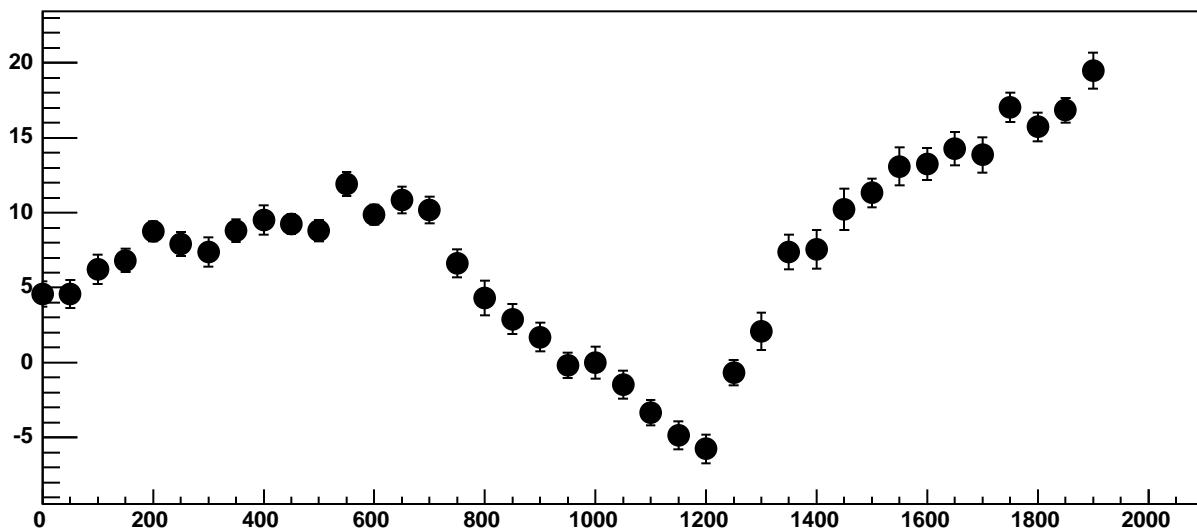
$\chi^2 / \text{ndf}$  185.1 / 11  
p0  $2.99\text{e}+04 \pm 1.768$   
p1  $-0.01079 \pm 0.001609$



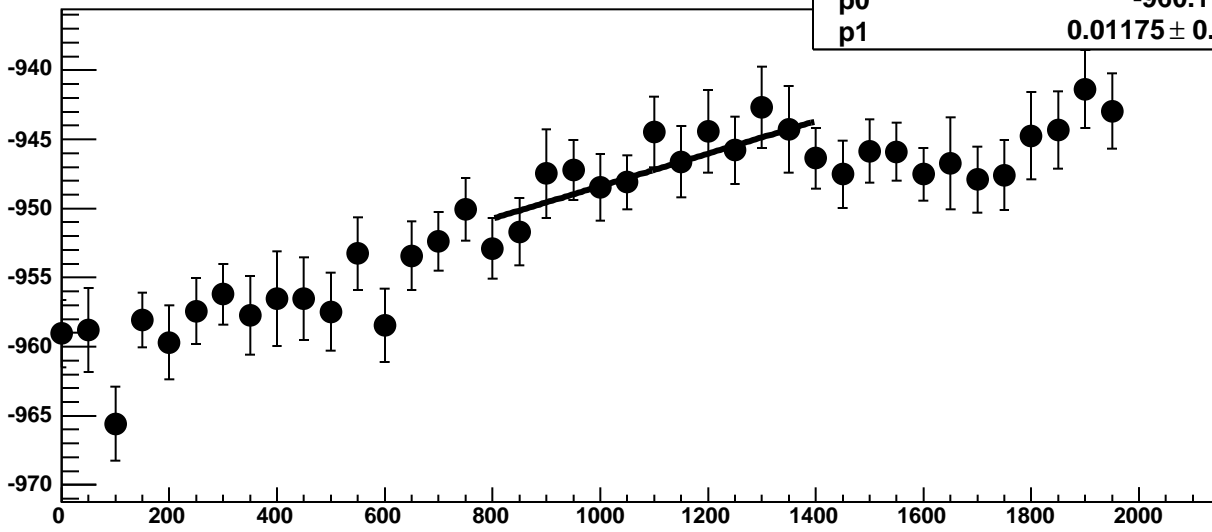
Chip 4, Channel 16, Enable 5!, Hold=30, ADC Noise vs DAC



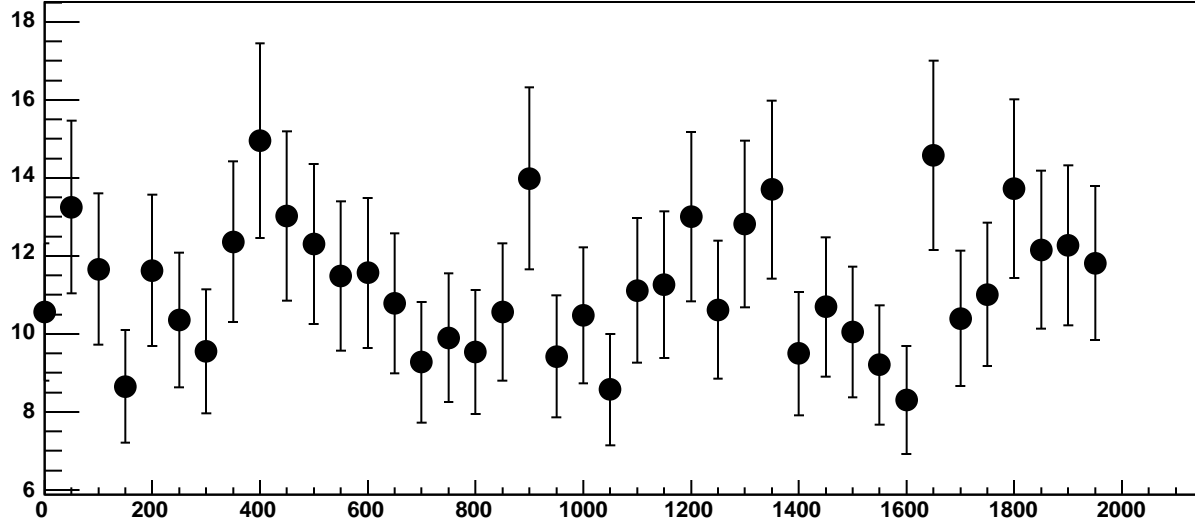
Chip 4, Channel 16, Enable 5!, Hold=30, ADC Residuals vs DAC



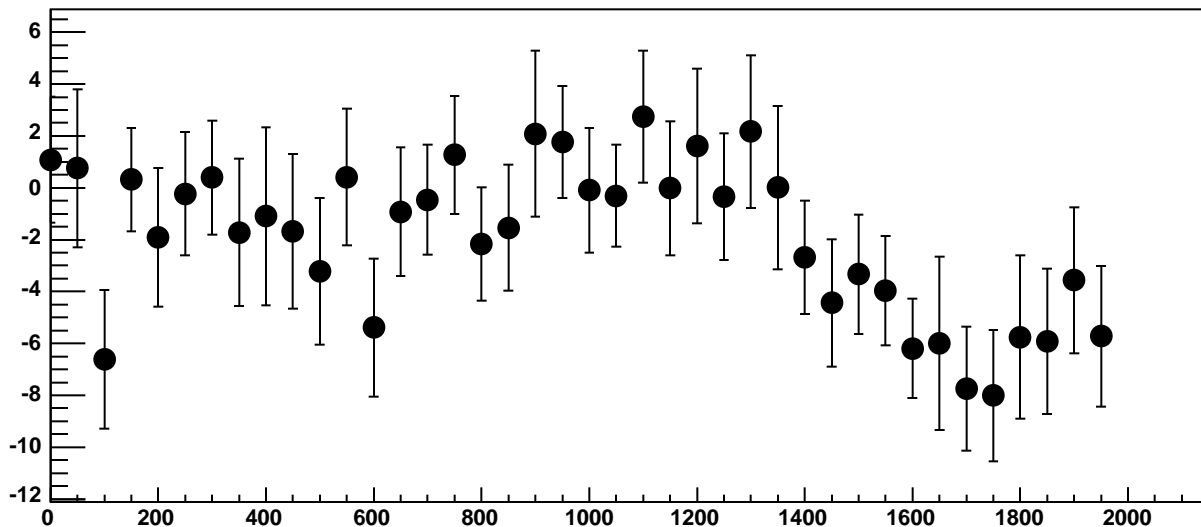
Chip 4, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC



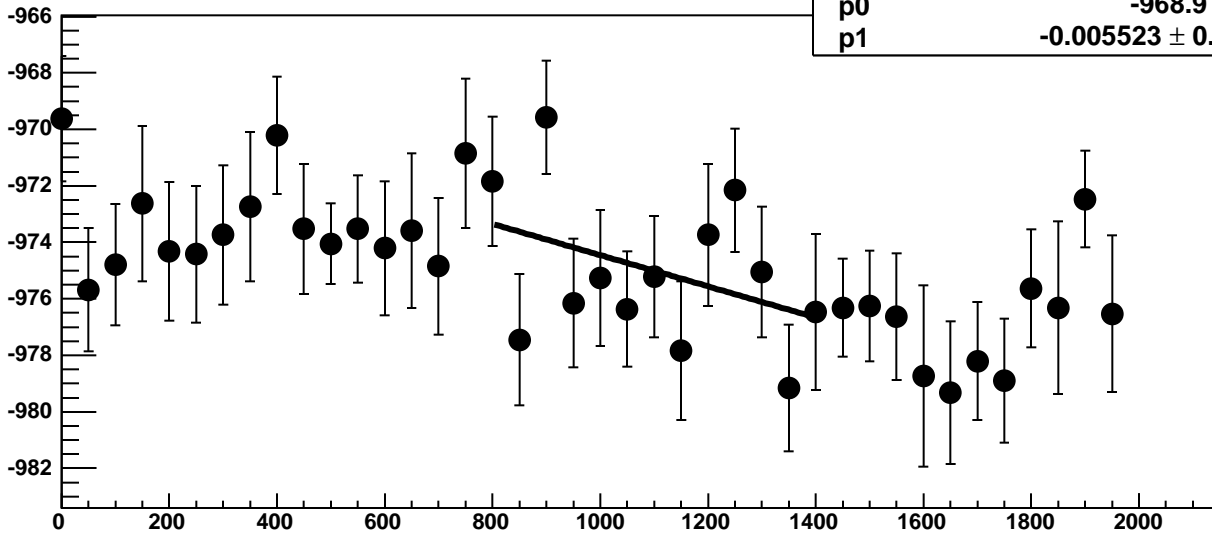
Chip 4, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC



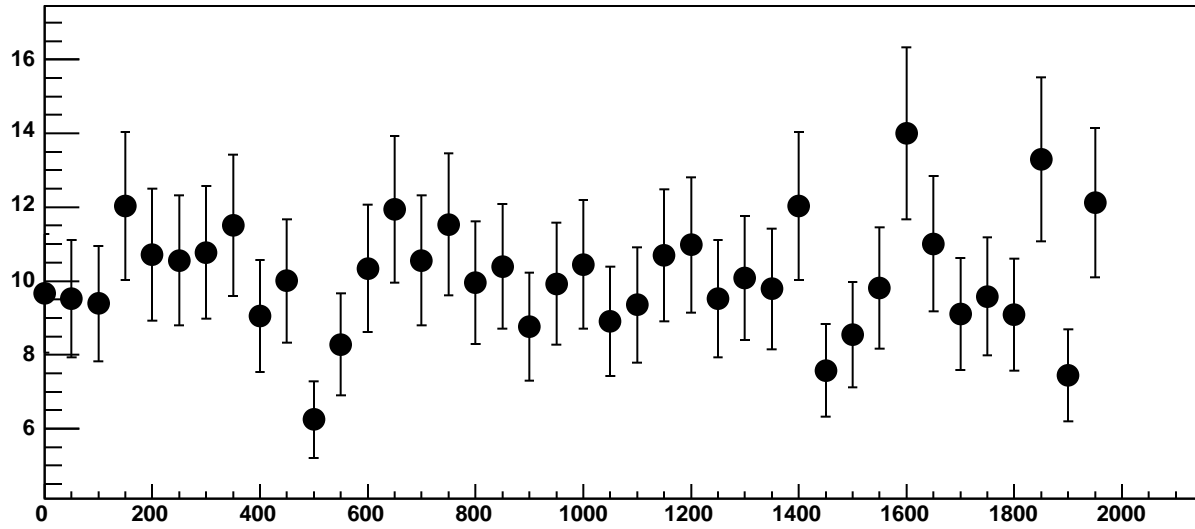
Chip 4, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC



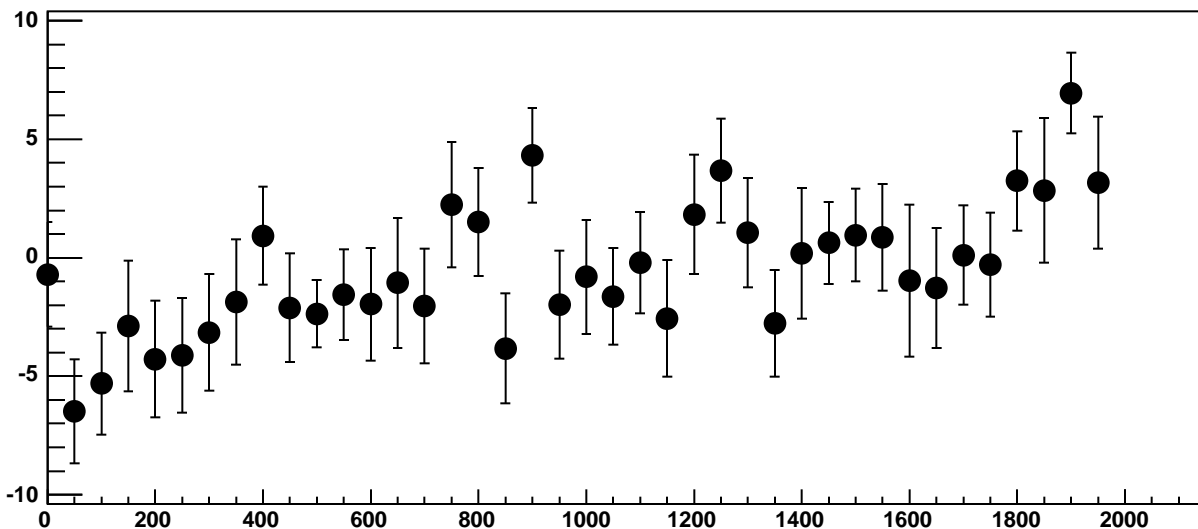
Chip 4, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC



Chip 4, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC

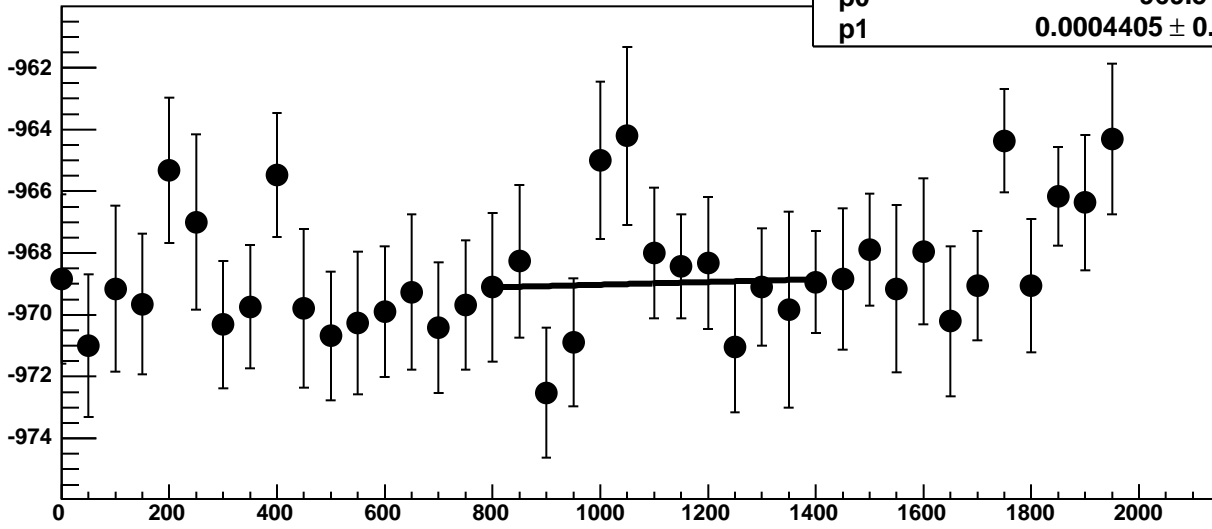


Chip 4, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC

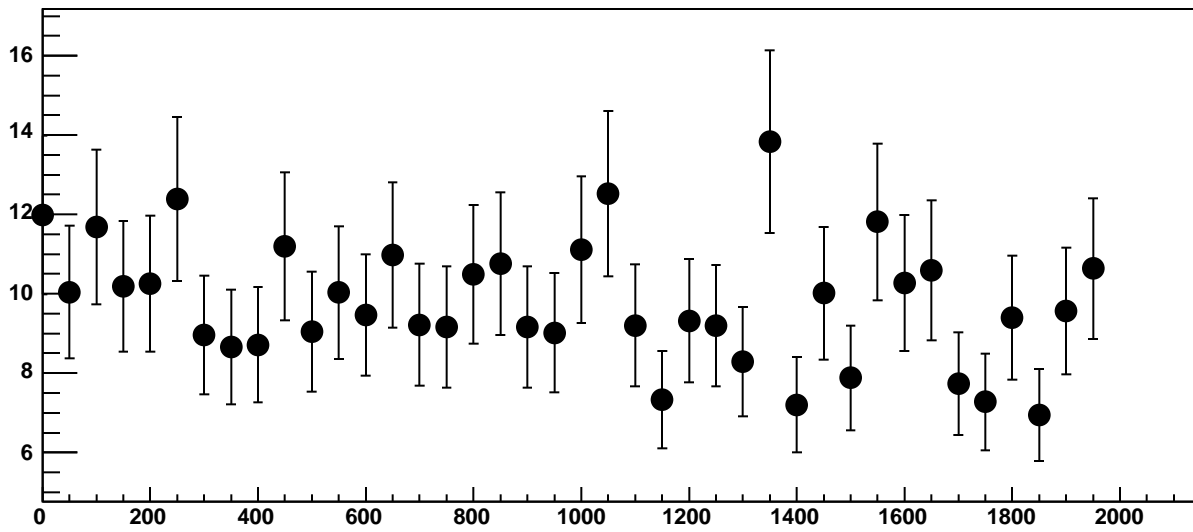


Chip 4, Channel 17, Enable 2!, Hold=30, ADC Mean vs DAC

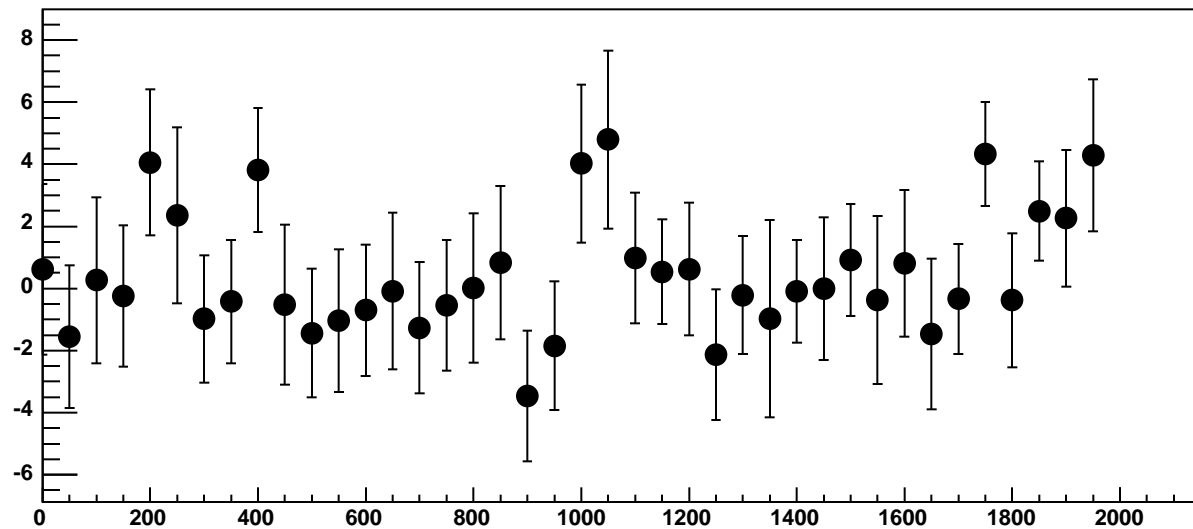
$\chi^2 / \text{ndf}$  10.43 / 11  
p0  $-969.5 \pm 3.628$   
p1  $0.0004405 \pm 0.003183$



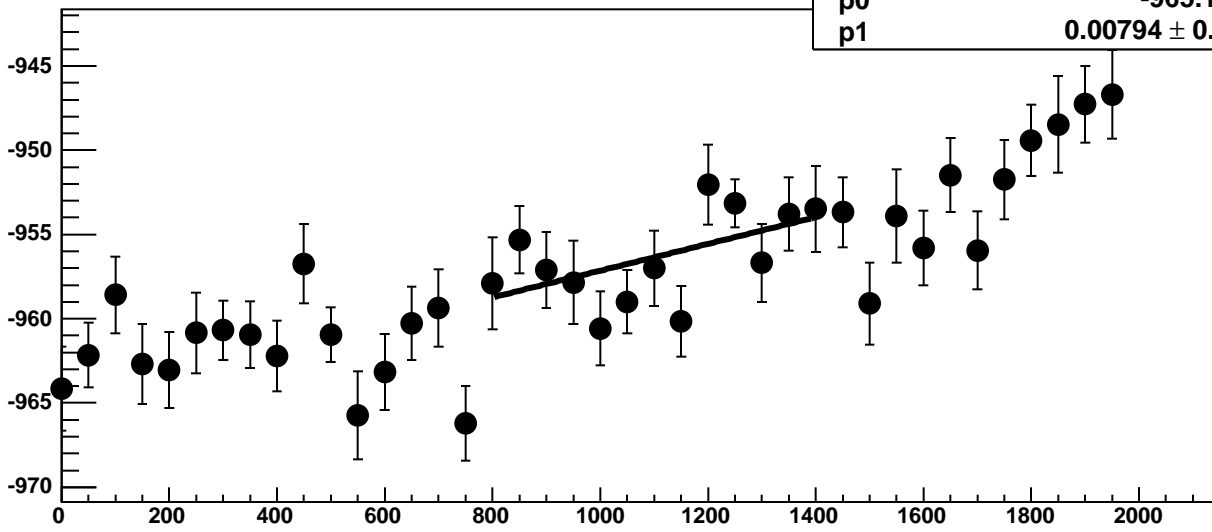
Chip 4, Channel 17, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 4, Channel 17, Enable 2!, Hold=30, ADC Residuals vs DAC

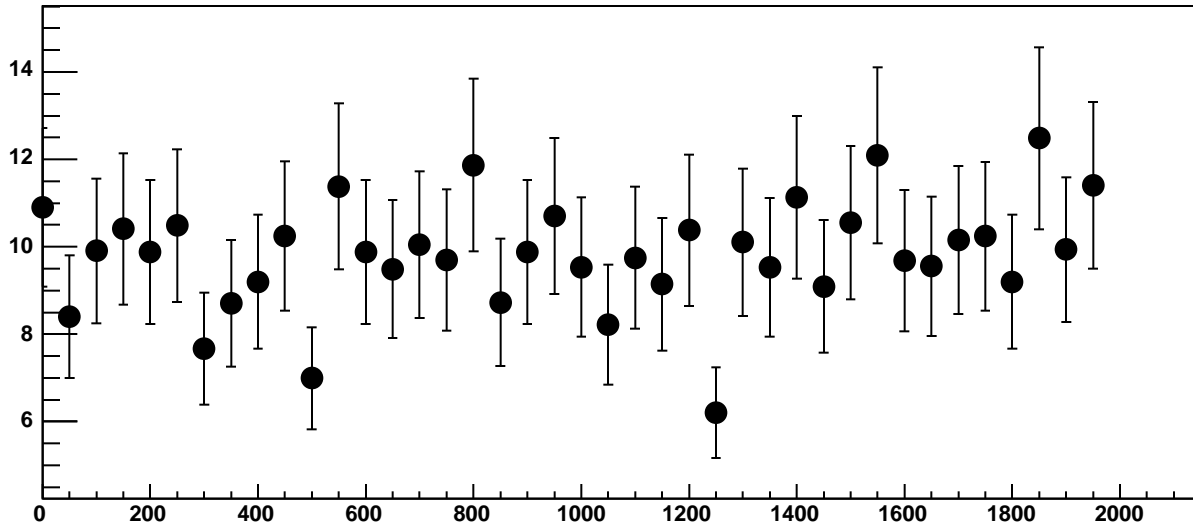


Chip 4, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC

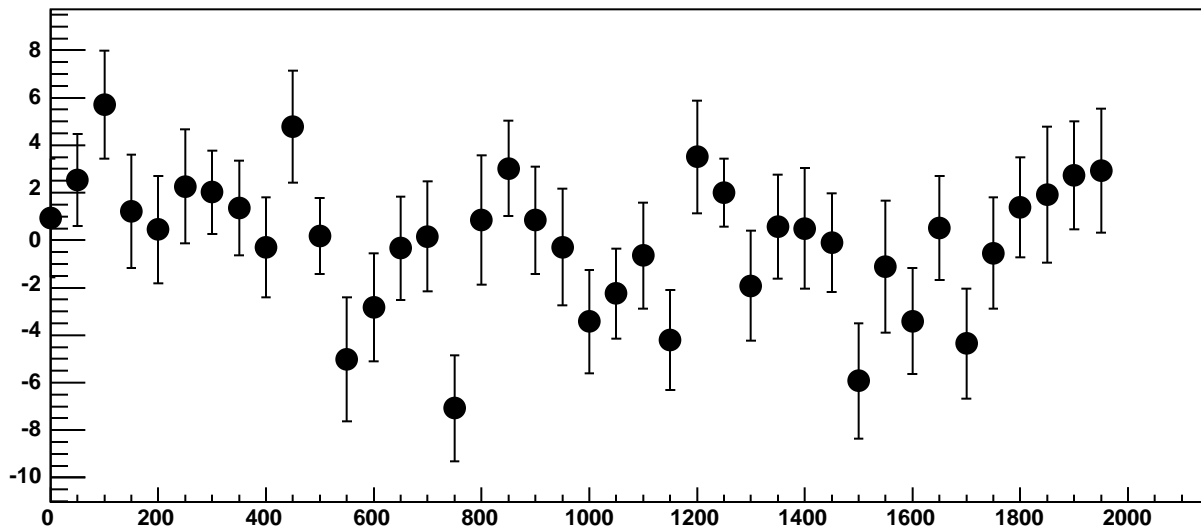


$\chi^2 / \text{ndf}$  15.45 / 11  
p0  $-965.1 \pm 3.77$   
p1  $0.00794 \pm 0.003346$

Chip 4, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC

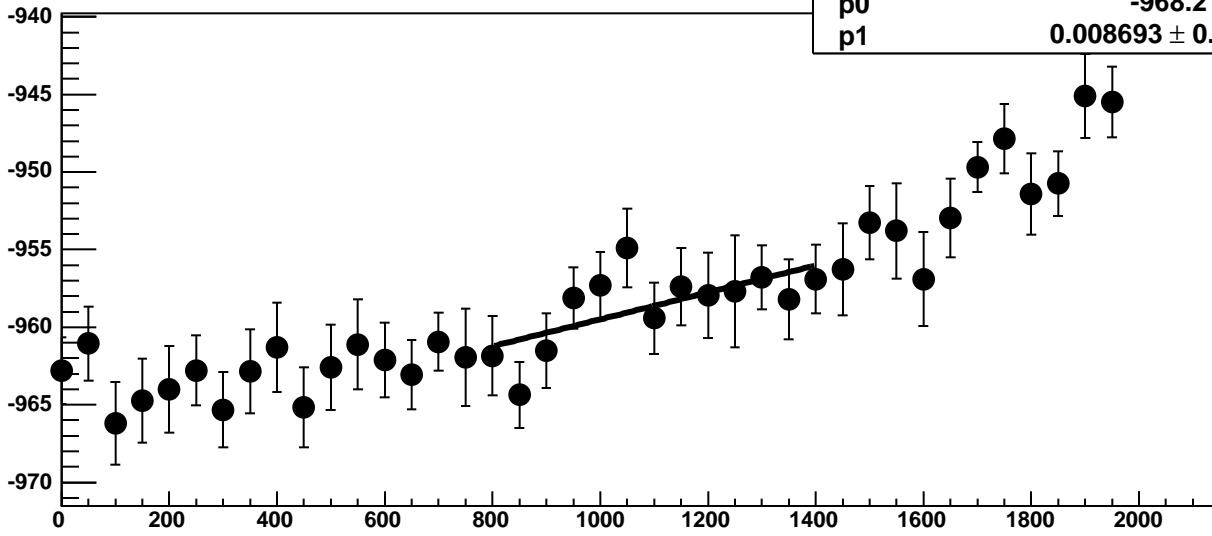


Chip 4, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC



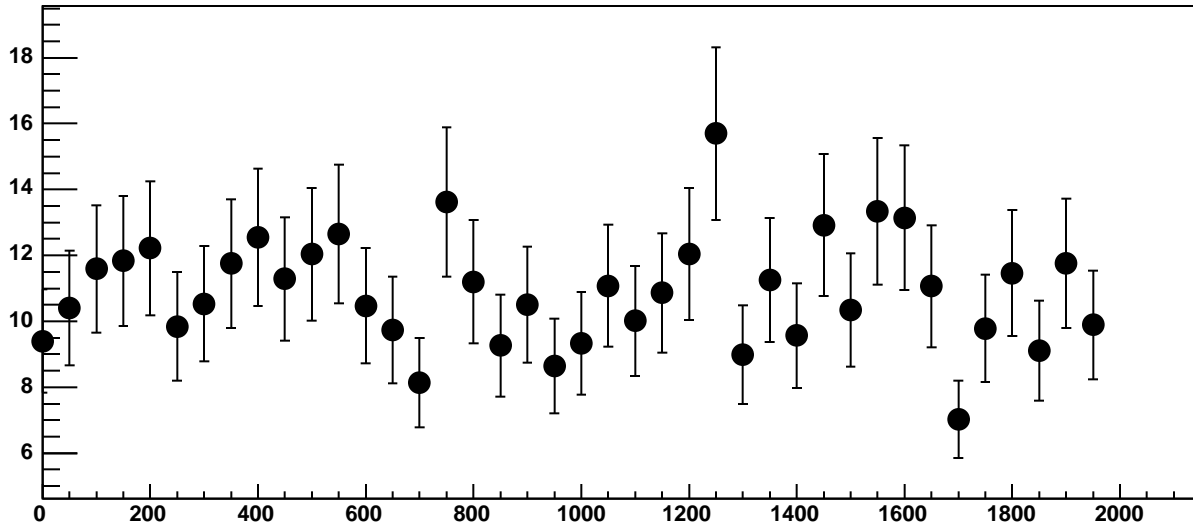


Chip 4, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC

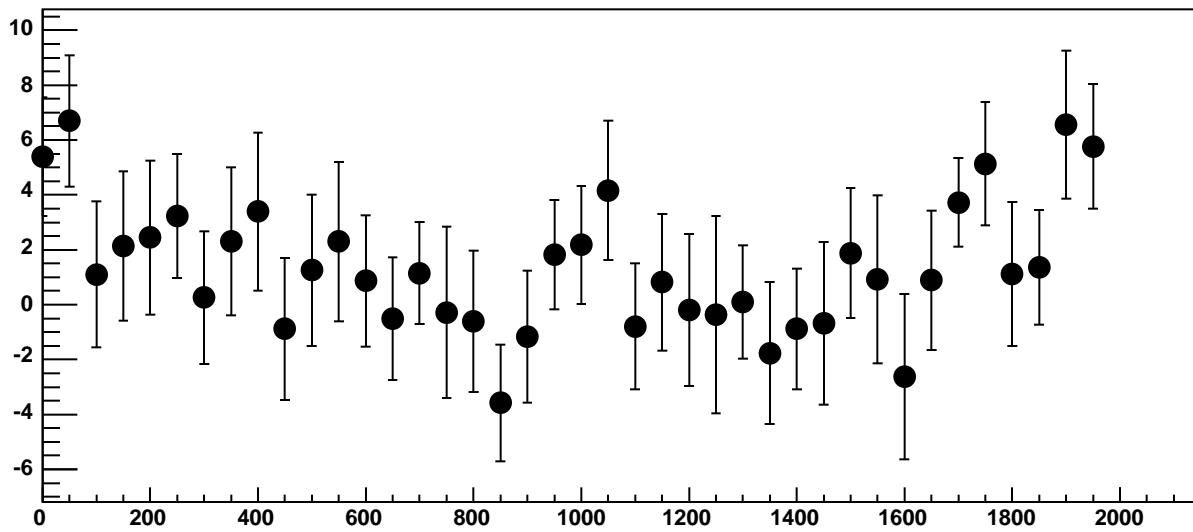


$\chi^2 / \text{ndf}$  8.545 / 11  
p0  $-968.2 \pm 3.833$   
p1  $0.008693 \pm 0.003465$

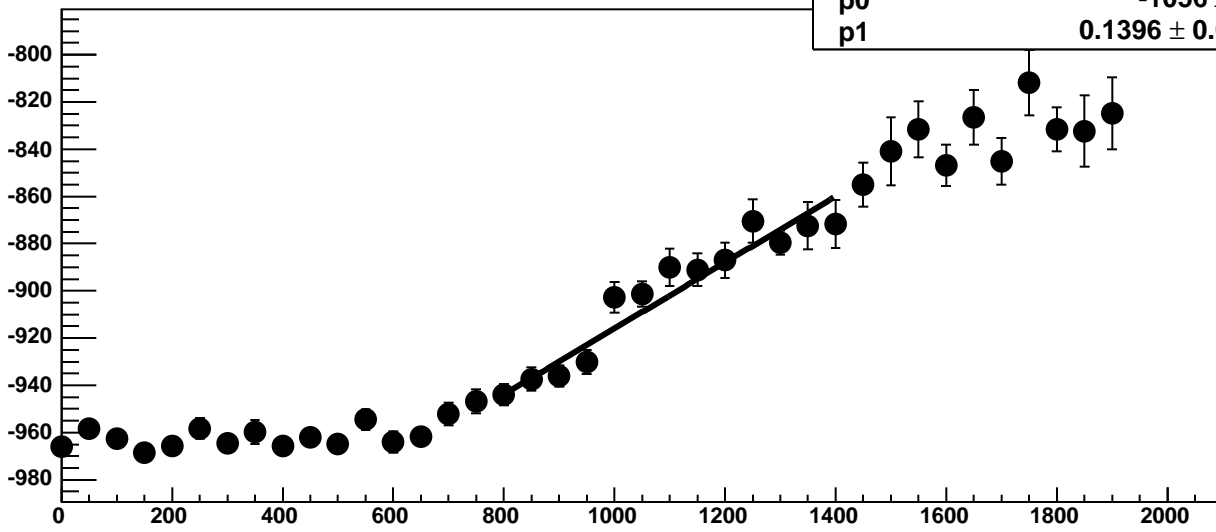
Chip 4, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC



Chip 4, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC

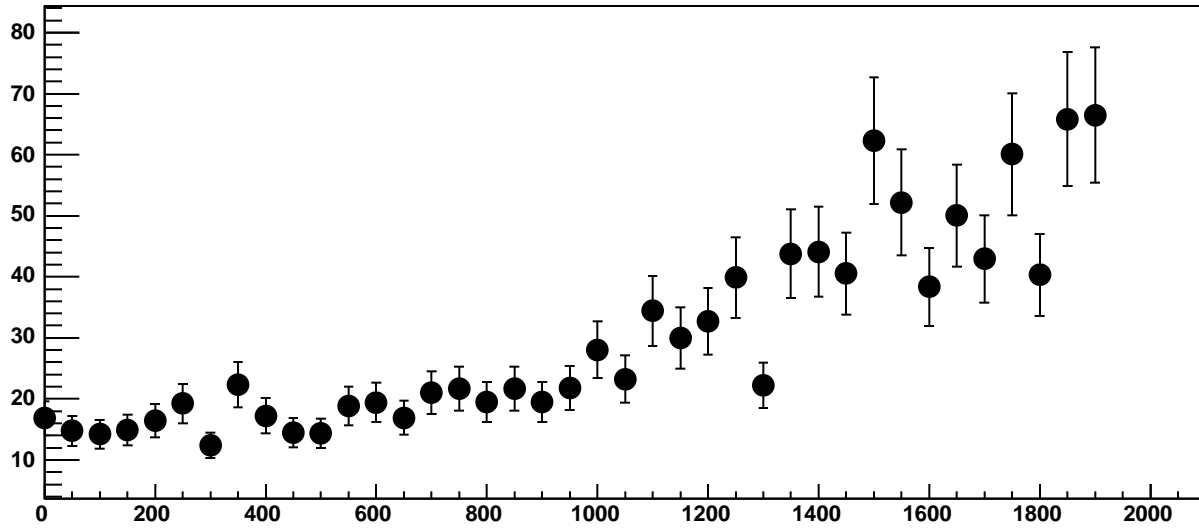


Chip 4, Channel 17, Enable 5, Hold=30, ADC Mean vs DAC

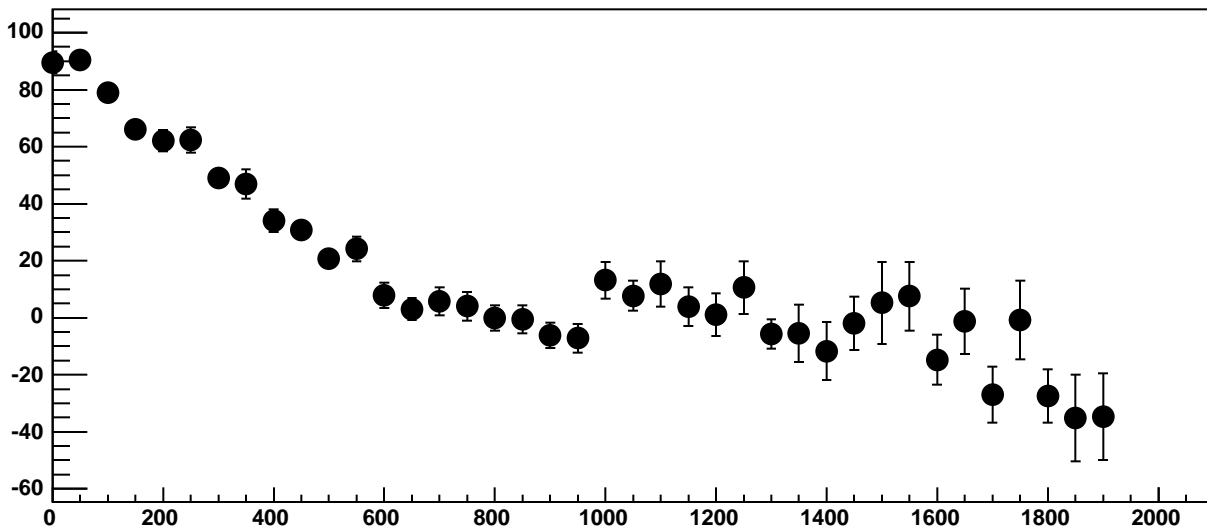


$\chi^2 / \text{ndf}$  17.13 / 11  
p0  $-1056 \pm 9.607$   
p1  $0.1396 \pm 0.009236$

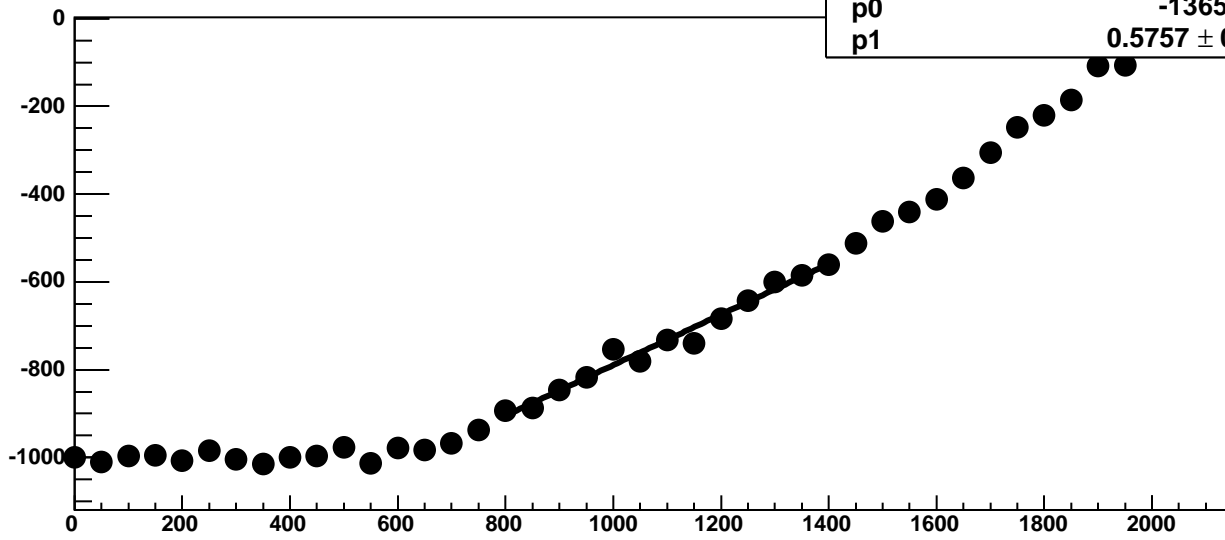
Chip 4, Channel 17, Enable 5, Hold=30, ADC Noise vs DAC



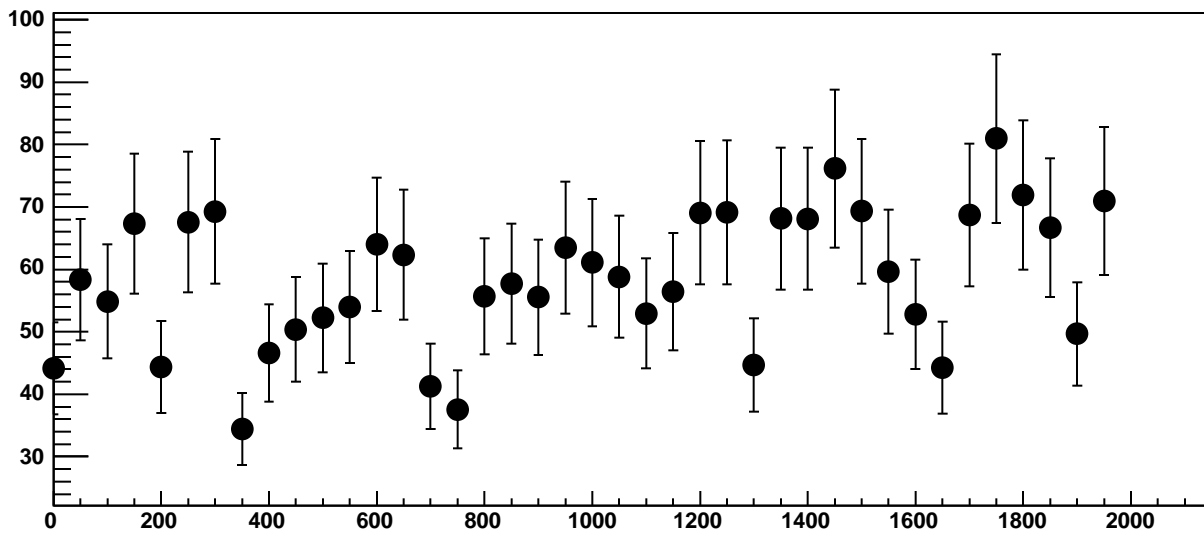
Chip 4, Channel 17, Enable 5, Hold=30, ADC Residuals vs DAC



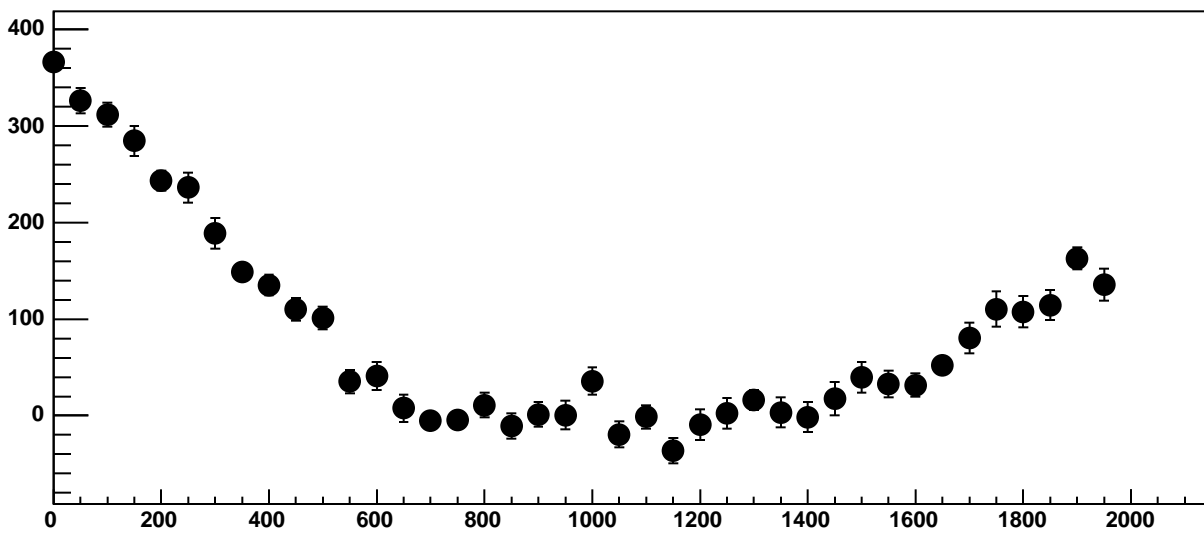
Chip 5, Channel 0, Enable 0, Hold=30, ADC Mean vs DAC



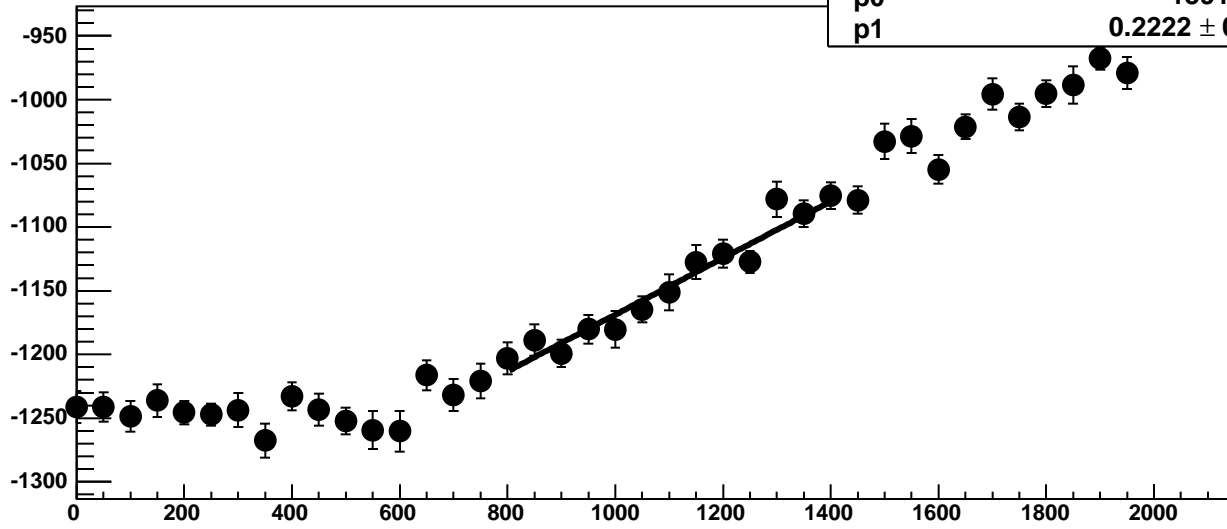
Chip 5, Channel 0, Enable 0, Hold=30, ADC Noise vs DAC



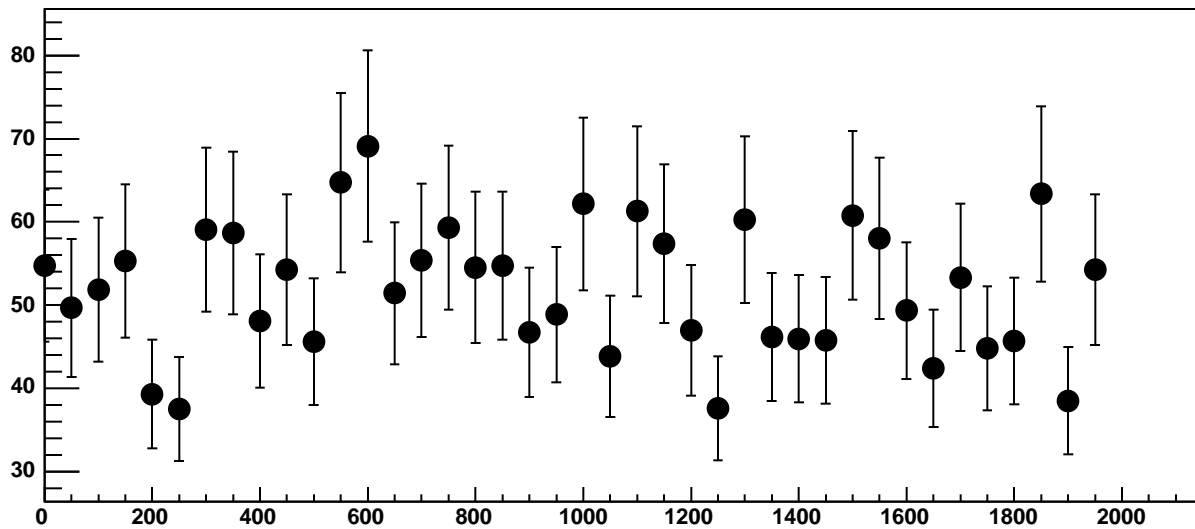
Chip 5, Channel 0, Enable 0, Hold=30, ADC Residuals vs DAC



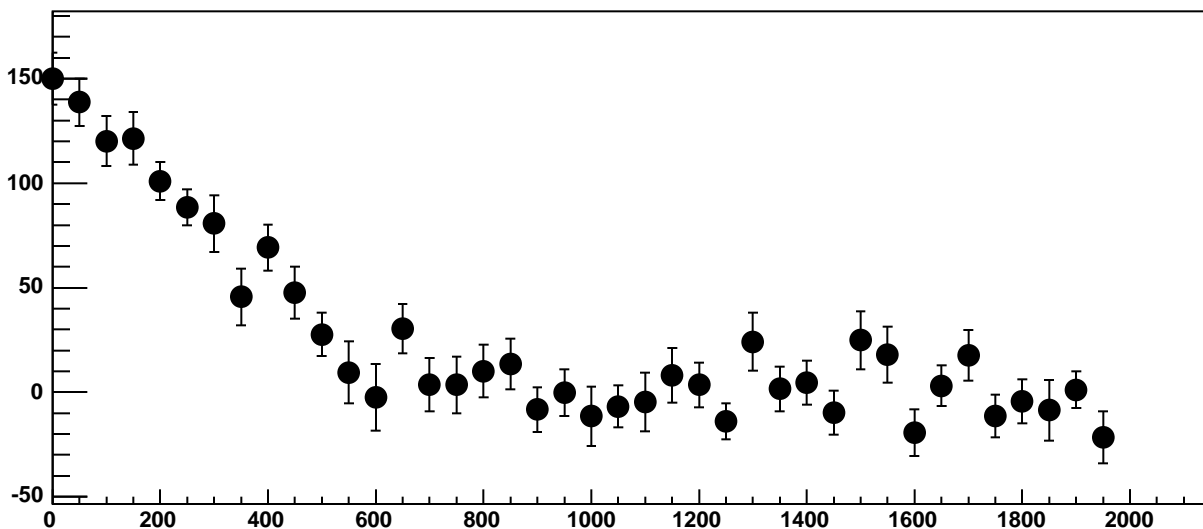
Chip 5, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC



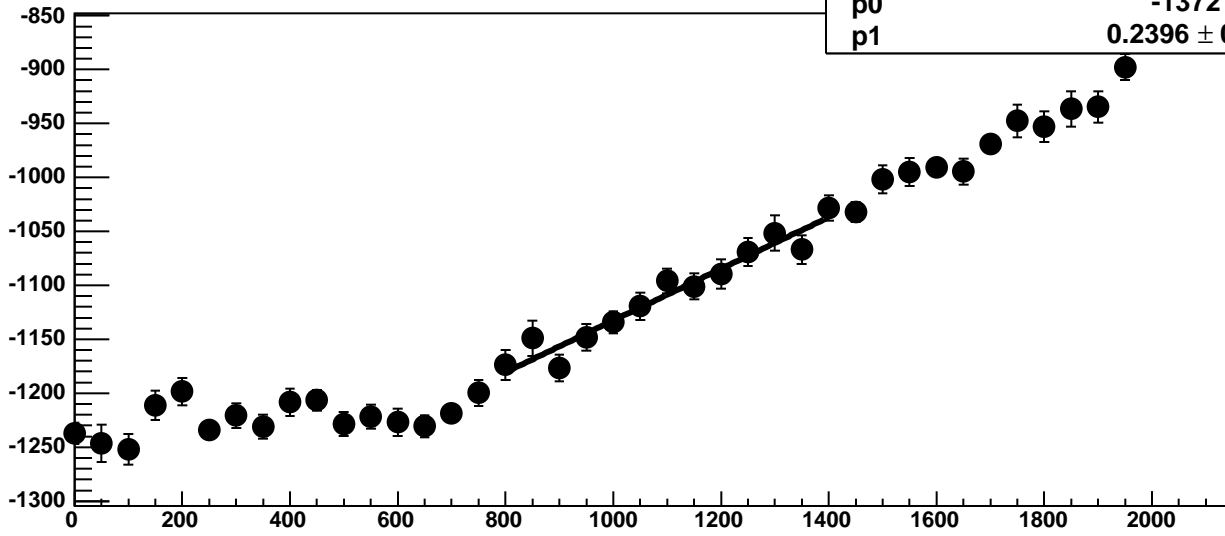
Chip 5, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC



Chip 5, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC

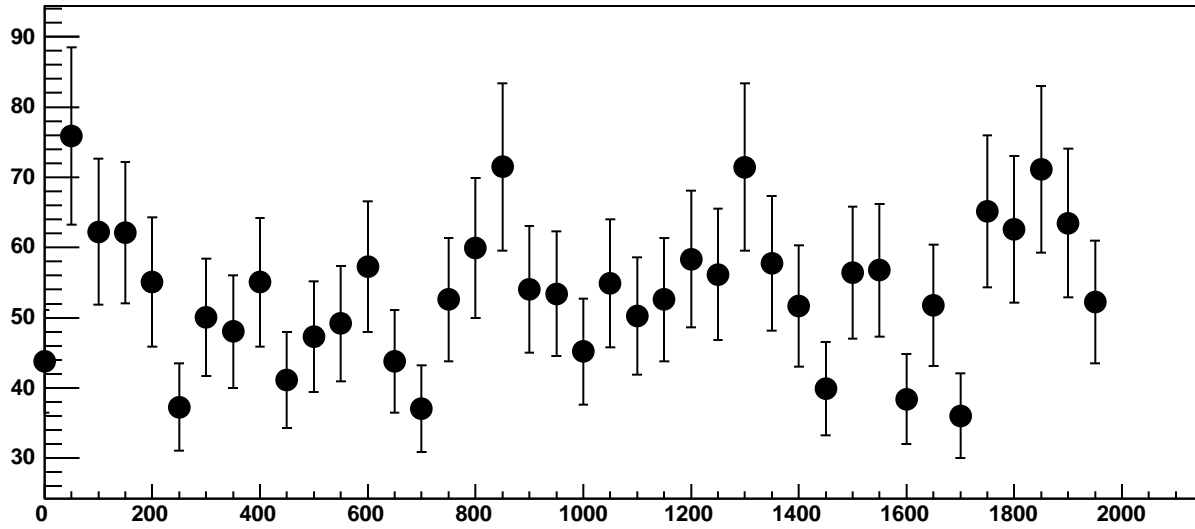


Chip 5, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC

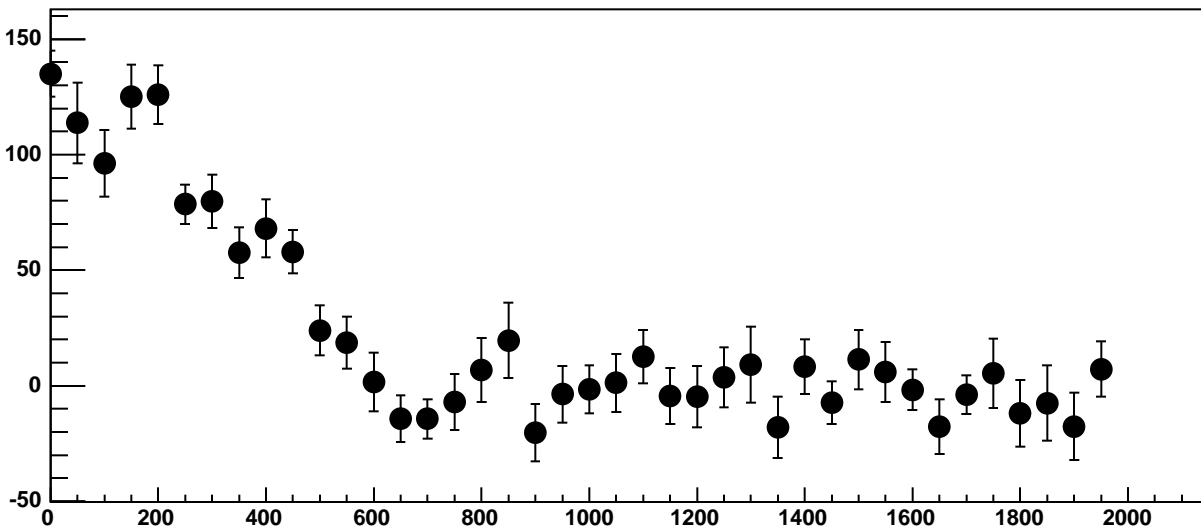


$\chi^2 / \text{ndf}$  8.651 / 11  
p0  $-1372 \pm 21.95$   
p1  $0.2396 \pm 0.01968$

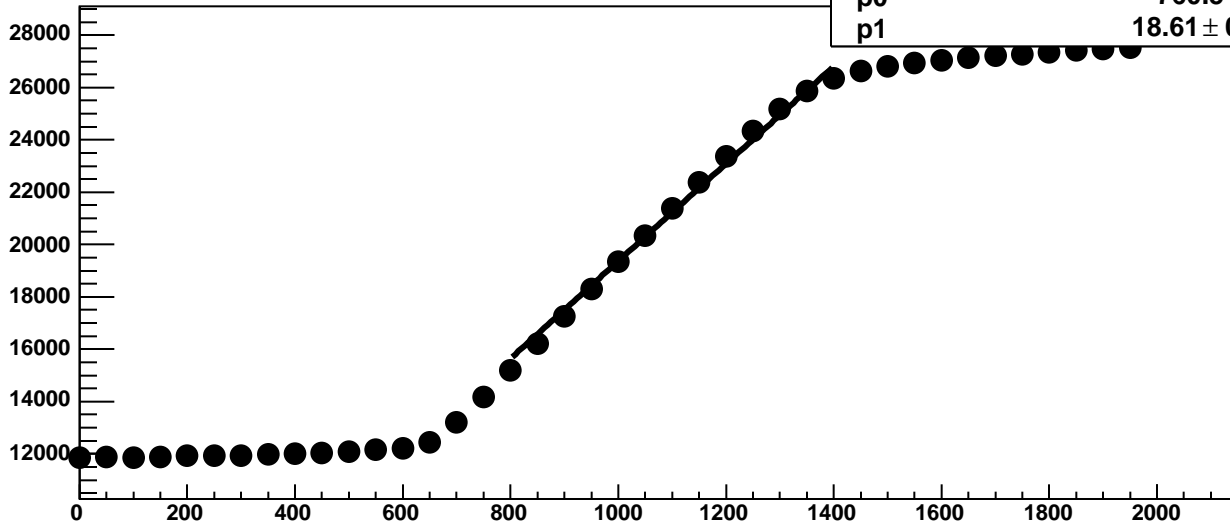
Chip 5, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC



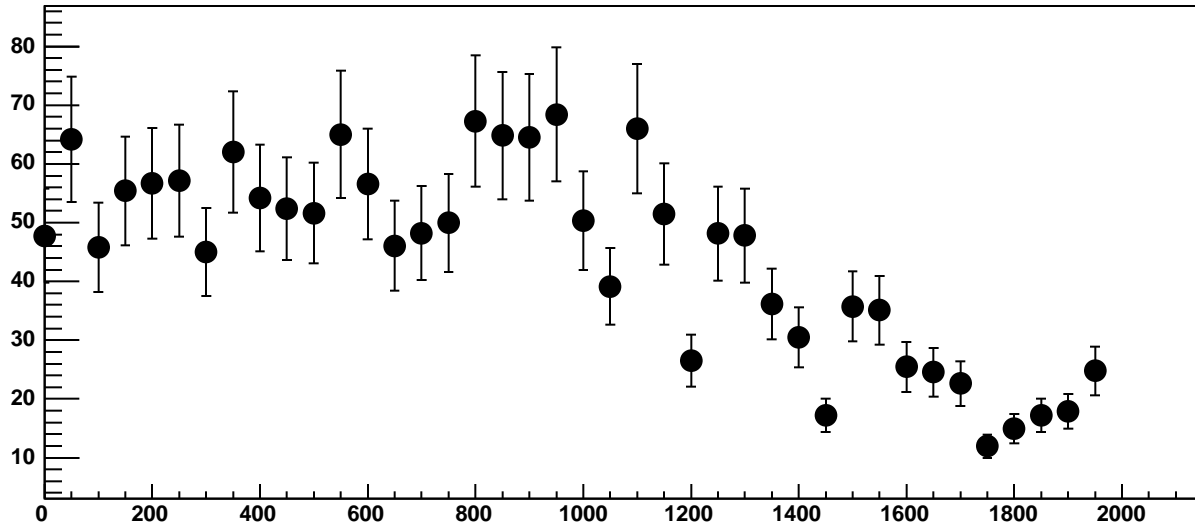
Chip 5, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC



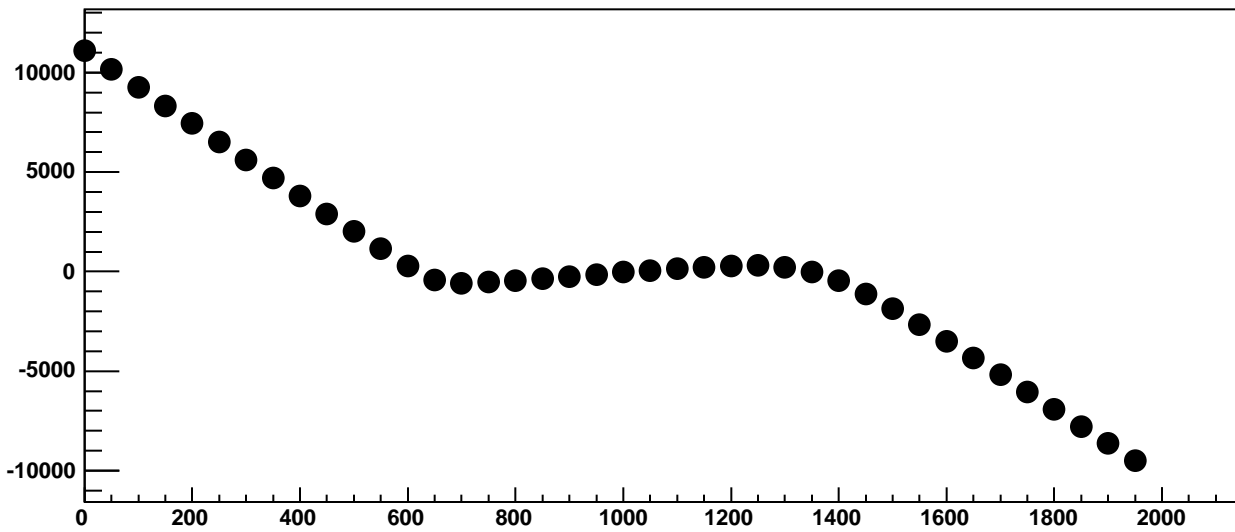
Chip 5, Channel 0, Enable 3!, Hold=30, ADC Mean vs DAC



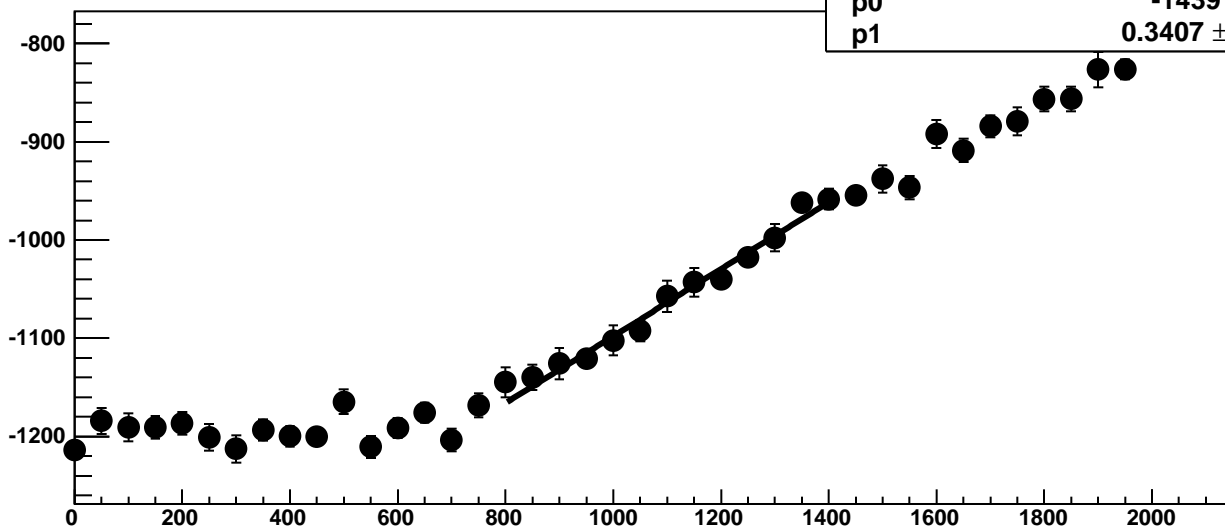
Chip 5, Channel 0, Enable 3!, Hold=30, ADC Noise vs DAC



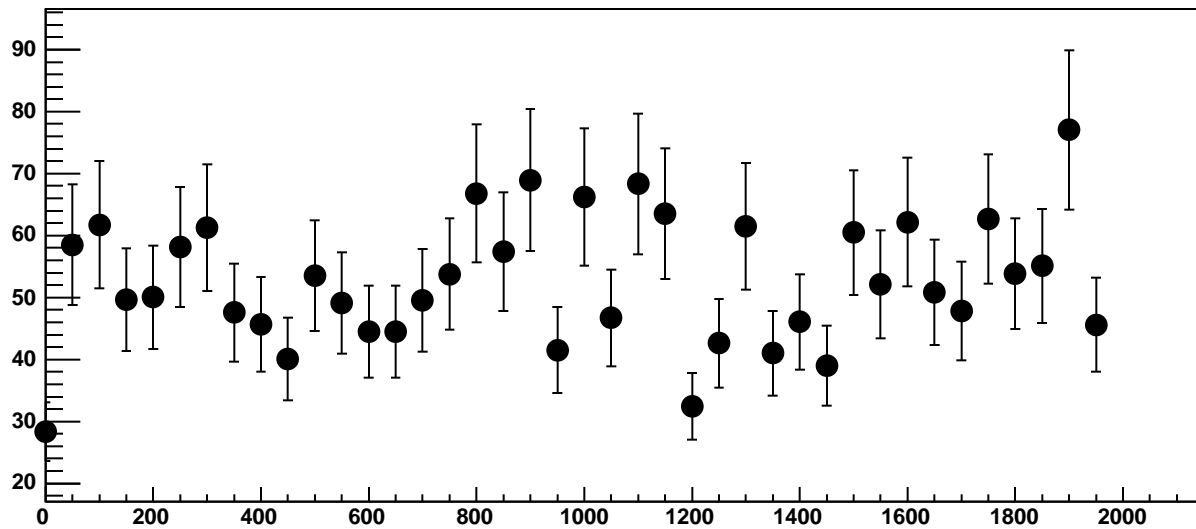
Chip 5, Channel 0, Enable 3!, Hold=30, ADC Residuals vs DAC



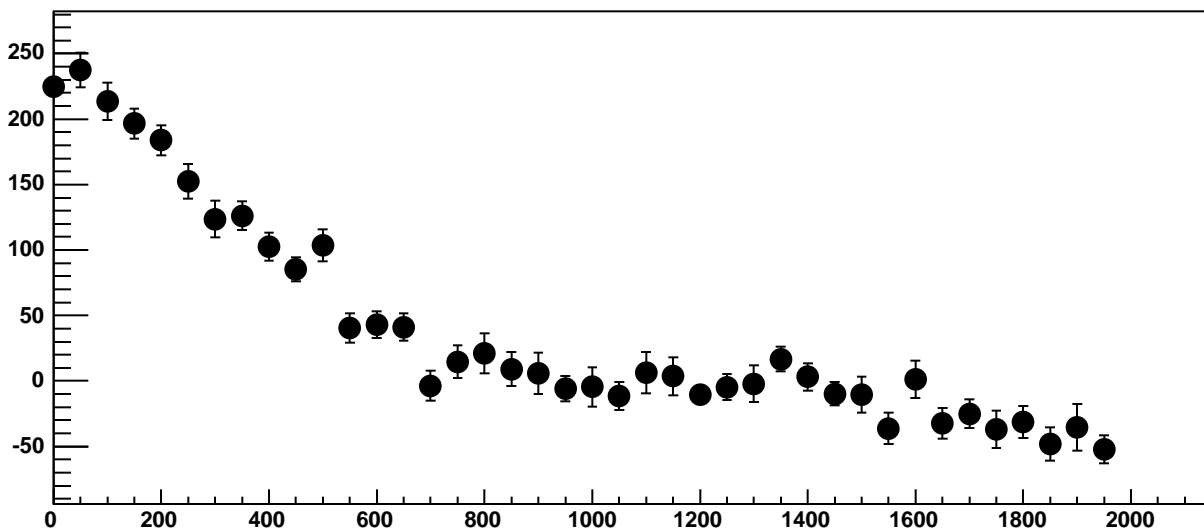
Chip 5, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC



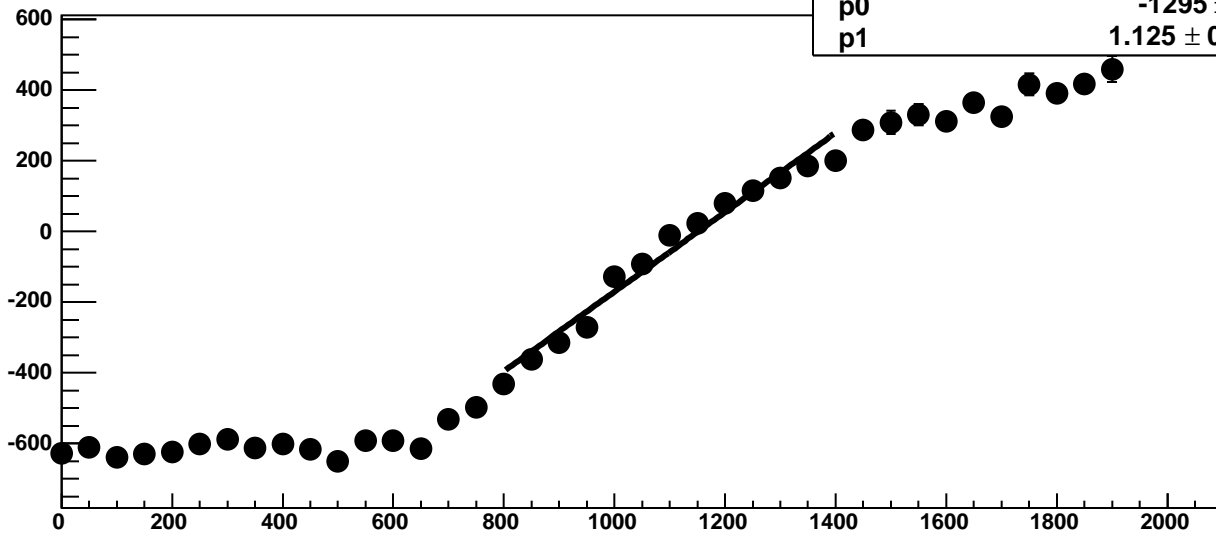
Chip 5, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC



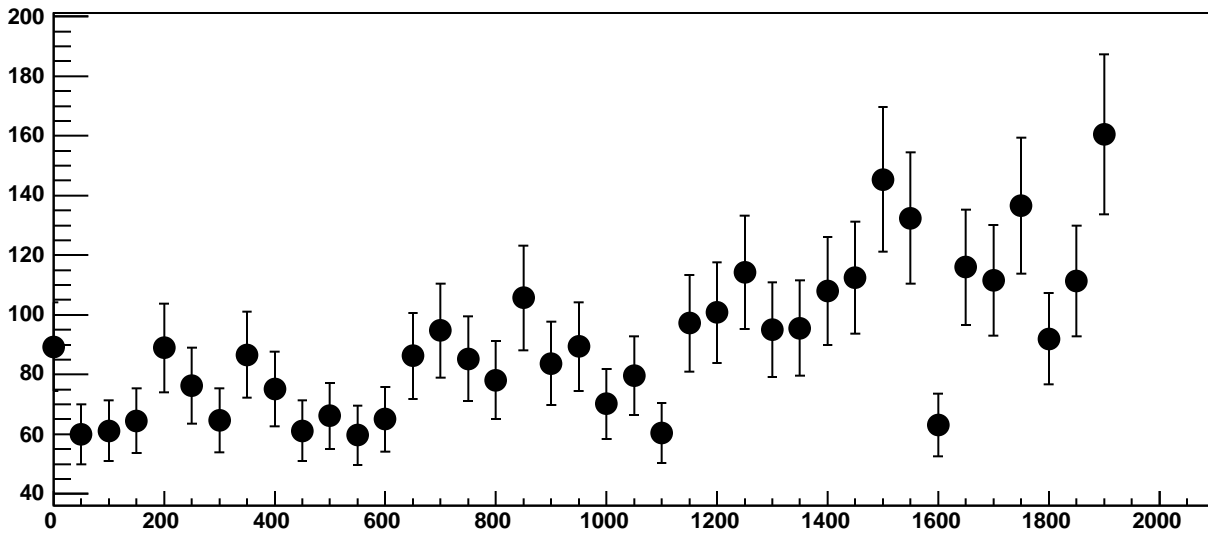
Chip 5, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC



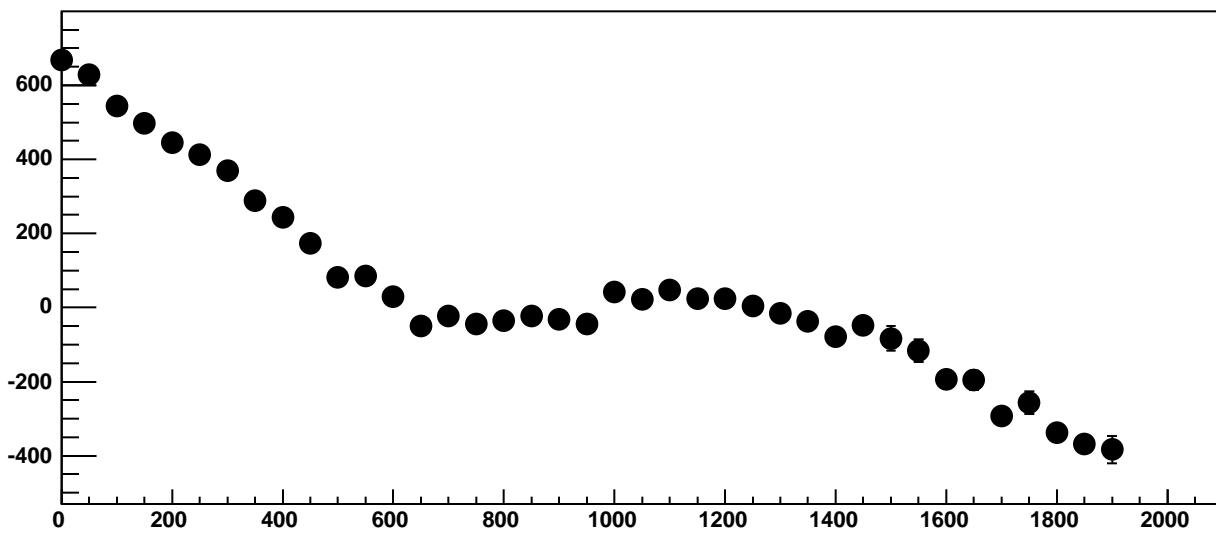
Chip 5, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC



Chip 5, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC

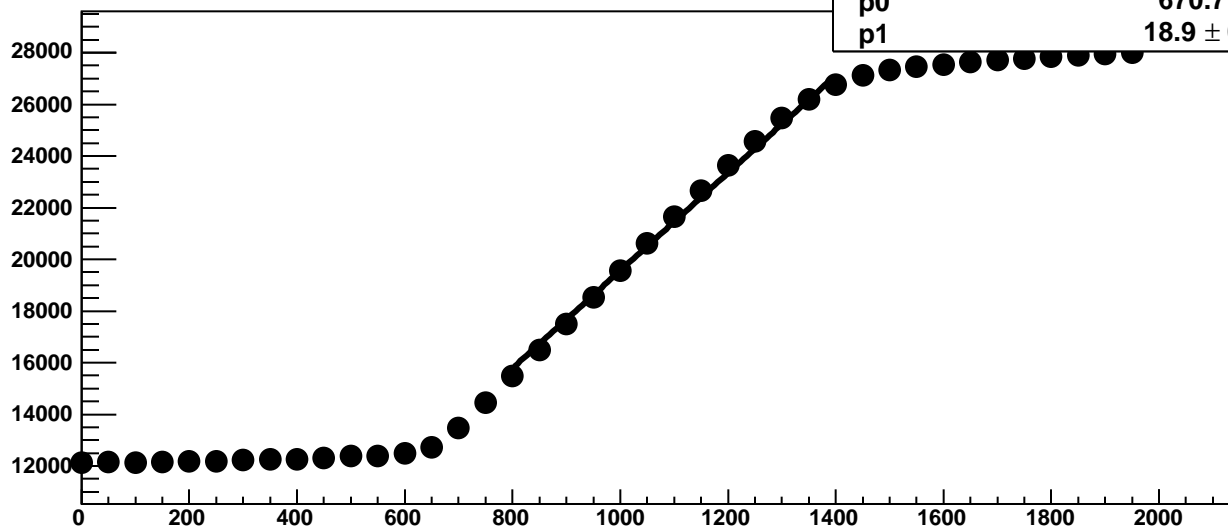


Chip 5, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC

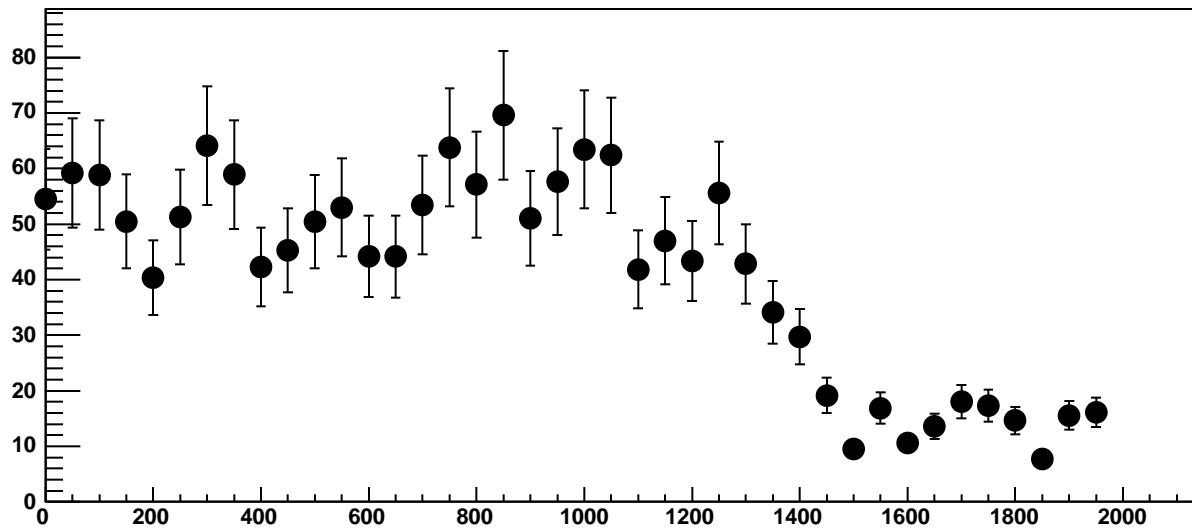




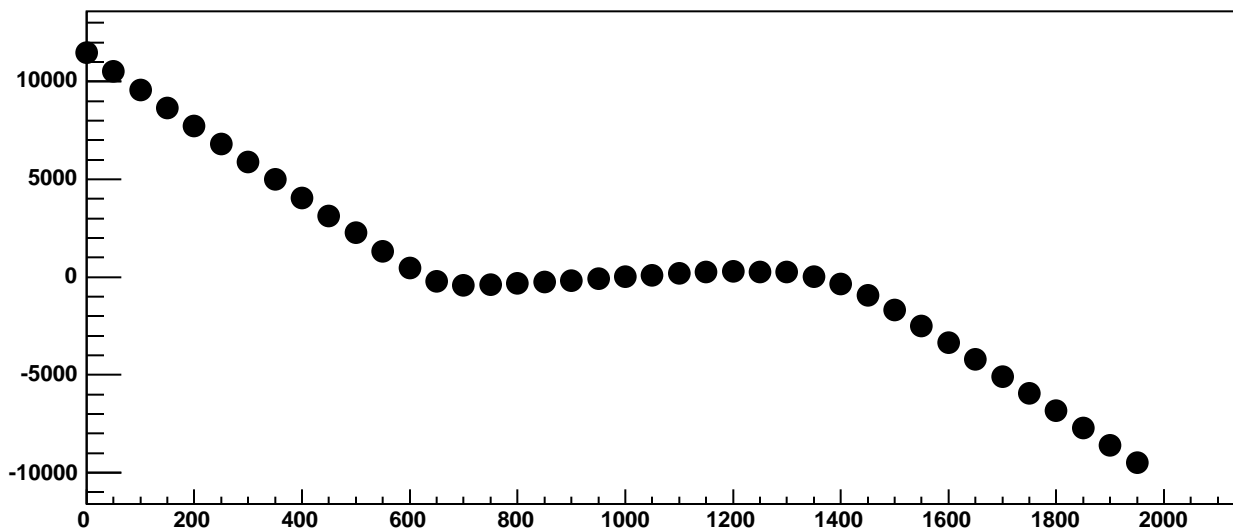
Chip 5, Channel 1, Enable 0!, Hold=30, ADC Mean vs DAC



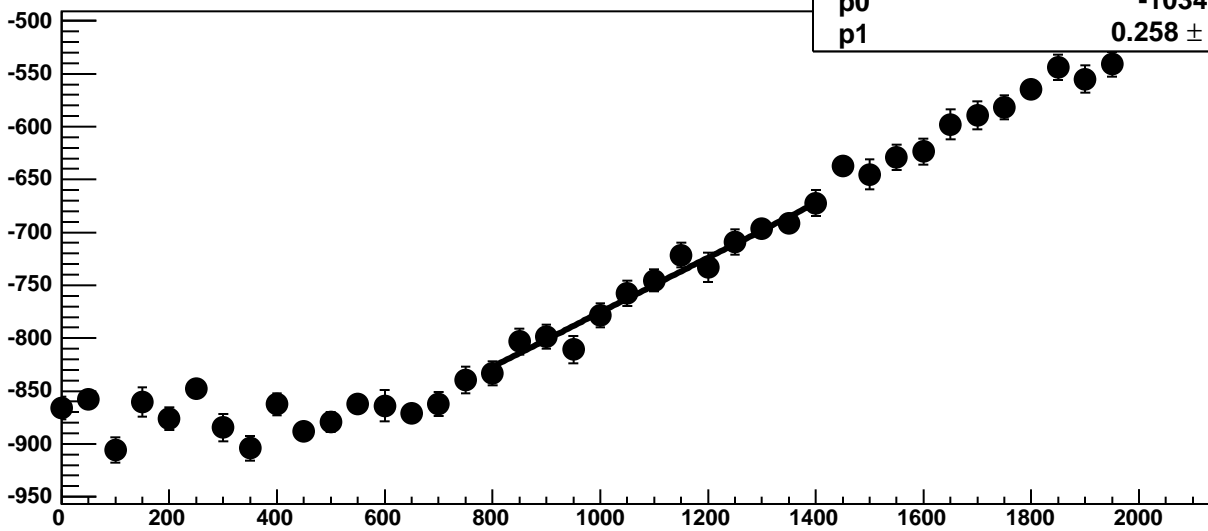
Chip 5, Channel 1, Enable 0!, Hold=30, ADC Noise vs DAC



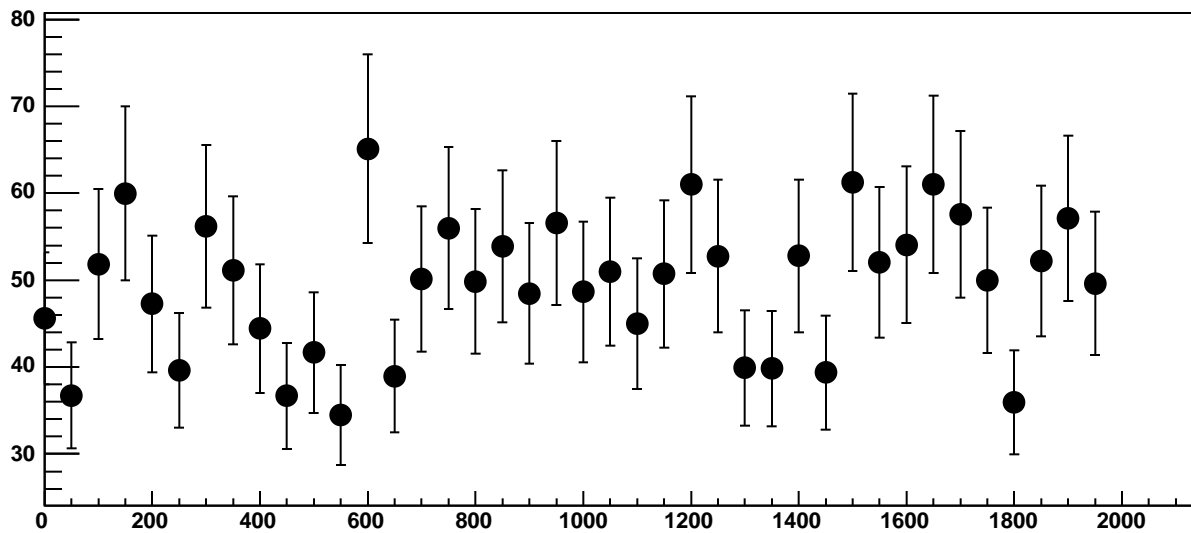
Chip 5, Channel 1, Enable 0!, Hold=30, ADC Residuals vs DAC



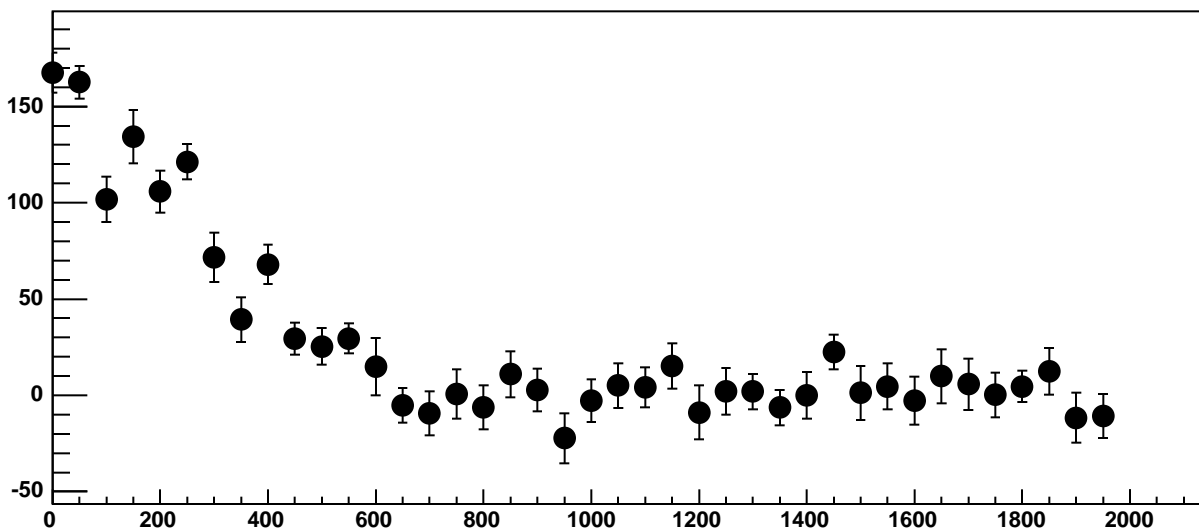
Chip 5, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC



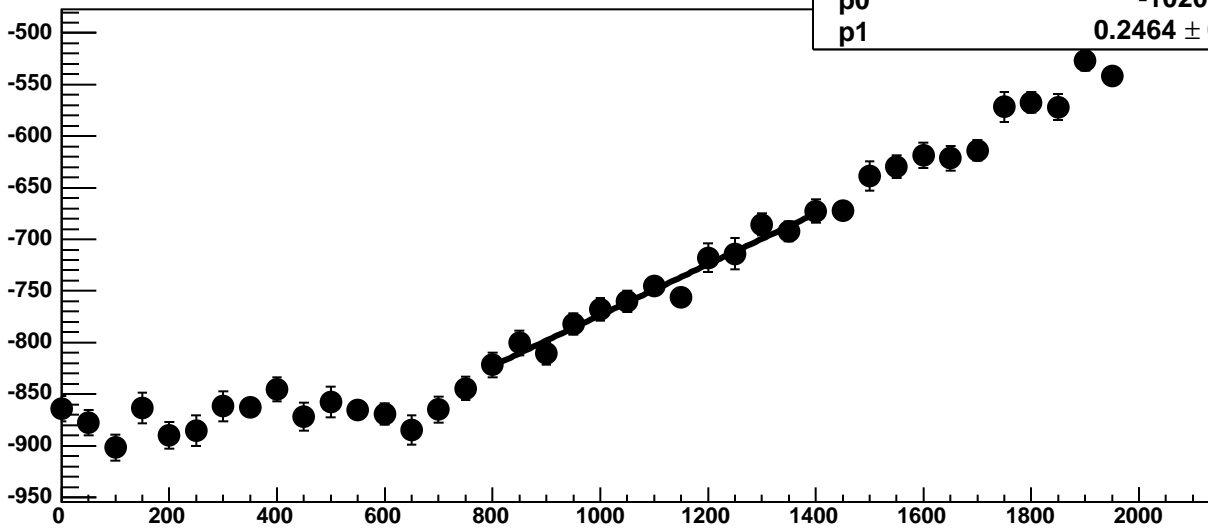
Chip 5, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC



Chip 5, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC

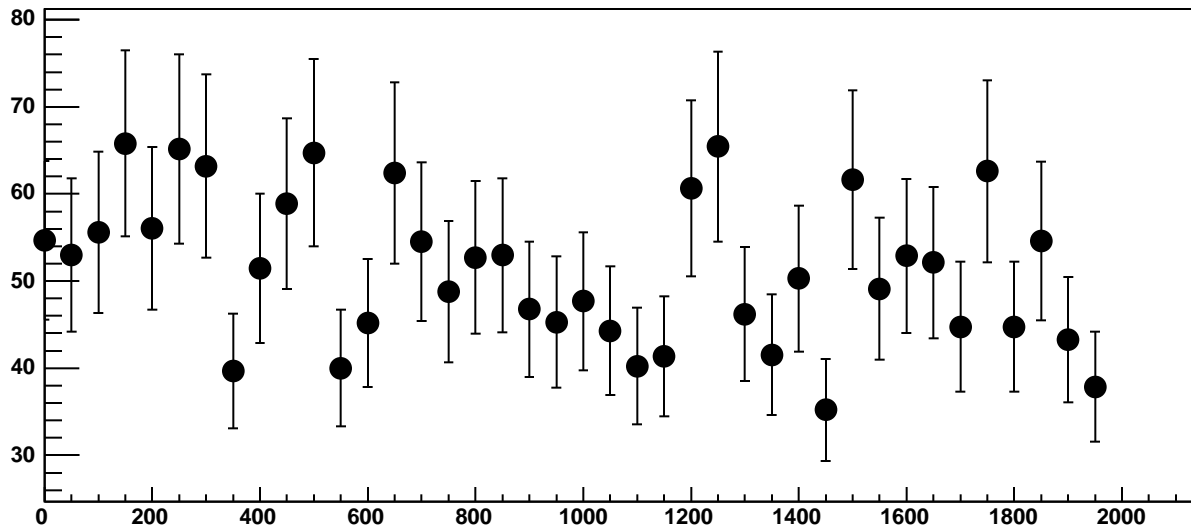


Chip 5, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC

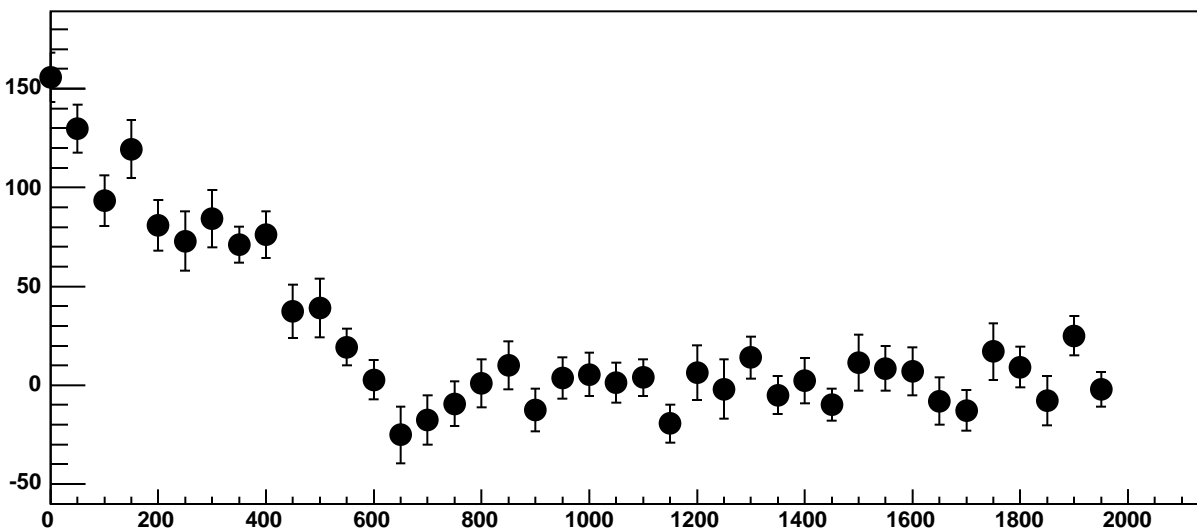


$\chi^2 / \text{ndf}$  9.084 / 11  
p0  $-1020 \pm 18.63$   
p1  $0.2464 \pm 0.01666$

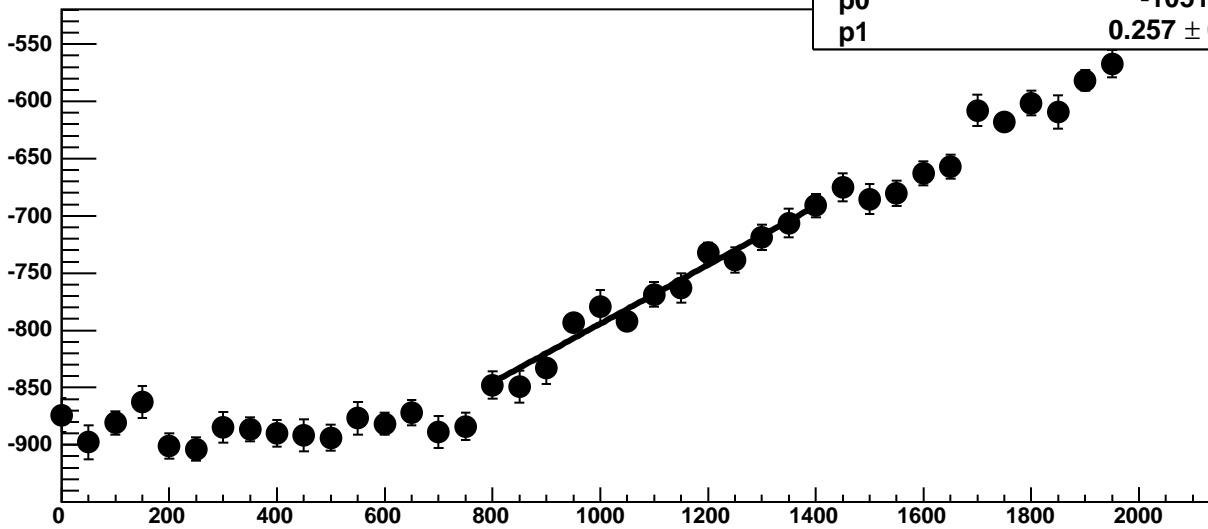
Chip 5, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC



Chip 5, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC

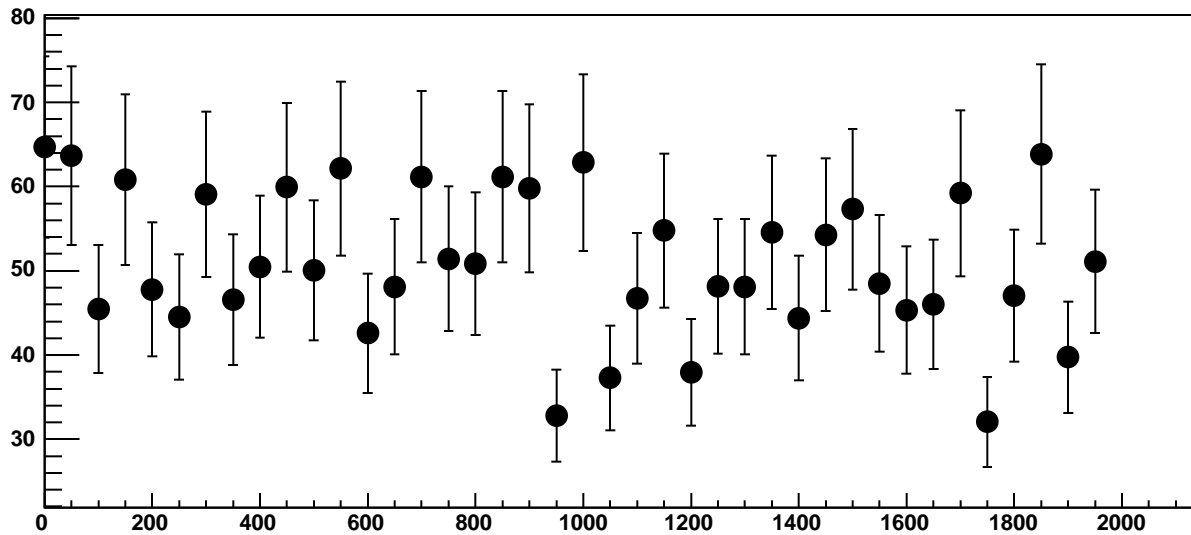


Chip 5, Channel 1, Enable 3, Hold=30, ADC Mean vs DAC

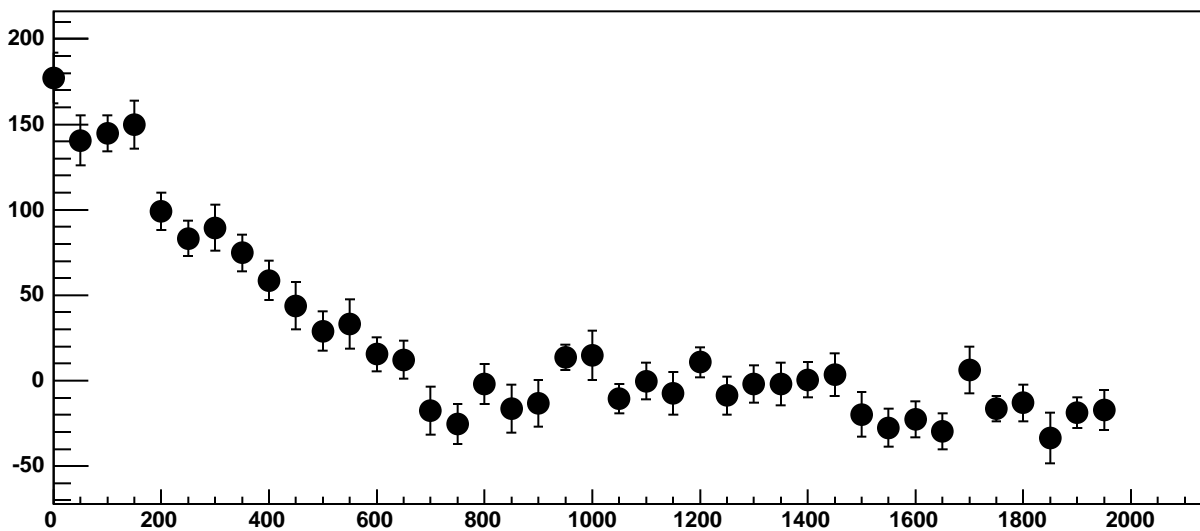


$\chi^2 / \text{ndf}$  10.74 / 11  
p0  $-1051 \pm 18.68$   
p1  $0.257 \pm 0.01668$

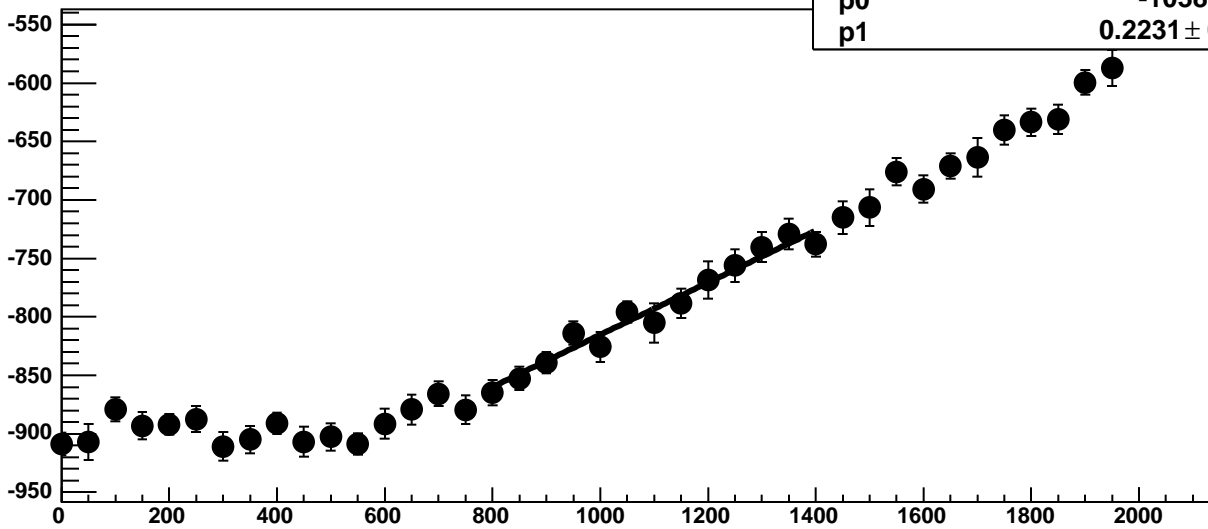
Chip 5, Channel 1, Enable 3, Hold=30, ADC Noise vs DAC



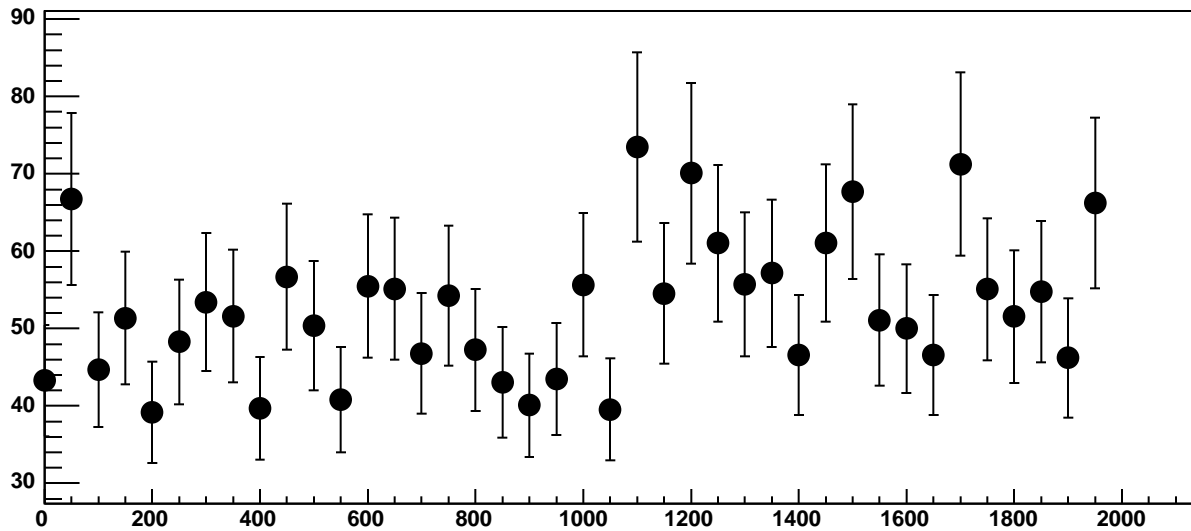
Chip 5, Channel 1, Enable 3, Hold=30, ADC Residuals vs DAC



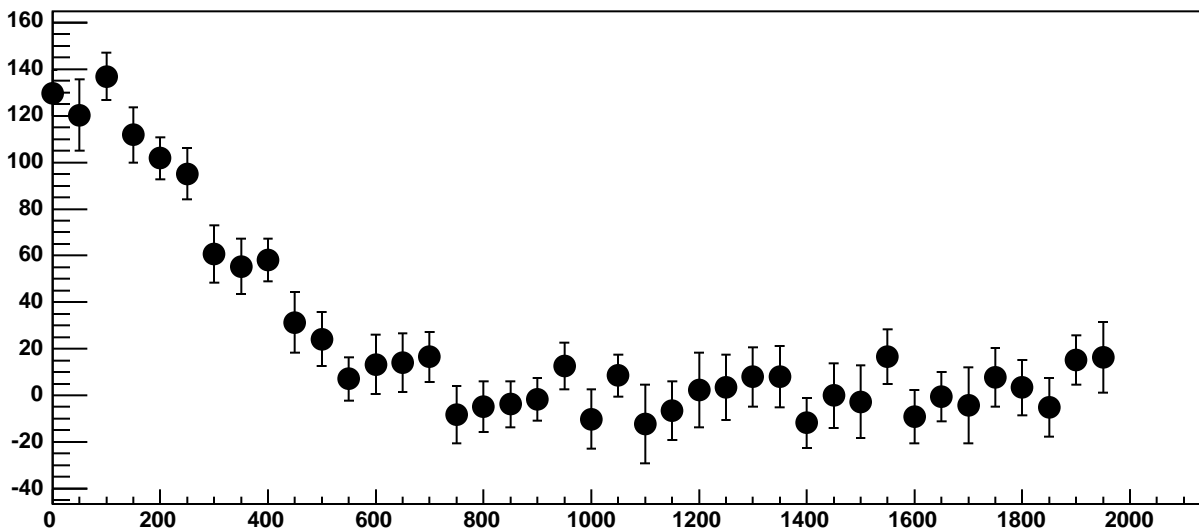
Chip 5, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC



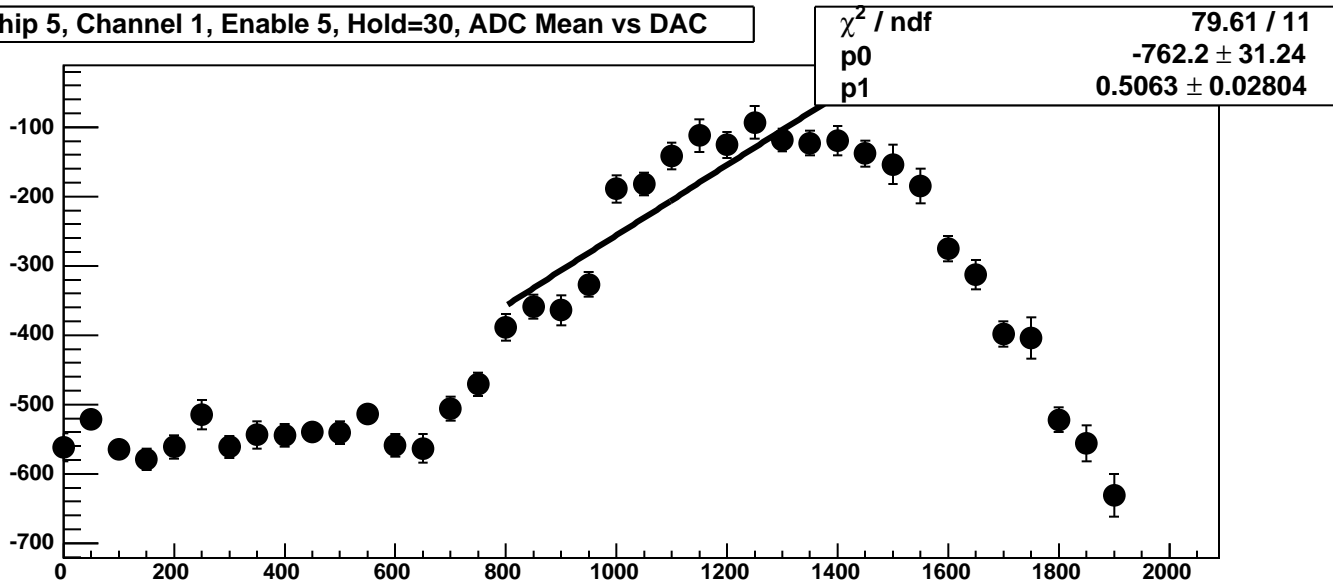
Chip 5, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC



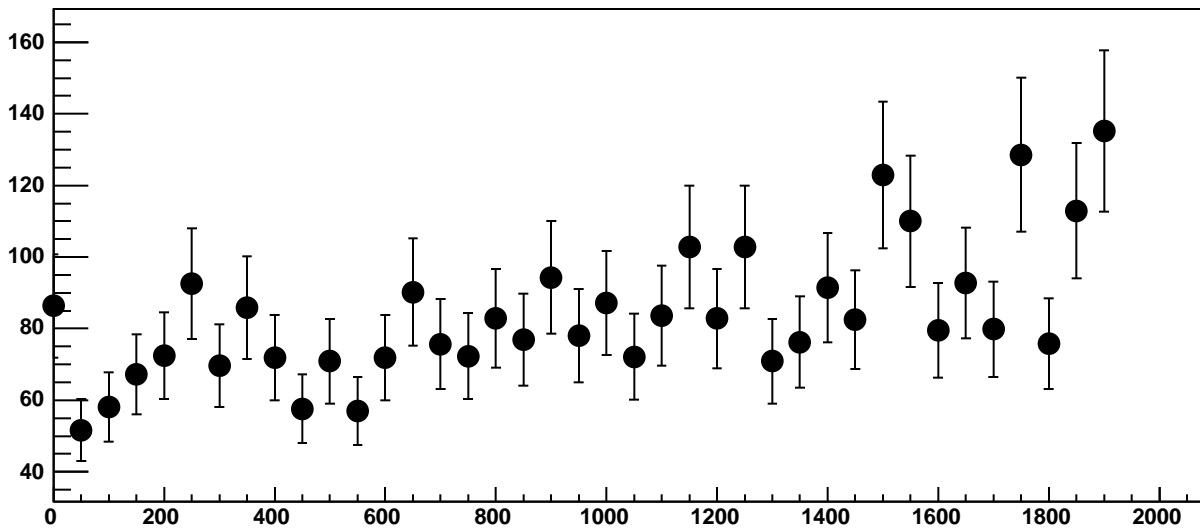
Chip 5, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC



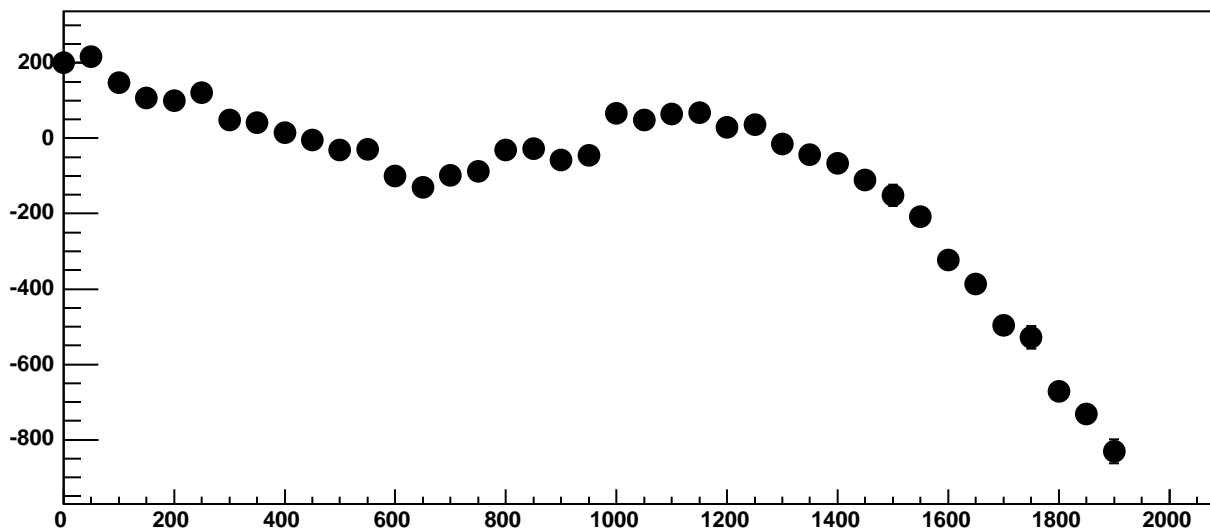
Chip 5, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC



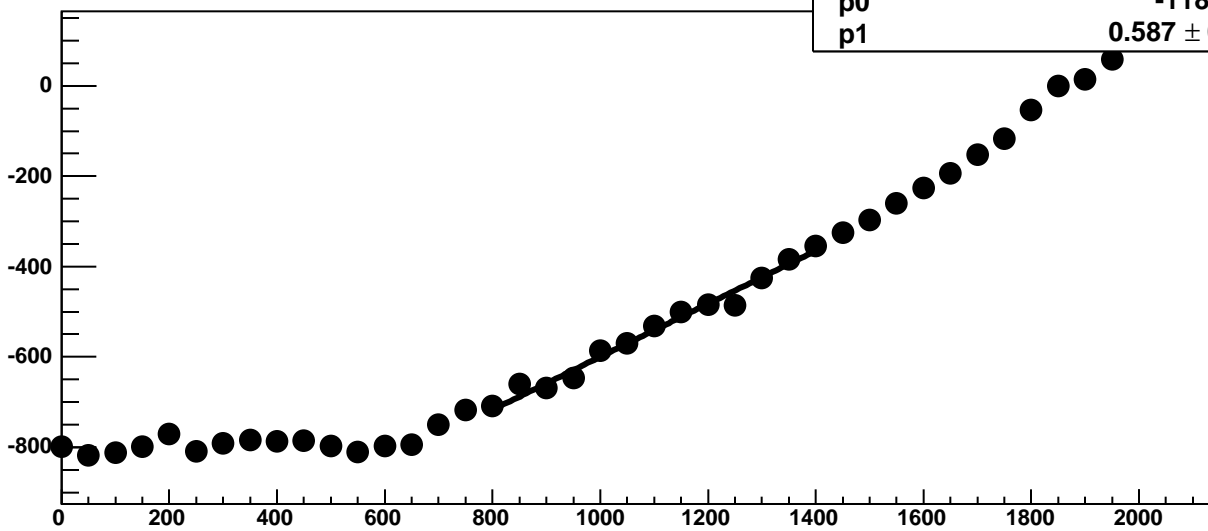
Chip 5, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC



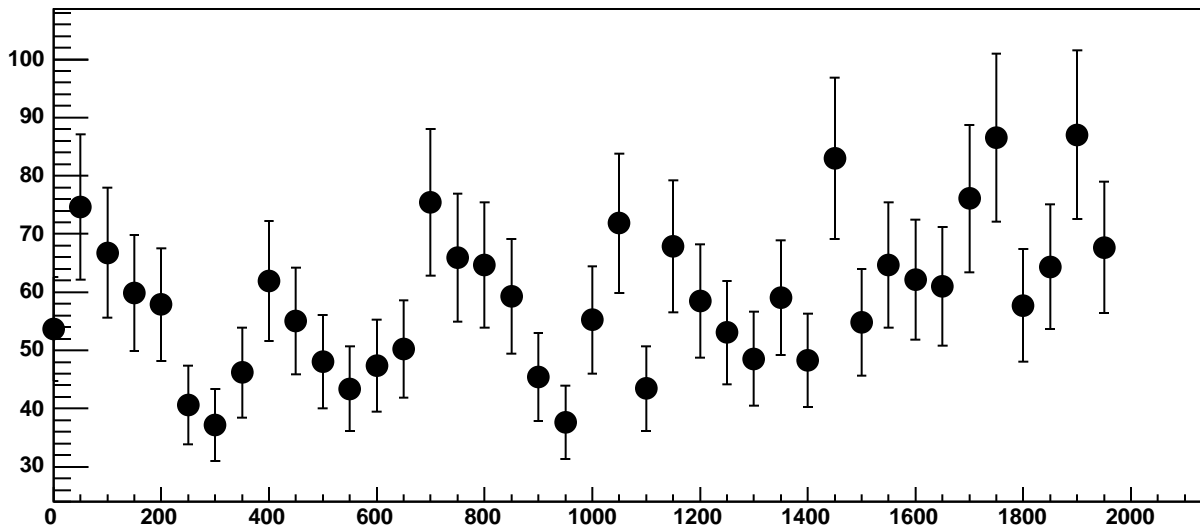
Chip 5, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC



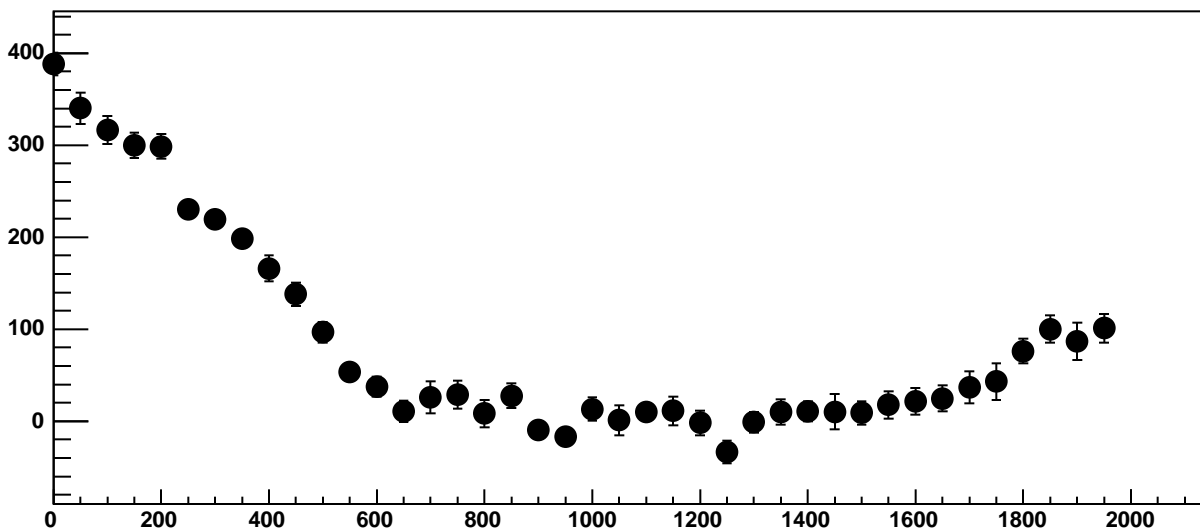
Chip 5, Channel 2, Enable 0, Hold=30, ADC Mean vs DAC



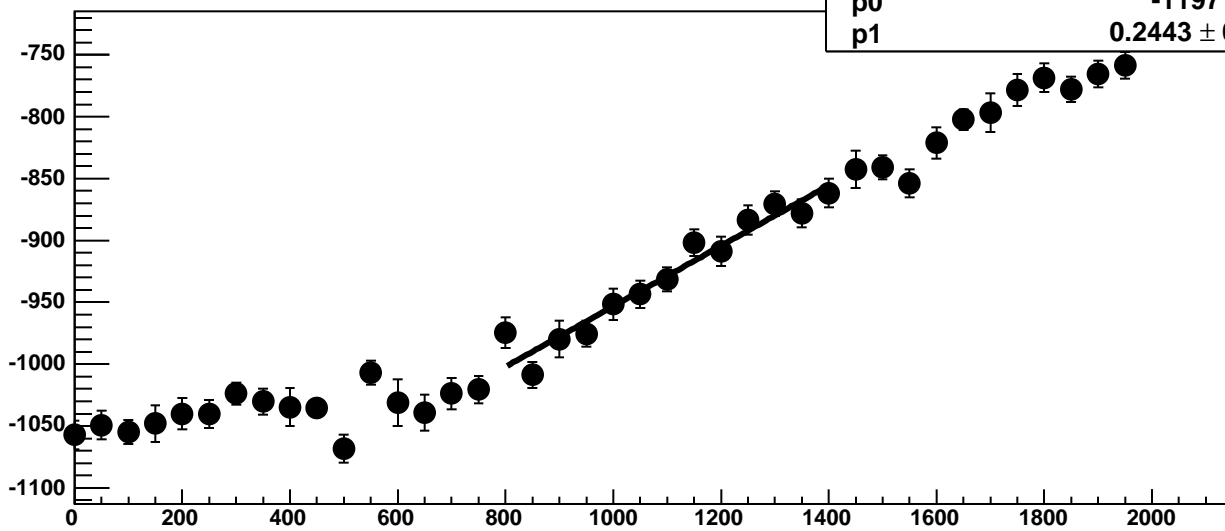
Chip 5, Channel 2, Enable 0, Hold=30, ADC Noise vs DAC



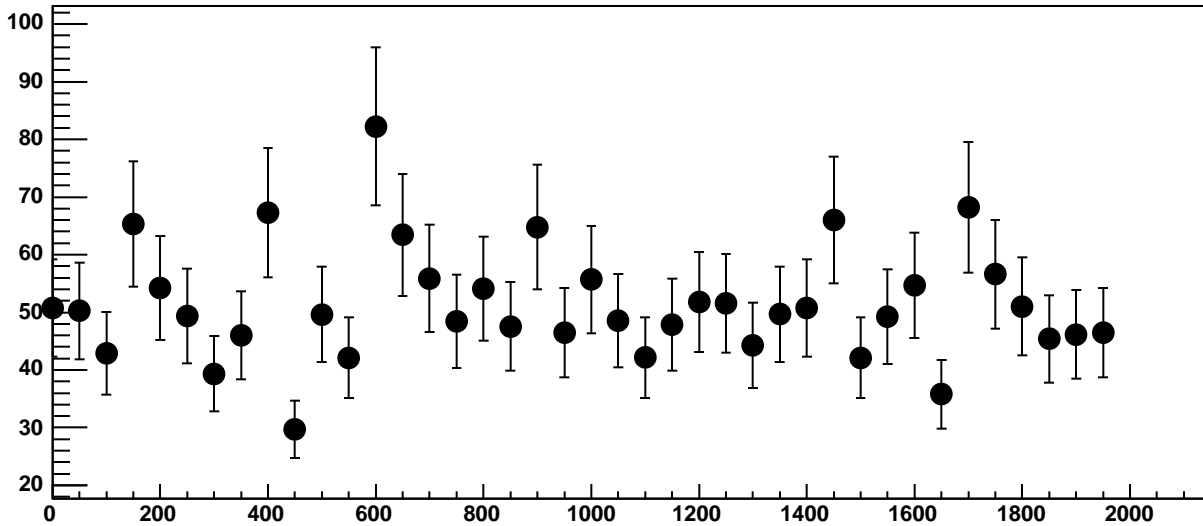
Chip 5, Channel 2, Enable 0, Hold=30, ADC Residuals vs DAC



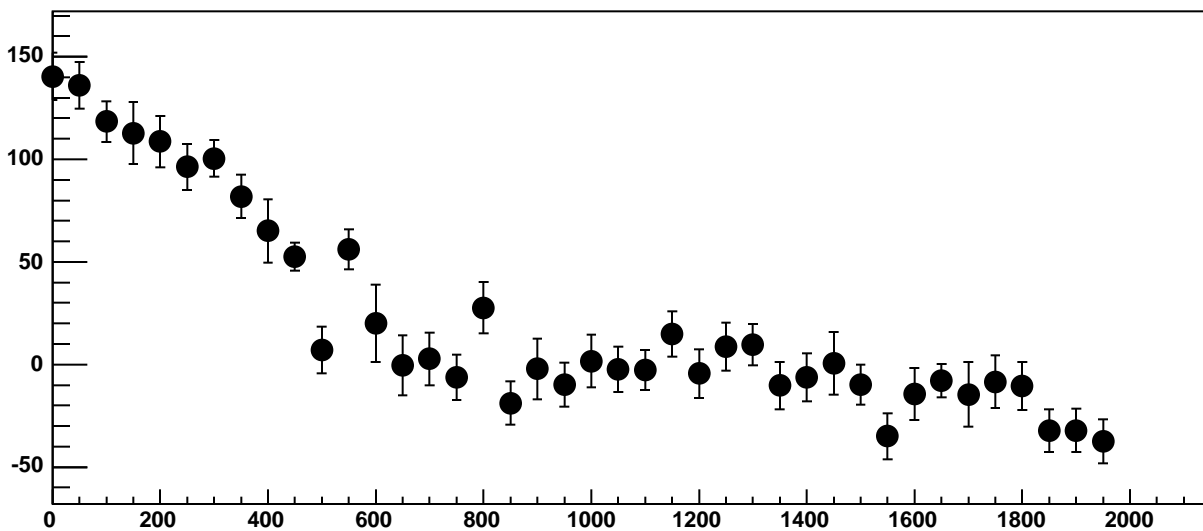
Chip 5, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC



Chip 5, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC

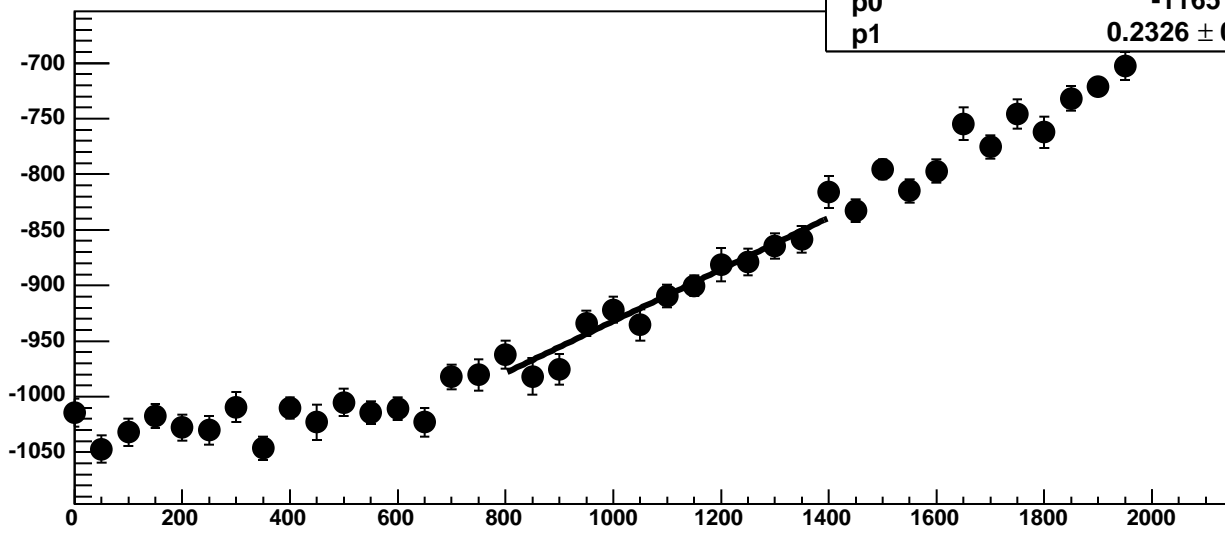


Chip 5, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC

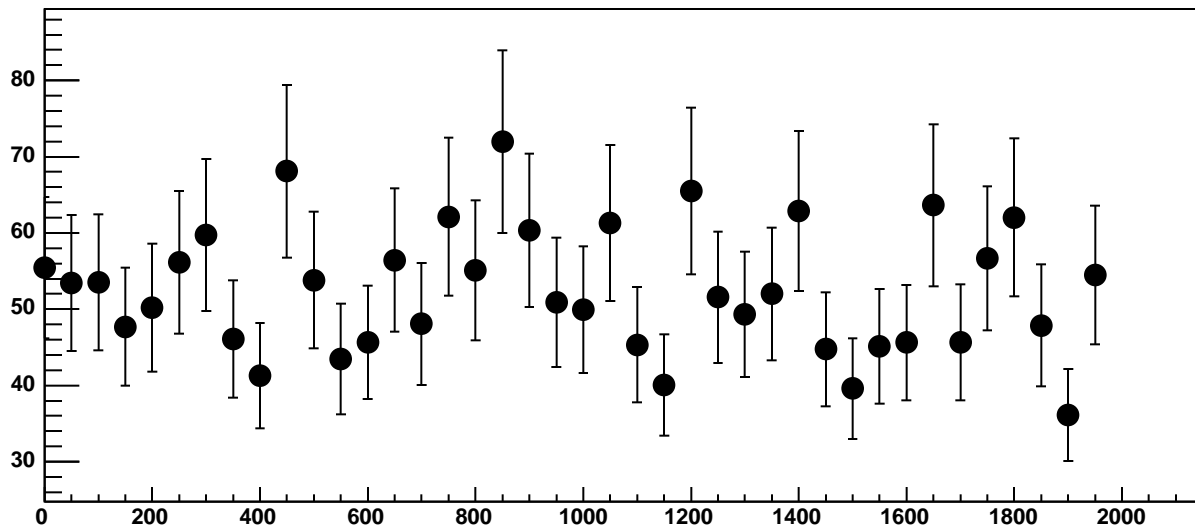




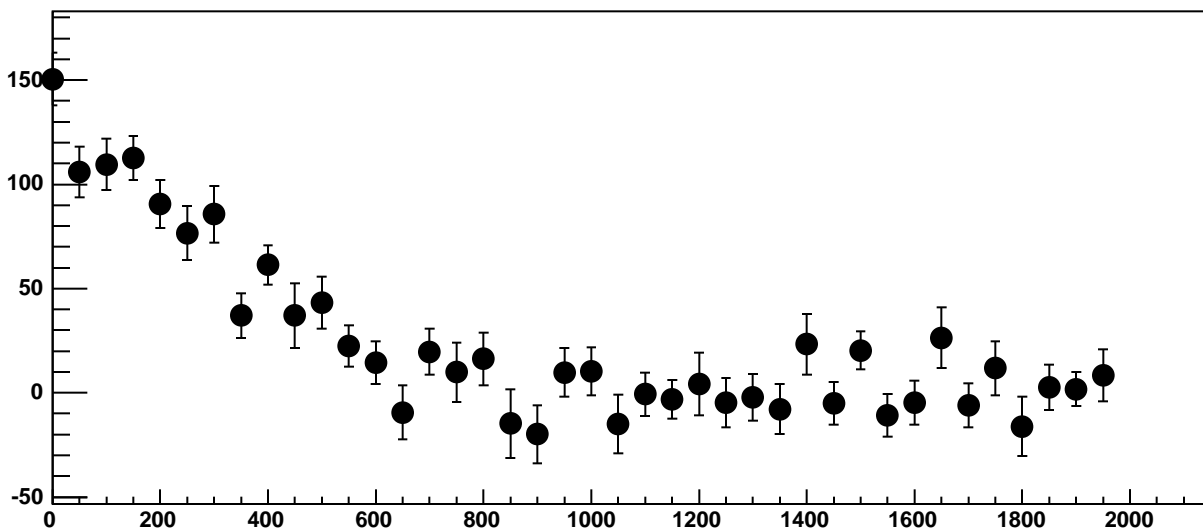
Chip 5, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC



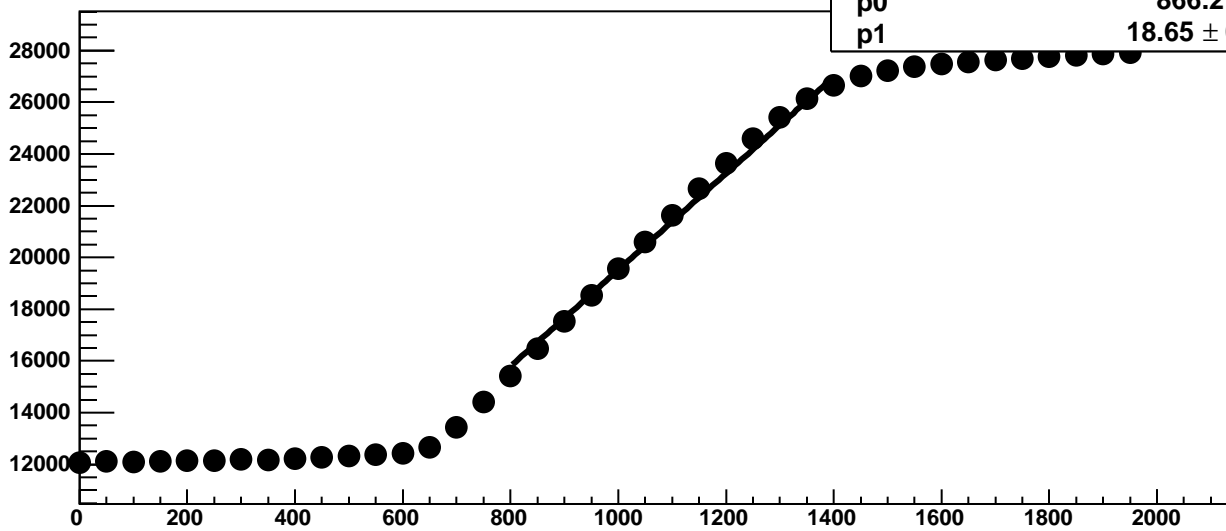
Chip 5, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC



Chip 5, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC

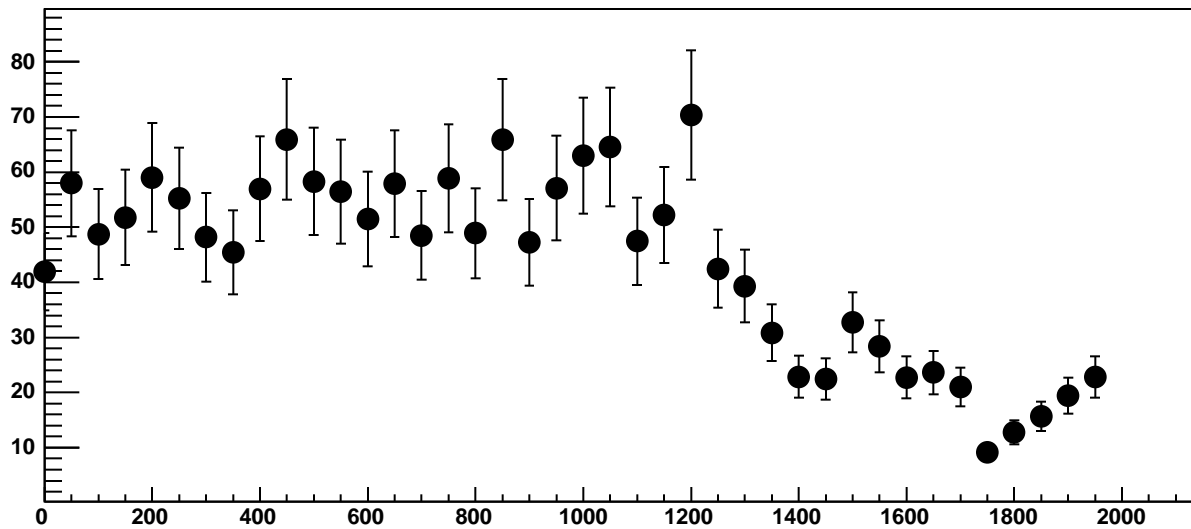


Chip 5, Channel 2, Enable 3!, Hold=30, ADC Mean vs DAC

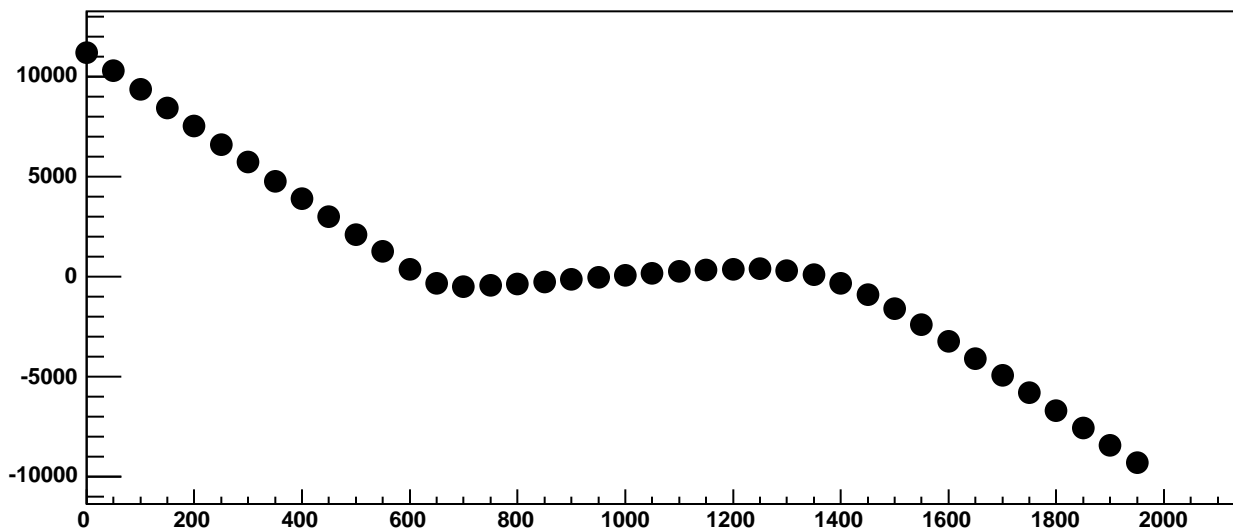


$\chi^2 / \text{ndf}$	1.037e+04 / 11
p0	866.2 ± 16.62
p1	18.65 ± 0.01362

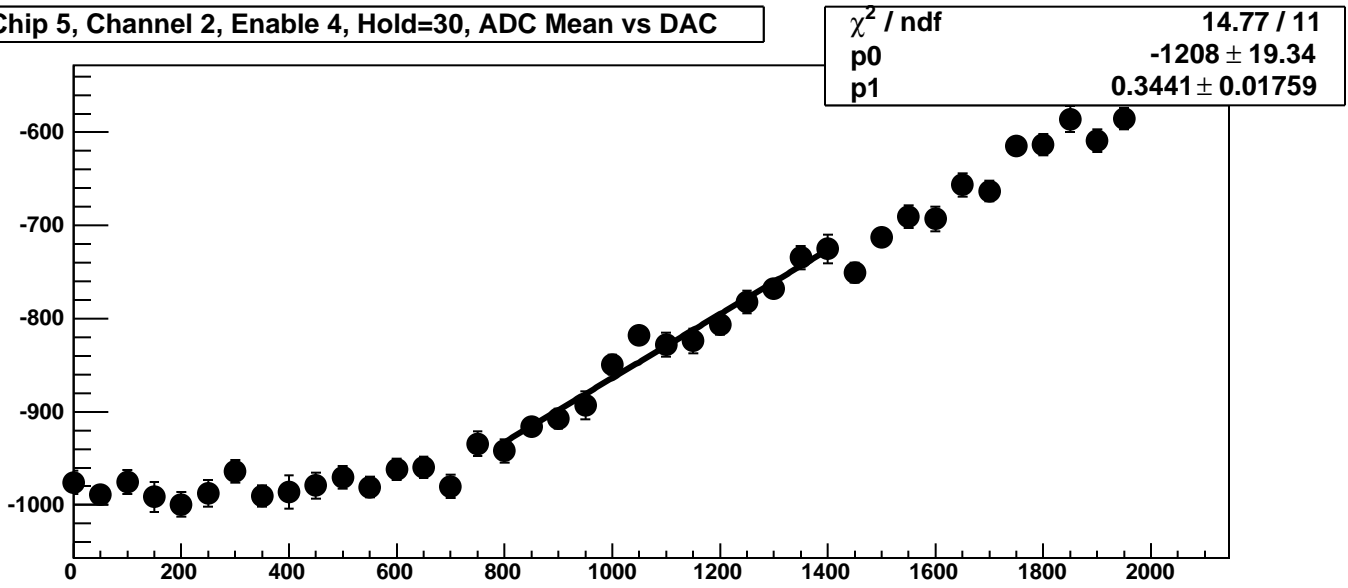
Chip 5, Channel 2, Enable 3!, Hold=30, ADC Noise vs DAC



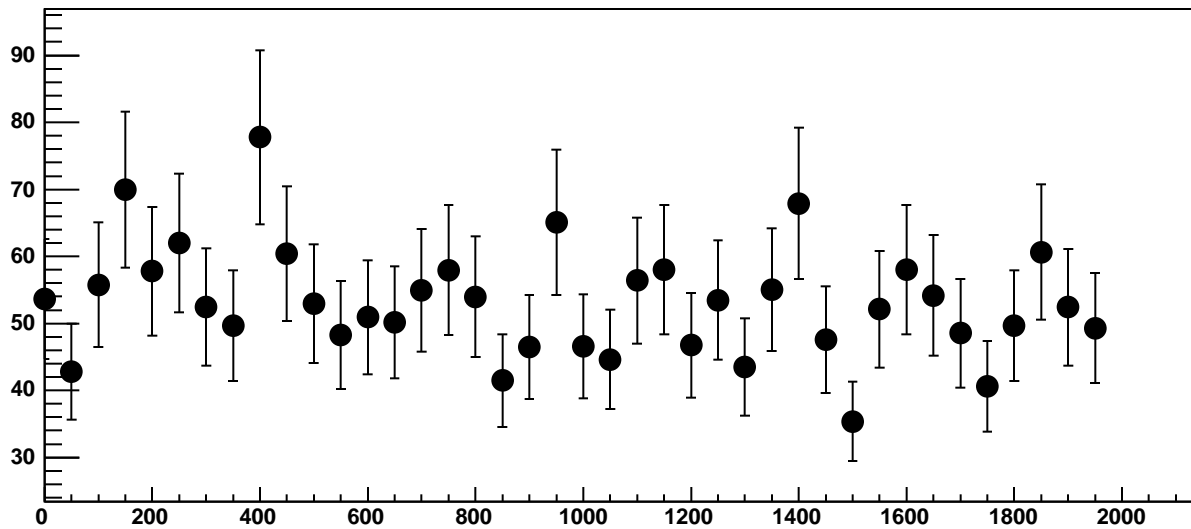
Chip 5, Channel 2, Enable 3!, Hold=30, ADC Residuals vs DAC



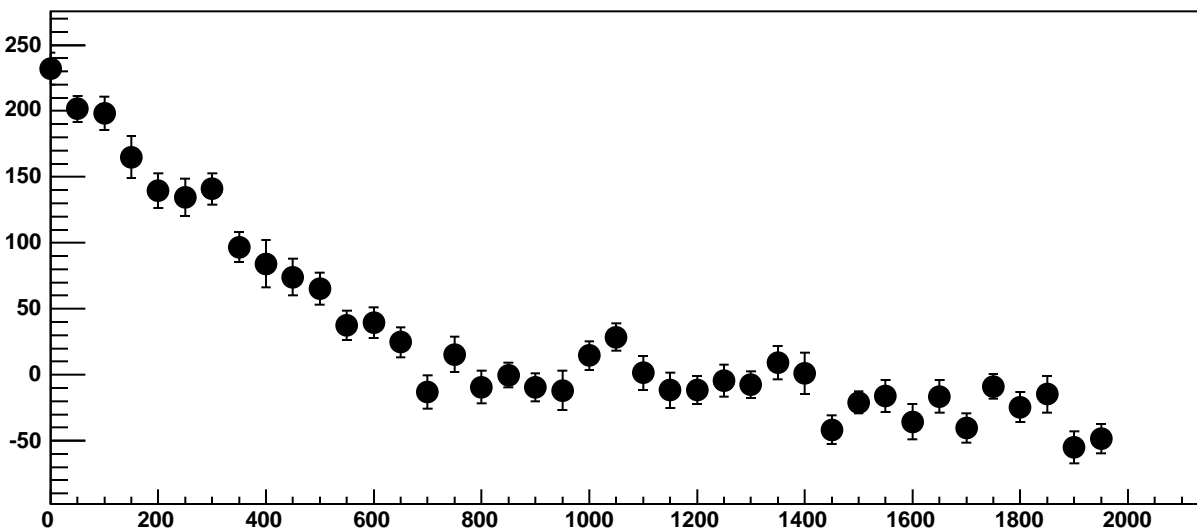
Chip 5, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC



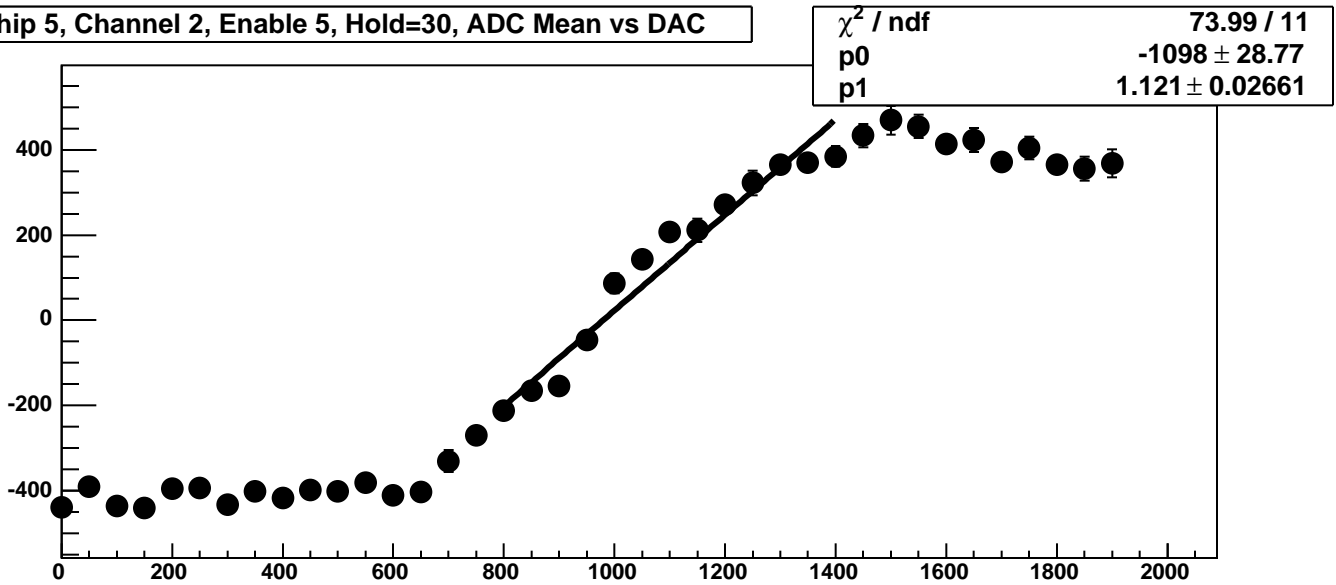
Chip 5, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC



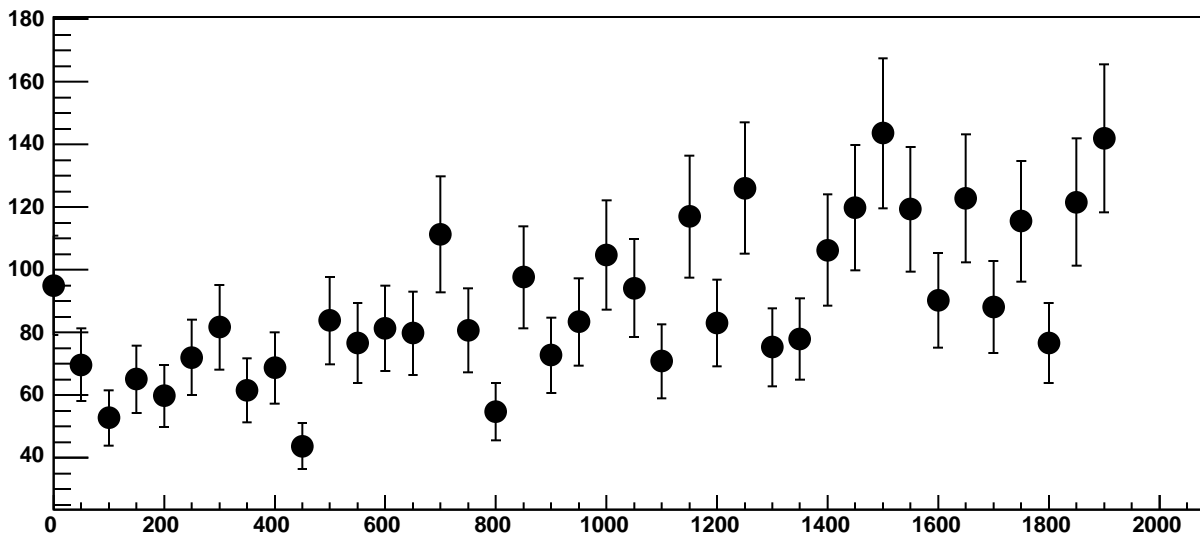
Chip 5, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC



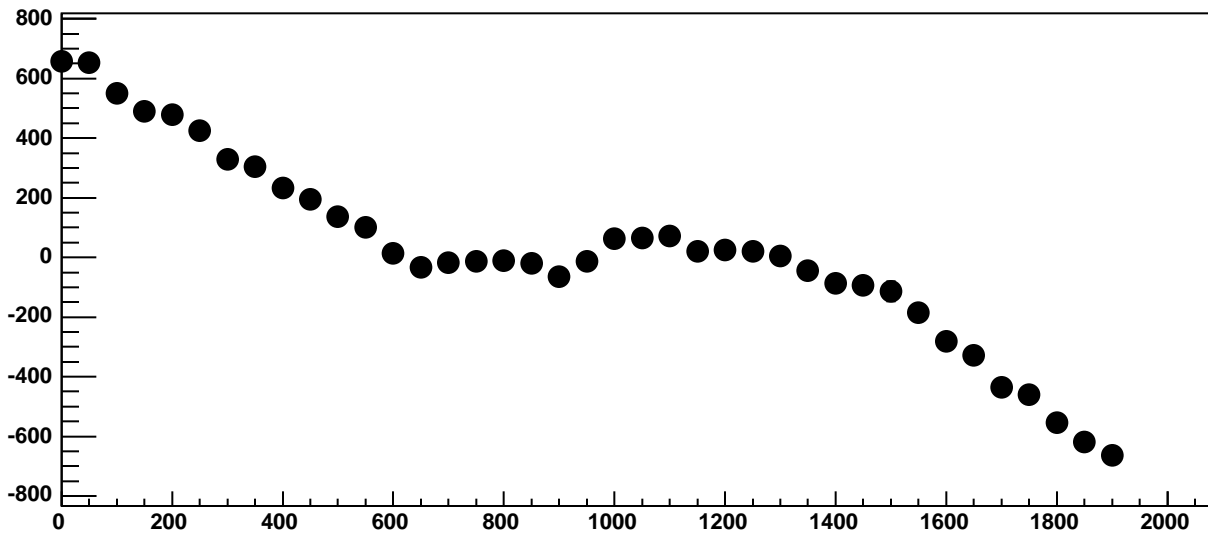
Chip 5, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC



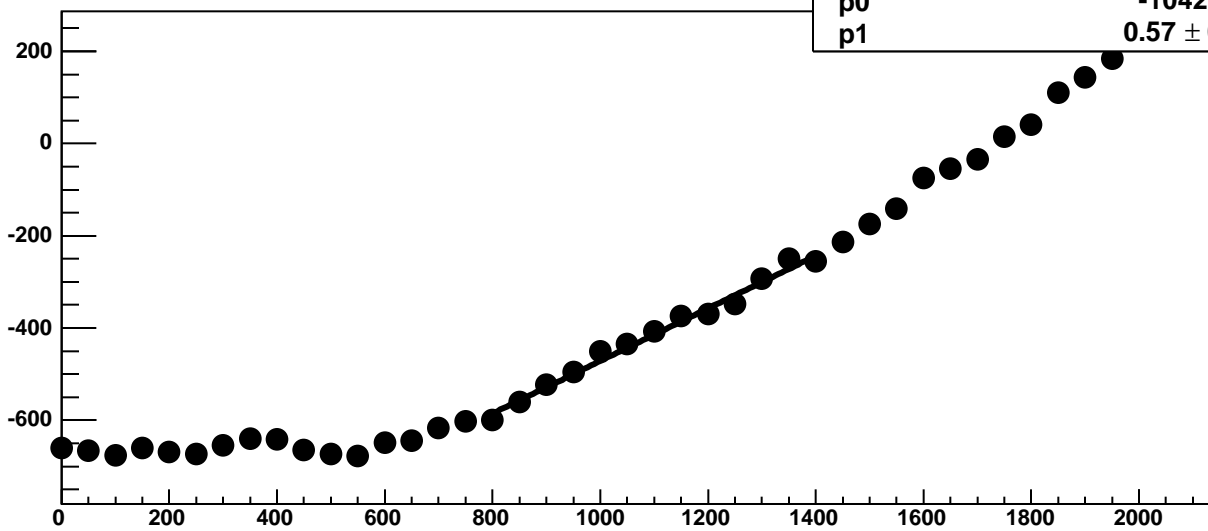
Chip 5, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC



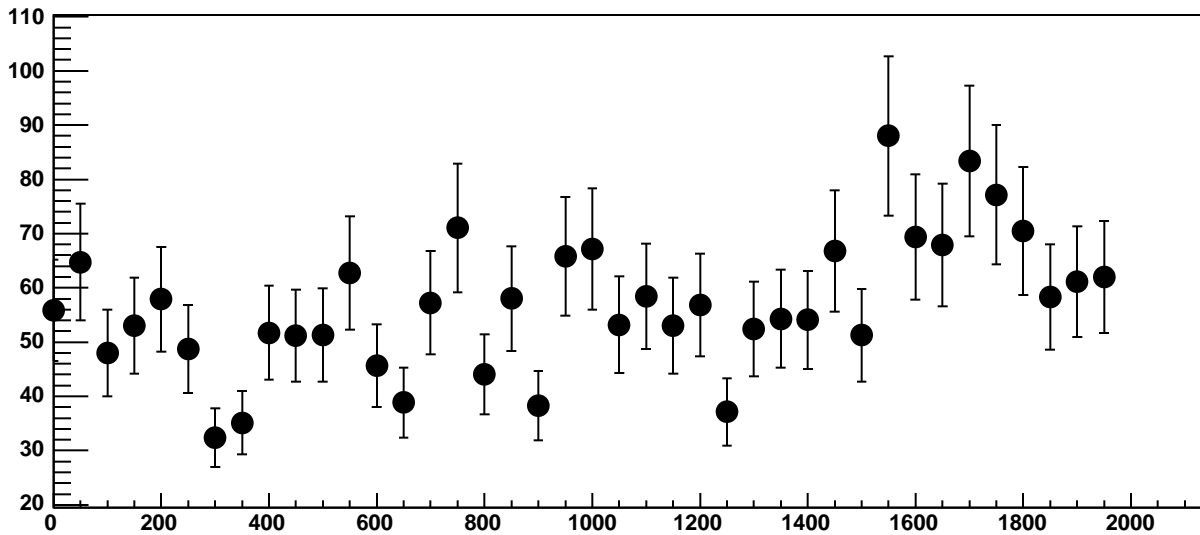
Chip 5, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC



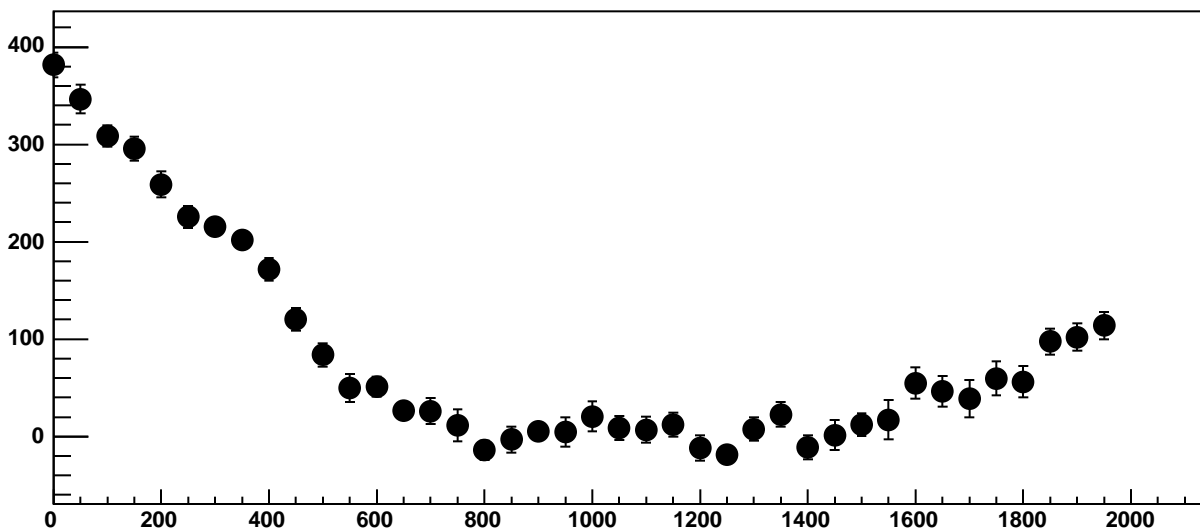
Chip 5, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC



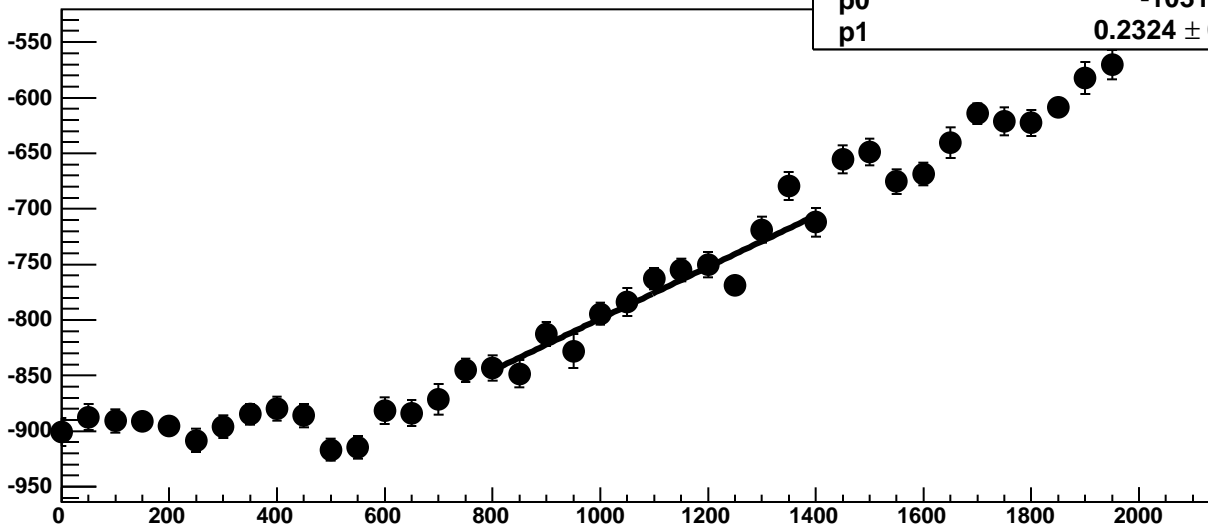
Chip 5, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC



Chip 5, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC

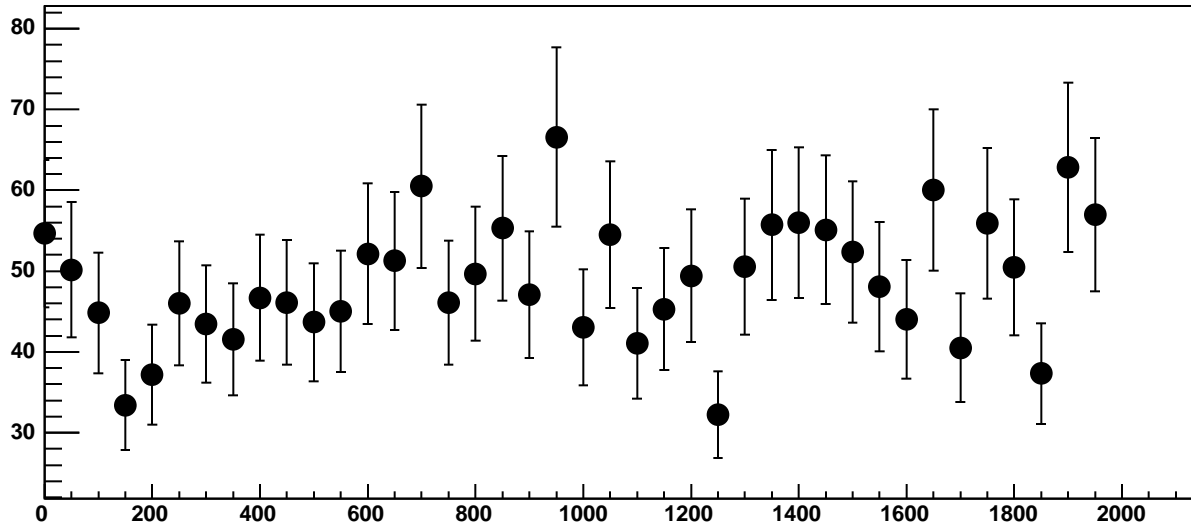


Chip 5, Channel 3, Enable 1, Hold=30, ADC Mean vs DAC

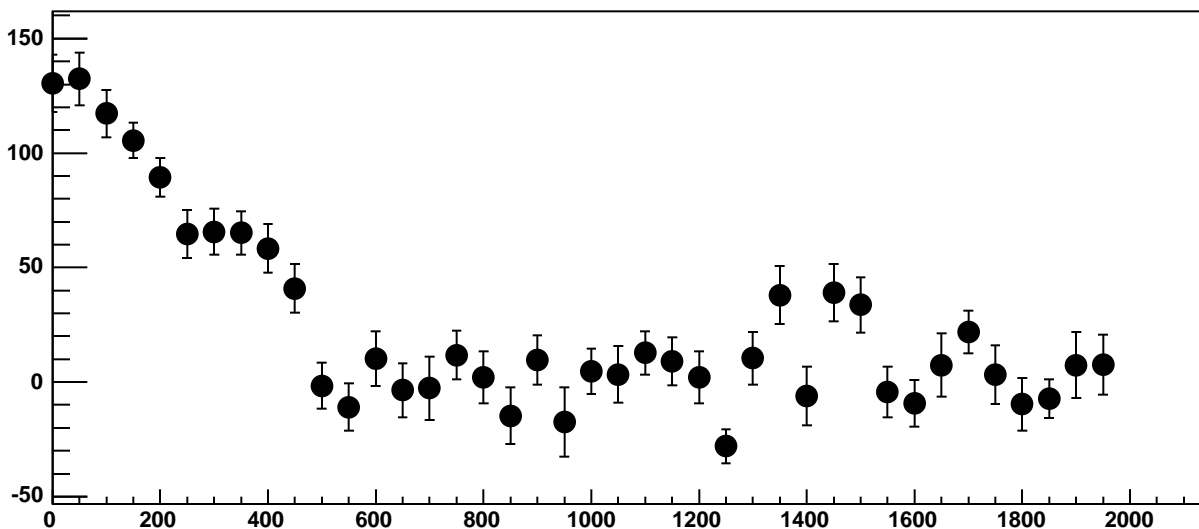


$\chi^2 / \text{ndf}$  30.59 / 11  
p0  $-1031 \pm 19.33$   
p1  $0.2324 \pm 0.01719$

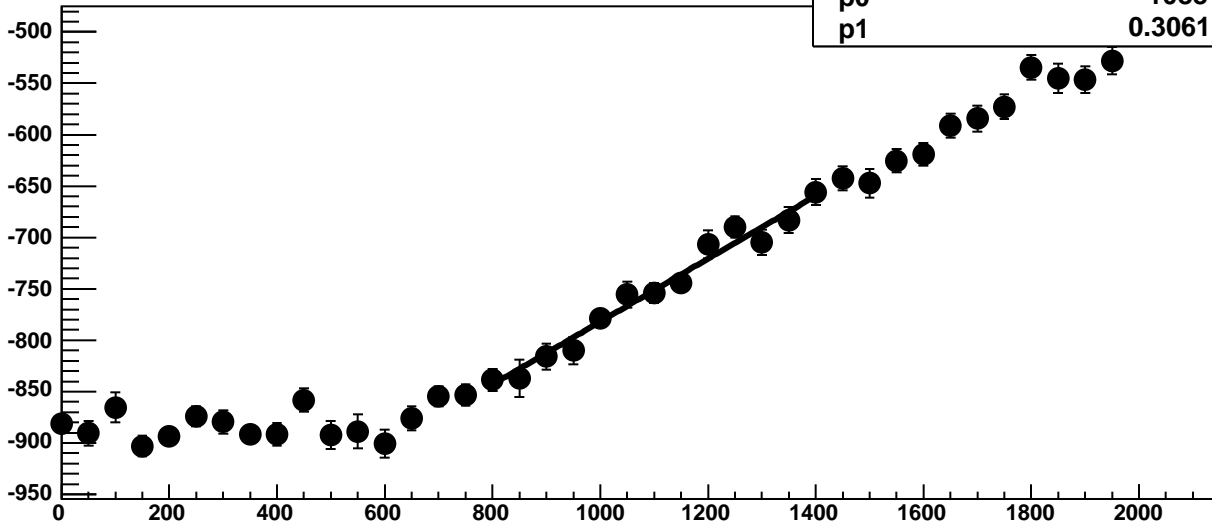
Chip 5, Channel 3, Enable 1, Hold=30, ADC Noise vs DAC



Chip 5, Channel 3, Enable 1, Hold=30, ADC Residuals vs DAC

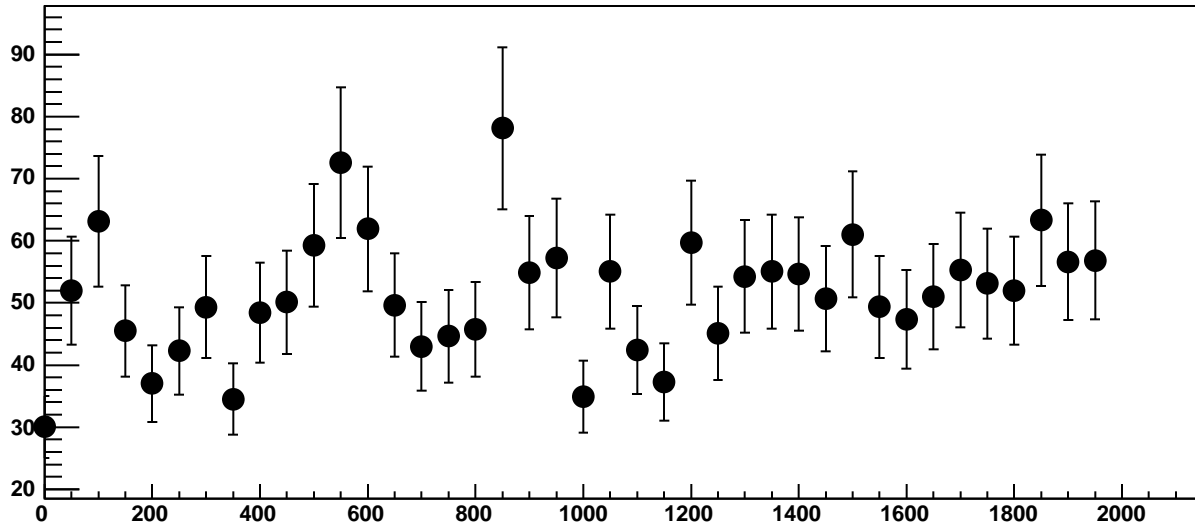


Chip 5, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC

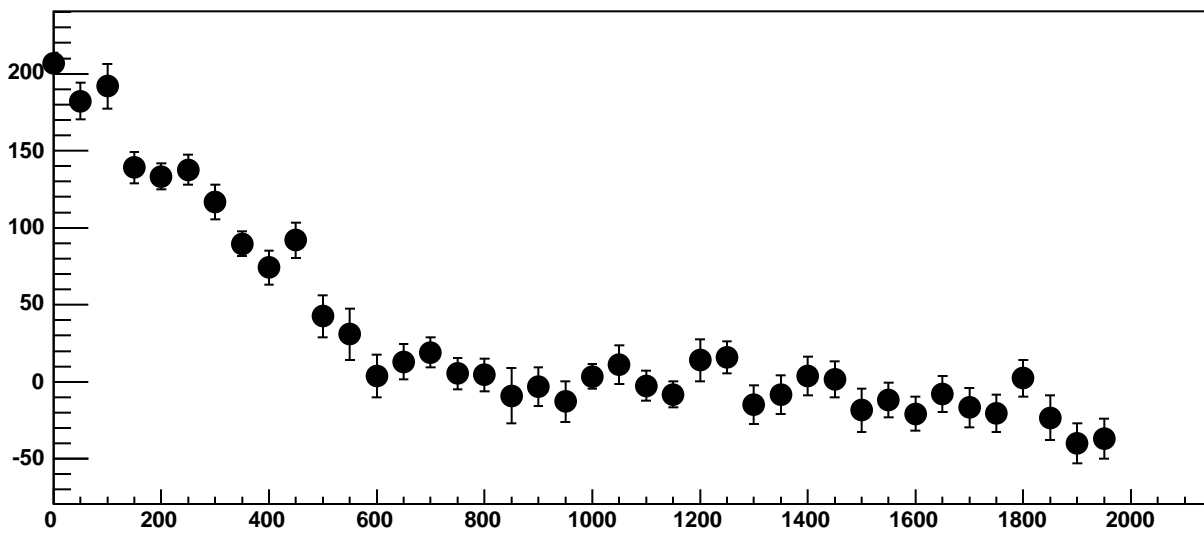


$\chi^2 / \text{ndf}$  8.729 / 11  
p0  $-1088 \pm 20.02$   
p1  $0.3061 \pm 0.018$

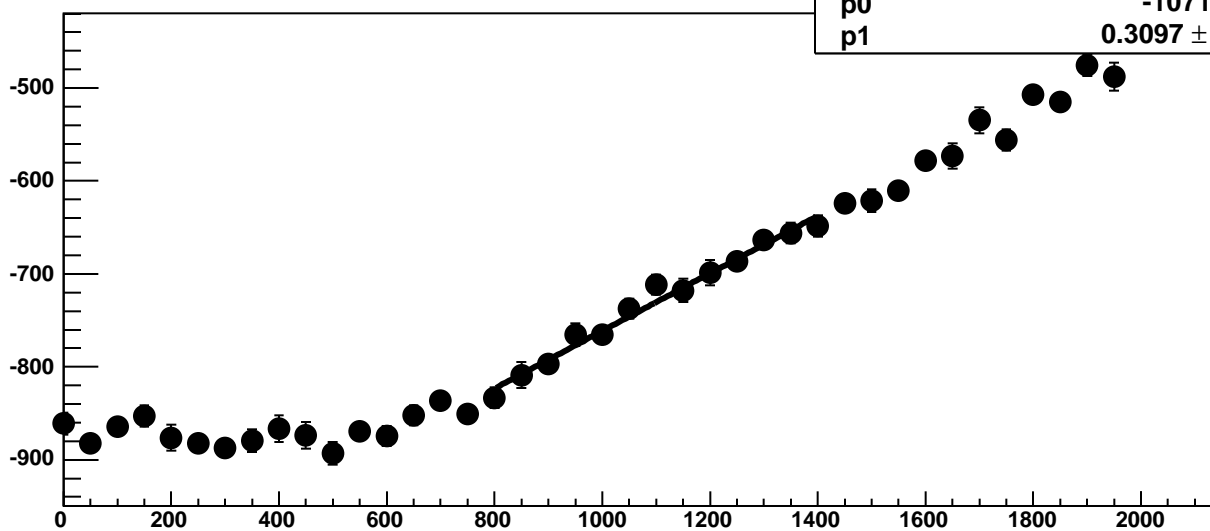
Chip 5, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC



Chip 5, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 5, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

7.369 / 11

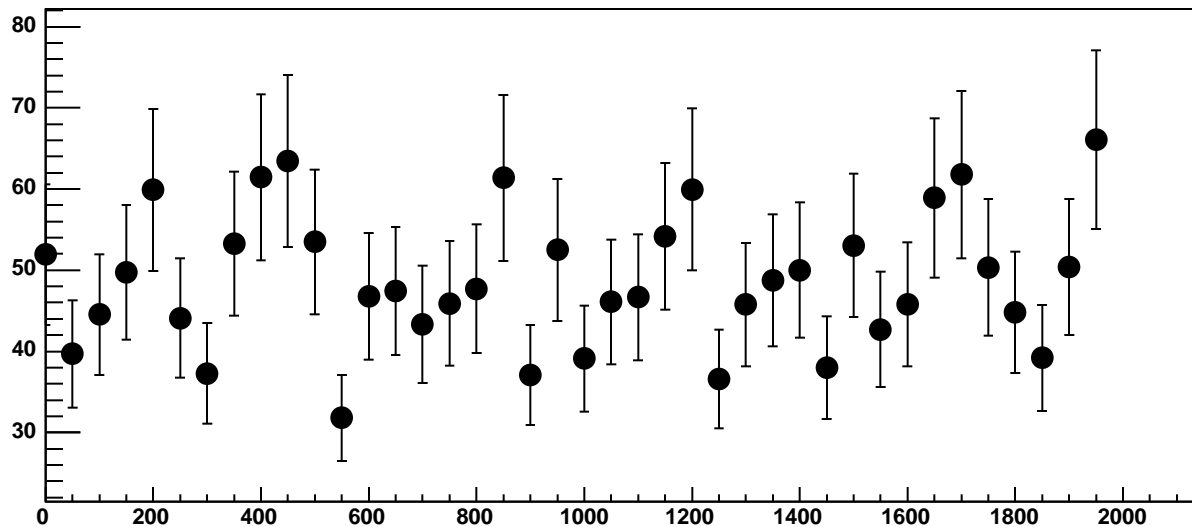
p0

$-1071 \pm 17.94$

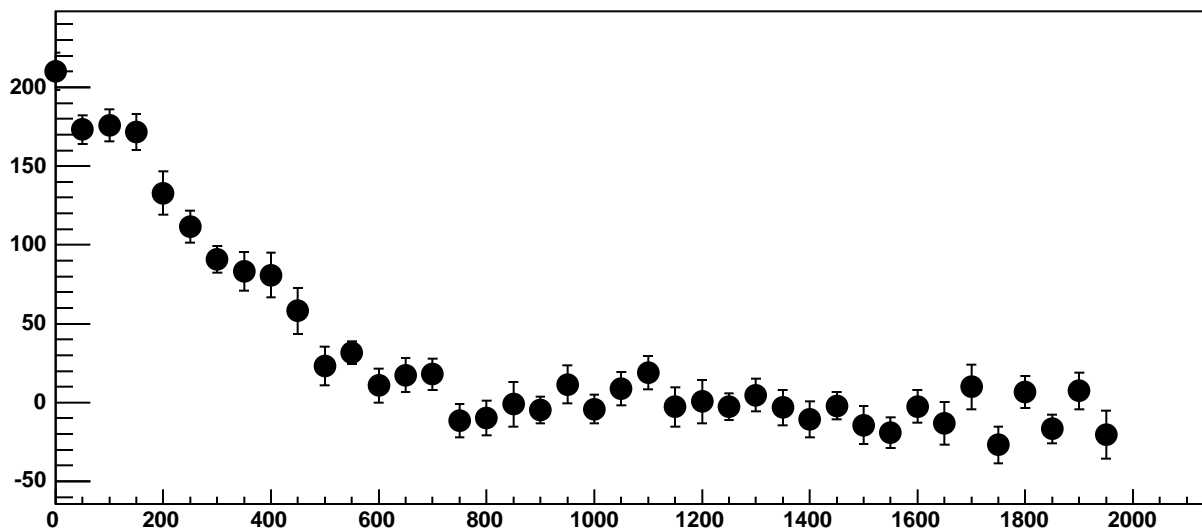
p1

$0.3097 \pm 0.01611$

Chip 5, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC

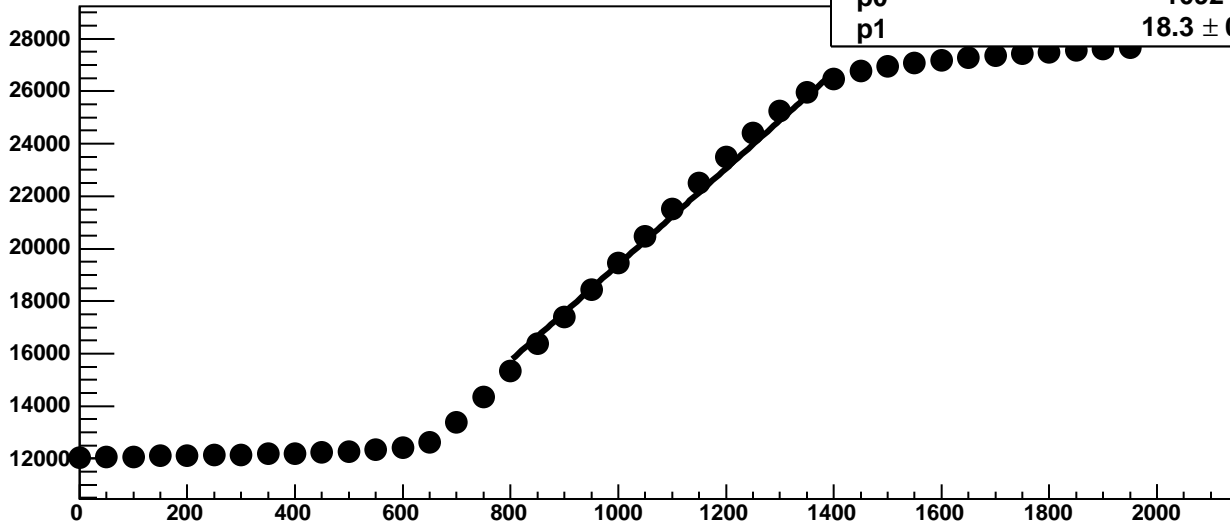


Chip 5, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC



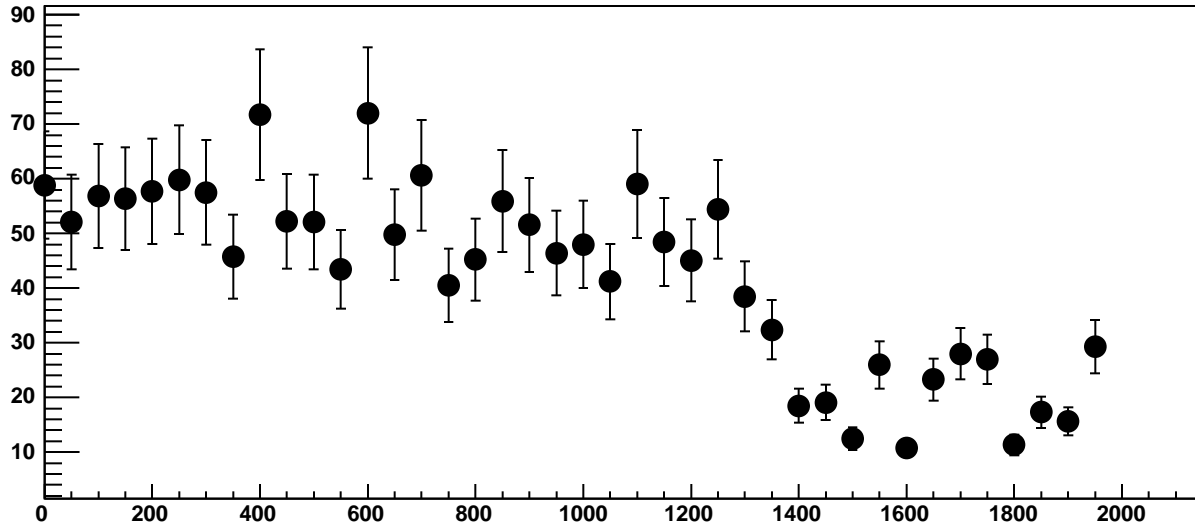


Chip 5, Channel 3, Enable 4!, Hold=30, ADC Mean vs DAC

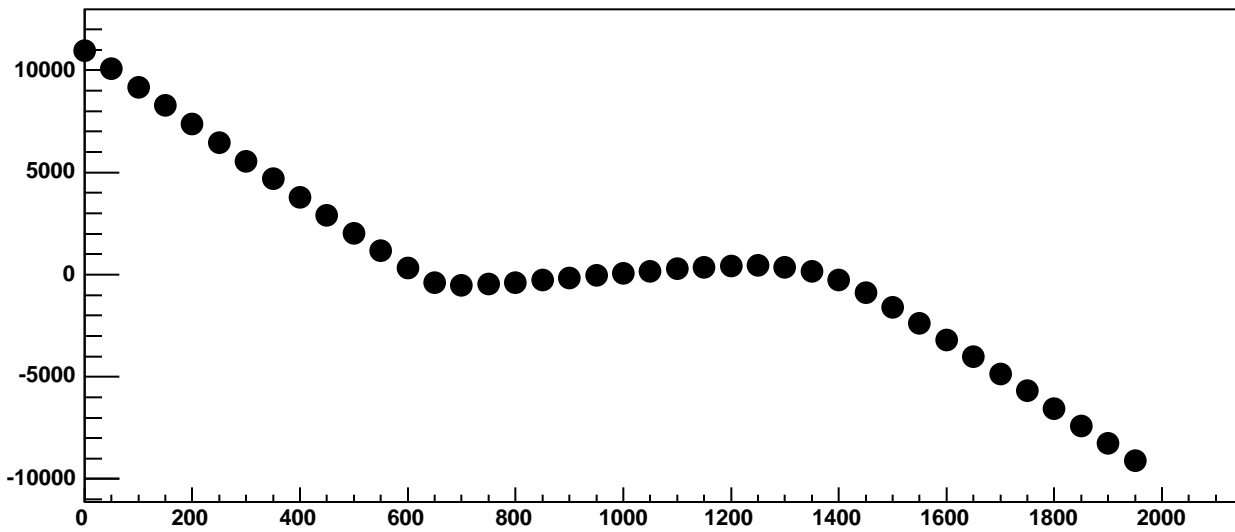


$\chi^2 / \text{ndf}$	1.271e+04 / 11
p0	1092 ± 14.77
p1	18.3 ± 0.01208

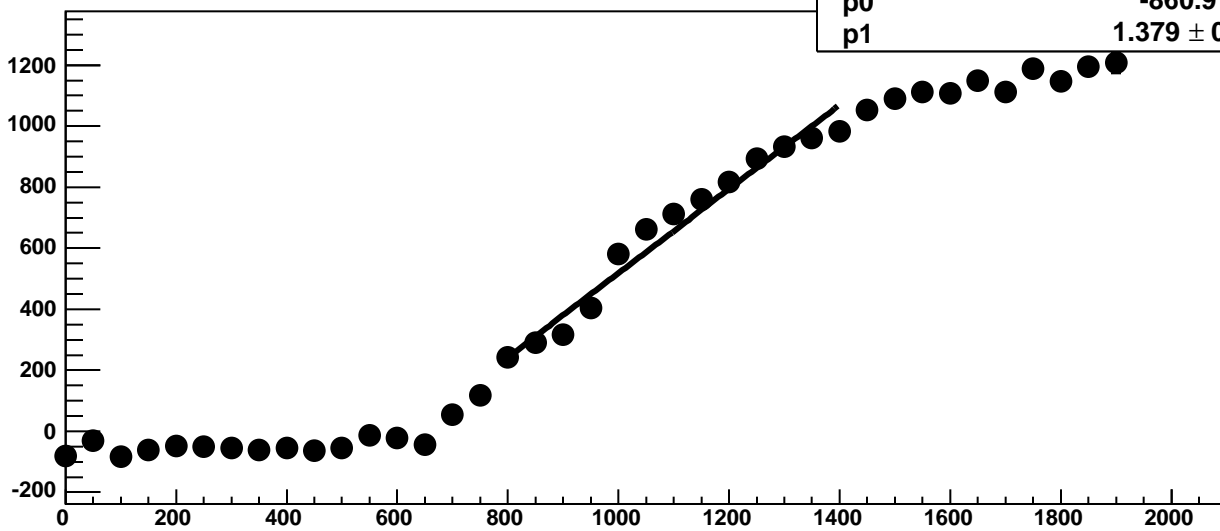
Chip 5, Channel 3, Enable 4!, Hold=30, ADC Noise vs DAC



Chip 5, Channel 3, Enable 4!, Hold=30, ADC Residuals vs DAC

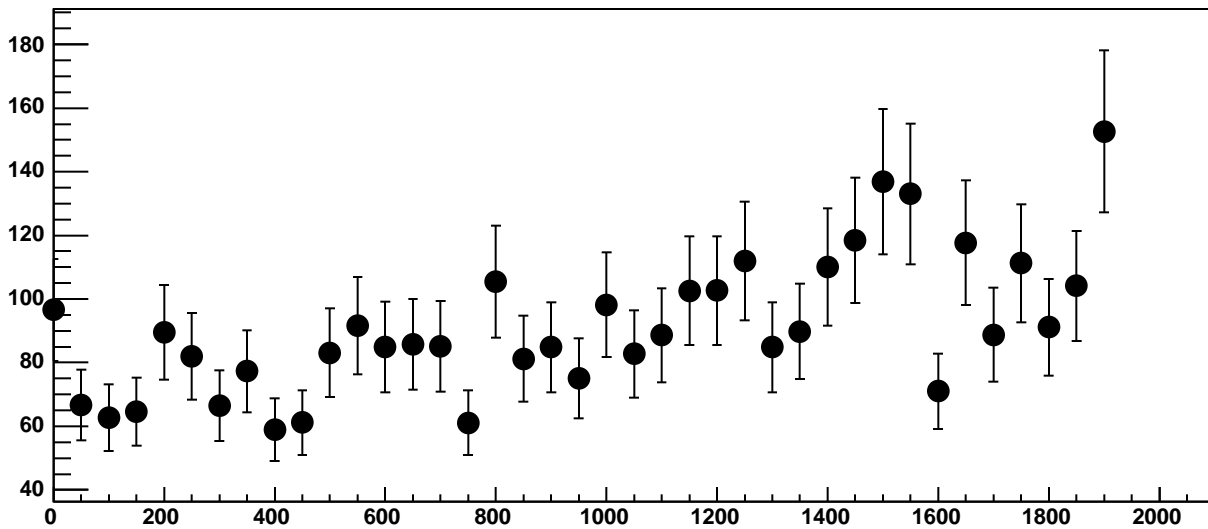


Chip 5, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC

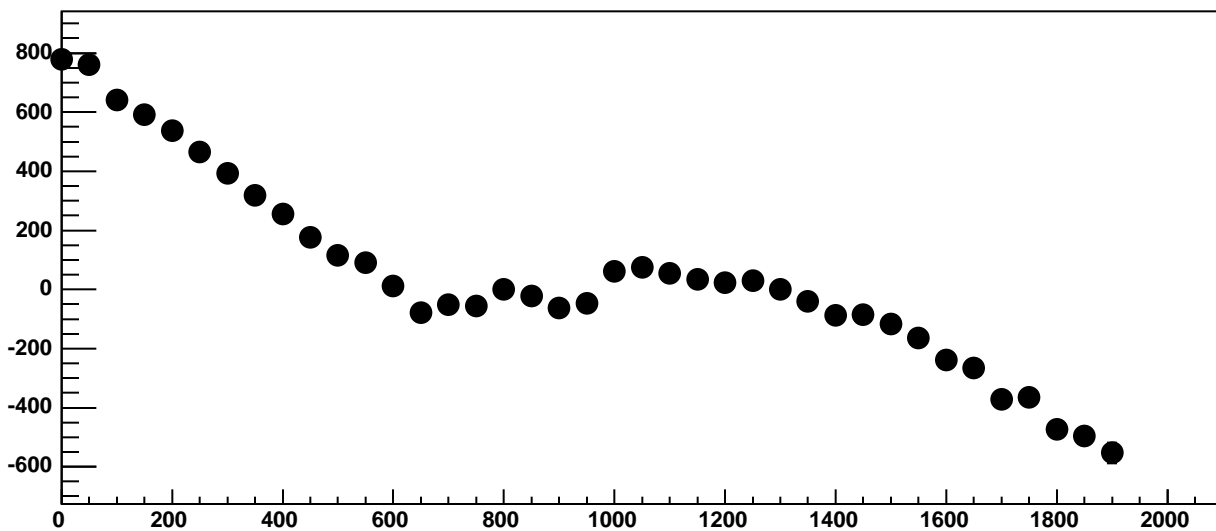


$\chi^2 / \text{ndf}$  69.39 / 11  
p0  $-860.9 \pm 35.01$   
p1  $1.379 \pm 0.03189$

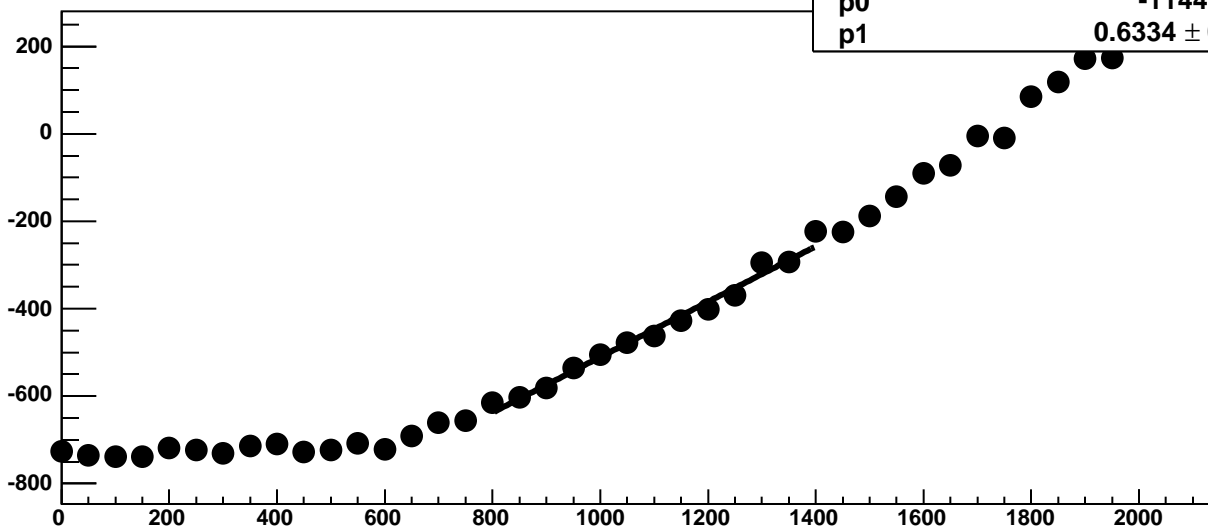
Chip 5, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC



Chip 5, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC

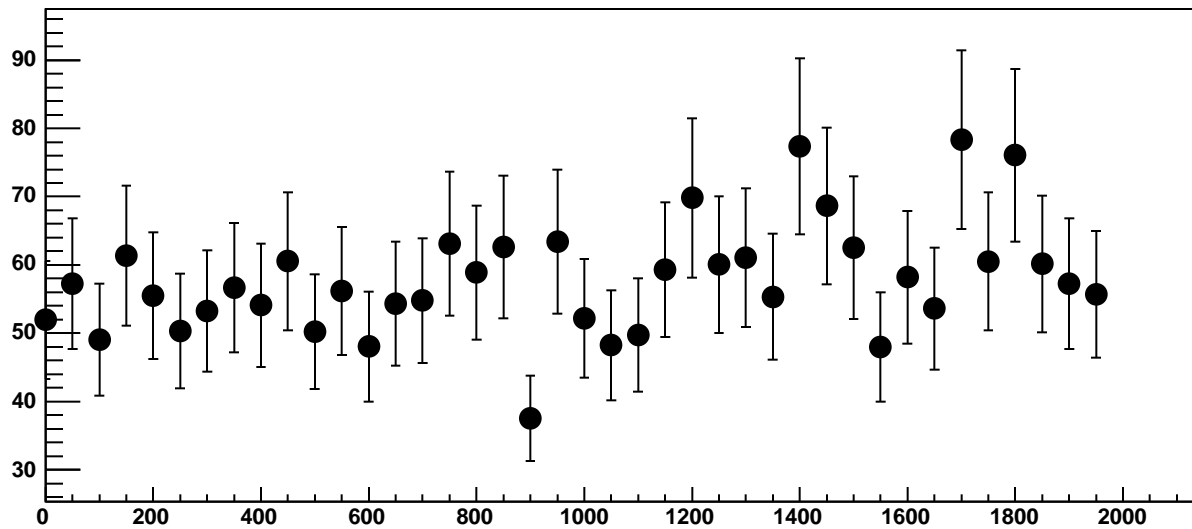


Chip 5, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC

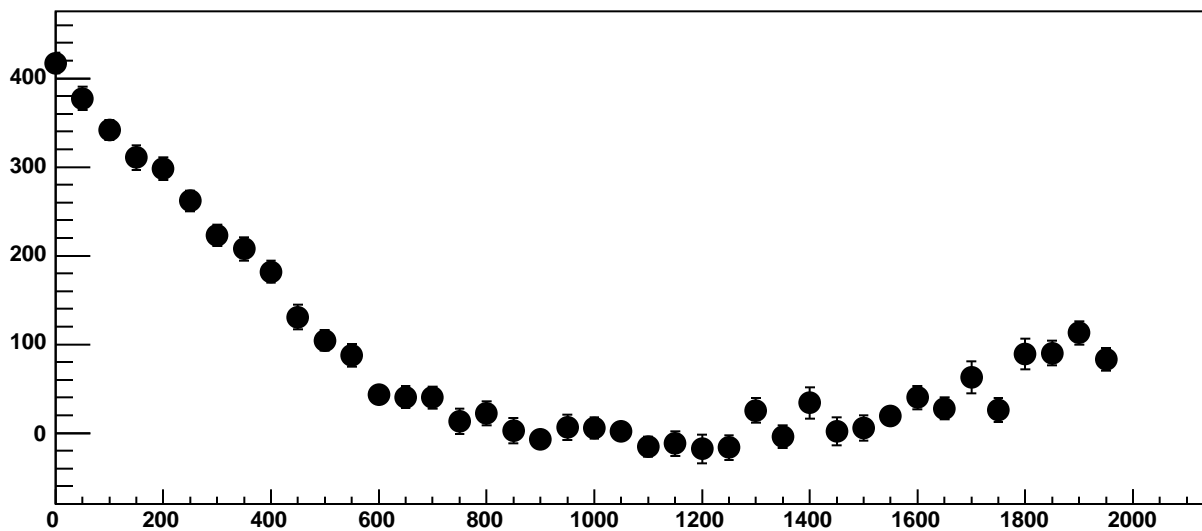


$\chi^2 / \text{ndf}$  16.23 / 11  
p0  $-1144 \pm 21.66$   
p1  $0.6334 \pm 0.01997$

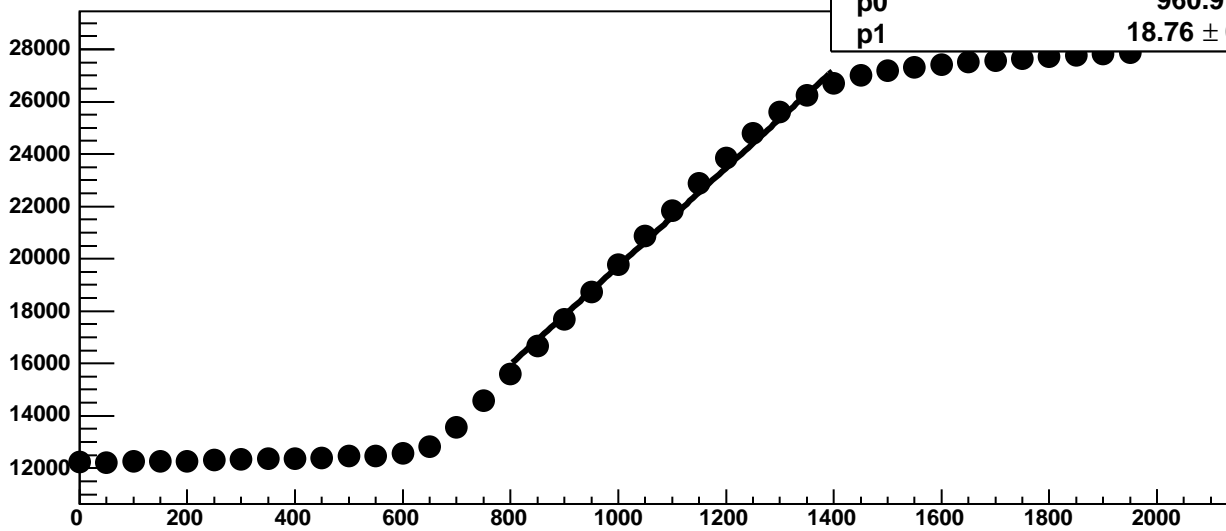
Chip 5, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC



Chip 5, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC

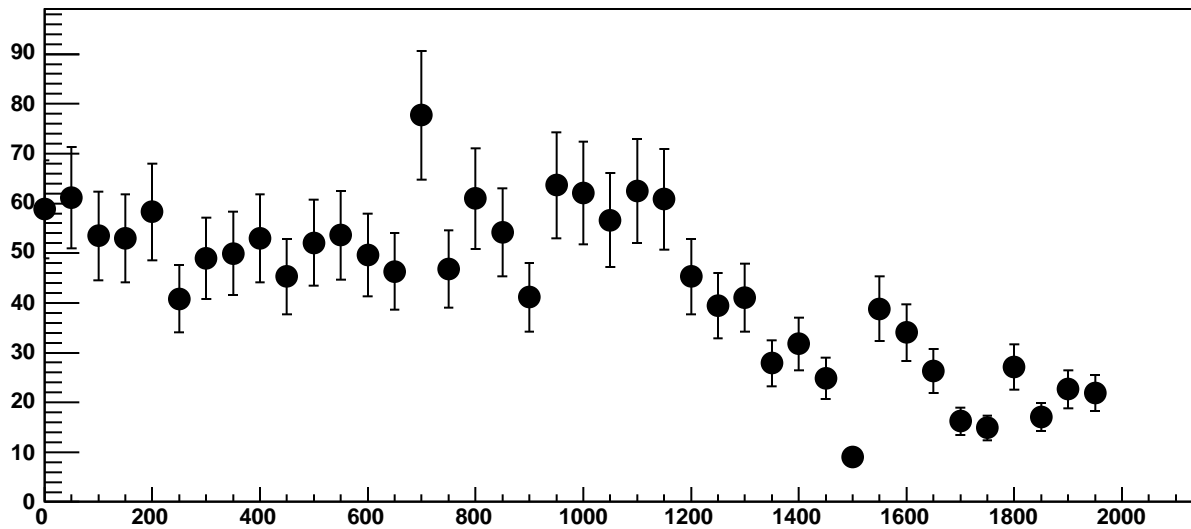


Chip 5, Channel 4, Enable 1!, Hold=30, ADC Mean vs DAC

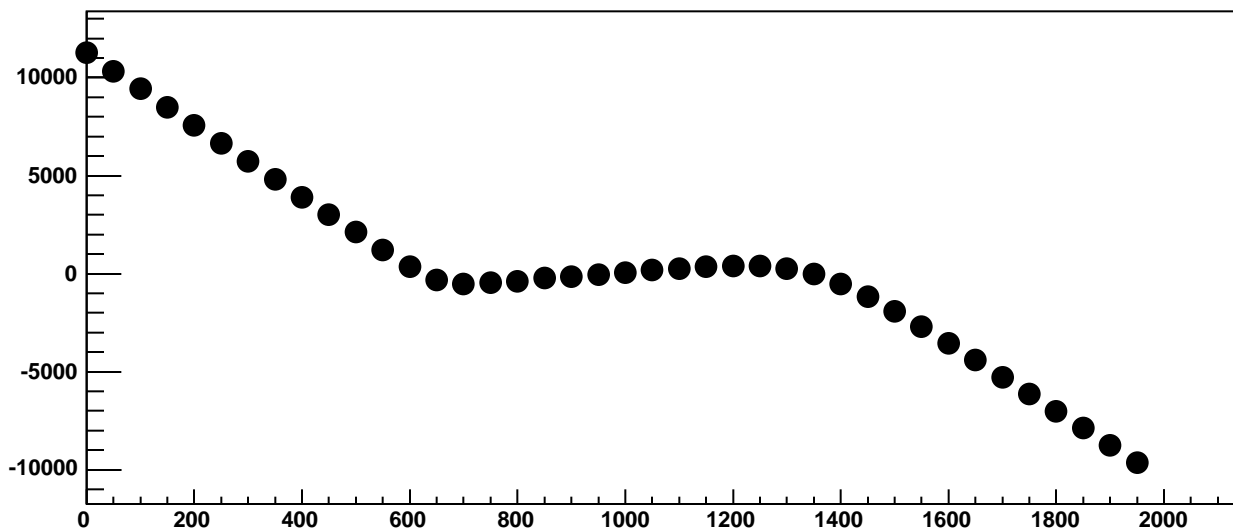


$\chi^2 / \text{ndf}$  1.148e+04 / 11  
p0  $960.9 \pm 17.37$   
p1  $18.76 \pm 0.01452$

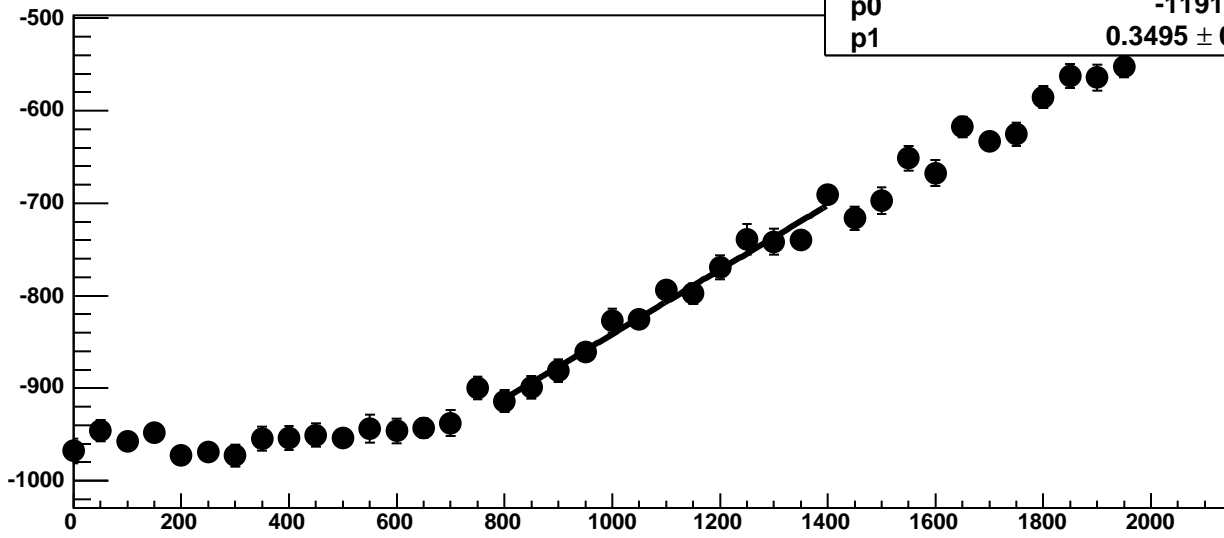
Chip 5, Channel 4, Enable 1!, Hold=30, ADC Noise vs DAC



Chip 5, Channel 4, Enable 1!, Hold=30, ADC Residuals vs DAC

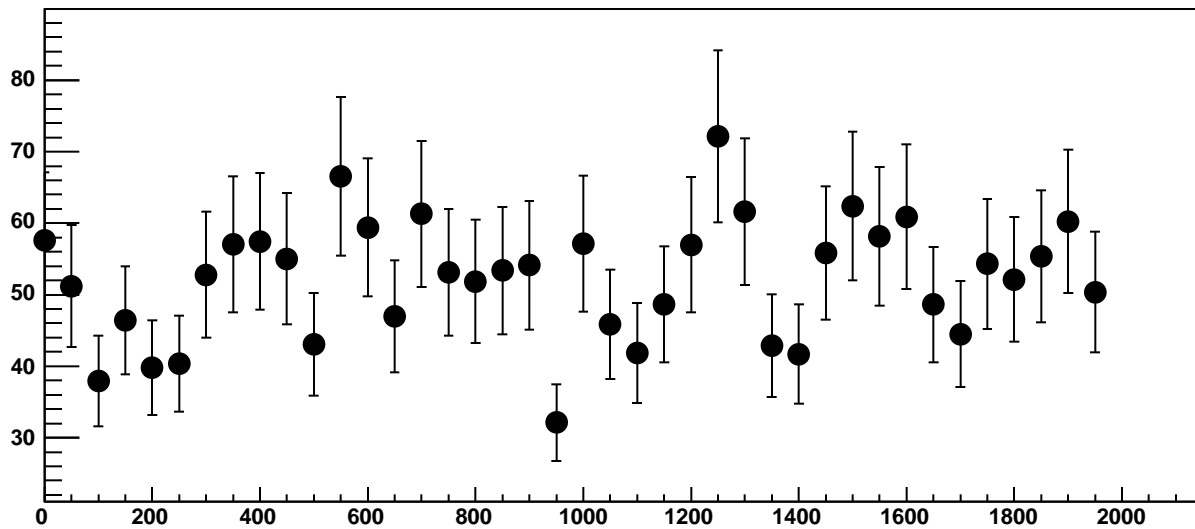


Chip 5, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC

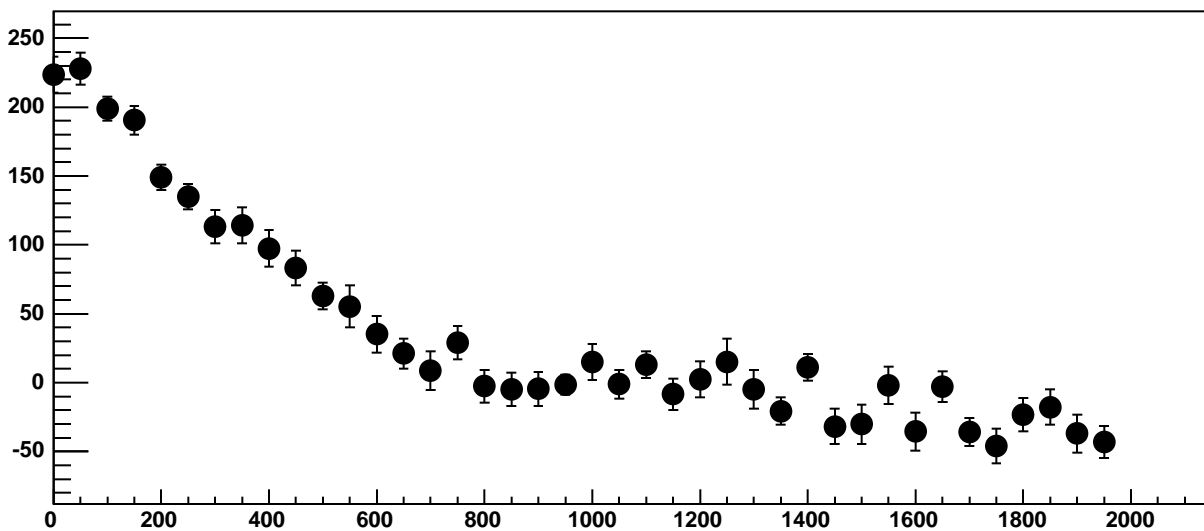


$\chi^2 / \text{ndf}$  10.8 / 11  
p0  $-1191 \pm 18.04$   
p1  $0.3495 \pm 0.01623$

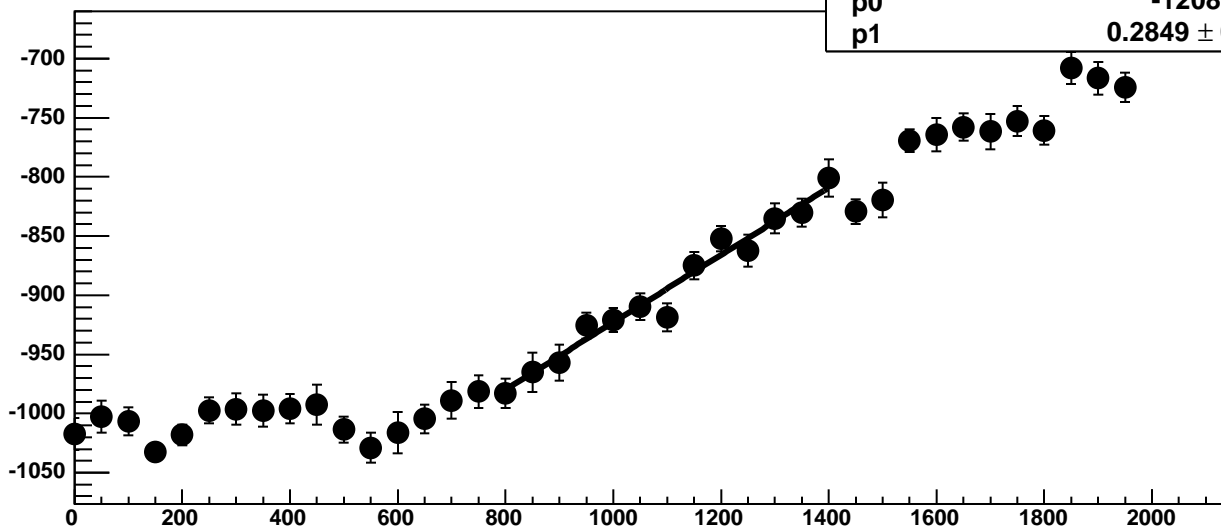
Chip 5, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC



Chip 5, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC

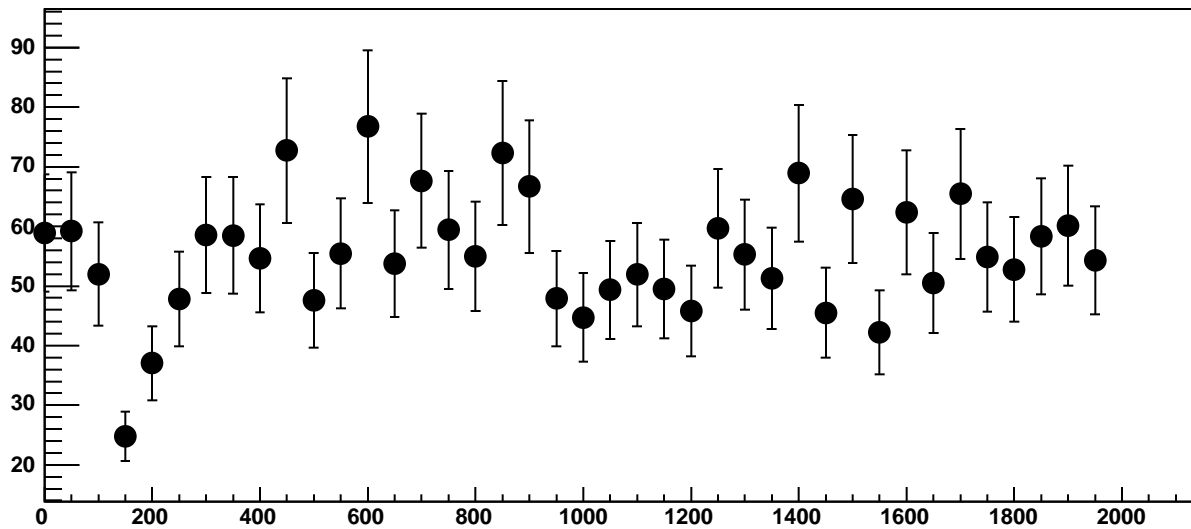


Chip 5, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC

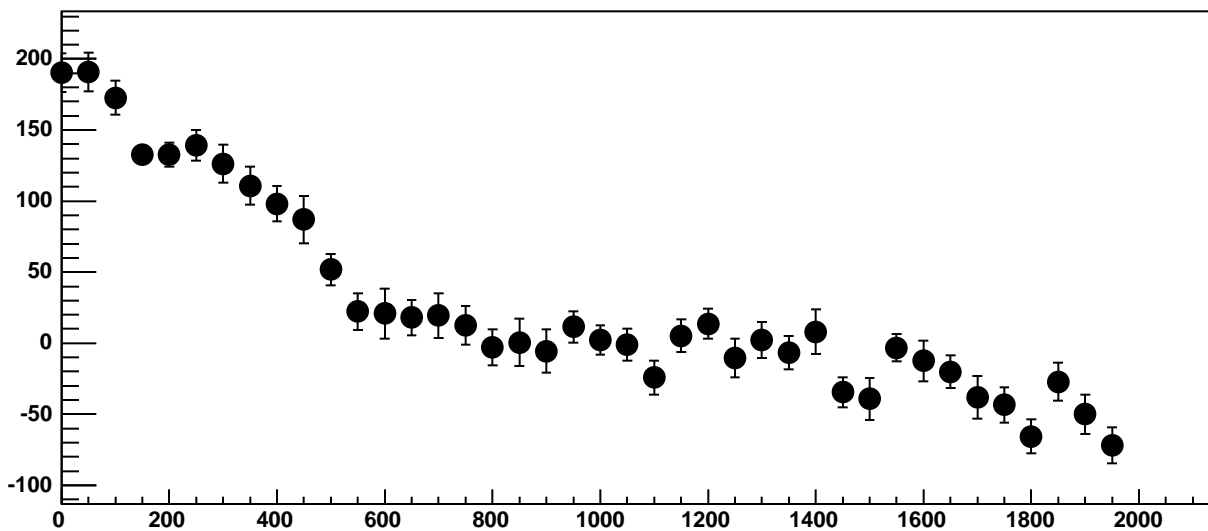


$\chi^2 / \text{ndf}$  8.582 / 11  
p0  $-1208 \pm 22.05$   
p1  $0.2849 \pm 0.01978$

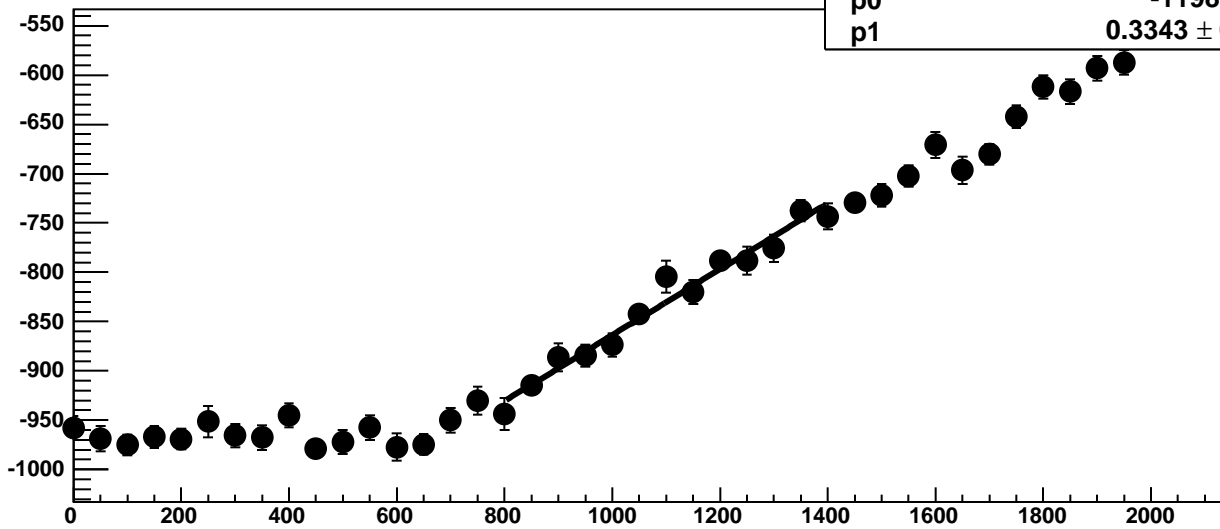
Chip 5, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC



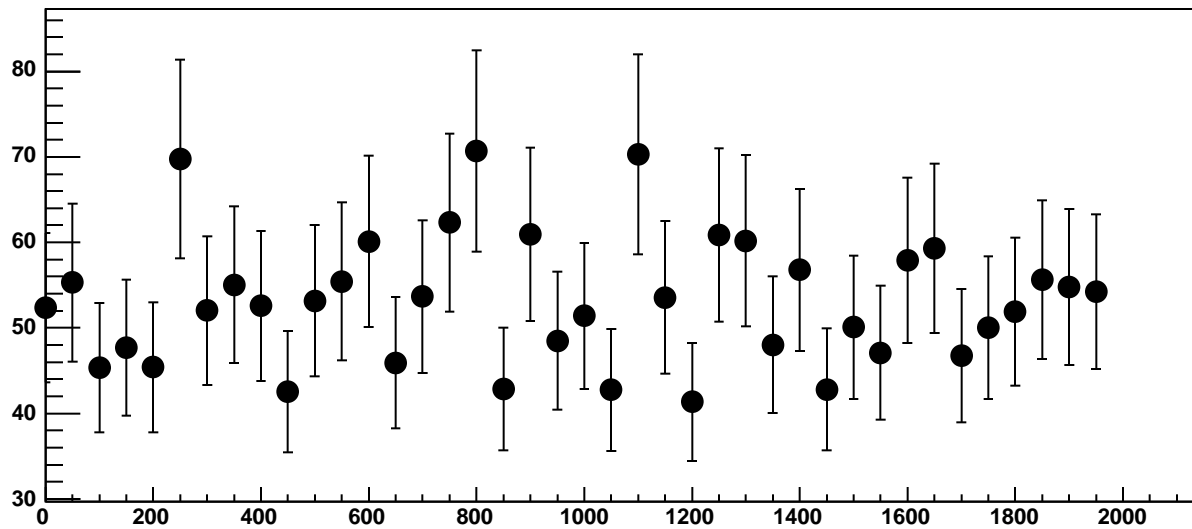
Chip 5, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC



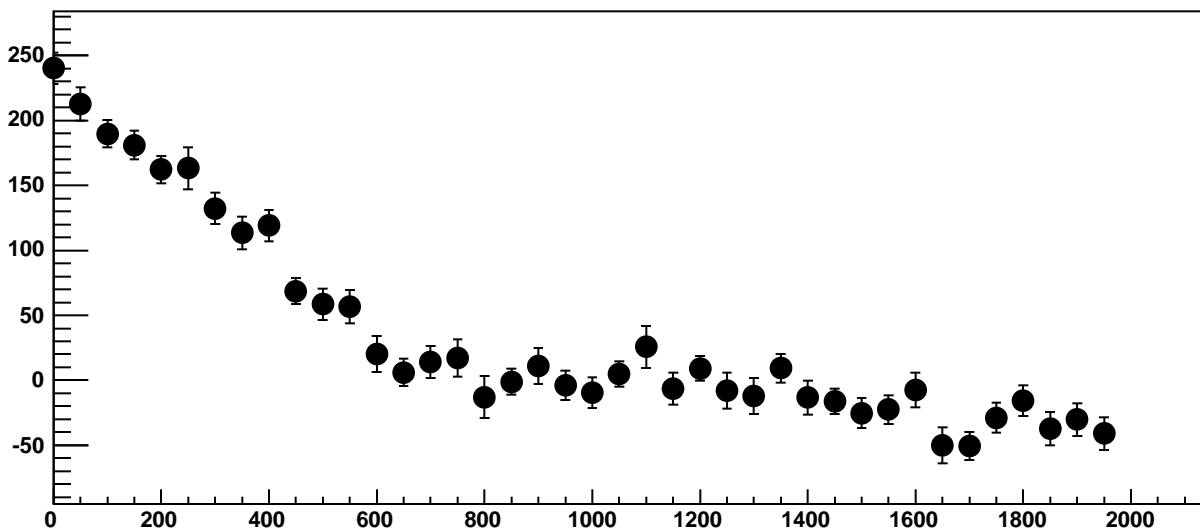
Chip 5, Channel 4, Enable 4, Hold=30, ADC Mean vs DAC



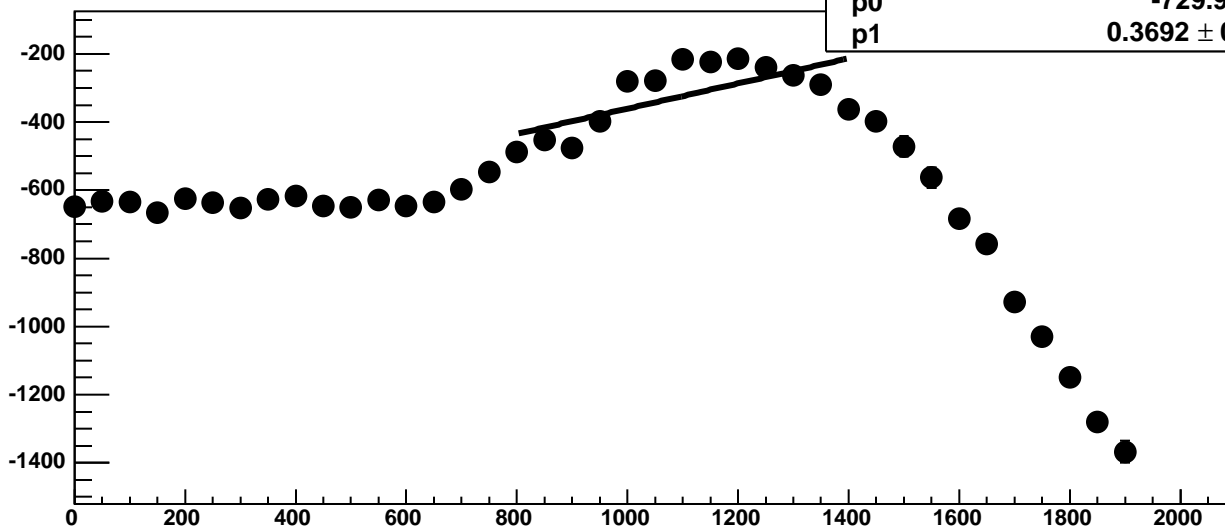
Chip 5, Channel 4, Enable 4, Hold=30, ADC Noise vs DAC



Chip 5, Channel 4, Enable 4, Hold=30, ADC Residuals vs DAC

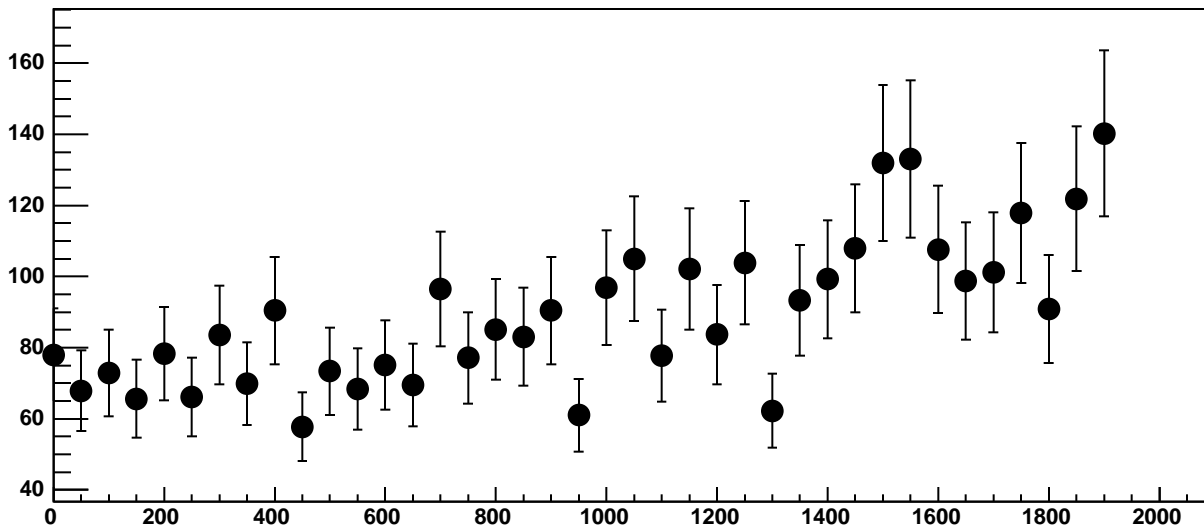


Chip 5, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC

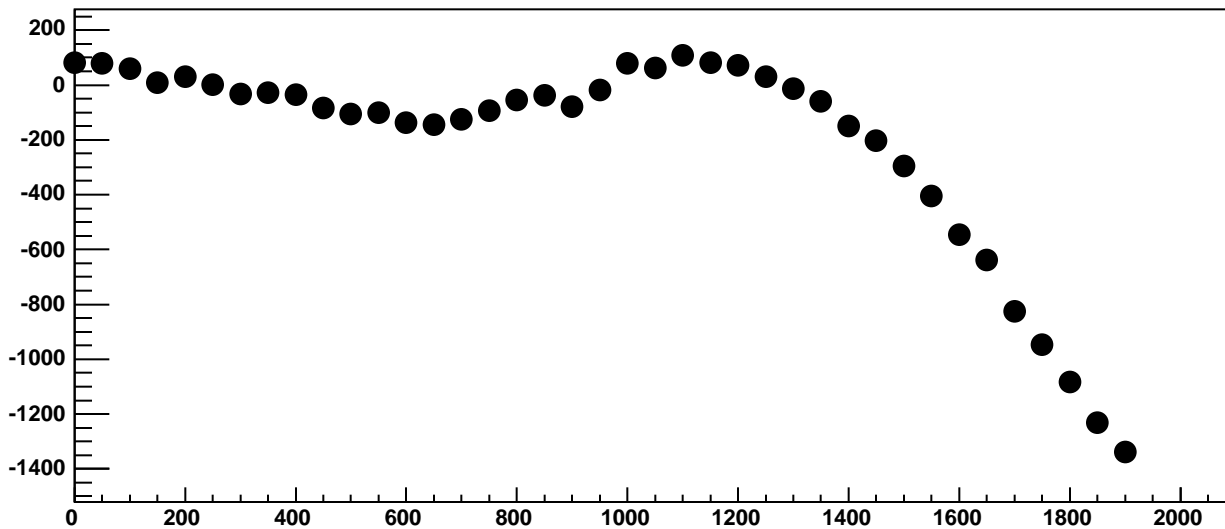


$\chi^2 / \text{ndf}$  163.5 / 11  
p0  $-729.9 \pm 31.8$   
p1  $0.3692 \pm 0.02869$

Chip 5, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC

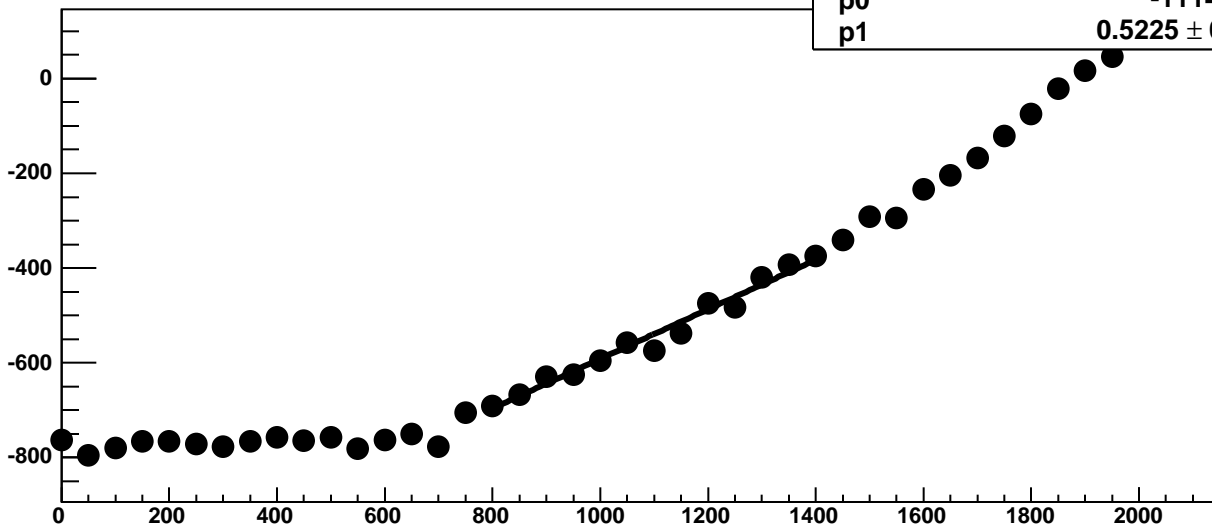


Chip 5, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC

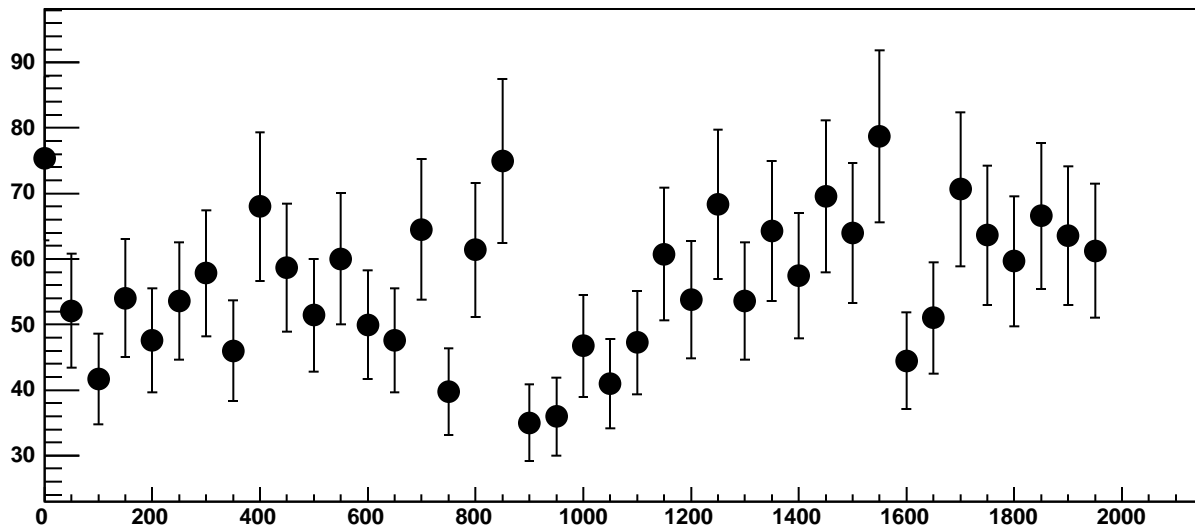




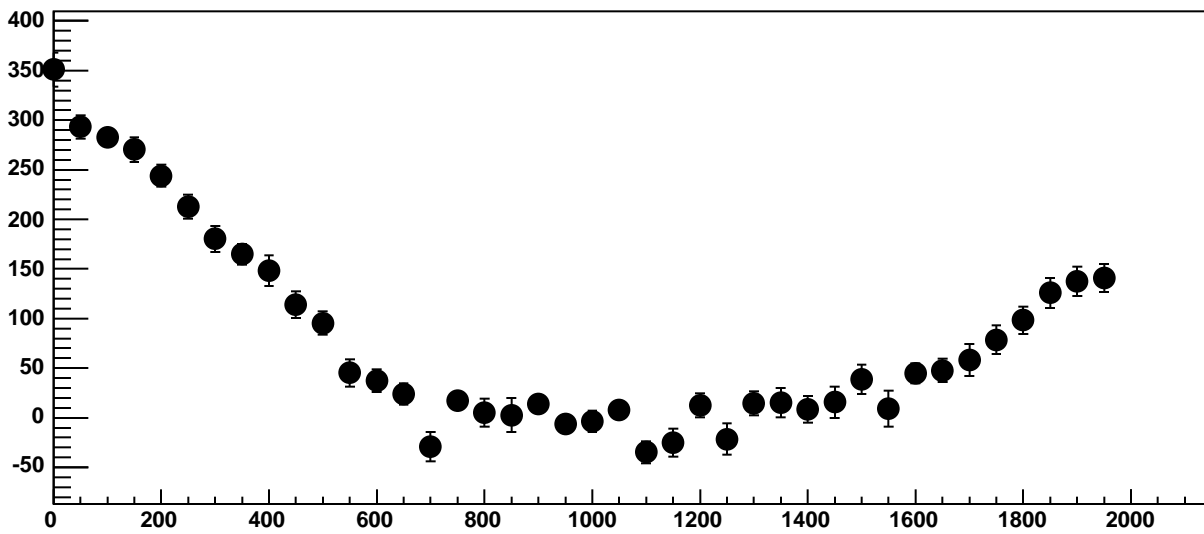
Chip 5, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC



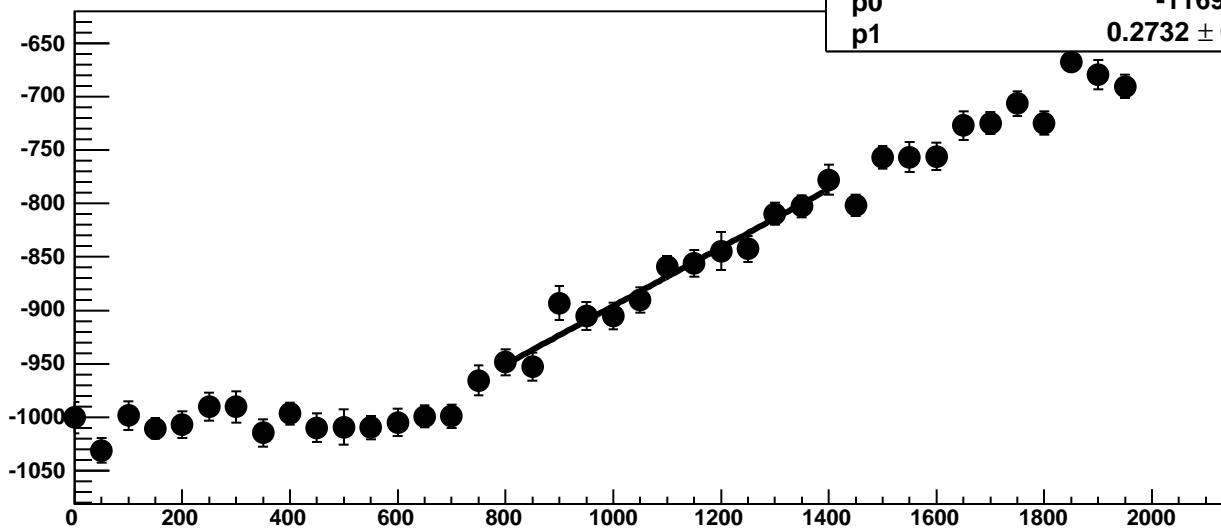
Chip 5, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC



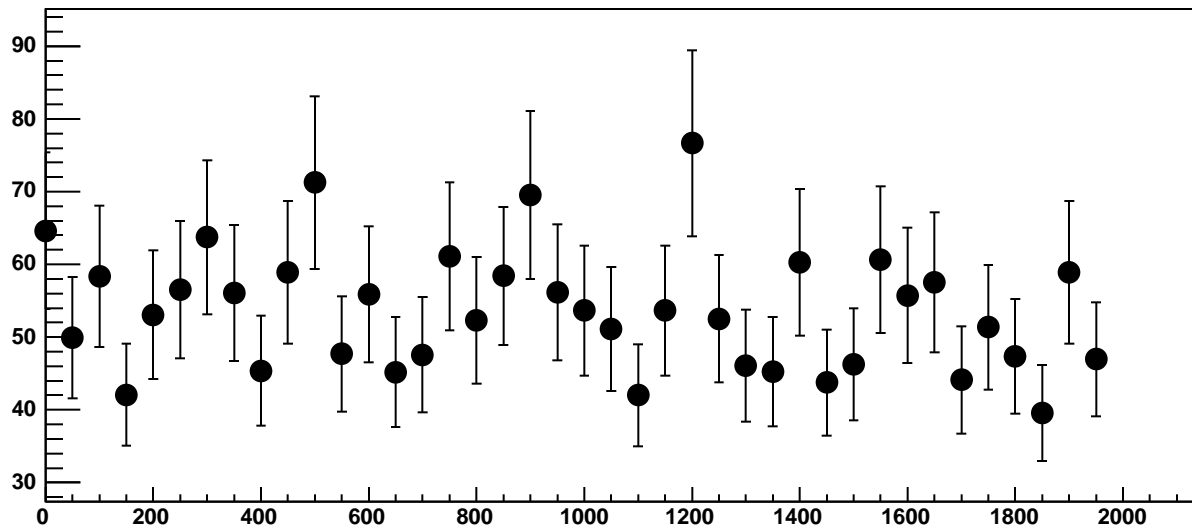
Chip 5, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC



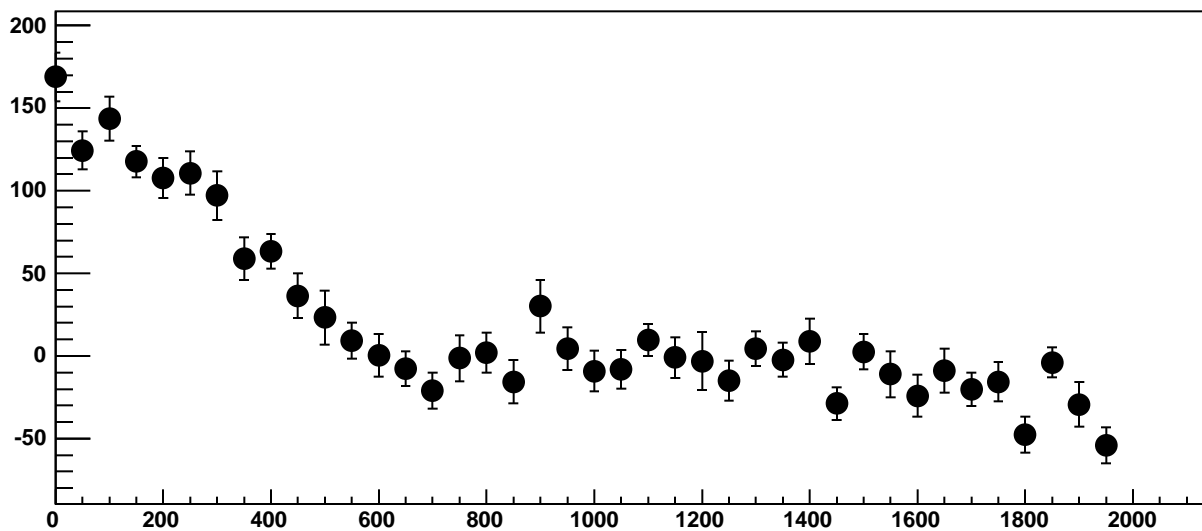
Chip 5, Channel 5, Enable 1, Hold=30, ADC Mean vs DAC



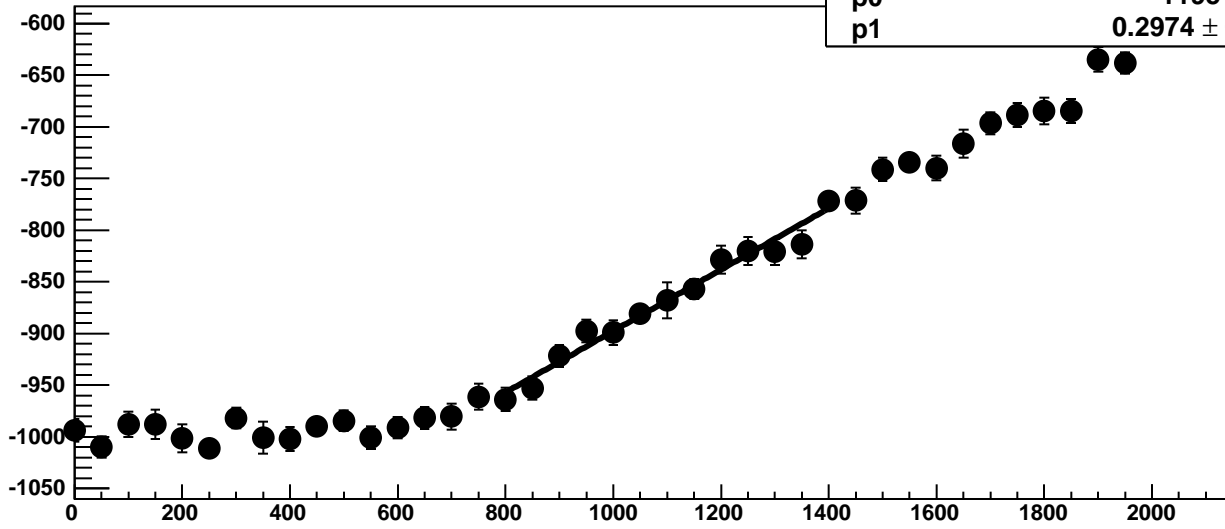
Chip 5, Channel 5, Enable 1, Hold=30, ADC Noise vs DAC



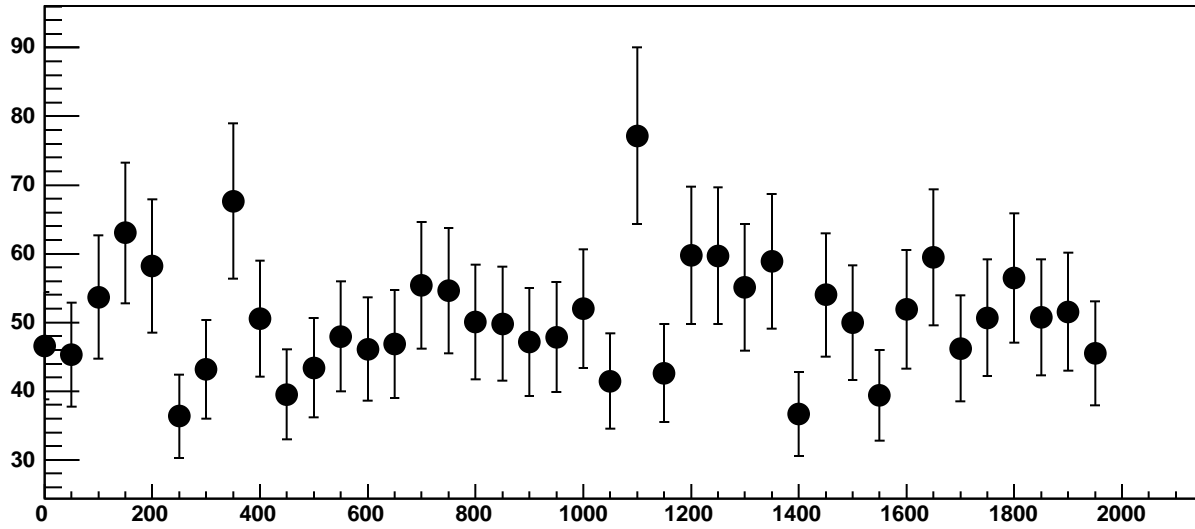
Chip 5, Channel 5, Enable 1, Hold=30, ADC Residuals vs DAC



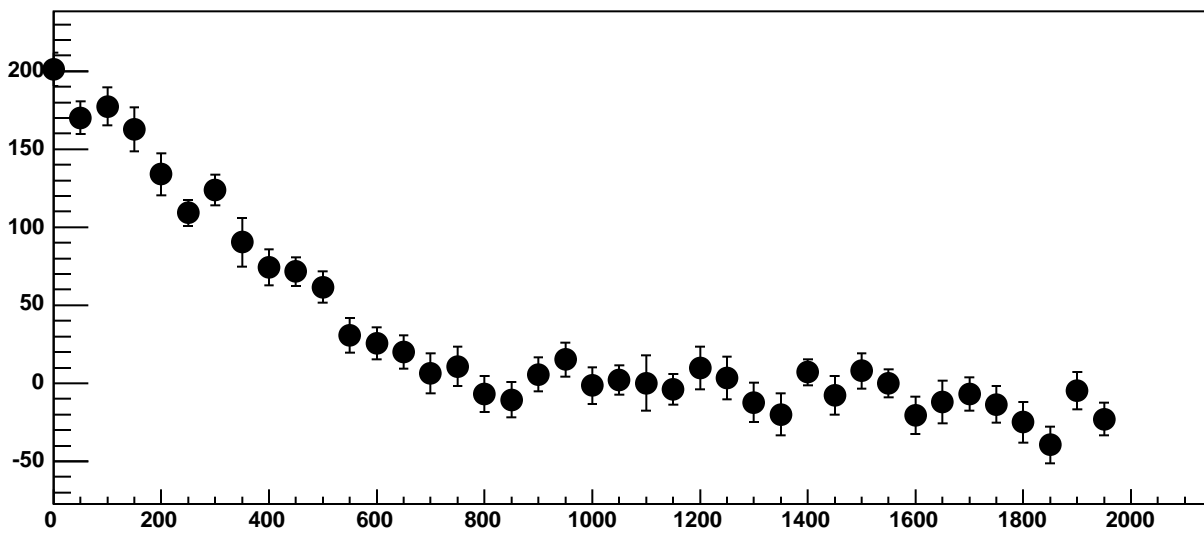
Chip 5, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC



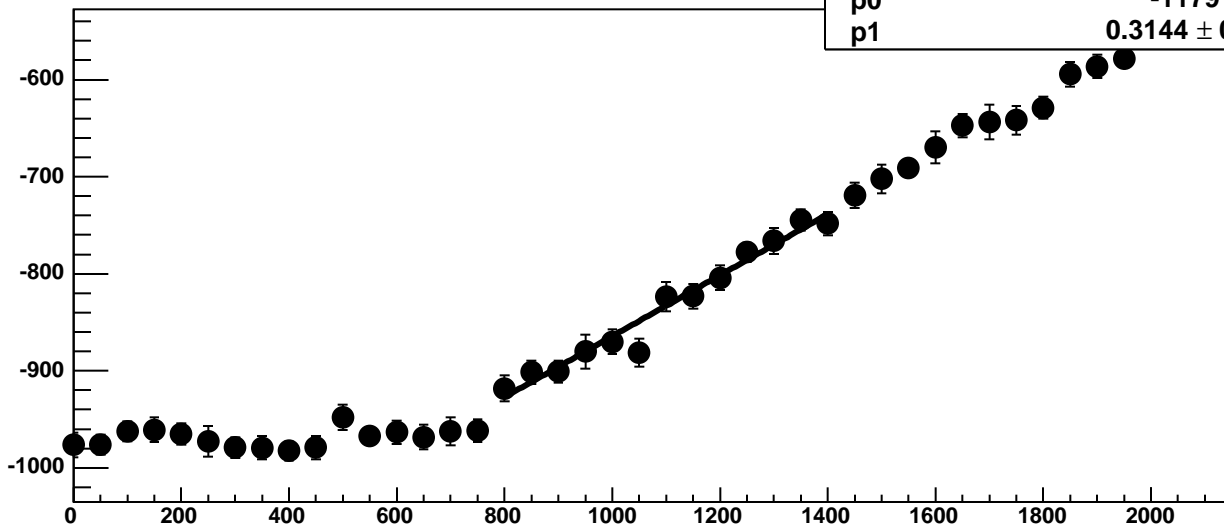
Chip 5, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC



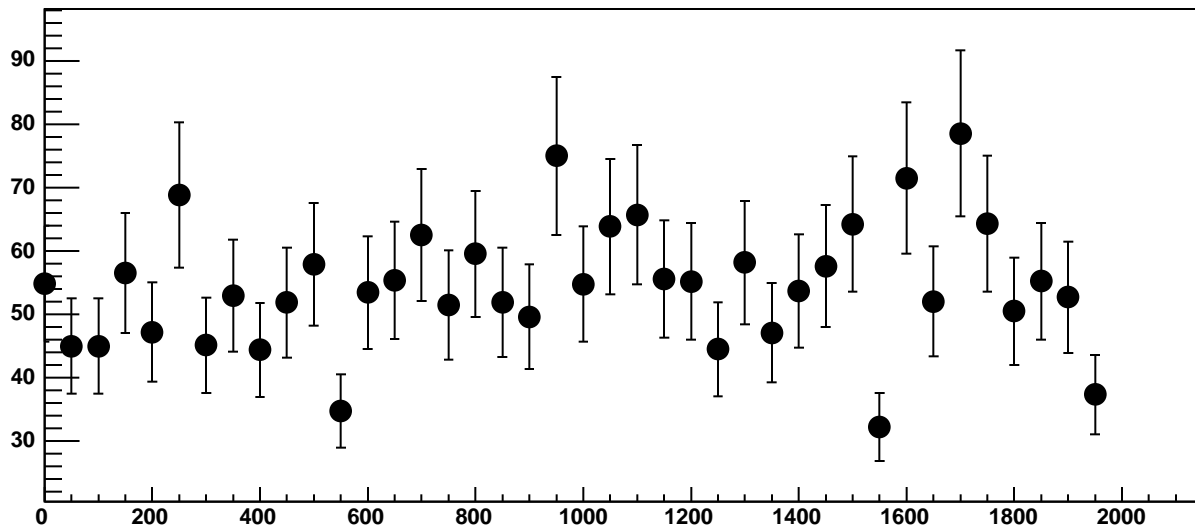
Chip 5, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC



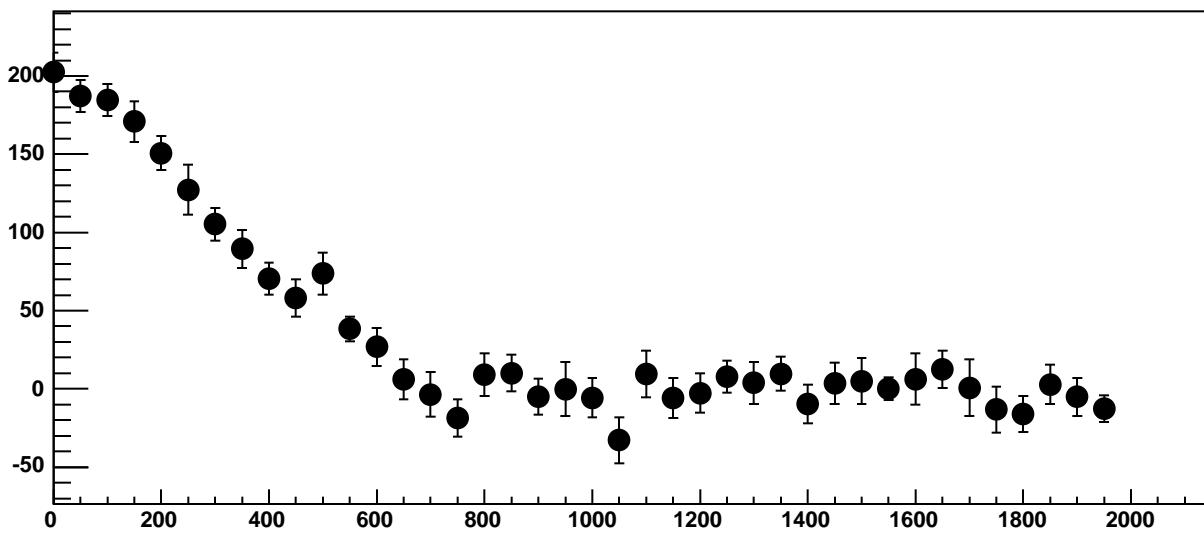
Chip 5, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC



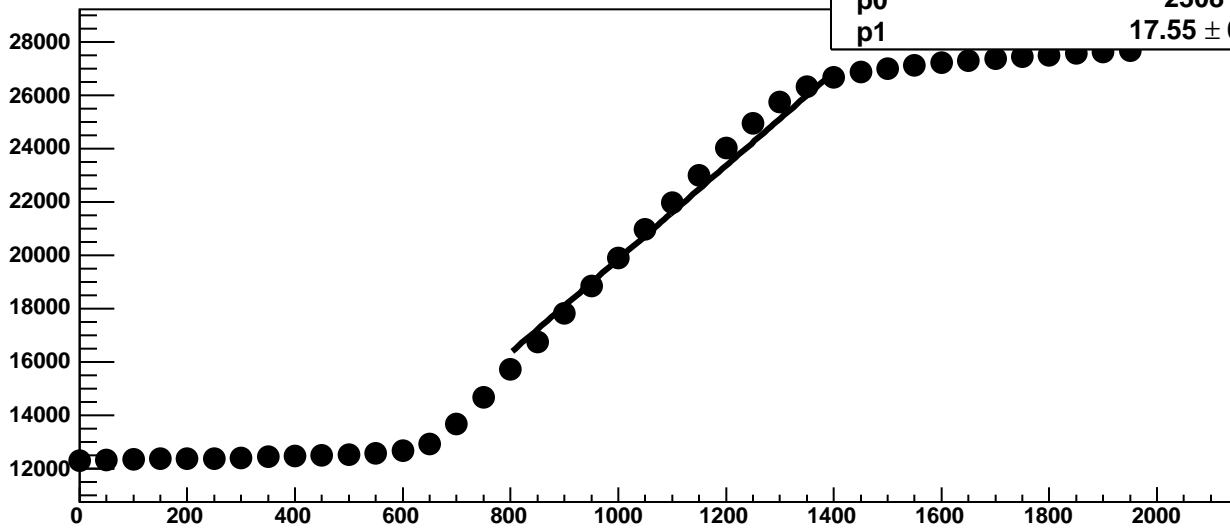
Chip 5, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC



Chip 5, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC

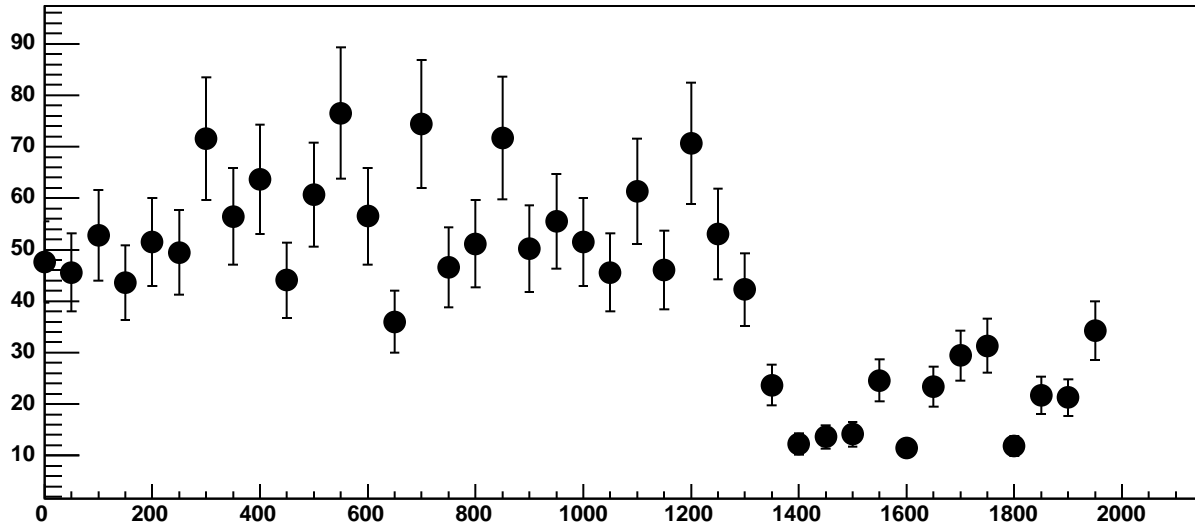


Chip 5, Channel 5, Enable 4!, Hold=30, ADC Mean vs DAC

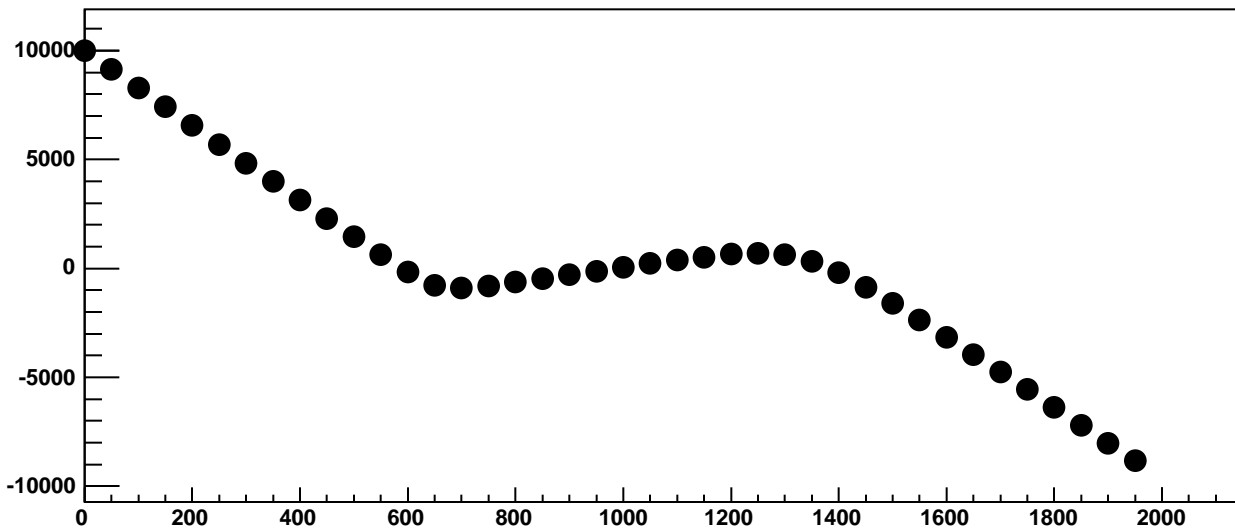


$\chi^2 / \text{ndf}$	2.58e+04 / 11
p0	2308 ± 14.97
p1	17.55 ± 0.01154

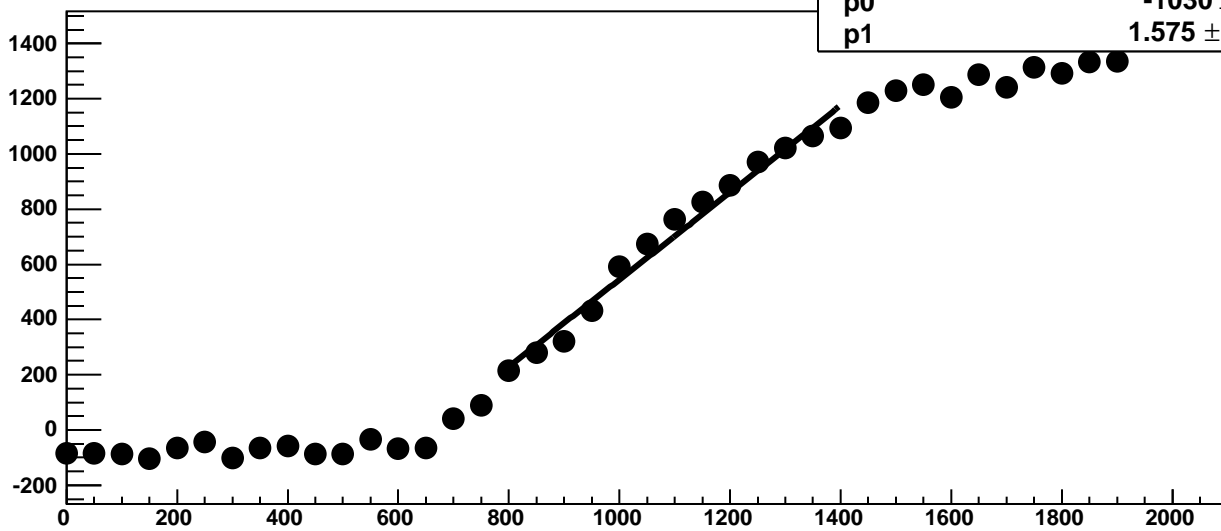
Chip 5, Channel 5, Enable 4!, Hold=30, ADC Noise vs DAC



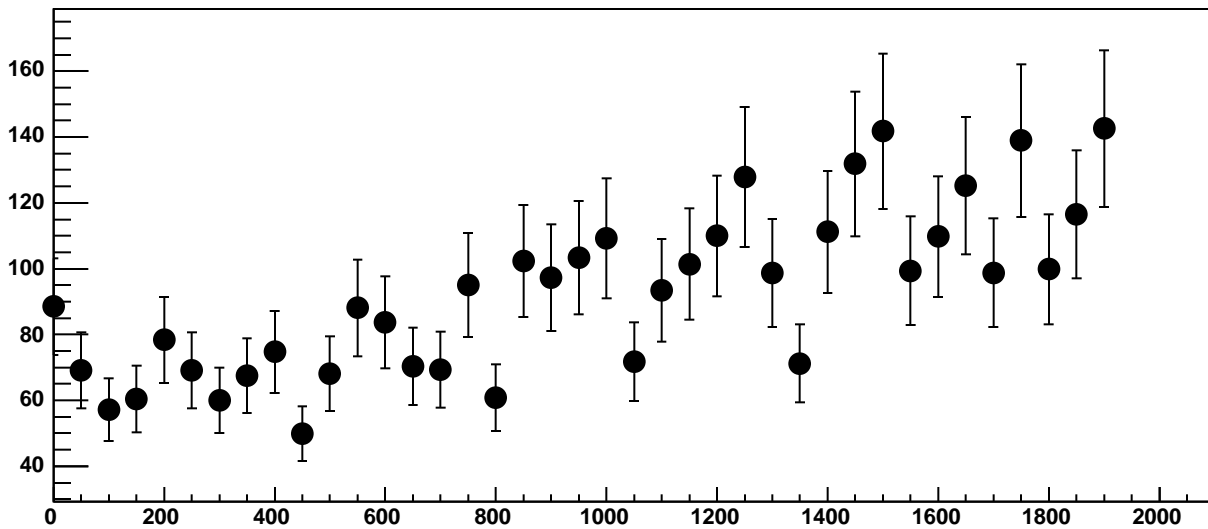
Chip 5, Channel 5, Enable 4!, Hold=30, ADC Residuals vs DAC



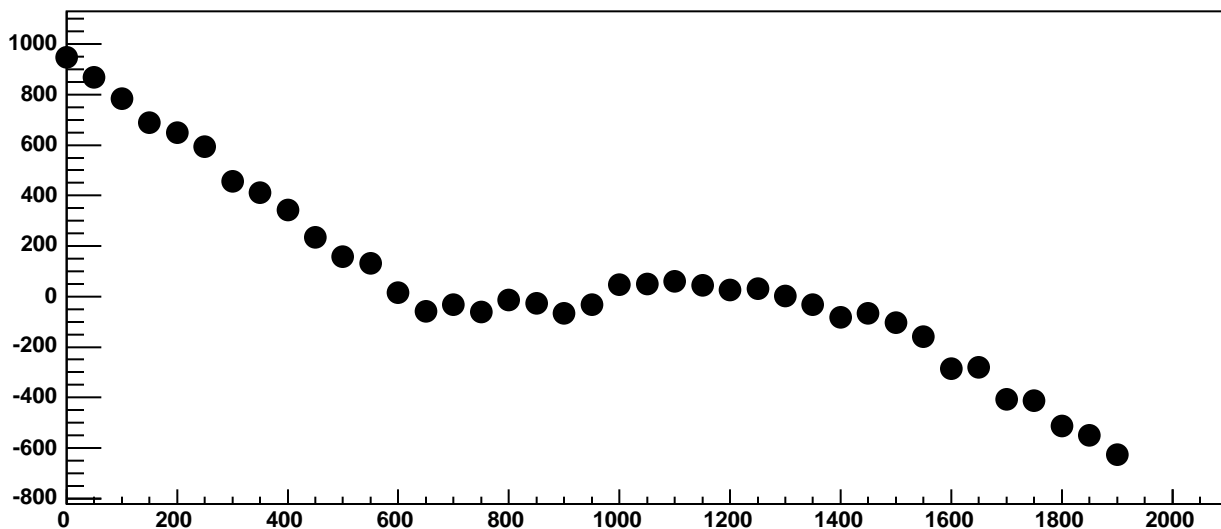
Chip 5, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC



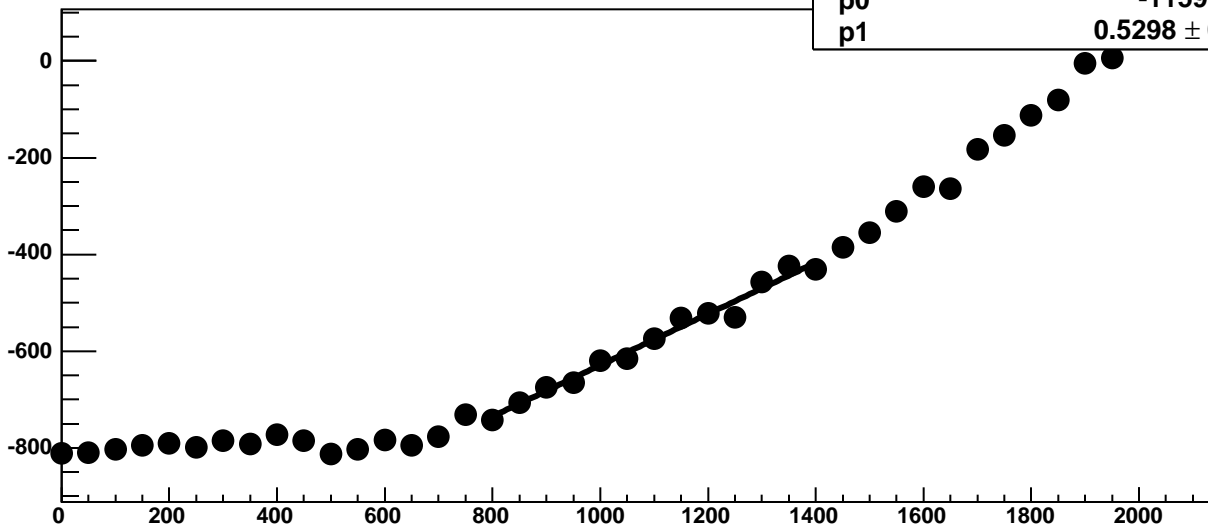
Chip 5, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC



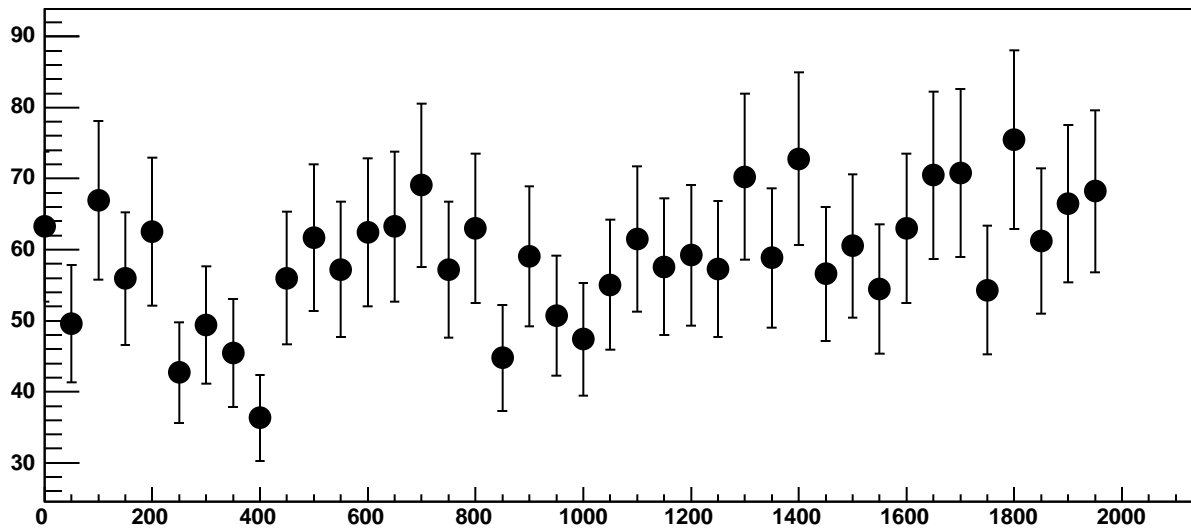
Chip 5, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC



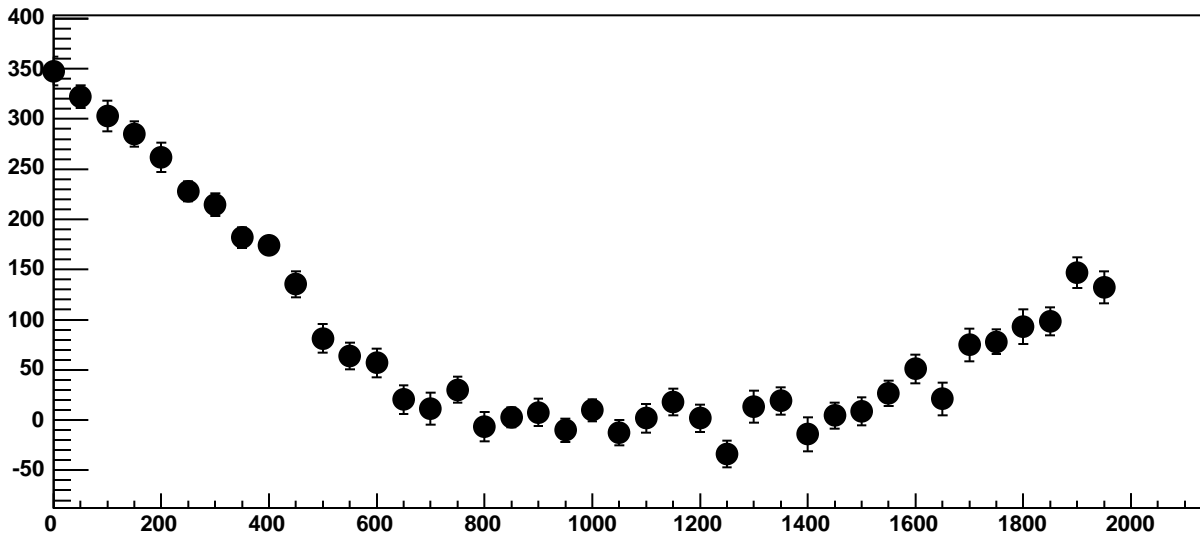
Chip 5, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC



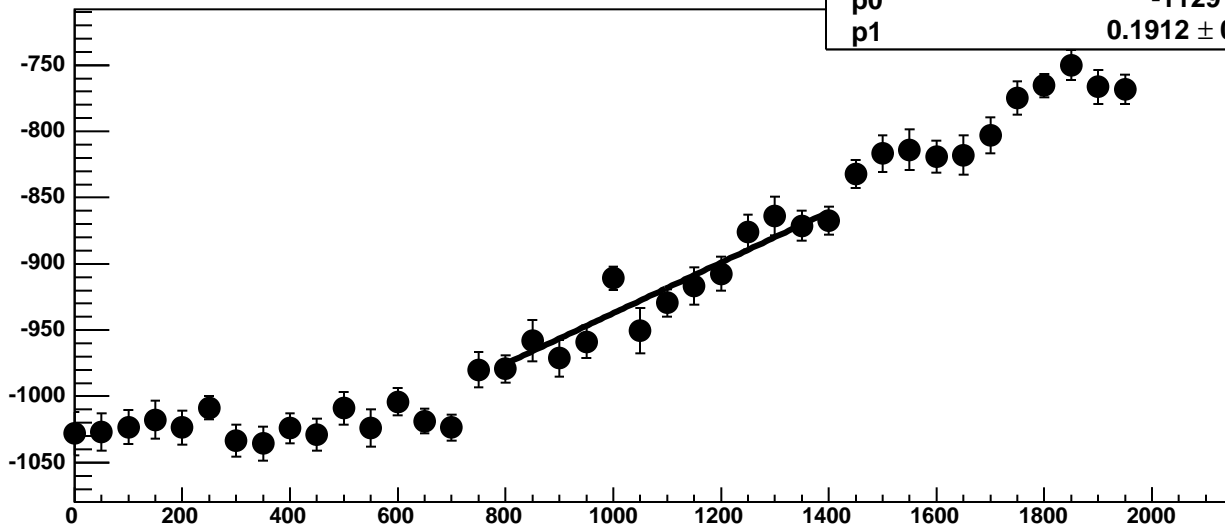
Chip 5, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC



Chip 5, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC

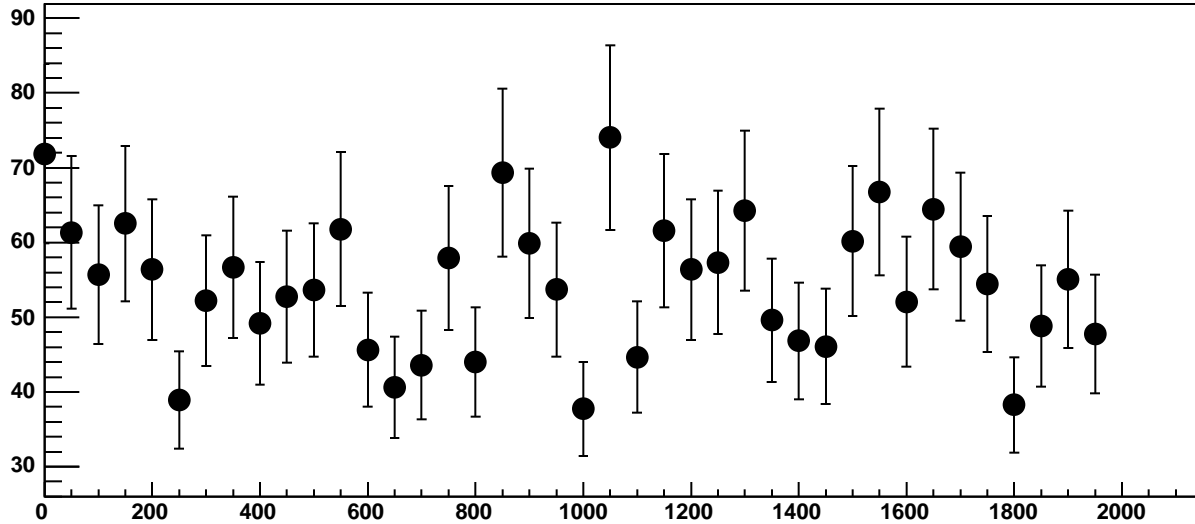


Chip 5, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC

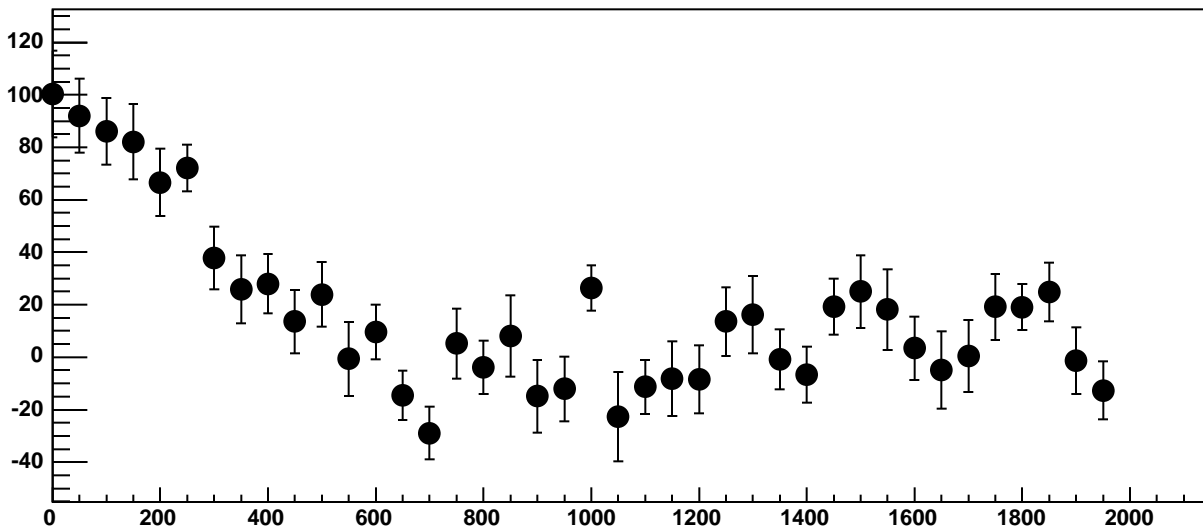


$\chi^2 / \text{ndf}$  18.24 / 11  
p0  $-1129 \pm 19.38$   
p1  $0.1912 \pm 0.01743$

Chip 5, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC

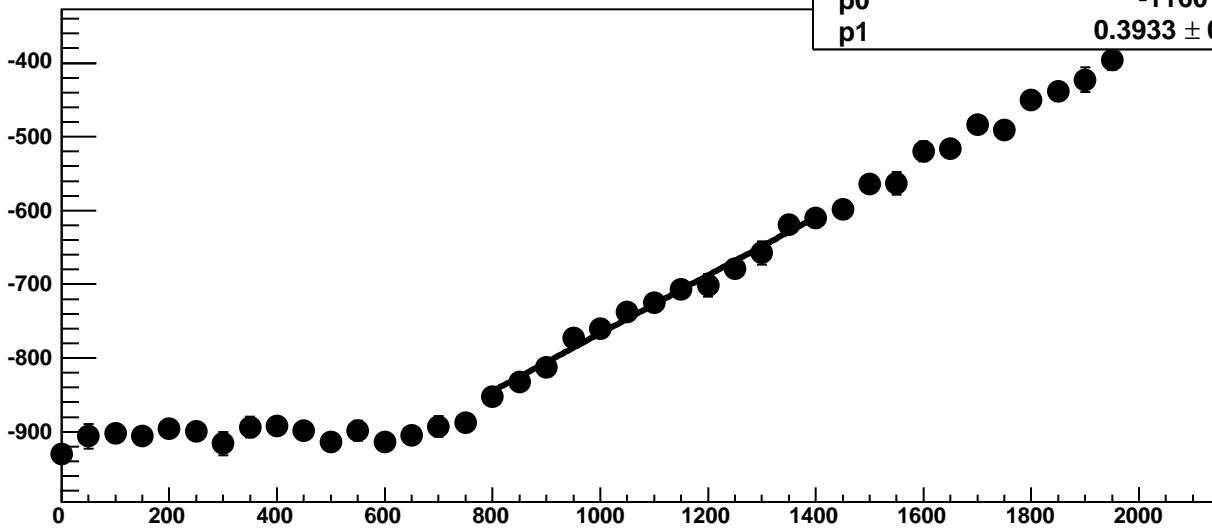


Chip 5, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC



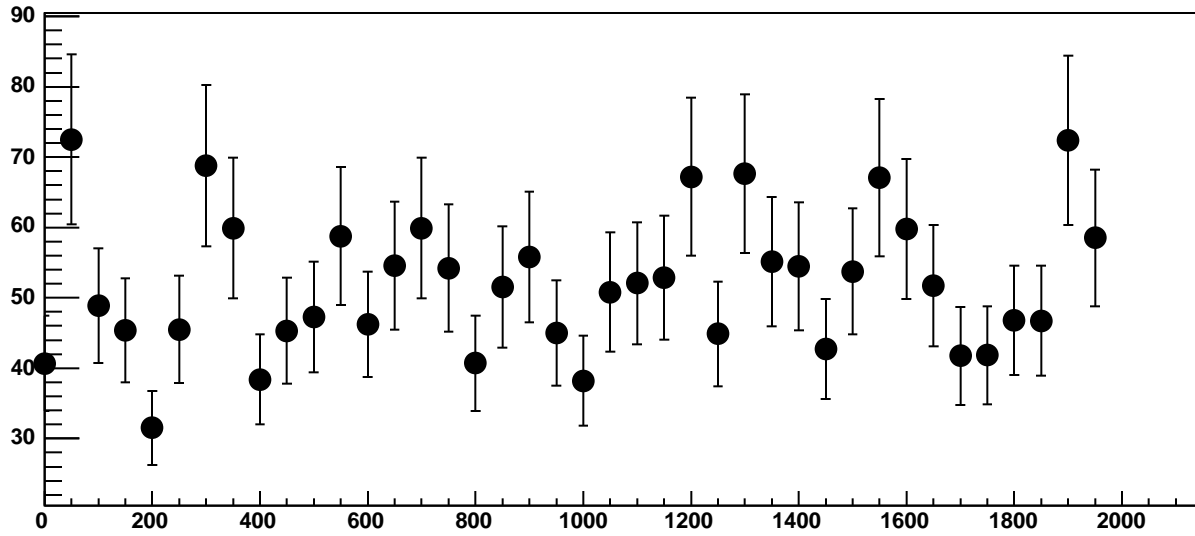


Chip 5, Channel 6, Enable 2, Hold=30, ADC Mean vs DAC

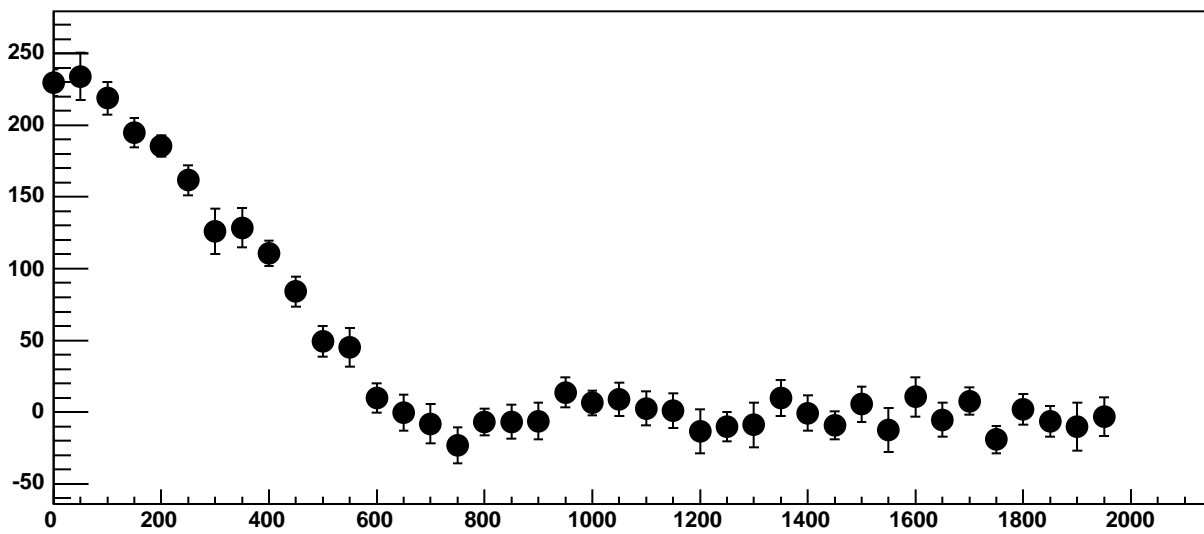


$\chi^2 / \text{ndf}$  6.741 / 11  
p0  $-1160 \pm 18.63$   
p1  $0.3933 \pm 0.01716$

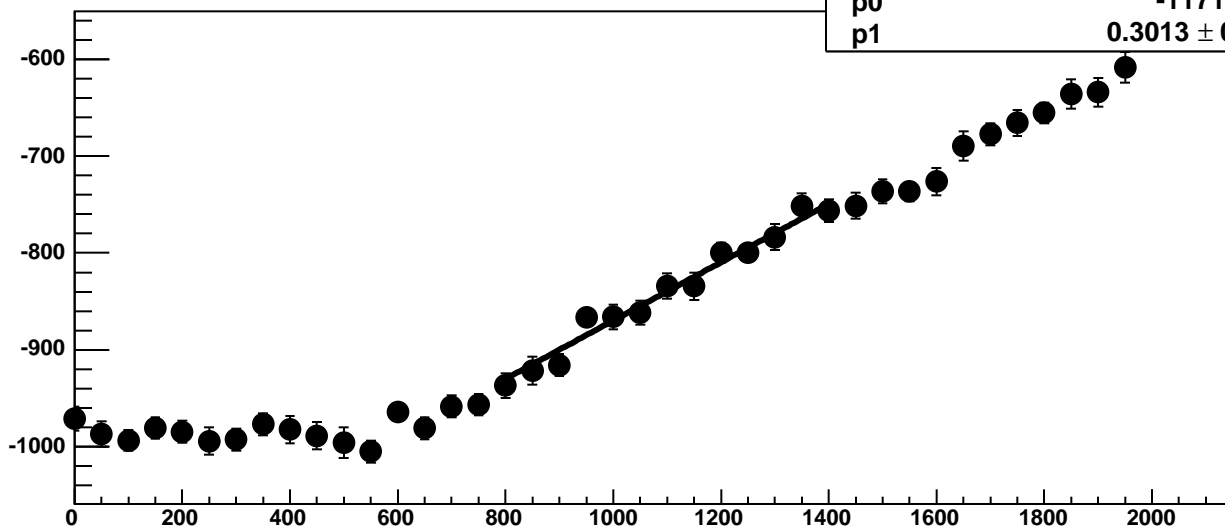
Chip 5, Channel 6, Enable 2, Hold=30, ADC Noise vs DAC



Chip 5, Channel 6, Enable 2, Hold=30, ADC Residuals vs DAC

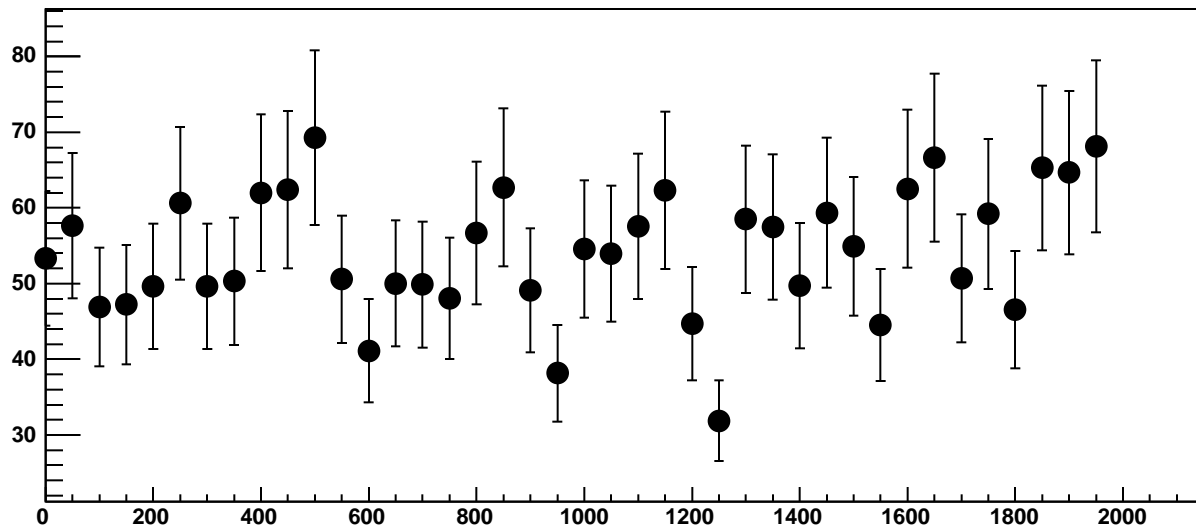


Chip 5, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC

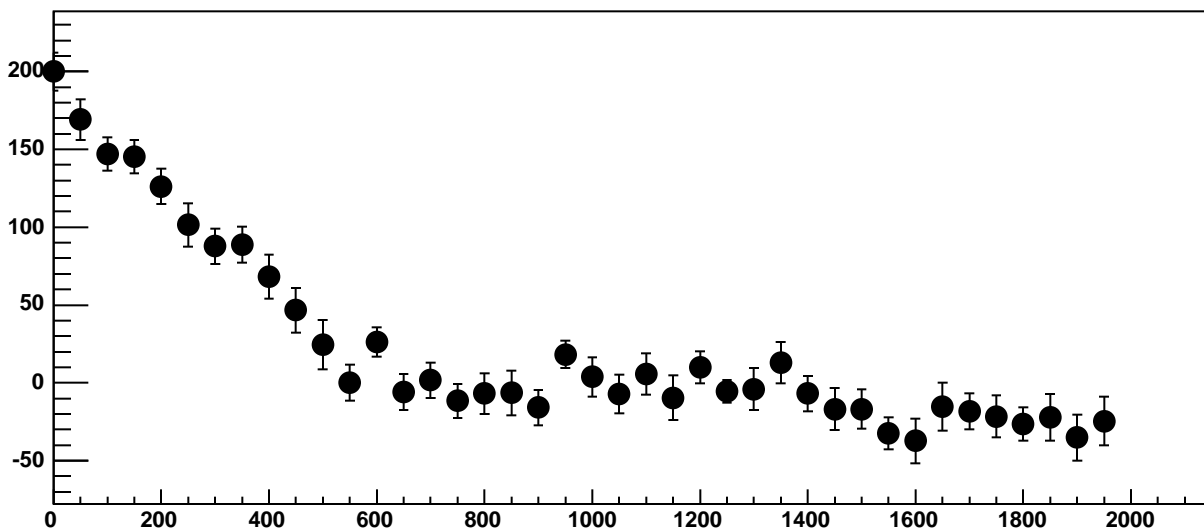


$\chi^2 / \text{ndf}$  10.72 / 11  
p0 -1171 ± 19.62  
p1 0.3013 ± 0.01739

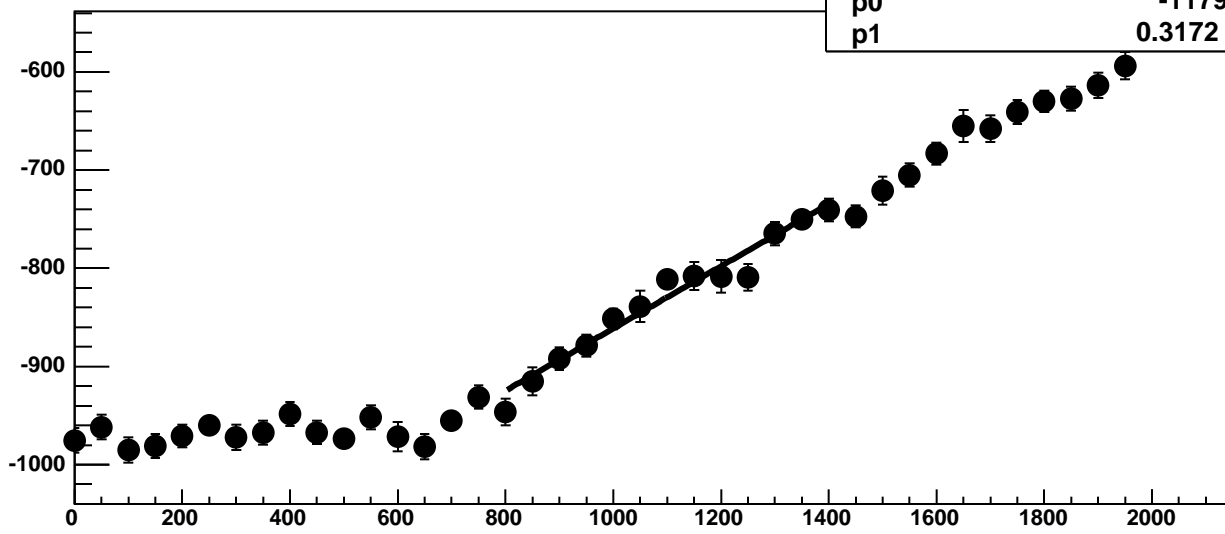
Chip 5, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC



Chip 5, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC

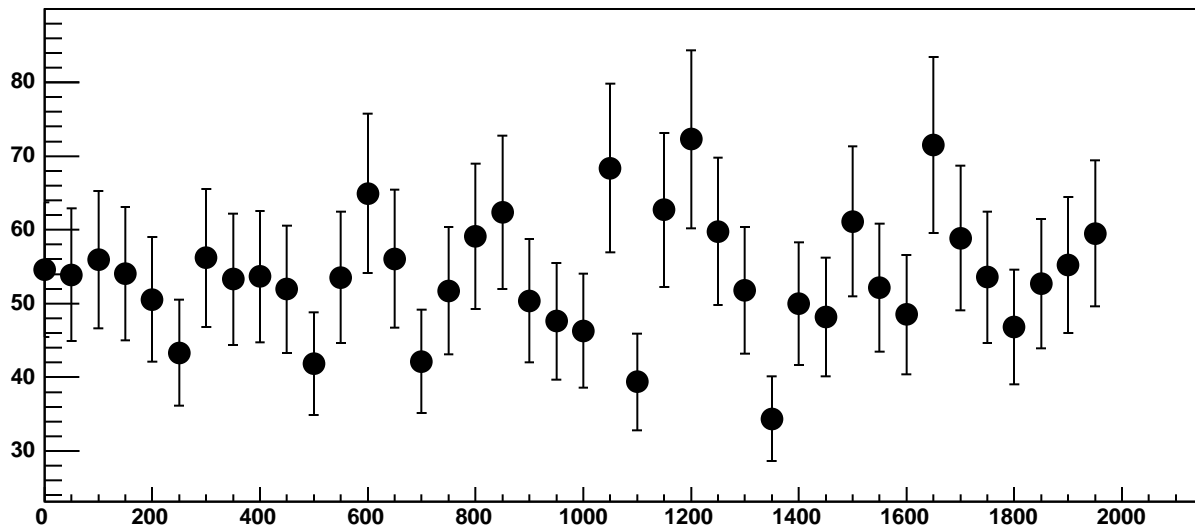


Chip 5, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC

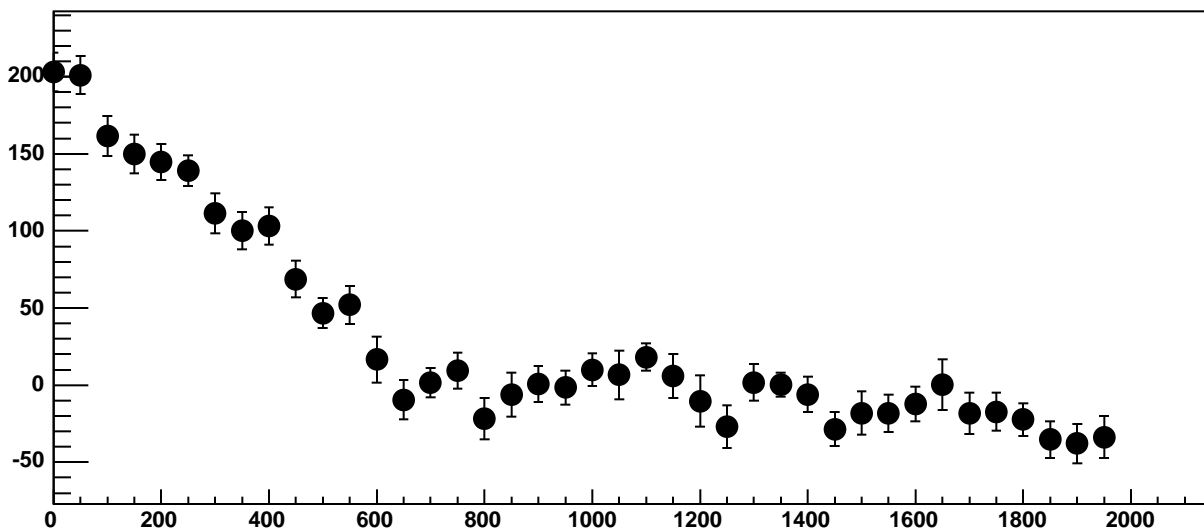


$\chi^2 / \text{ndf}$  12.68 / 11  
p0  $-1179 \pm 19.4$   
p1  $0.3172 \pm 0.017$

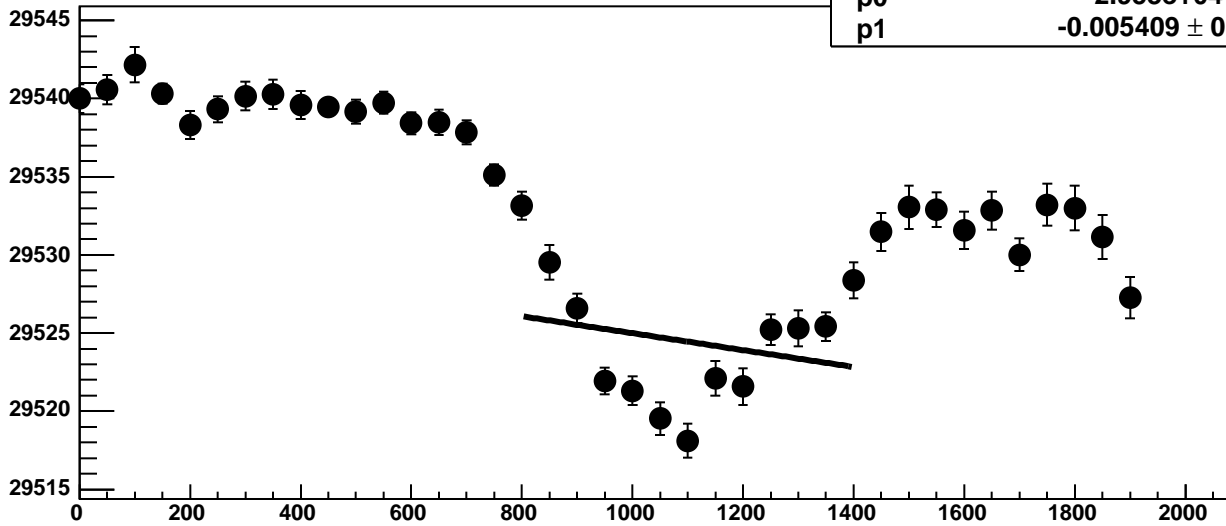
Chip 5, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC



Chip 5, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC



Chip 5, Channel 6, Enable 5!, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

207 / 11

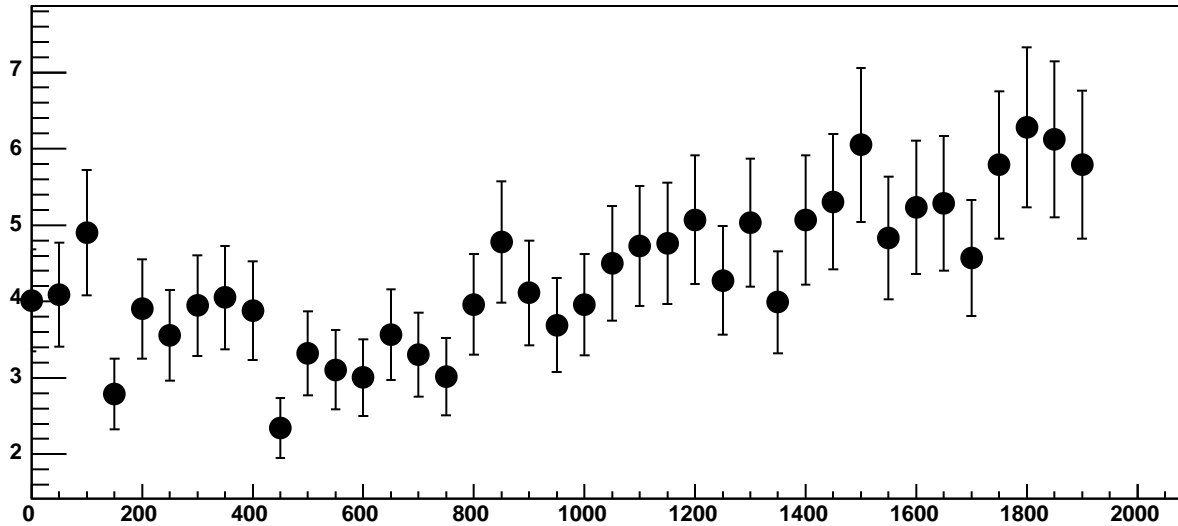
p0

$2.953e+04 \pm 1.634$

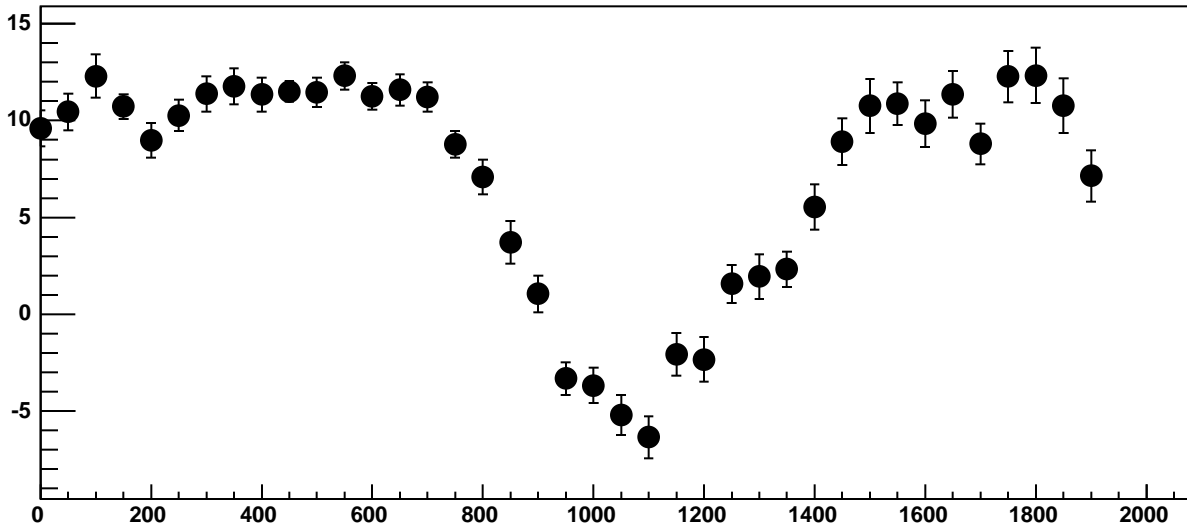
p1

$-0.005409 \pm 0.001488$

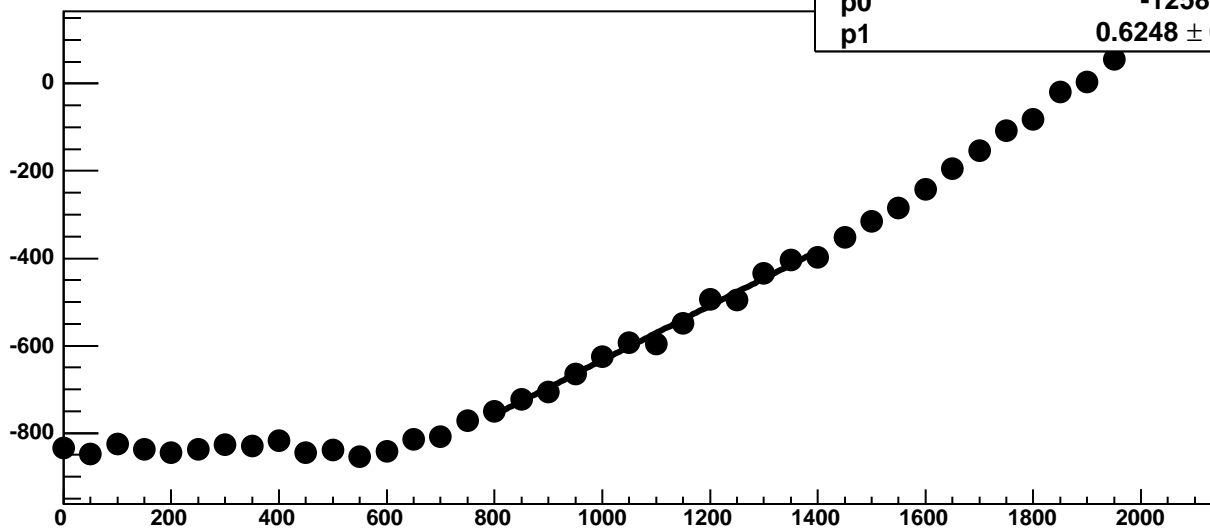
Chip 5, Channel 6, Enable 5!, Hold=30, ADC Noise vs DAC



Chip 5, Channel 6, Enable 5!, Hold=30, ADC Residuals vs DAC

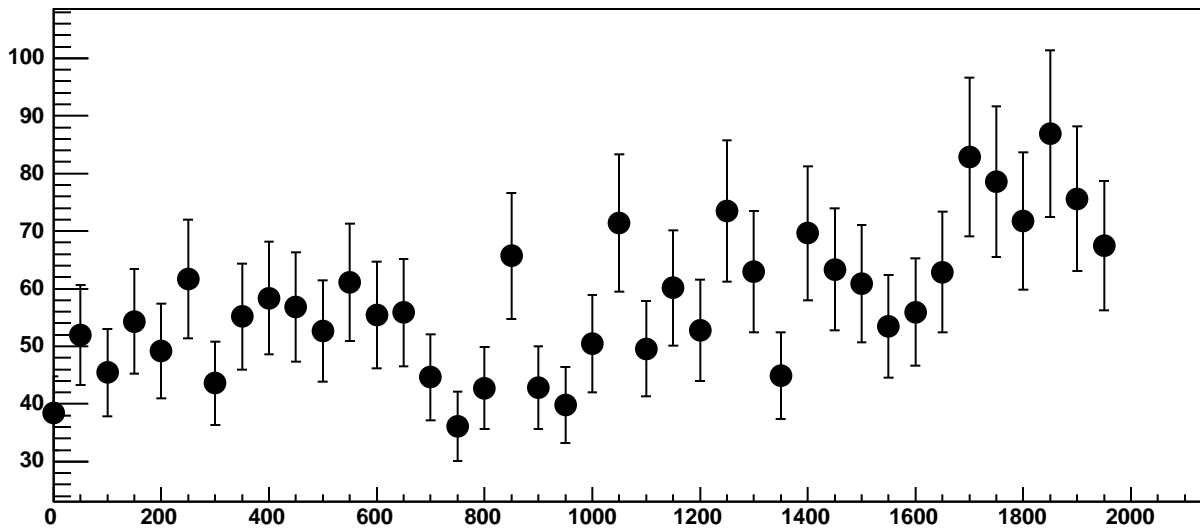


Chip 5, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC

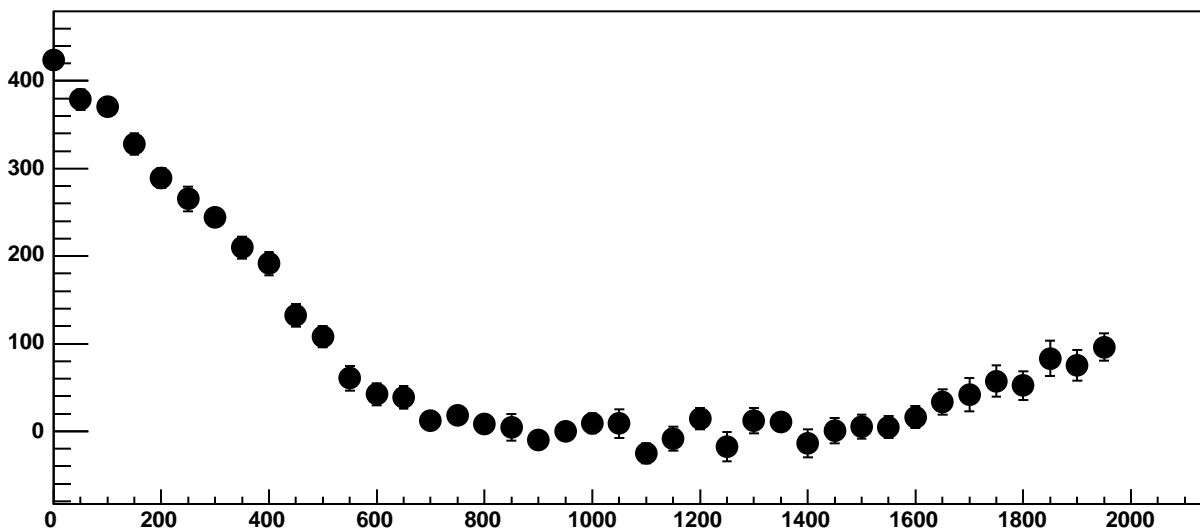


$\chi^2 / \text{ndf}$  12.84 / 11  
p0  $-1258 \pm 19.22$   
p1  $0.6248 \pm 0.01776$

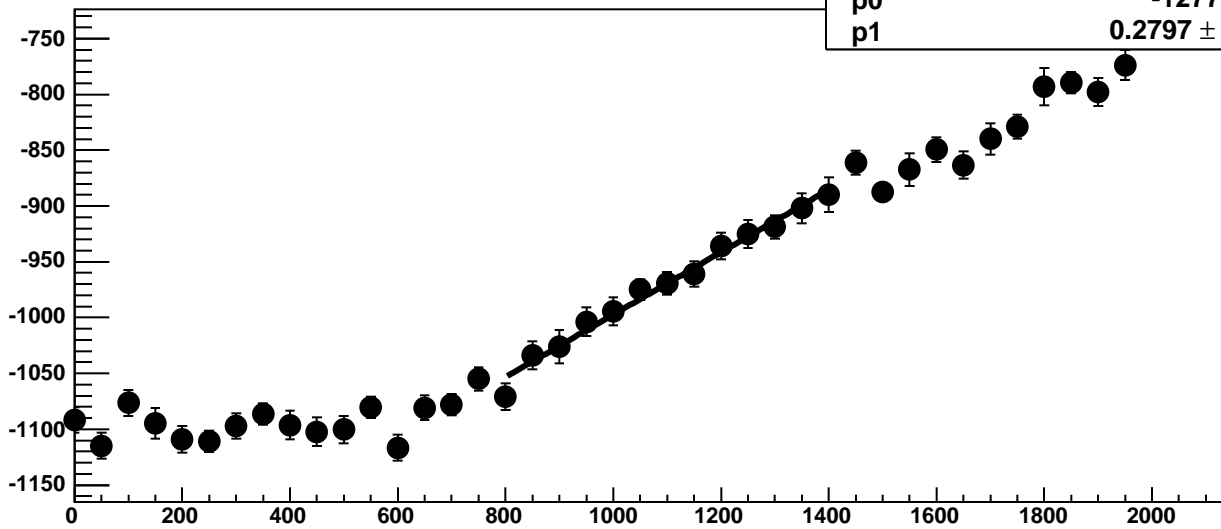
Chip 5, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC



Chip 5, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC

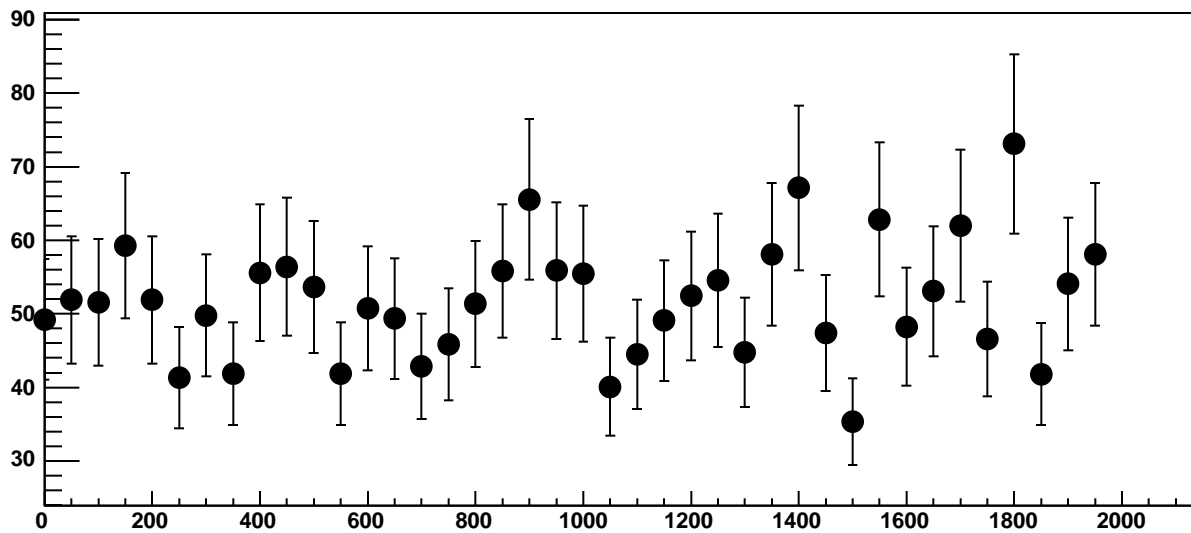


Chip 5, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC

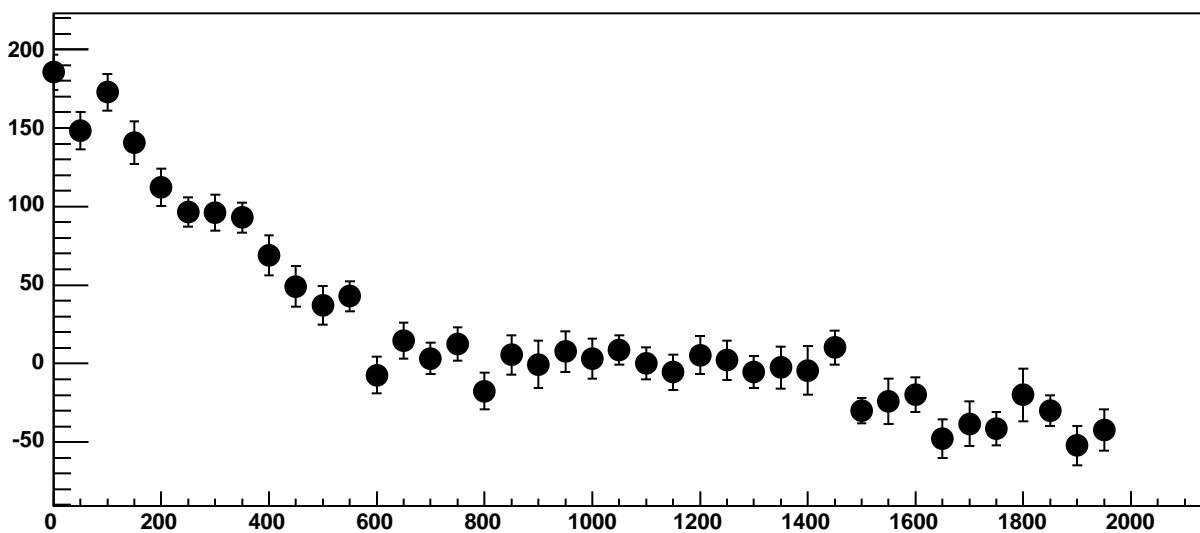


$\chi^2 / \text{ndf}$  4.556 / 11  
p0  $-1277 \pm 21.03$   
p1  $0.2797 \pm 0.01891$

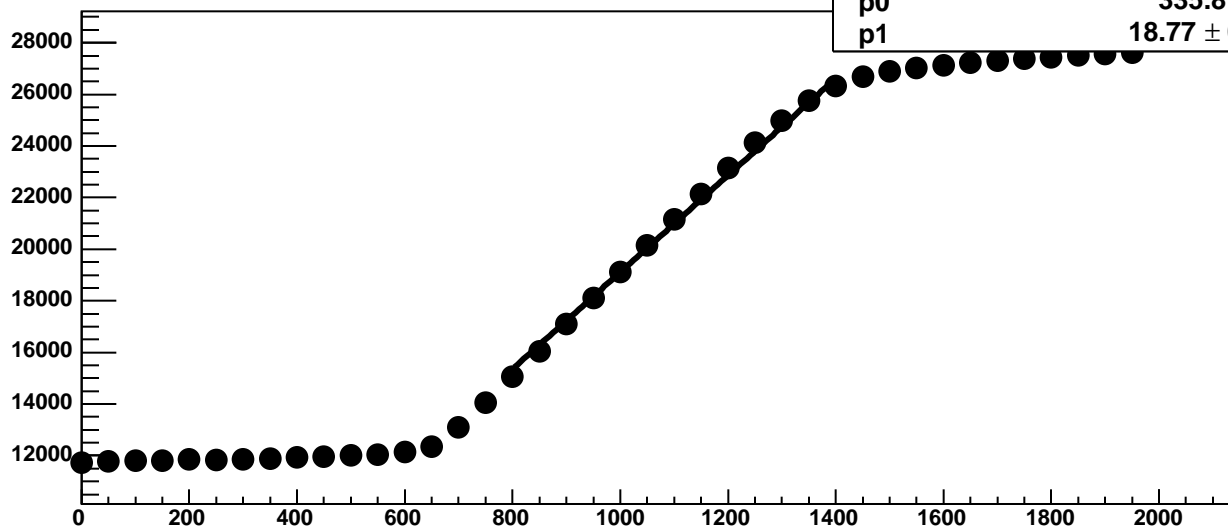
Chip 5, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC



Chip 5, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC

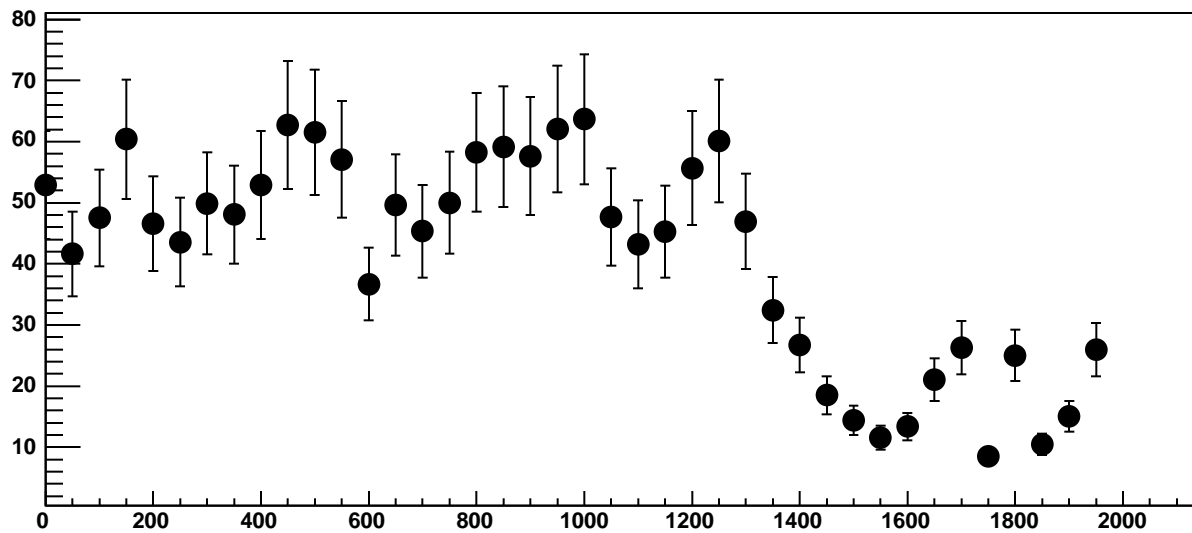


Chip 5, Channel 7, Enable 2!, Hold=30, ADC Mean vs DAC

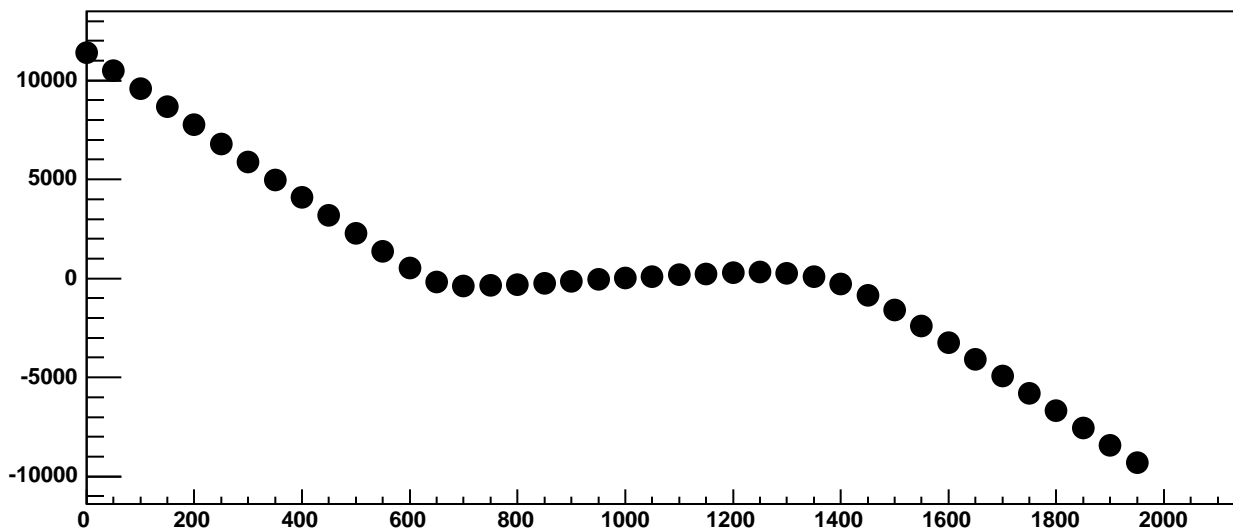


$\chi^2 / \text{ndf}$	5671 / 11
p0	$335.8 \pm 18.05$
p1	$18.77 \pm 0.01502$

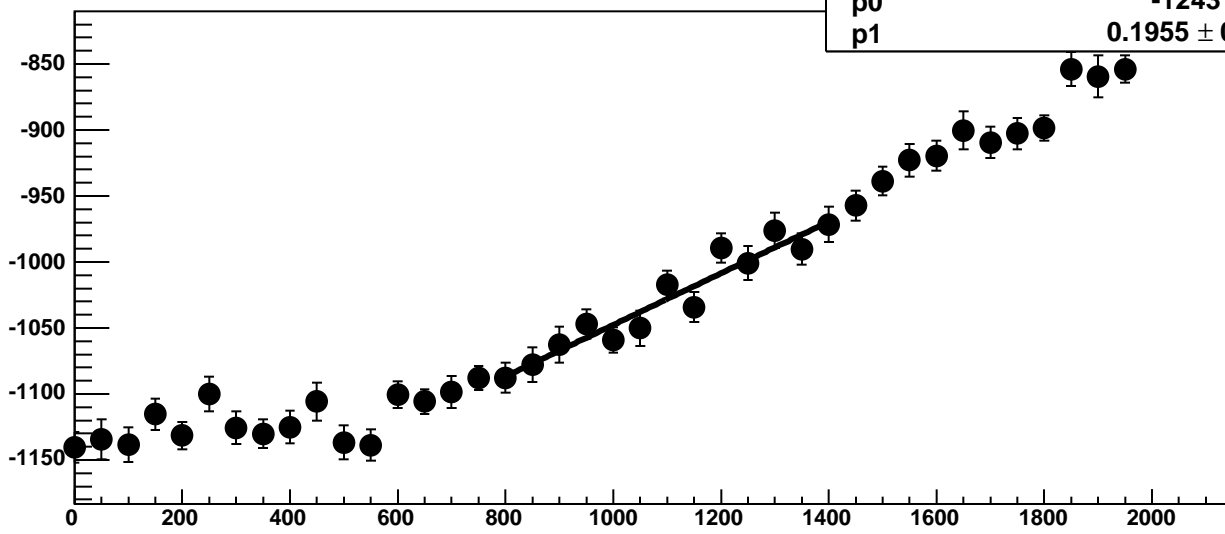
Chip 5, Channel 7, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 5, Channel 7, Enable 2!, Hold=30, ADC Residuals vs DAC

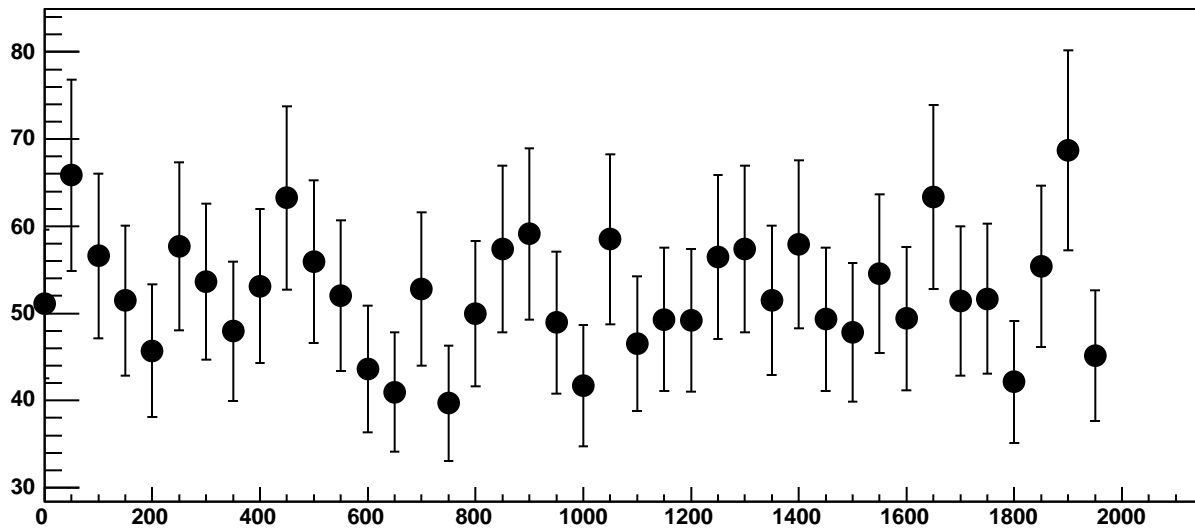


Chip 5, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC

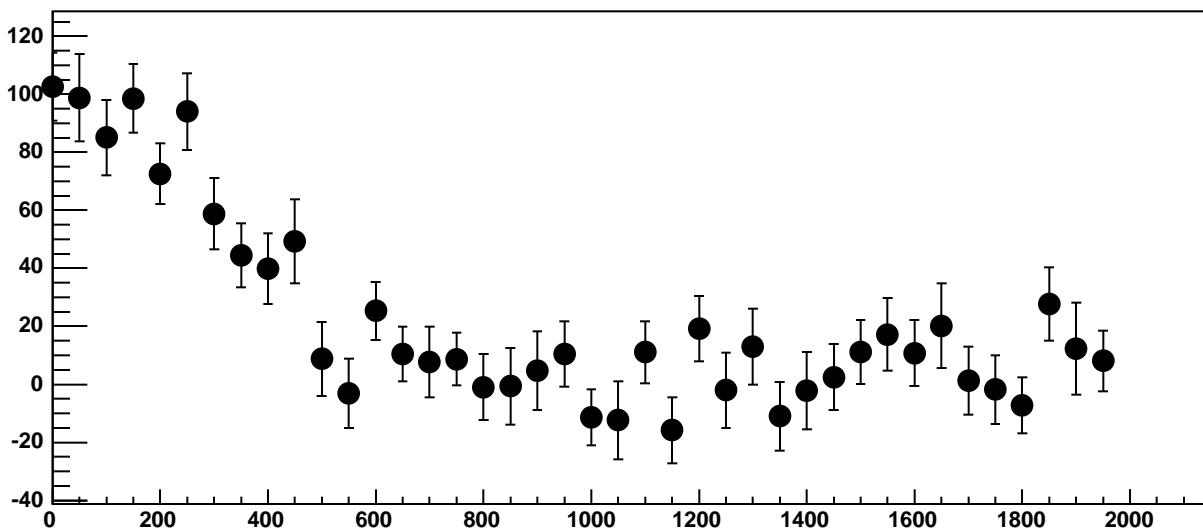


$\chi^2 / \text{ndf}$  11.05 / 11  
p0  $-1243 \pm 20.29$   
p1  $0.1955 \pm 0.01832$

Chip 5, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC

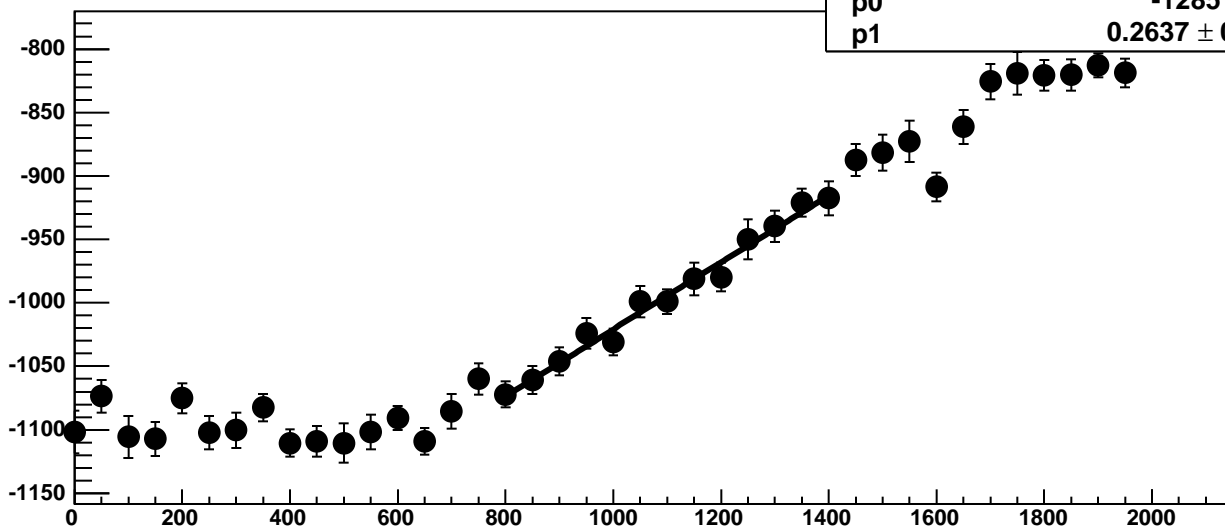


Chip 5, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC

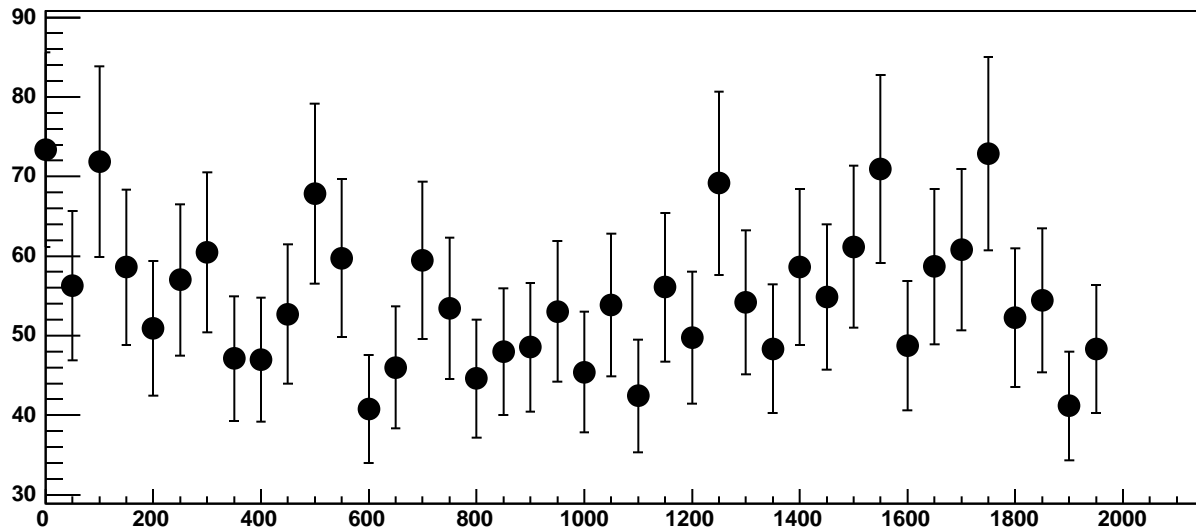




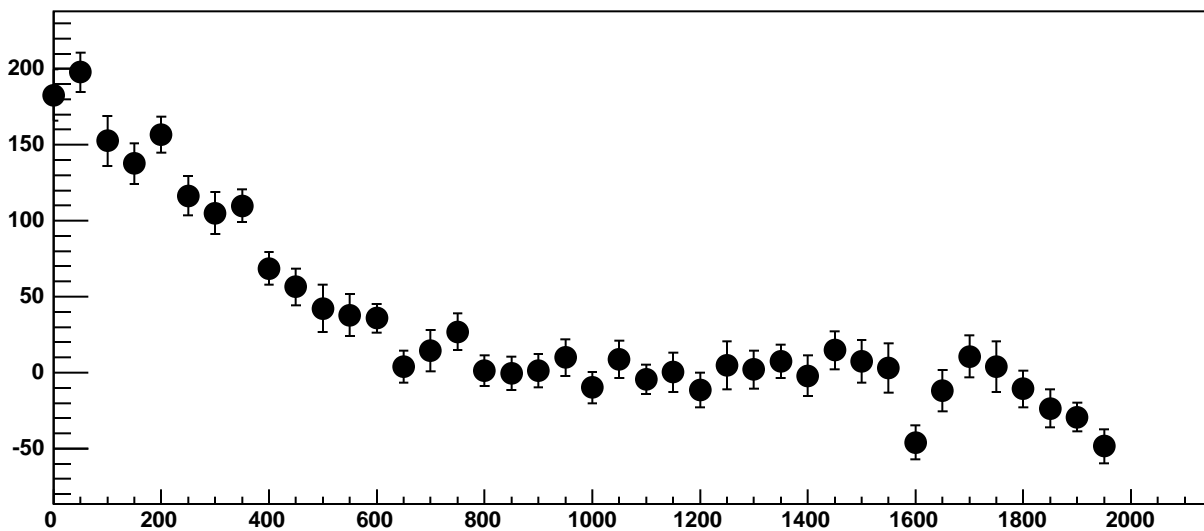
Chip 5, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC



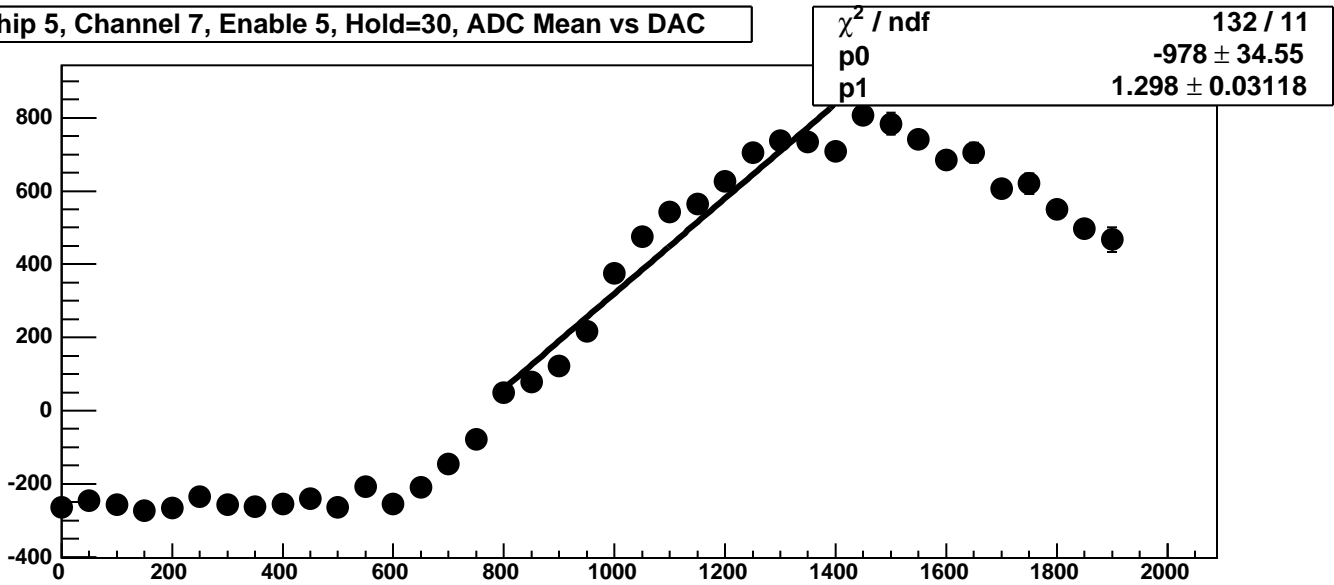
Chip 5, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC



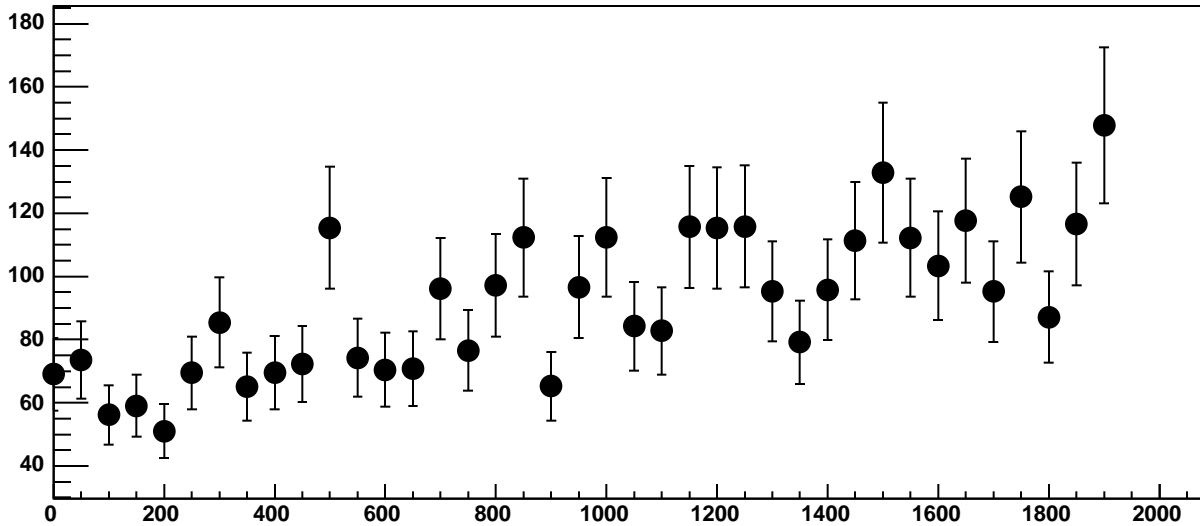
Chip 5, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC



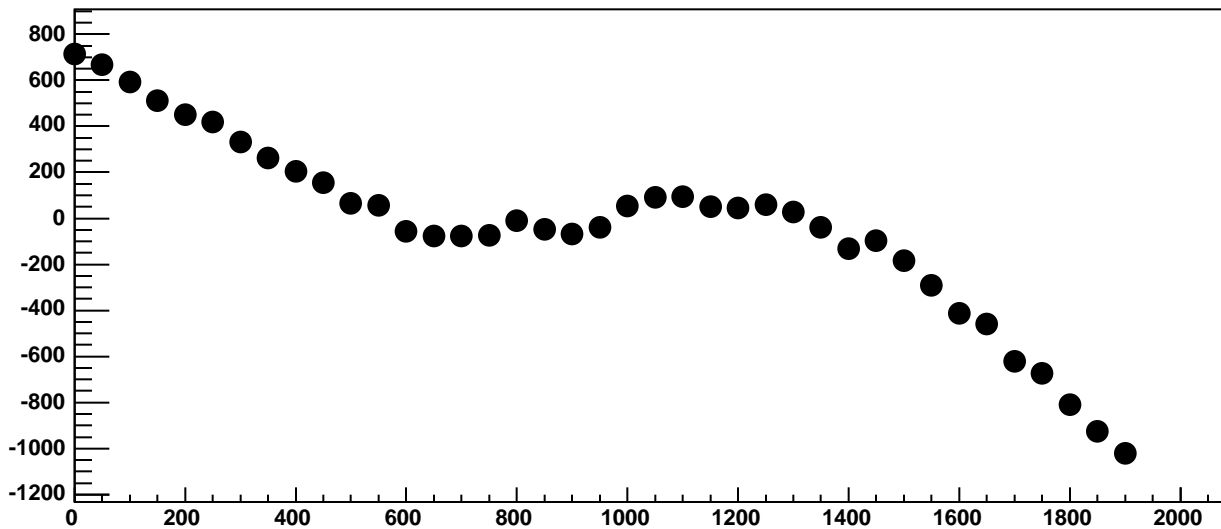
Chip 5, Channel 7, Enable 5, Hold=30, ADC Mean vs DAC



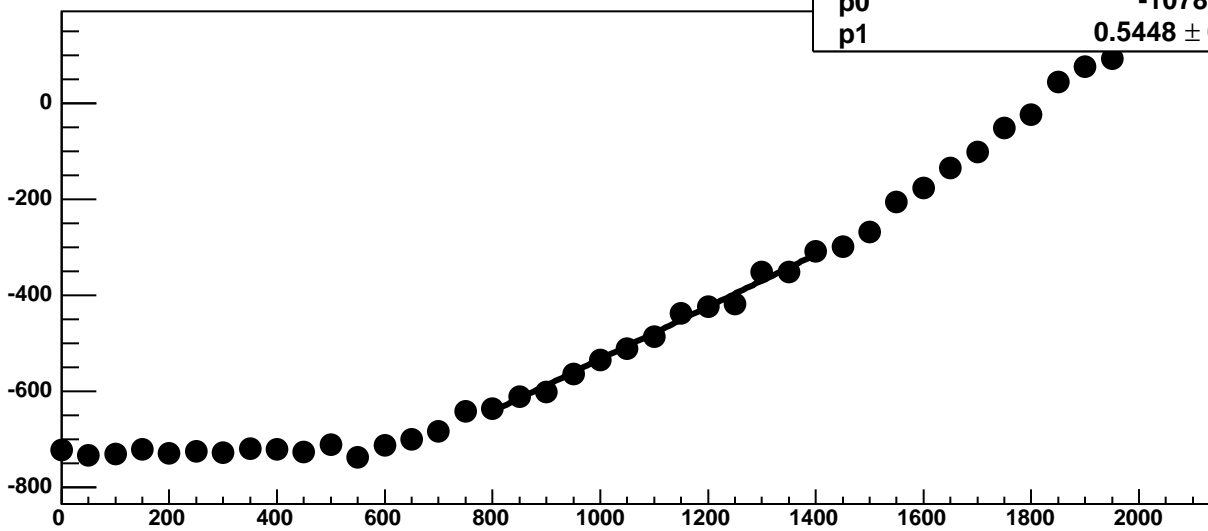
Chip 5, Channel 7, Enable 5, Hold=30, ADC Noise vs DAC



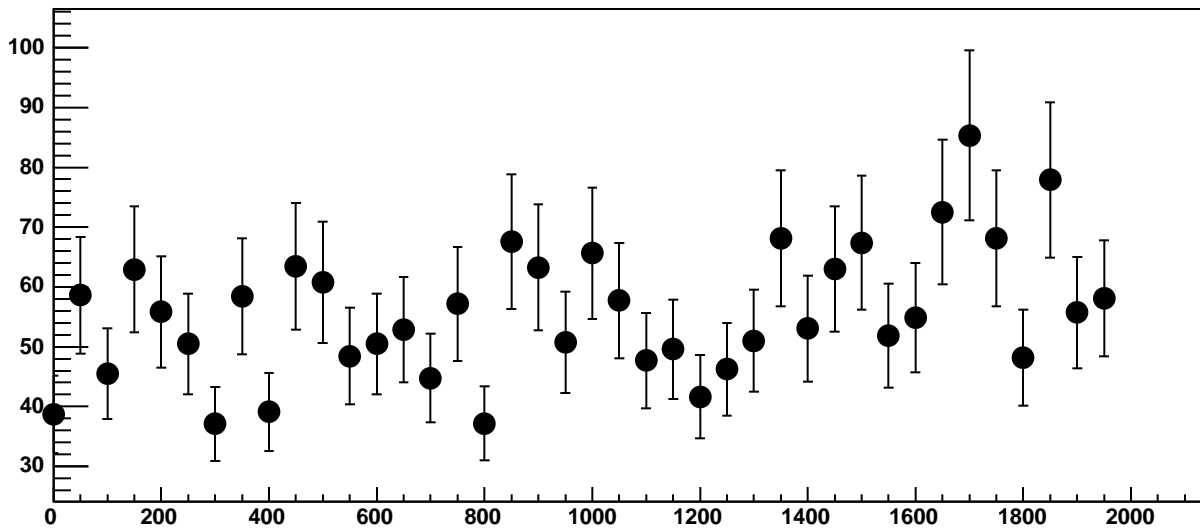
Chip 5, Channel 7, Enable 5, Hold=30, ADC Residuals vs DAC



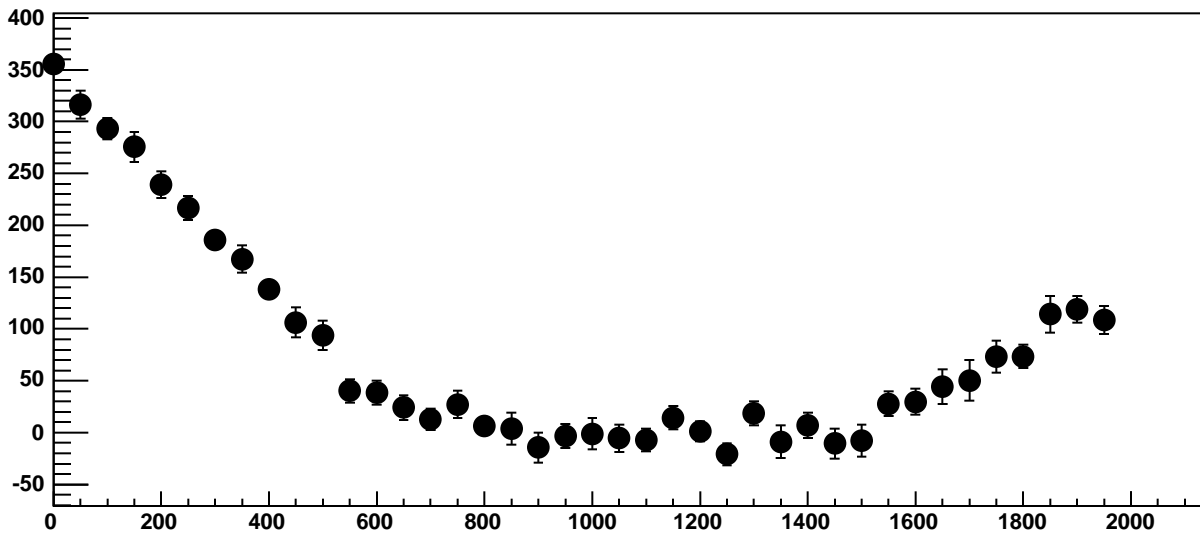
Chip 5, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC



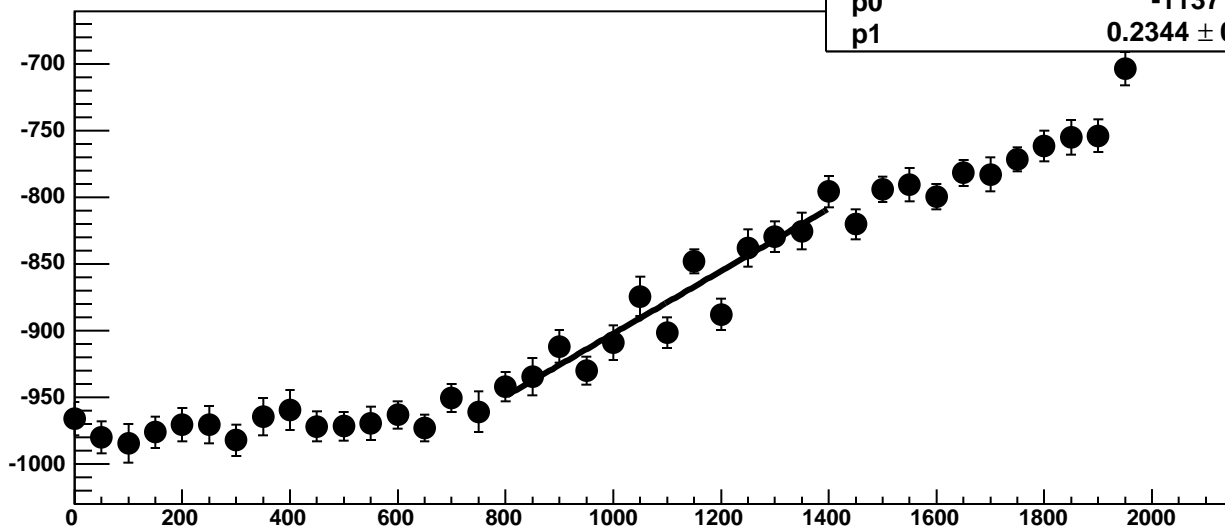
Chip 5, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC



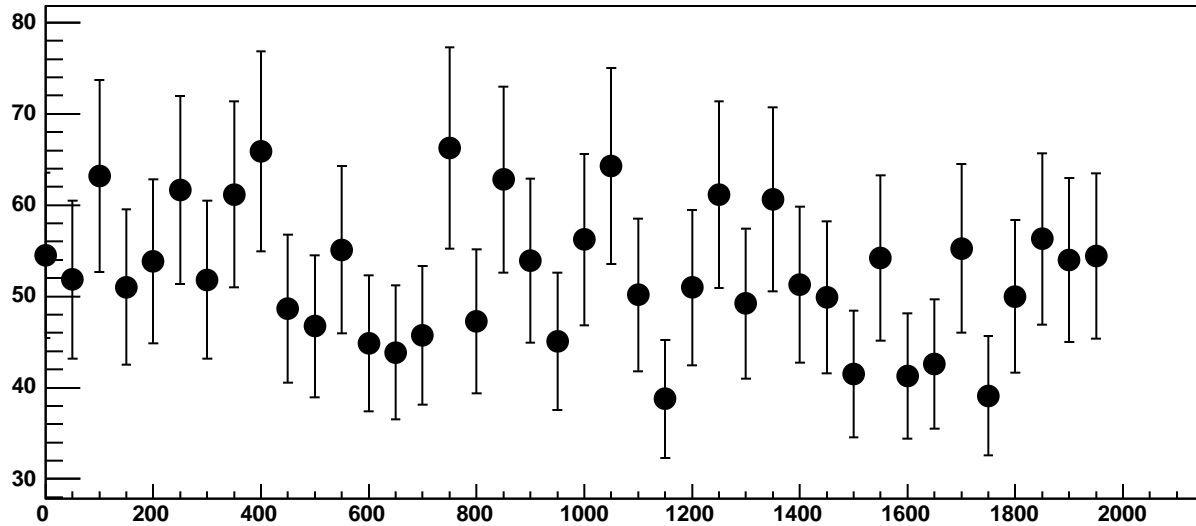
Chip 5, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC



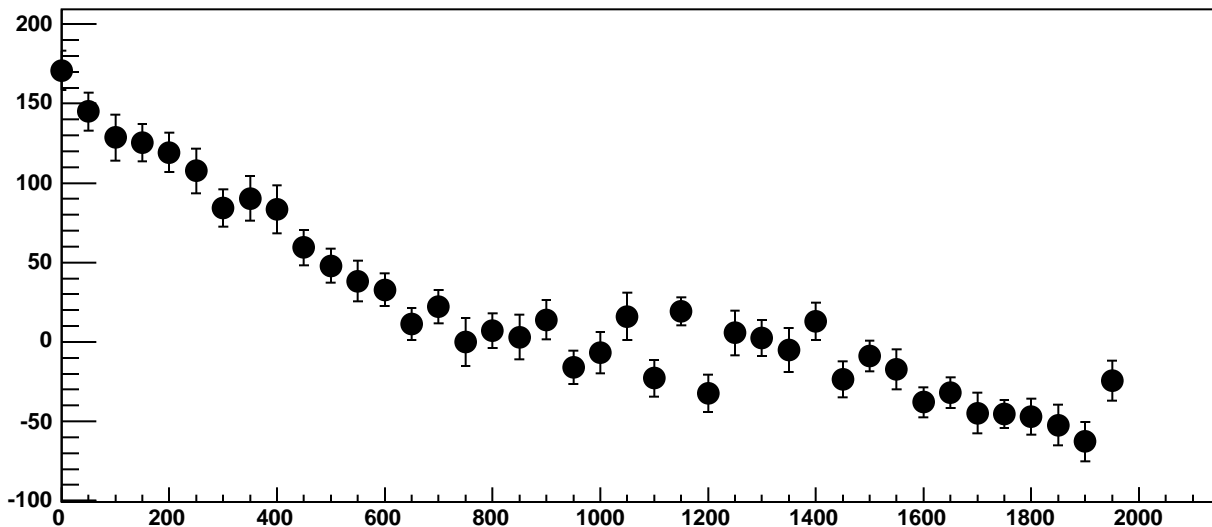
Chip 5, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC



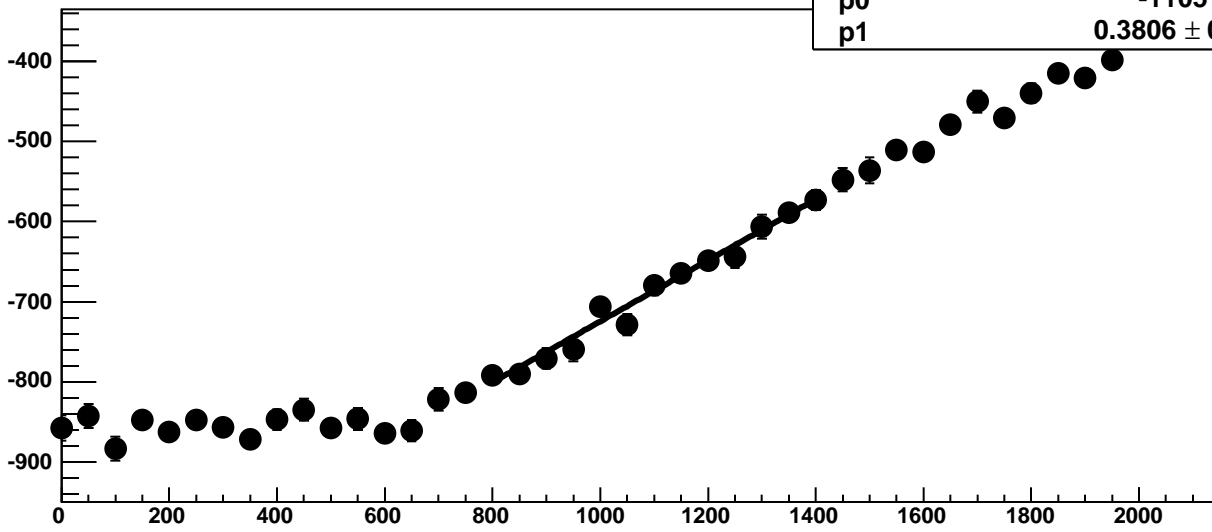
Chip 5, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC



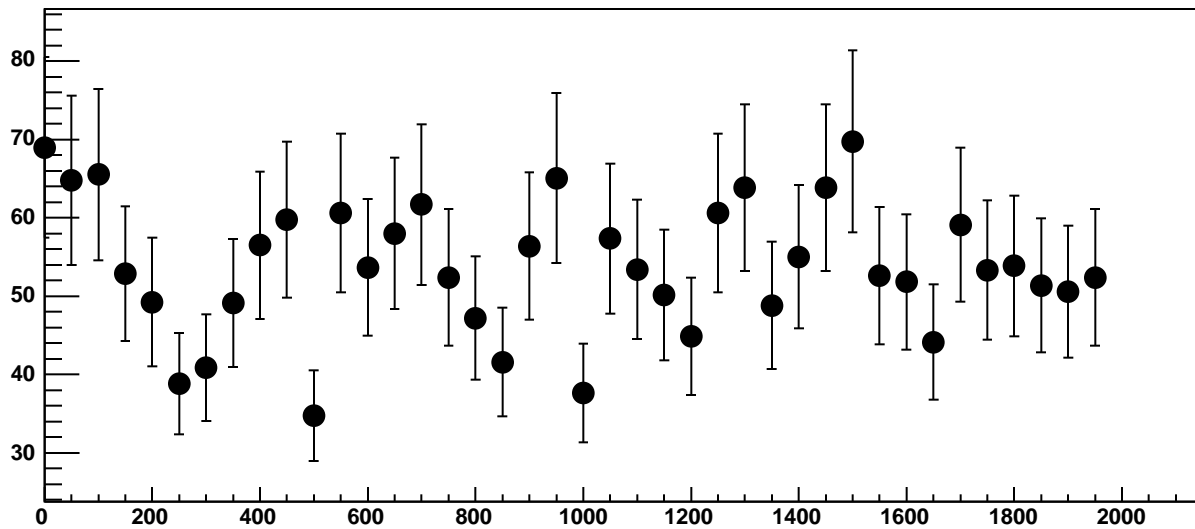
Chip 5, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC



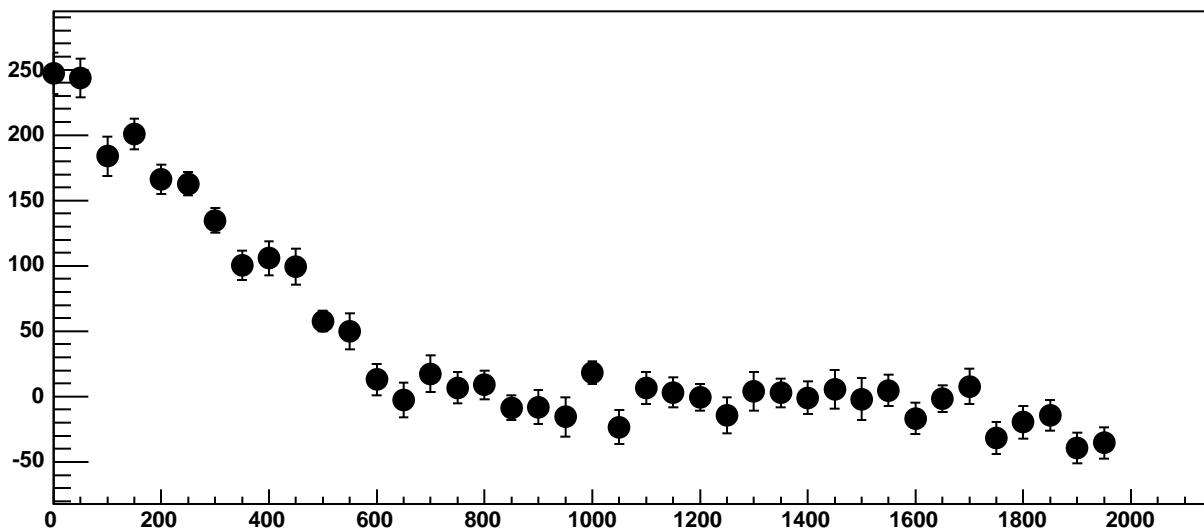
Chip 5, Channel 8, Enable 2, Hold=30, ADC Mean vs DAC



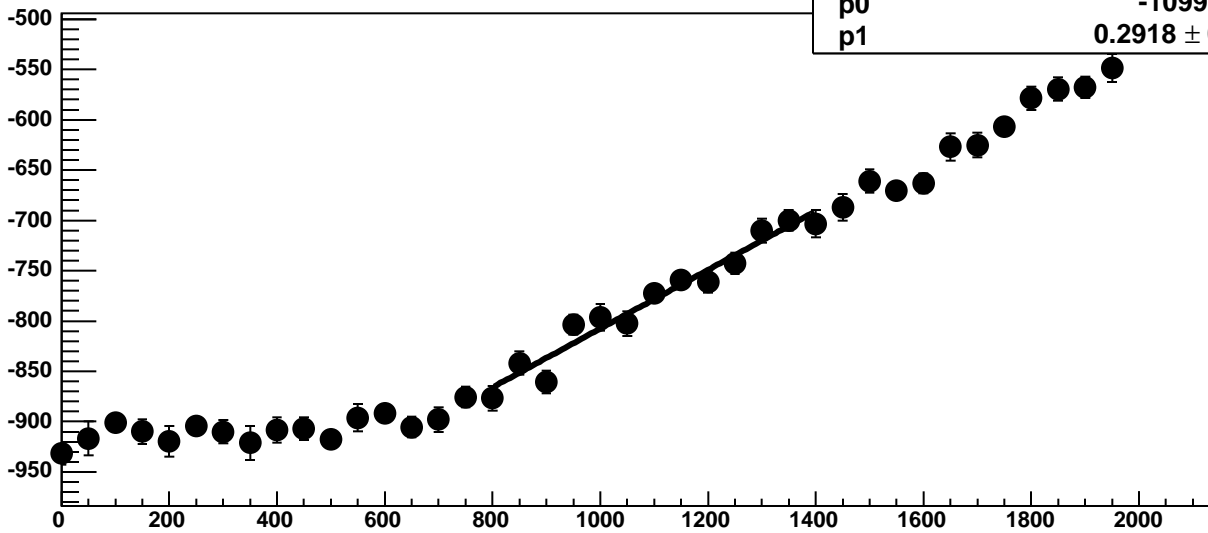
Chip 5, Channel 8, Enable 2, Hold=30, ADC Noise vs DAC



Chip 5, Channel 8, Enable 2, Hold=30, ADC Residuals vs DAC

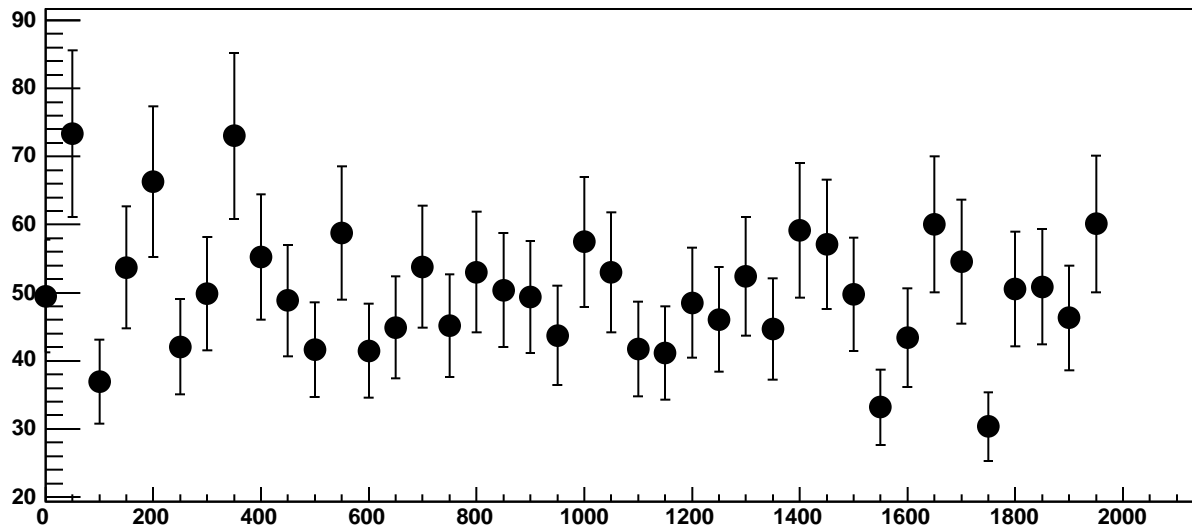


Chip 5, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC

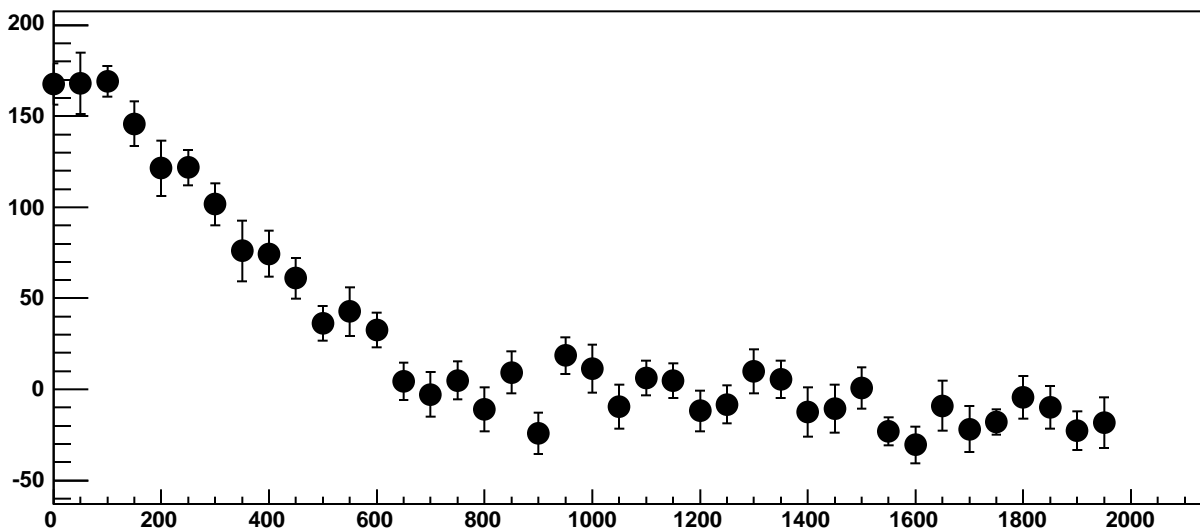


$\chi^2 / \text{ndf}$  14.91 / 11  
p0  $-1099 \pm 19.28$   
p1  $0.2918 \pm 0.01726$

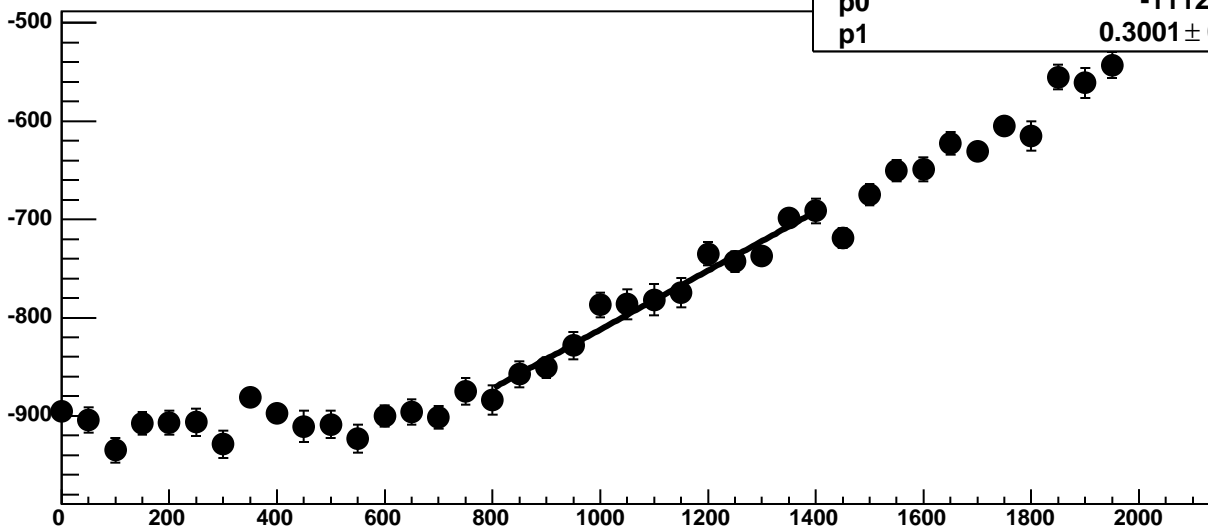
Chip 5, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC



Chip 5, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC

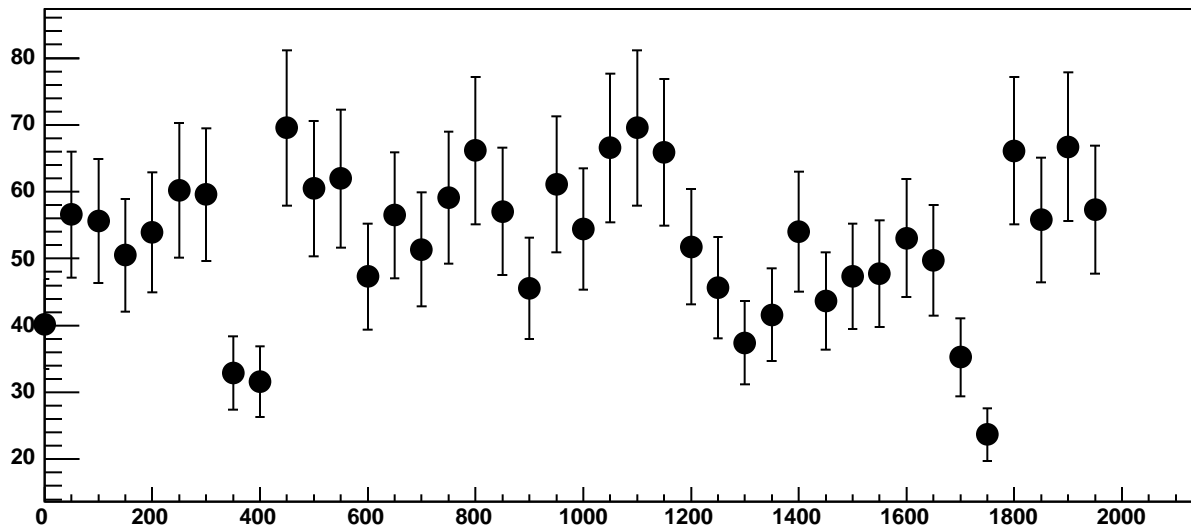


Chip 5, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC

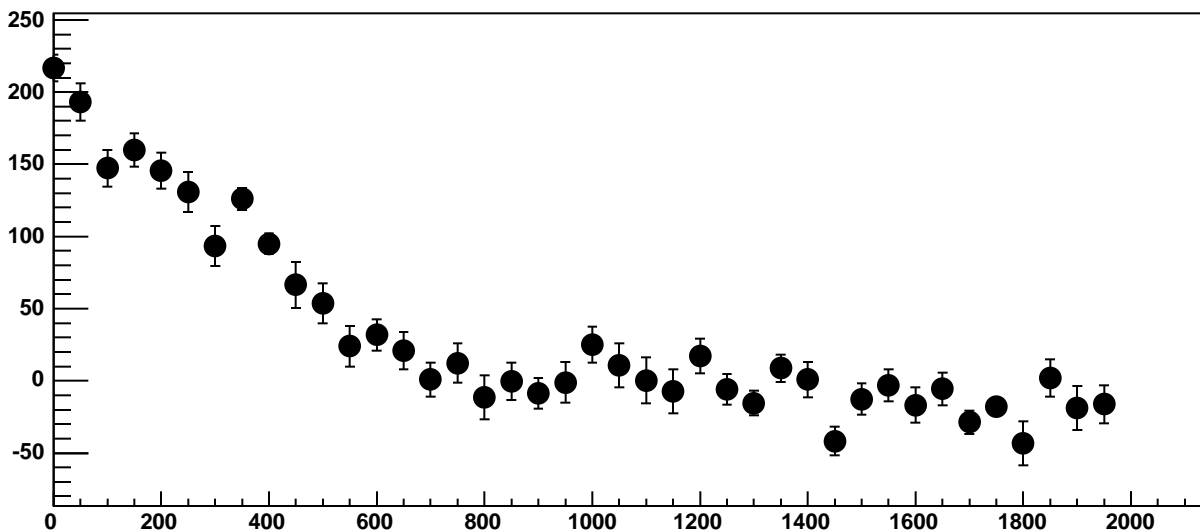


$\chi^2 / \text{ndf}$  12.56 / 11  
p0  $-1112 \pm 20.16$   
p1  $0.3001 \pm 0.01747$

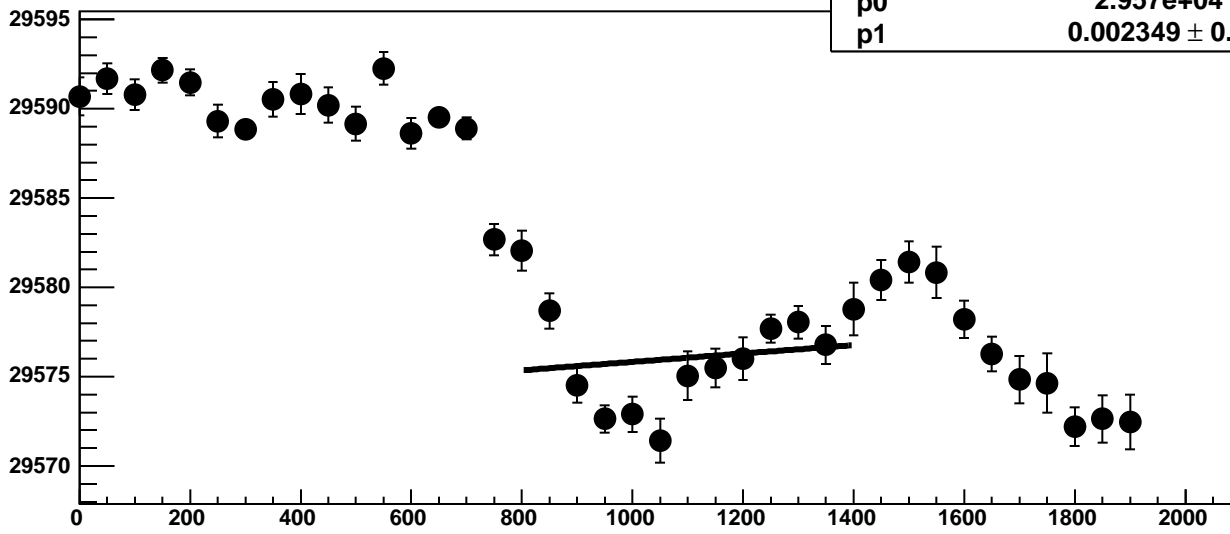
Chip 5, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC



Chip 5, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC



Chip 5, Channel 8, Enable 5!, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

92.65 / 11

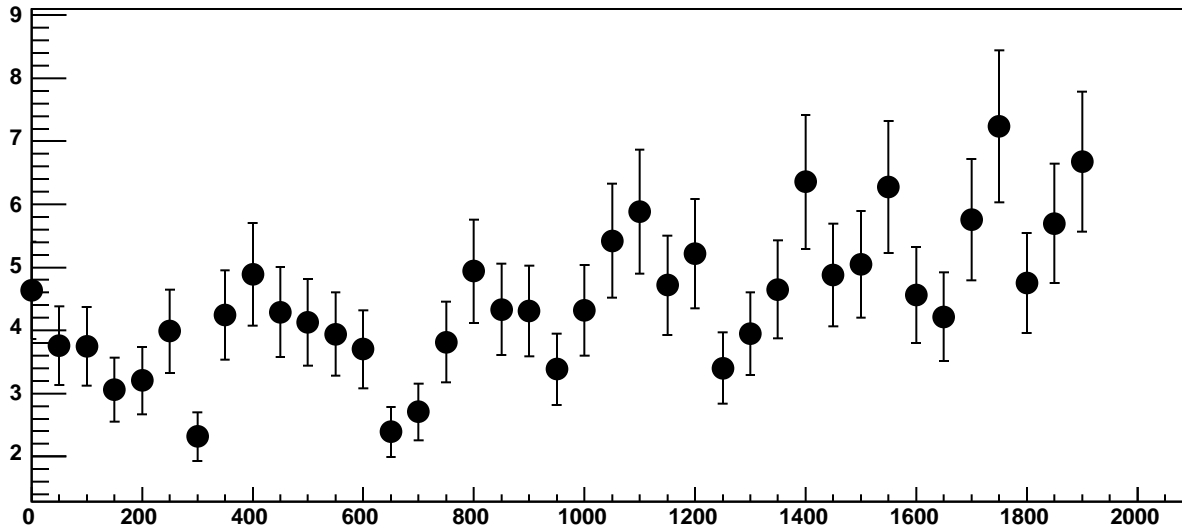
p0

$2.957\text{e}+04 \pm 1.719$

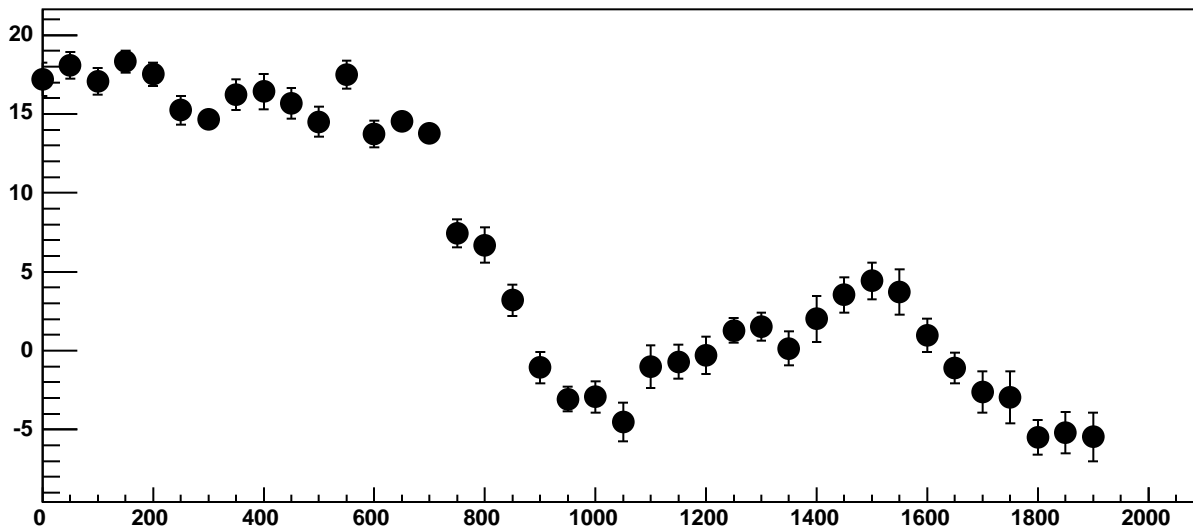
p1

$0.002349 \pm 0.001554$

Chip 5, Channel 8, Enable 5!, Hold=30, ADC Noise vs DAC

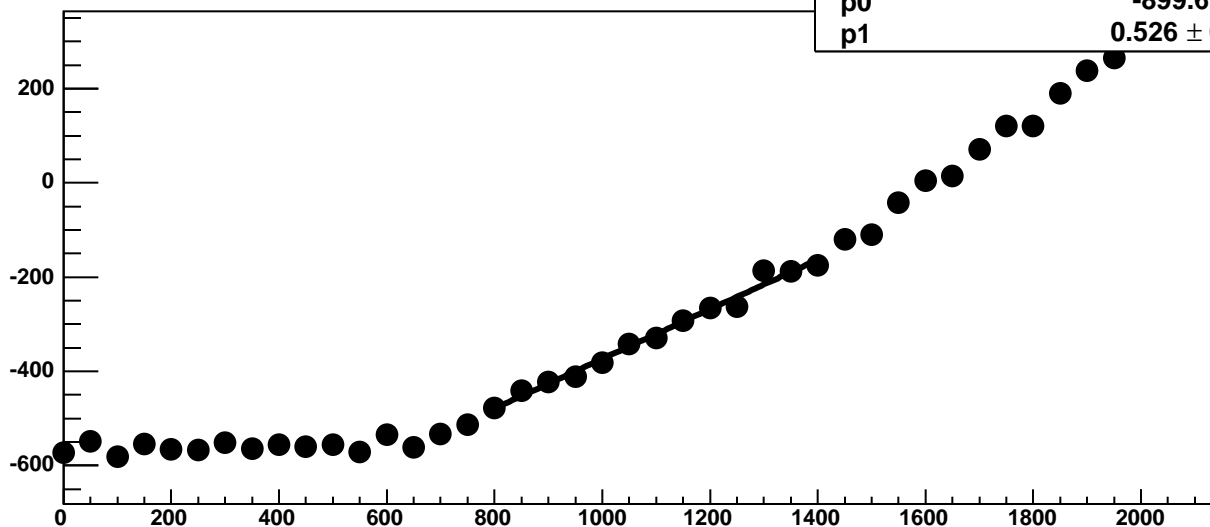


Chip 5, Channel 8, Enable 5!, Hold=30, ADC Residuals vs DAC



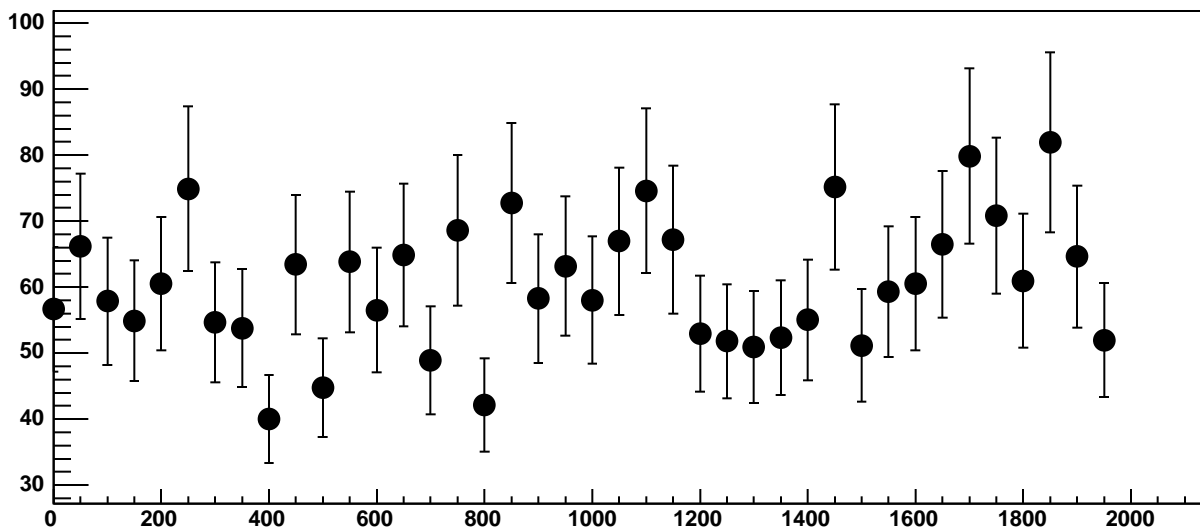


Chip 5, Channel 9, Enable 0, Hold=30, ADC Mean vs DAC

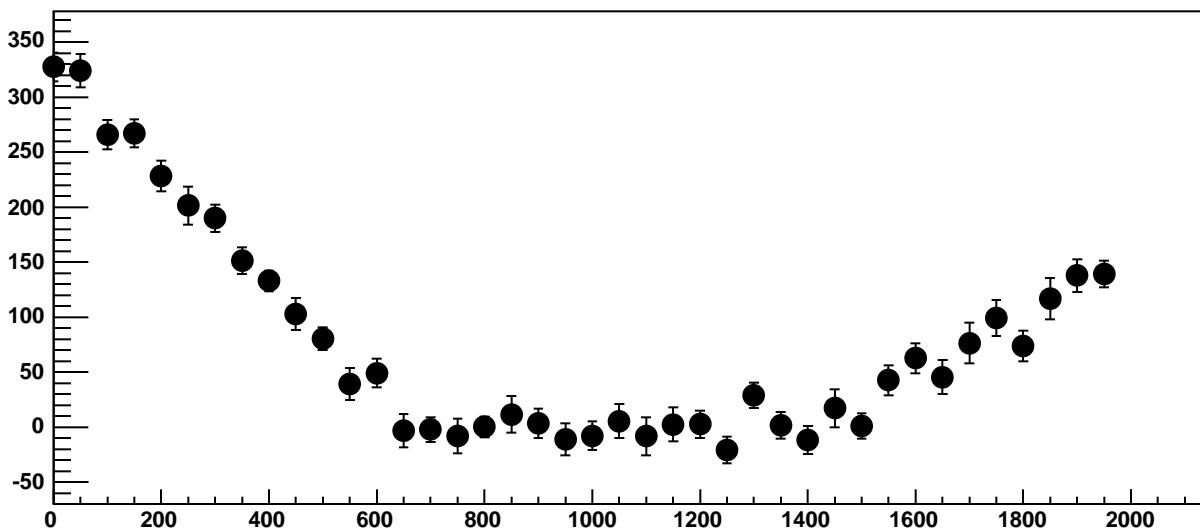


$\chi^2 / \text{ndf}$  11.93 / 11  
p0  $-899.6 \pm 20.12$   
p1  $0.526 \pm 0.01792$

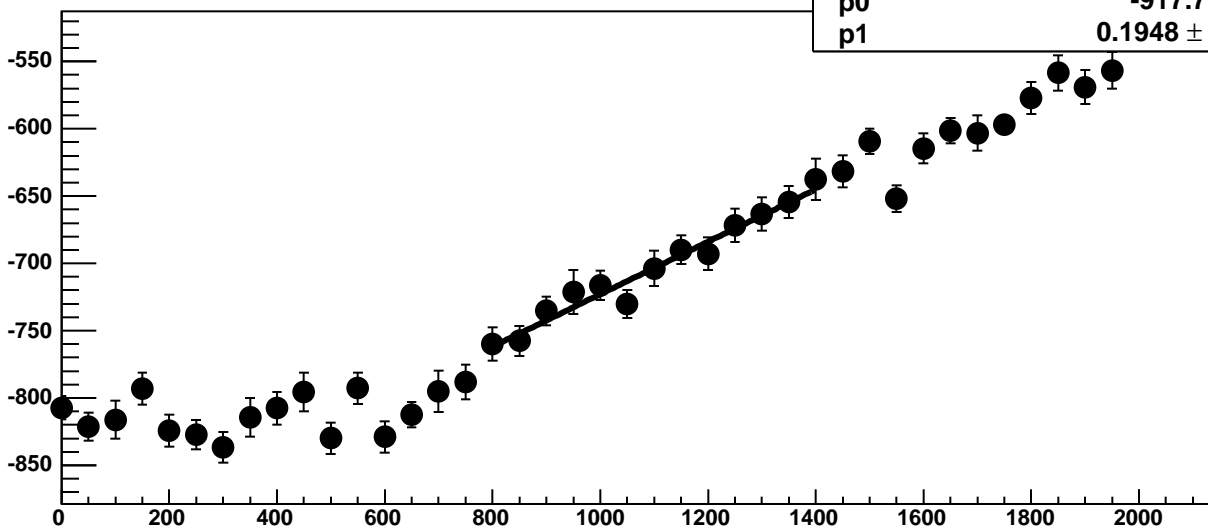
Chip 5, Channel 9, Enable 0, Hold=30, ADC Noise vs DAC



Chip 5, Channel 9, Enable 0, Hold=30, ADC Residuals vs DAC

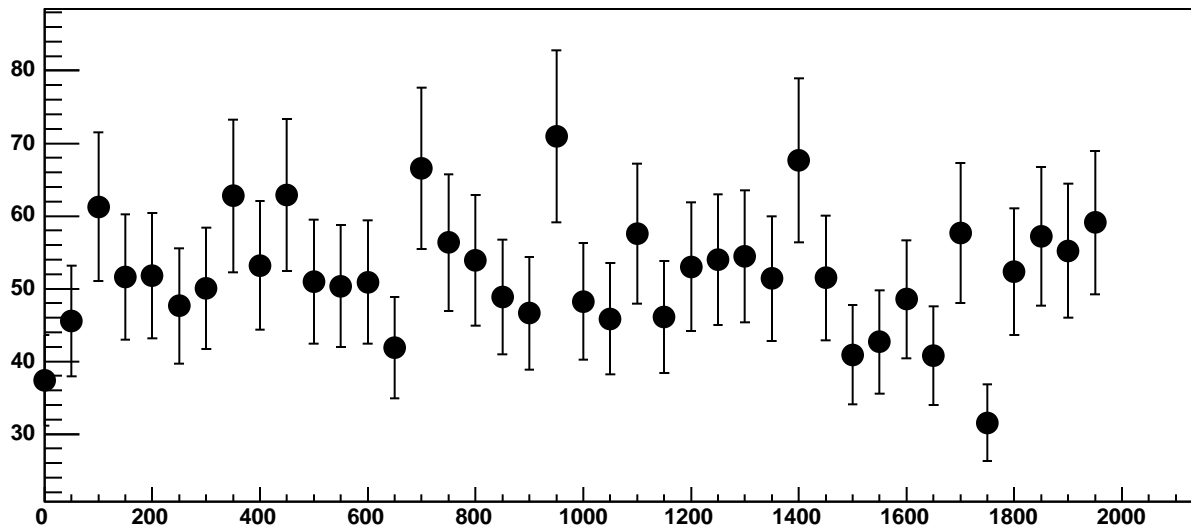


Chip 5, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC

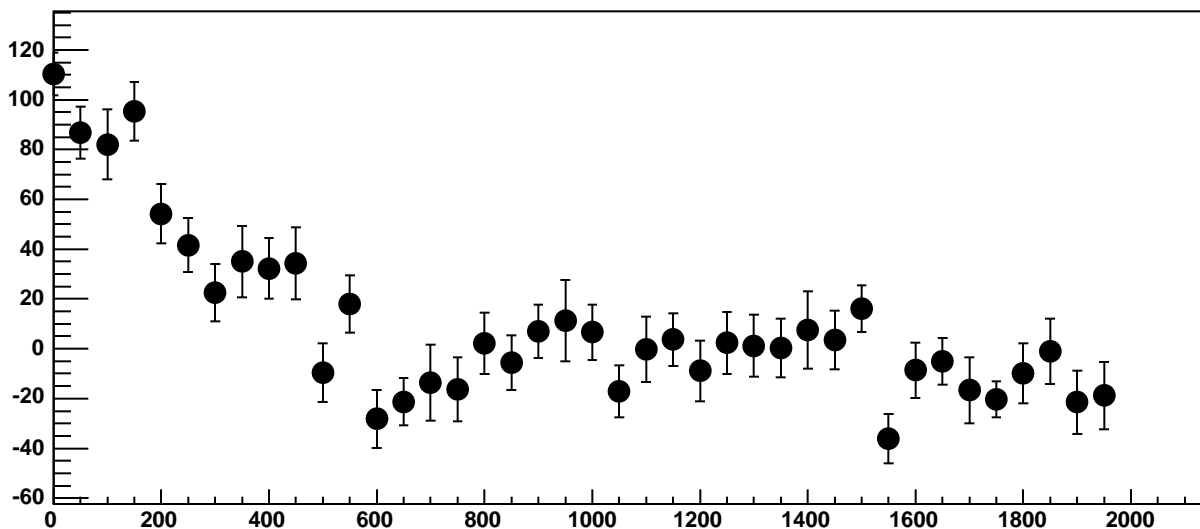


$\chi^2 / \text{ndf}$  5.141 / 11  
p0 -917.7 ± 20.28  
p1 0.1948 ± 0.01841

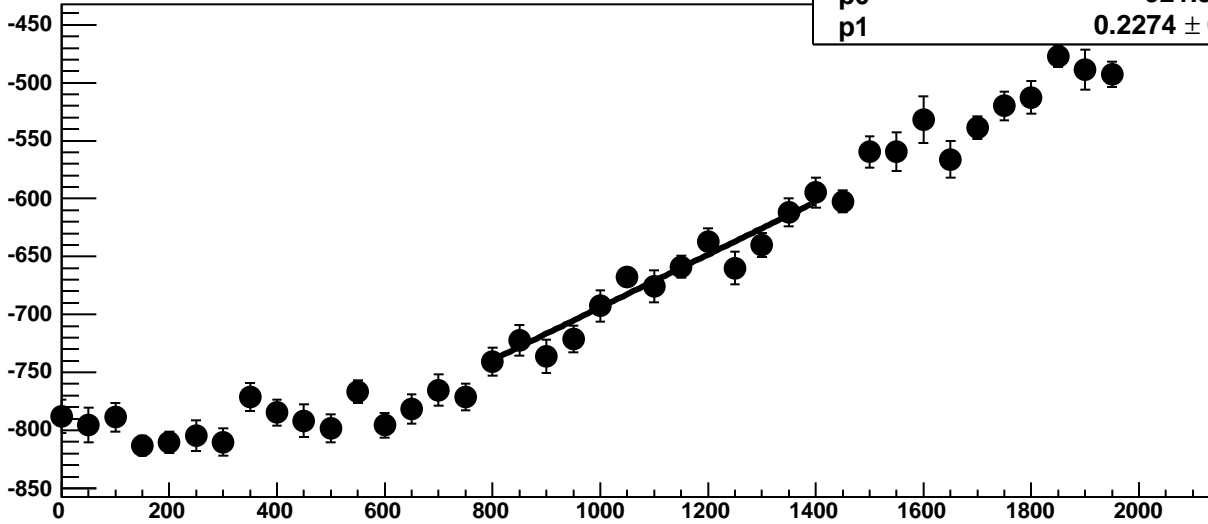
Chip 5, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC



Chip 5, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC

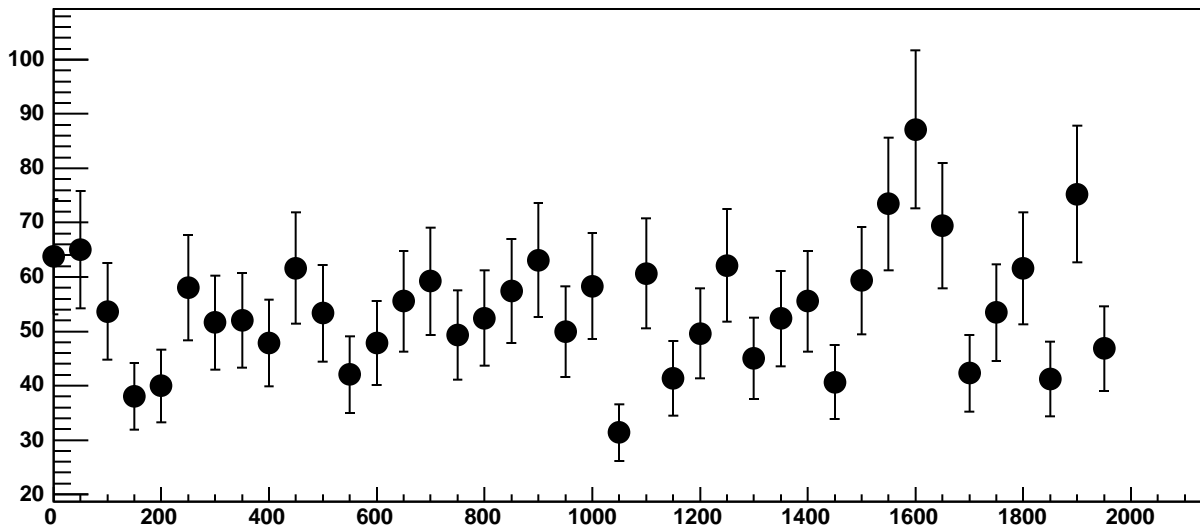


Chip 5, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC

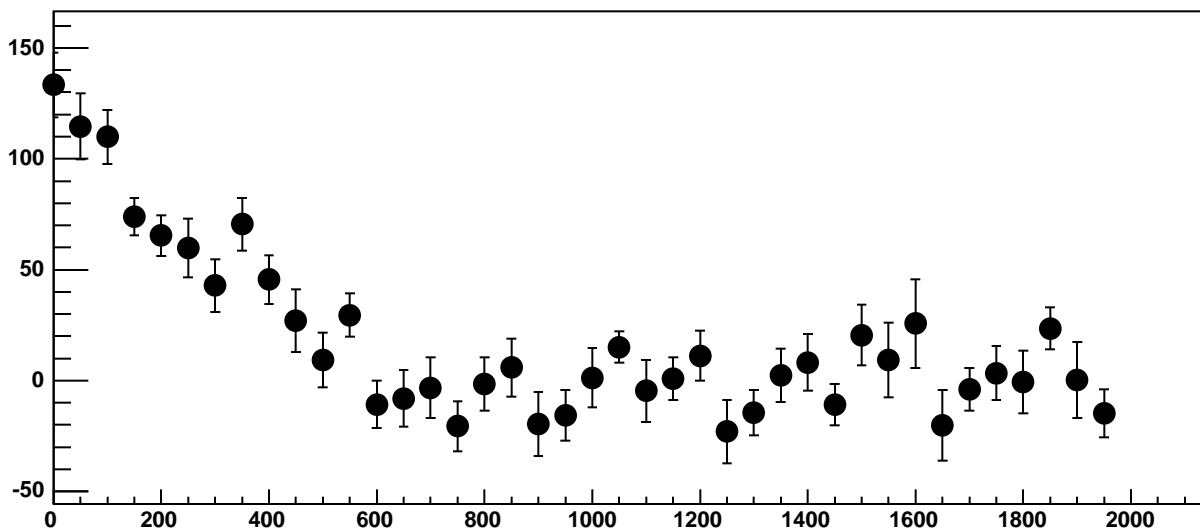


$\chi^2 / \text{ndf}$  14.44 / 11  
p0  $-921.3 \pm 20.31$   
p1  $0.2274 \pm 0.01818$

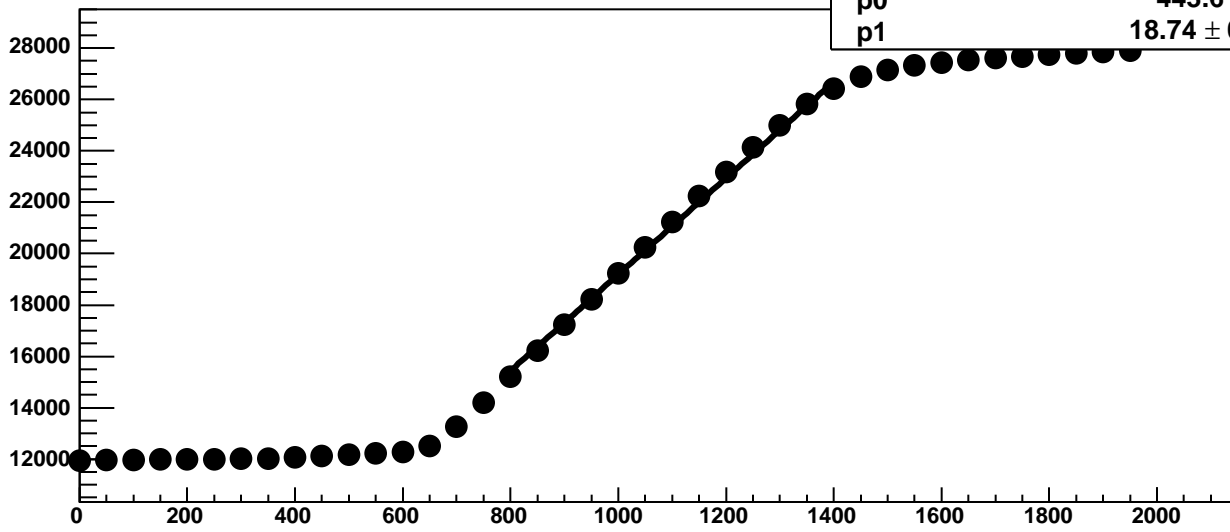
Chip 5, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC



Chip 5, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC

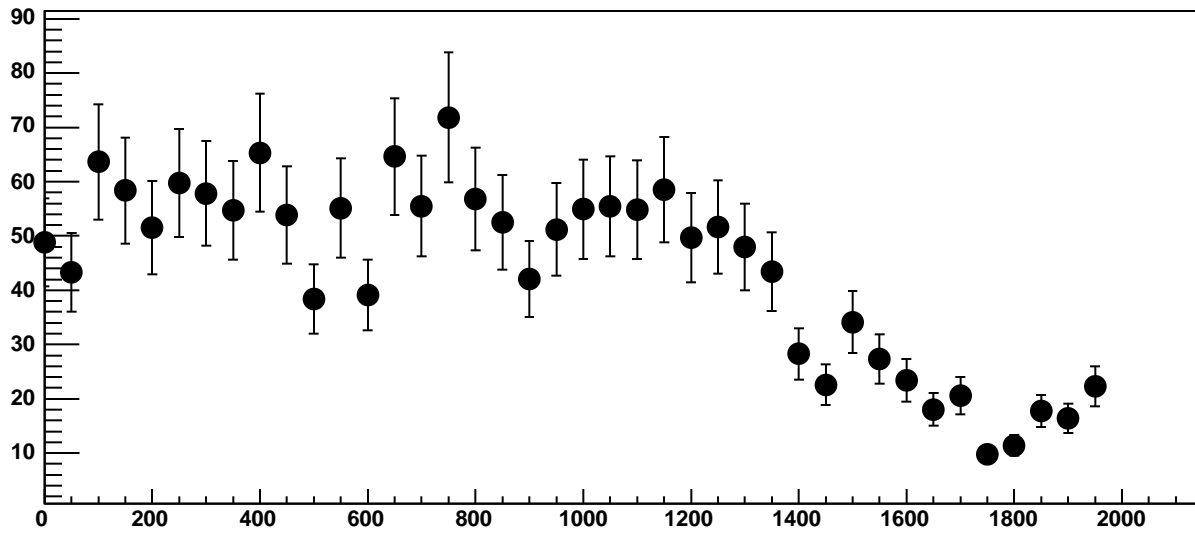


Chip 5, Channel 9, Enable 3!, Hold=30, ADC Mean vs DAC

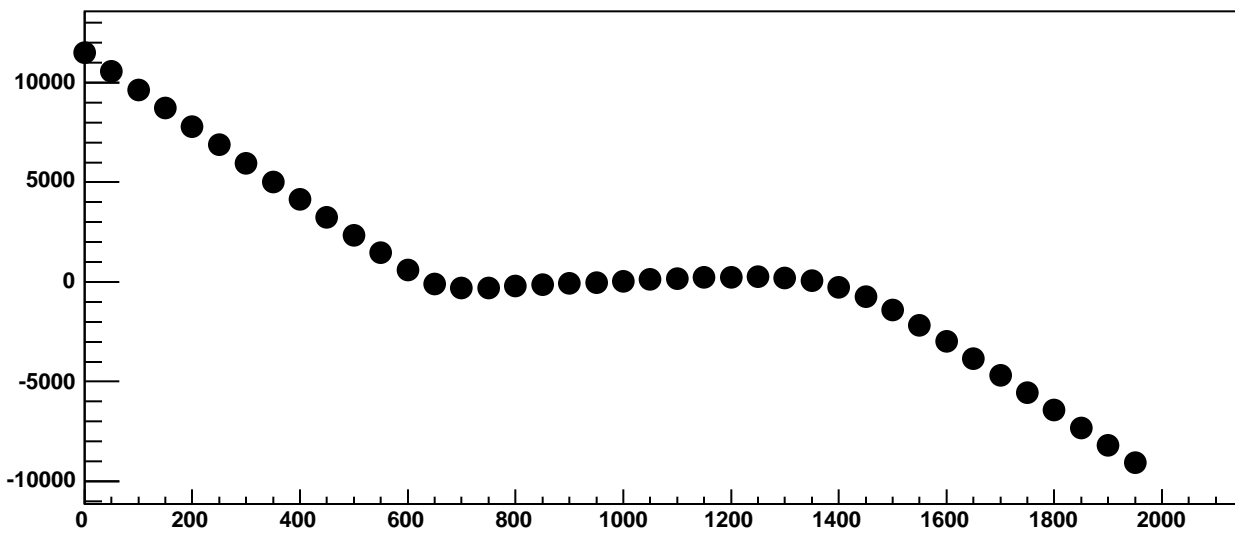


$\chi^2 / \text{ndf}$  4040 / 11  
p0  $443.6 \pm 17.08$   
p1  $18.74 \pm 0.01461$

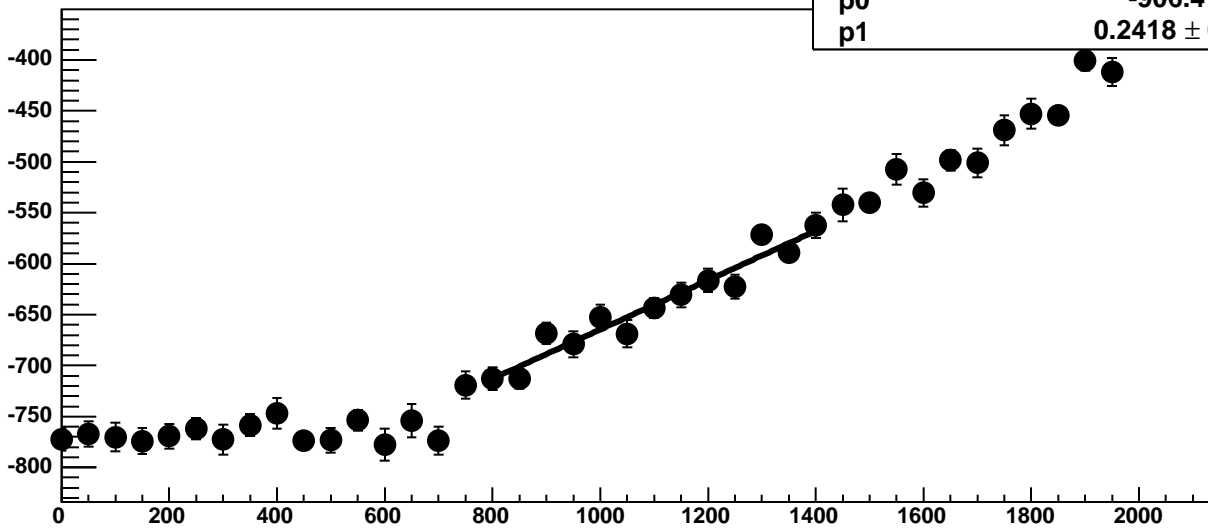
Chip 5, Channel 9, Enable 3!, Hold=30, ADC Noise vs DAC



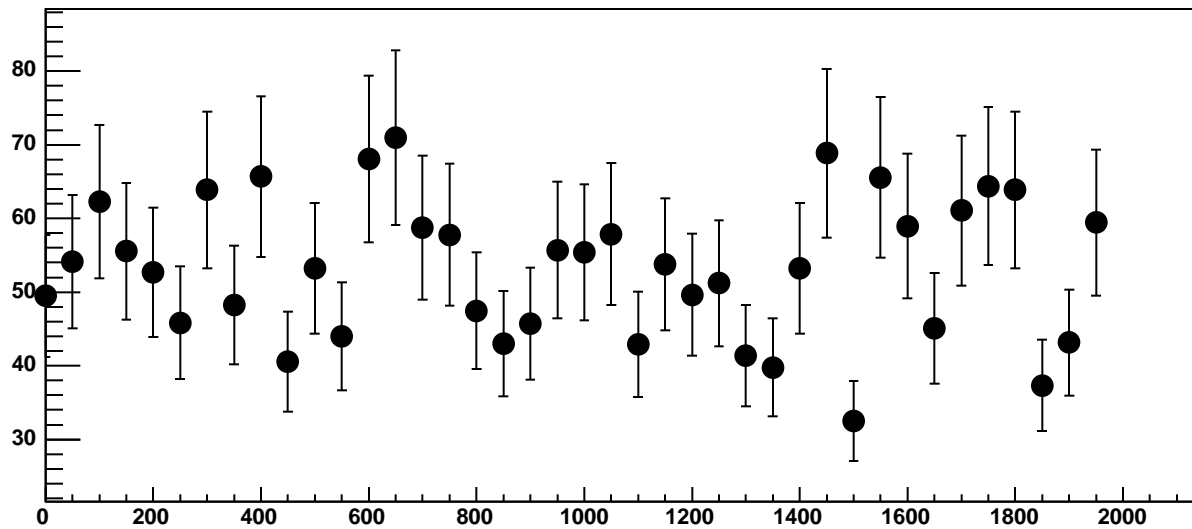
Chip 5, Channel 9, Enable 3!, Hold=30, ADC Residuals vs DAC



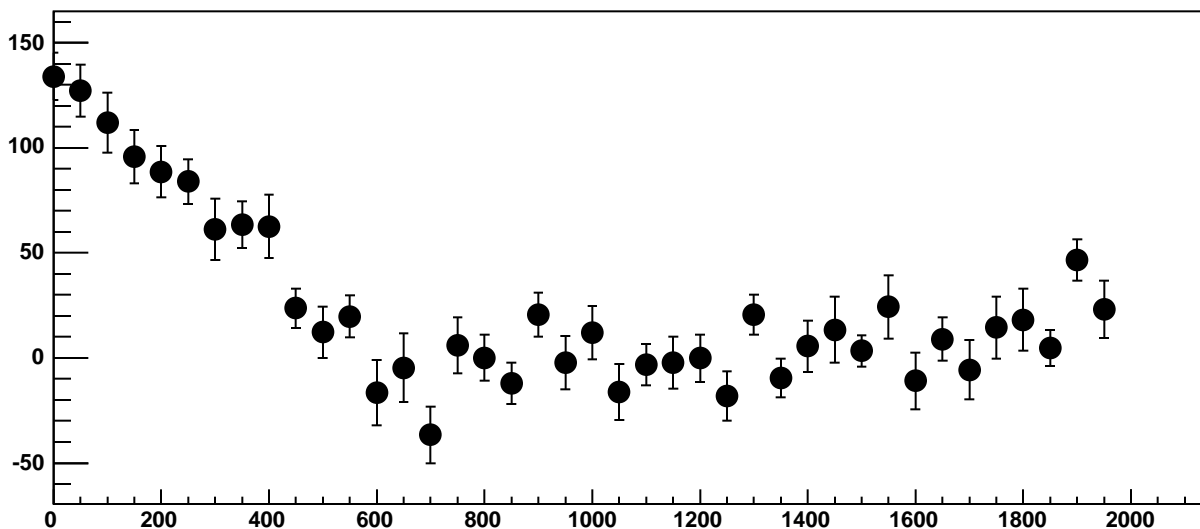
Chip 5, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC



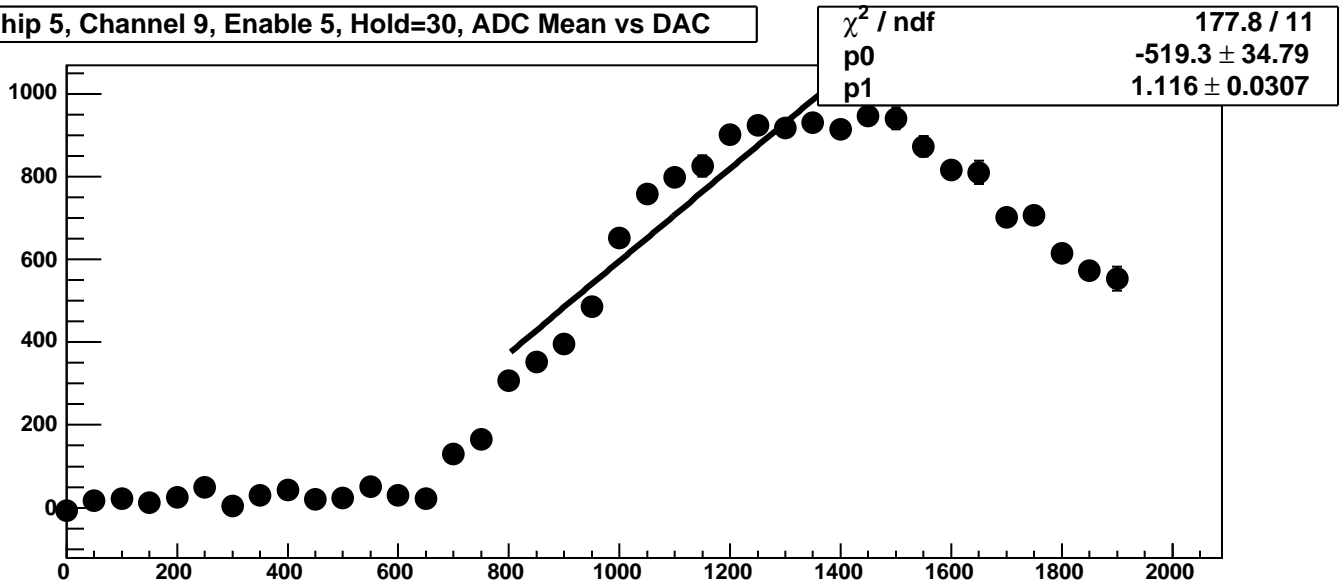
Chip 5, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC



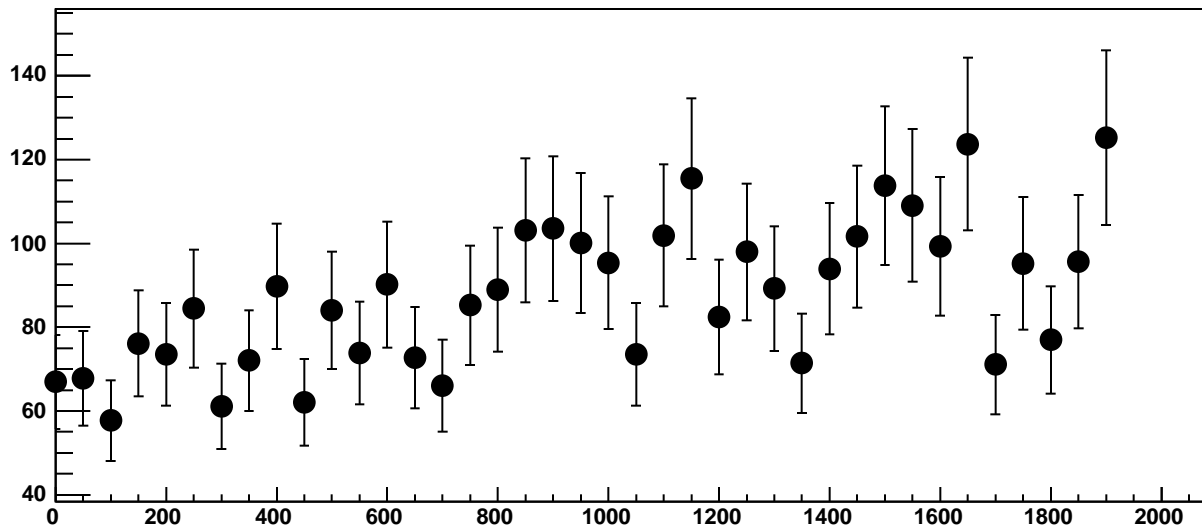
Chip 5, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC



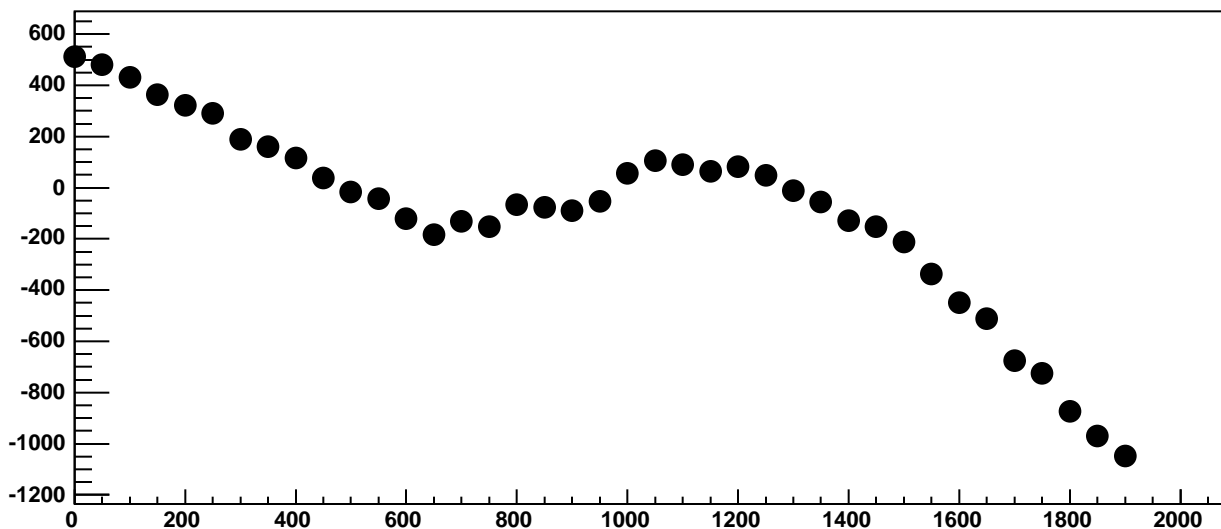
Chip 5, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC



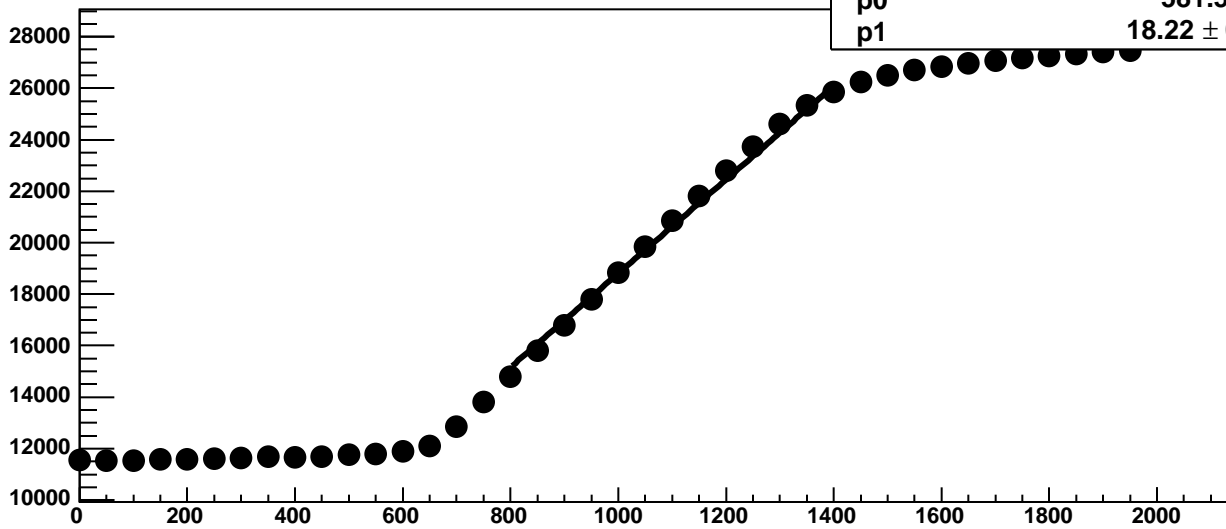
Chip 5, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC



Chip 5, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 5, Channel 10, Enable 0!, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

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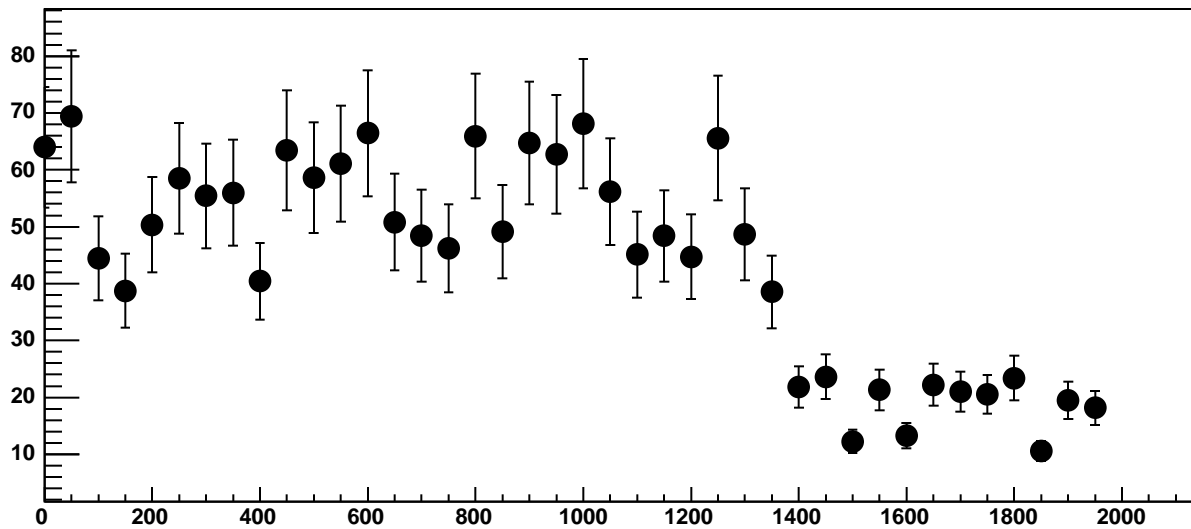
p0

$581.5 \pm 17.71$

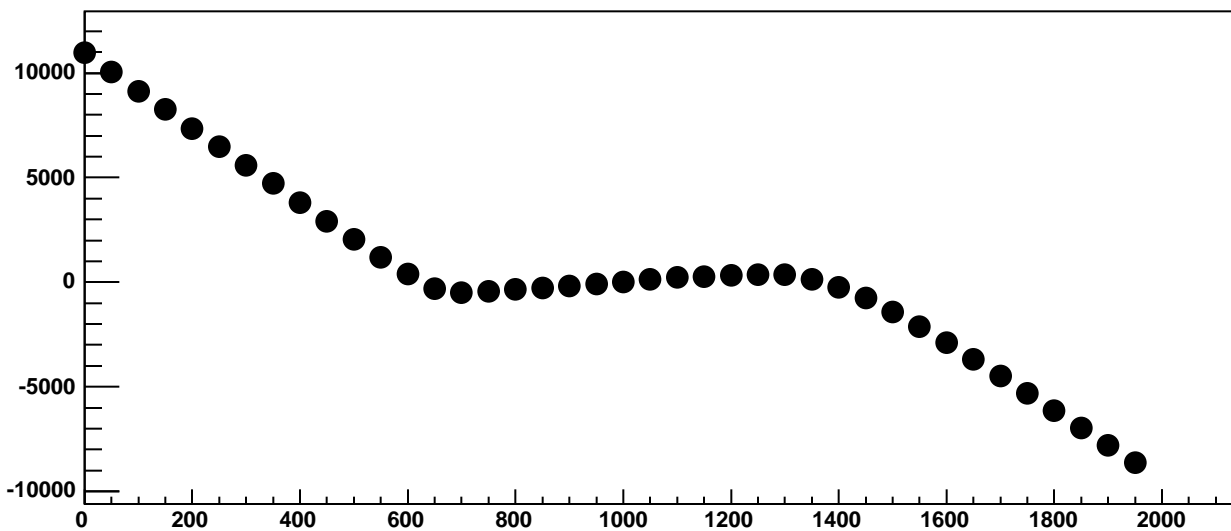
p1

$18.22 \pm 0.01449$

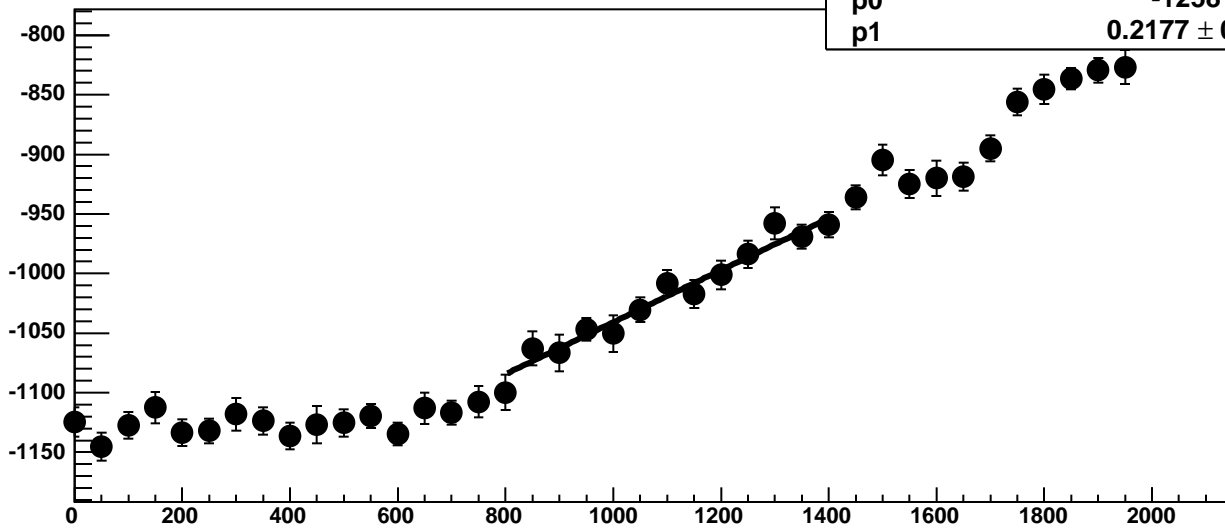
Chip 5, Channel 10, Enable 0!, Hold=30, ADC Noise vs DAC



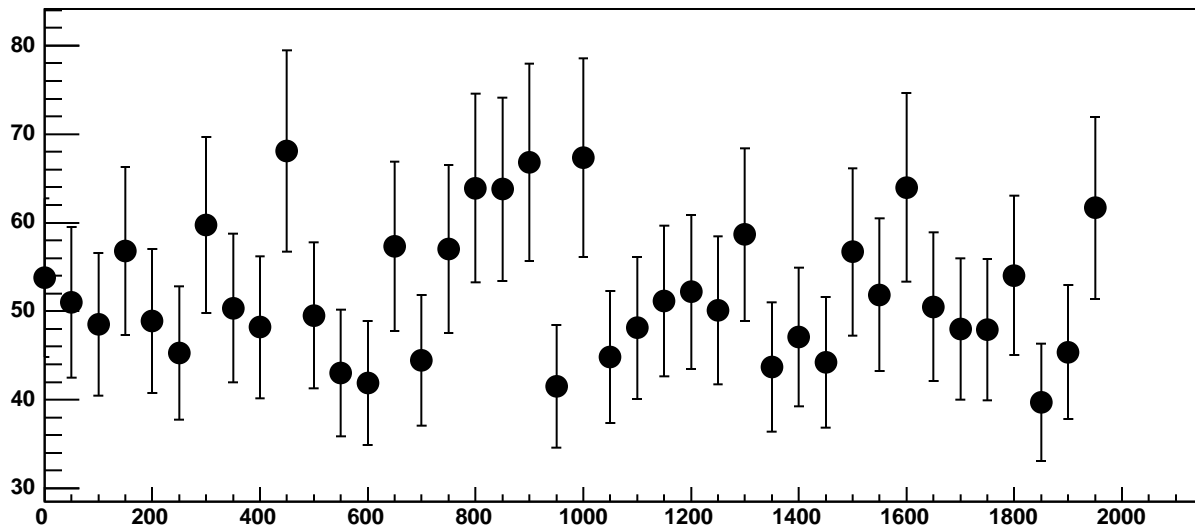
Chip 5, Channel 10, Enable 0!, Hold=30, ADC Residuals vs DAC



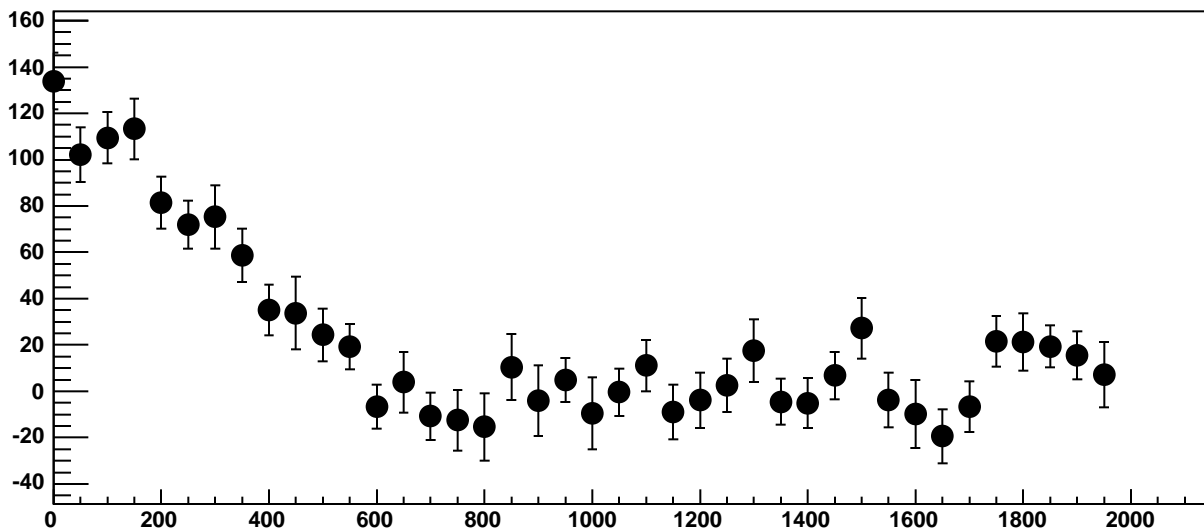
Chip 5, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC



Chip 5, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC

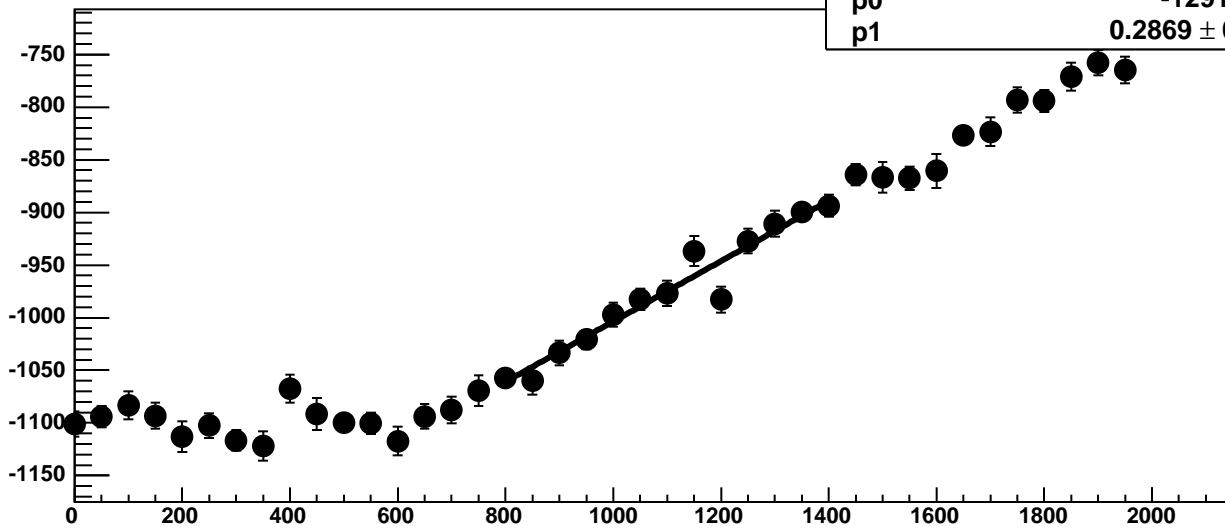


Chip 5, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC

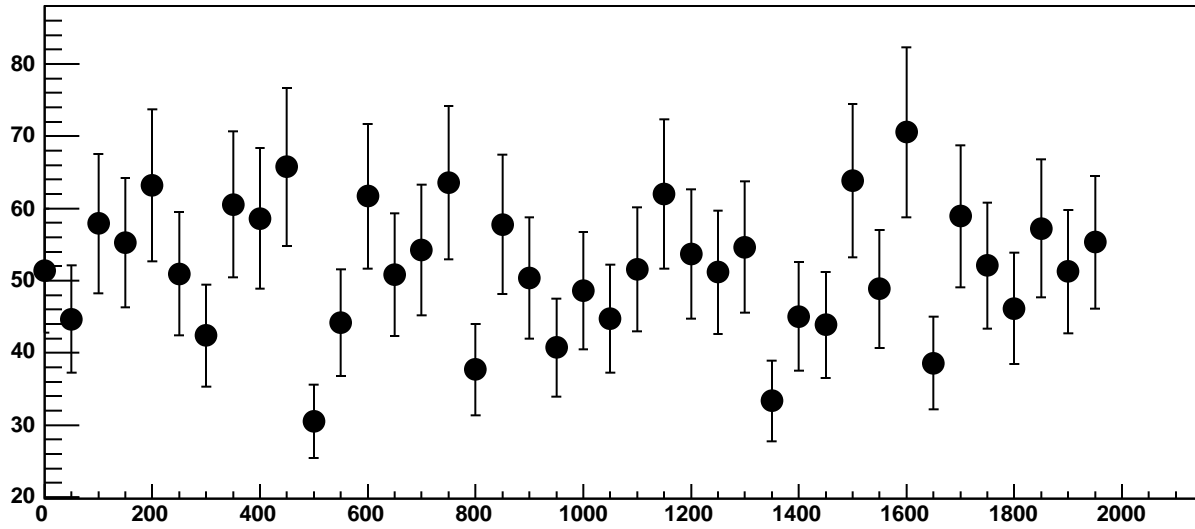




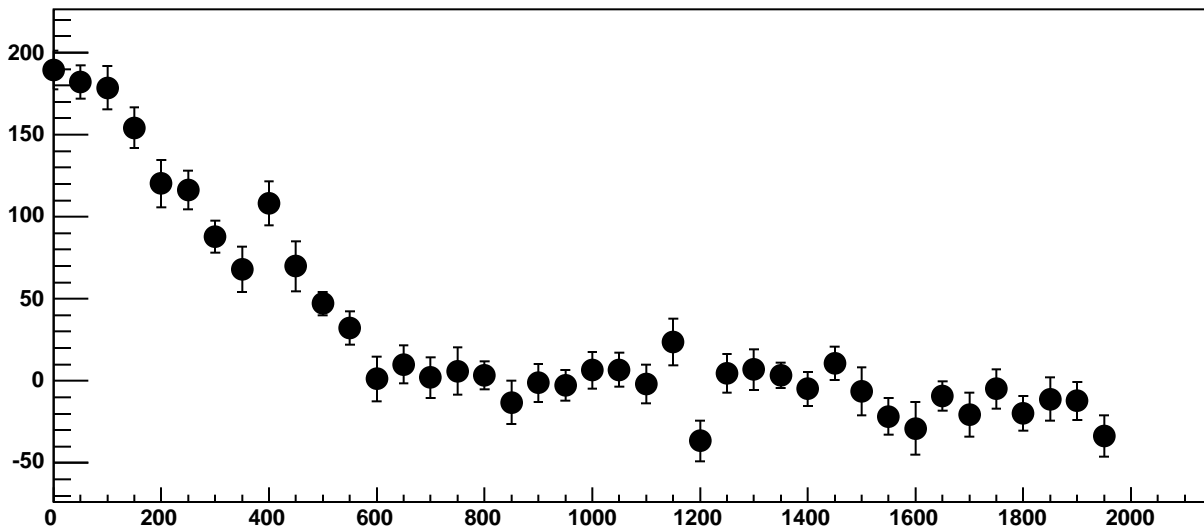
Chip 5, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC



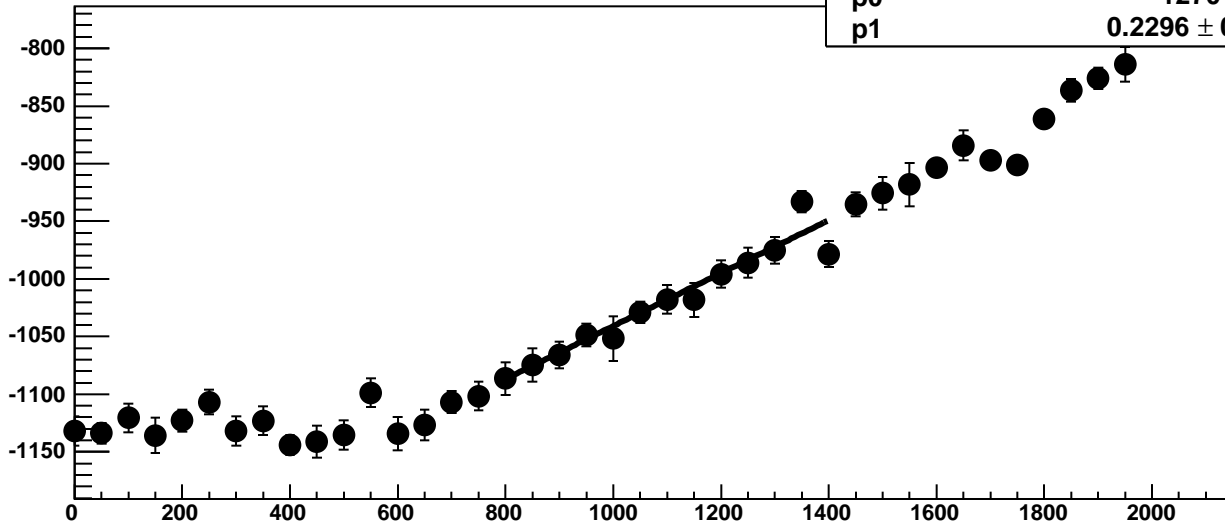
Chip 5, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC



Chip 5, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC

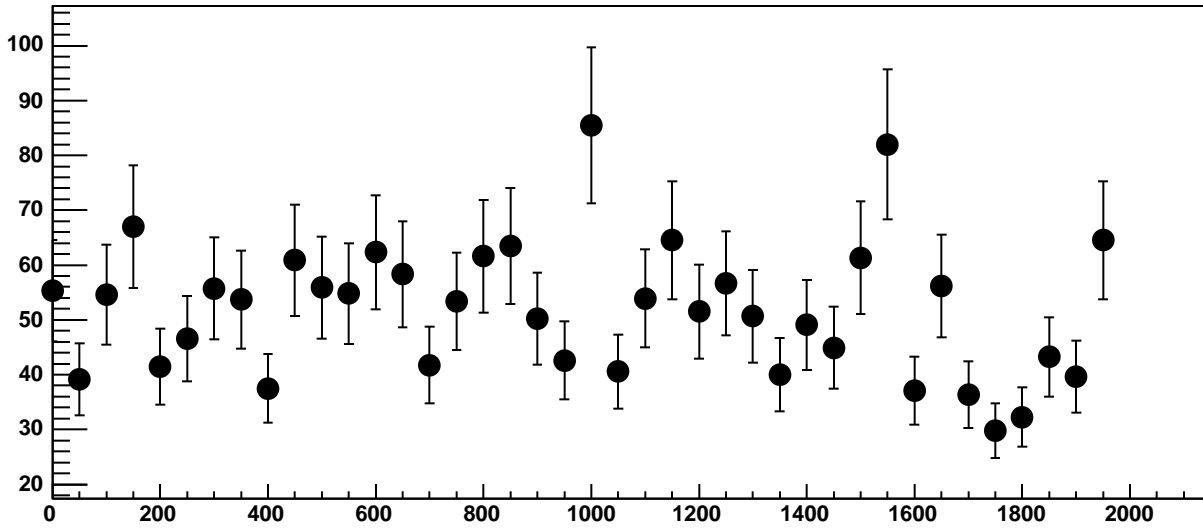


Chip 5, Channel 10, Enable 3, Hold=30, ADC Mean vs DAC

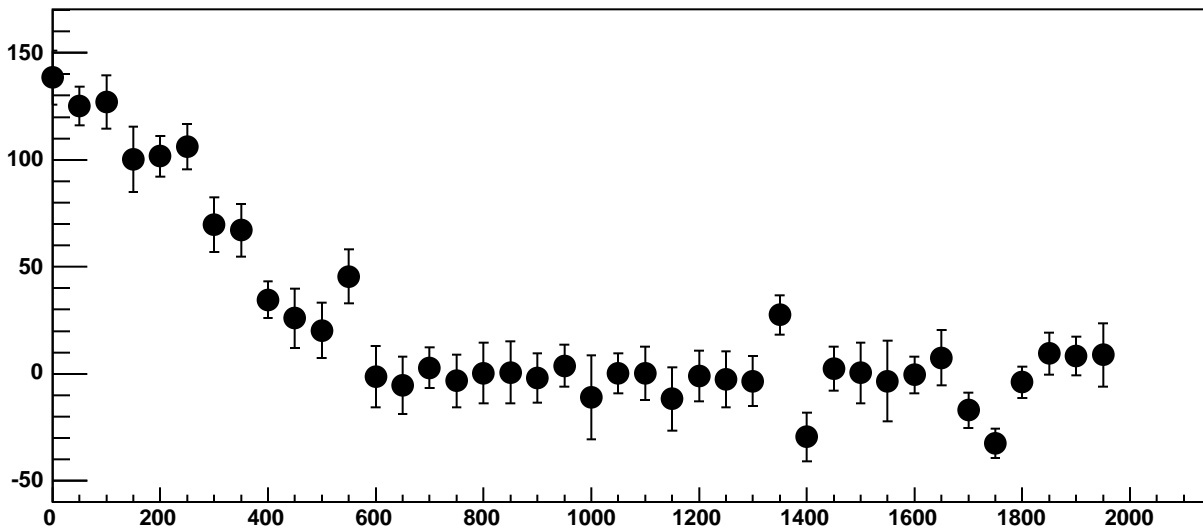


$\chi^2 / \text{ndf}$  17.11 / 11  
p0  $-1270 \pm 19.94$   
p1  $0.2296 \pm 0.01753$

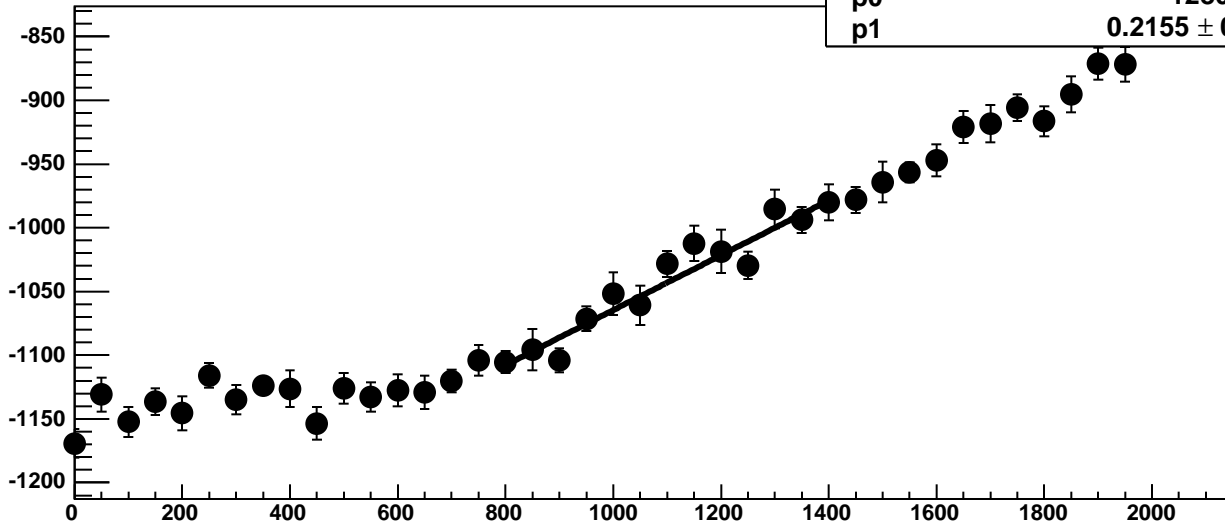
Chip 5, Channel 10, Enable 3, Hold=30, ADC Noise vs DAC



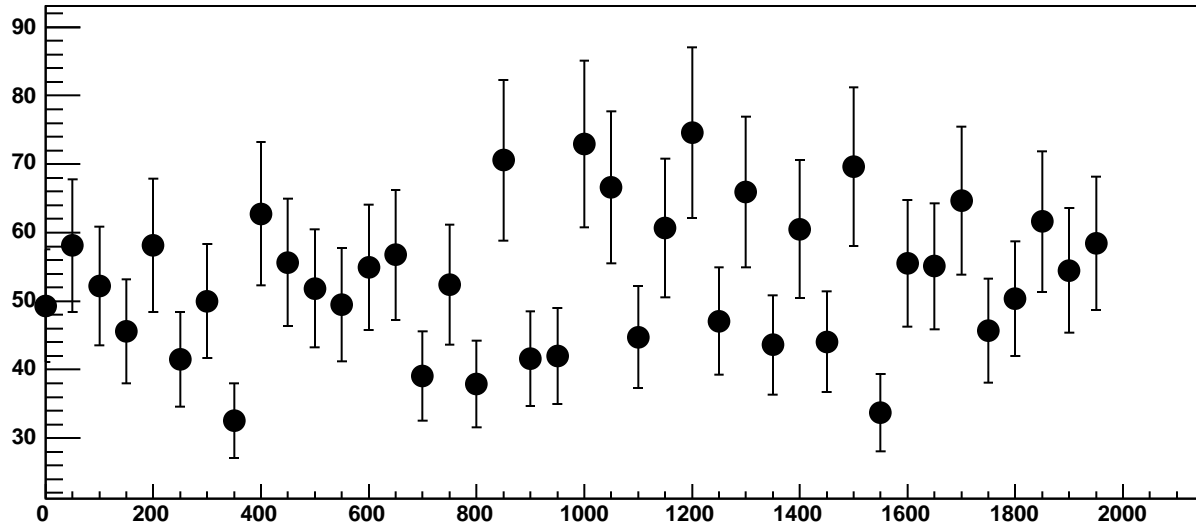
Chip 5, Channel 10, Enable 3, Hold=30, ADC Residuals vs DAC



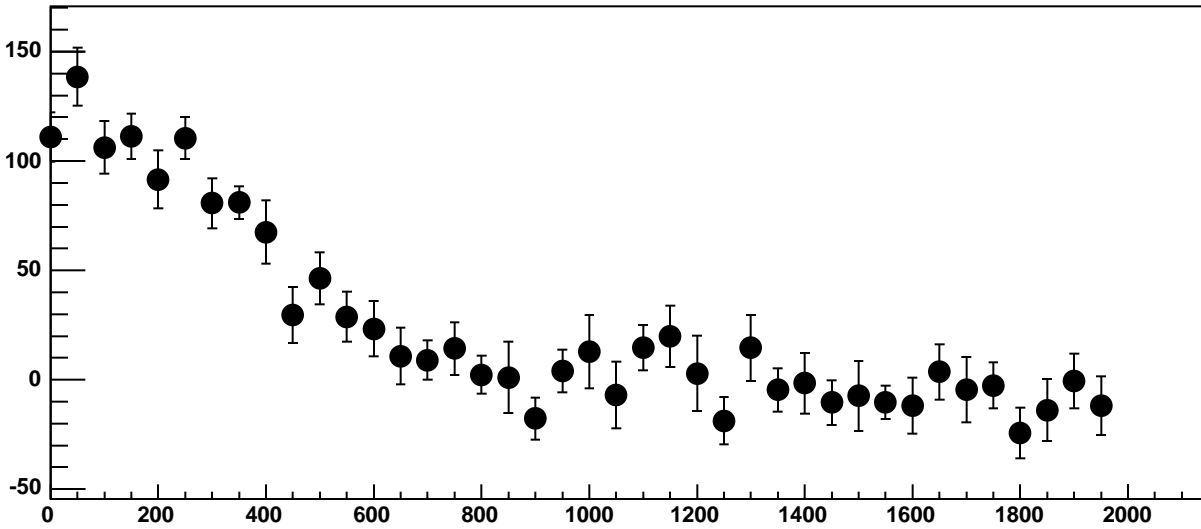
Chip 5, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC



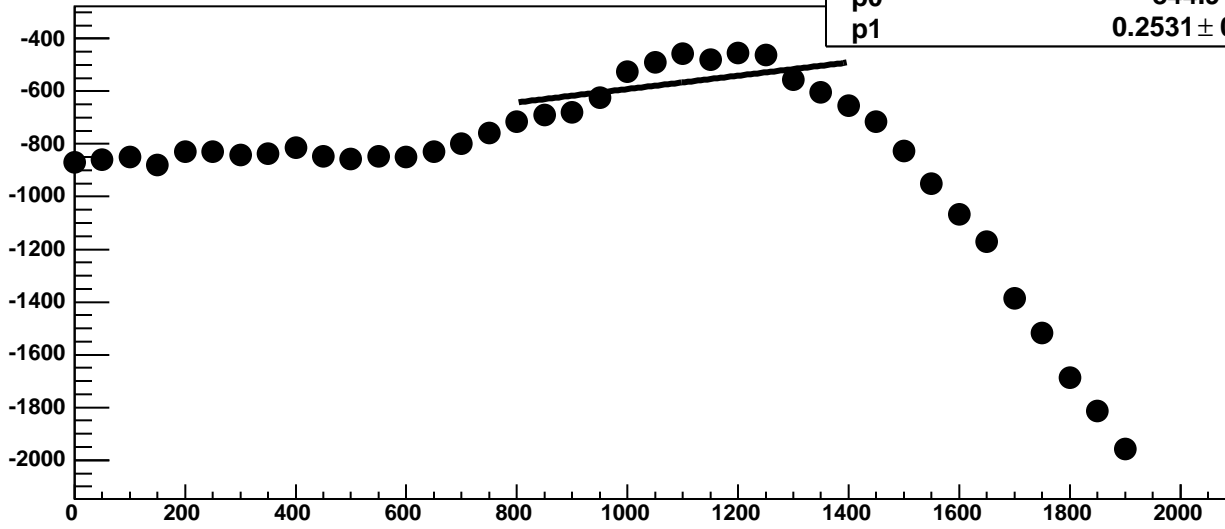
Chip 5, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC



Chip 5, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC



Chip 5, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

199.9 / 11

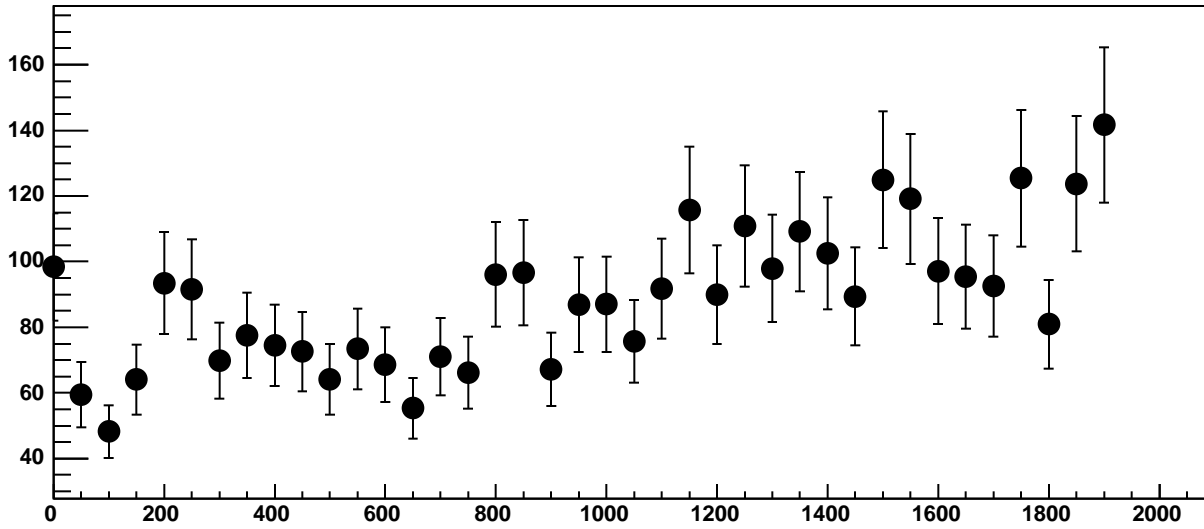
p0

$-844.9 \pm 35.24$

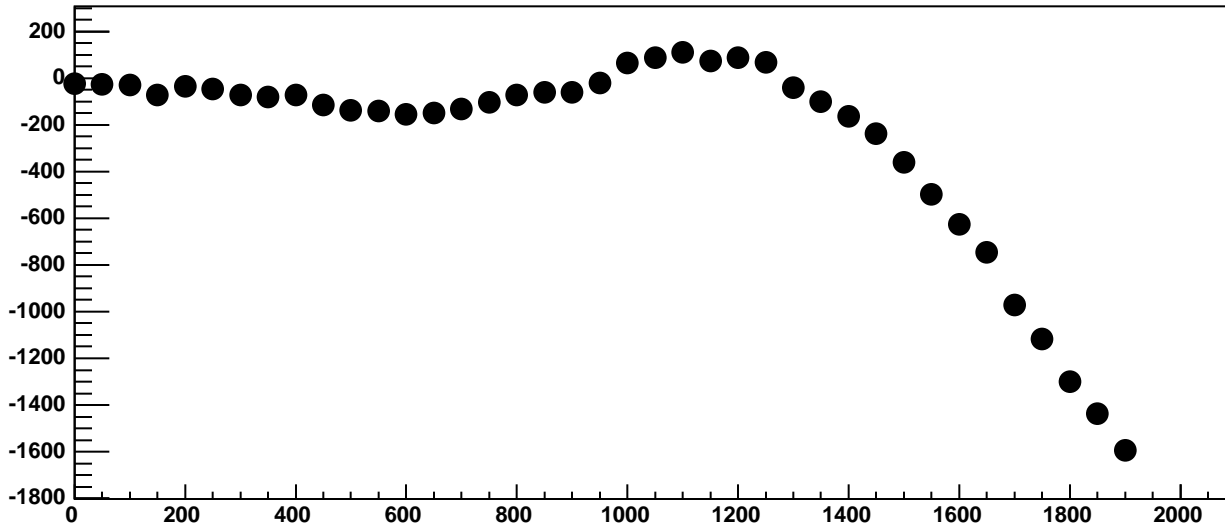
p1

$0.2531 \pm 0.03248$

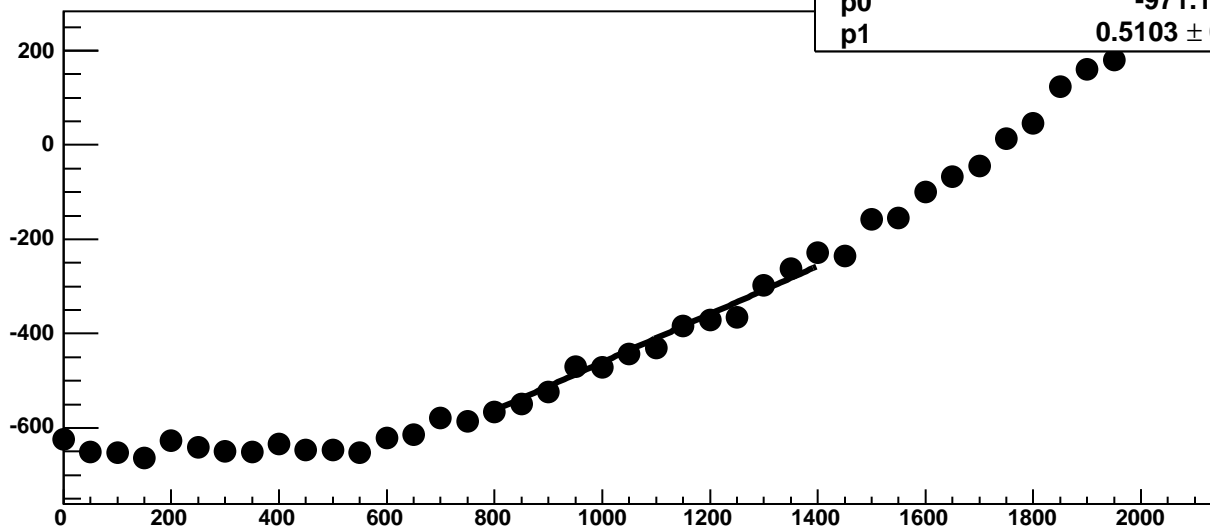
Chip 5, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC



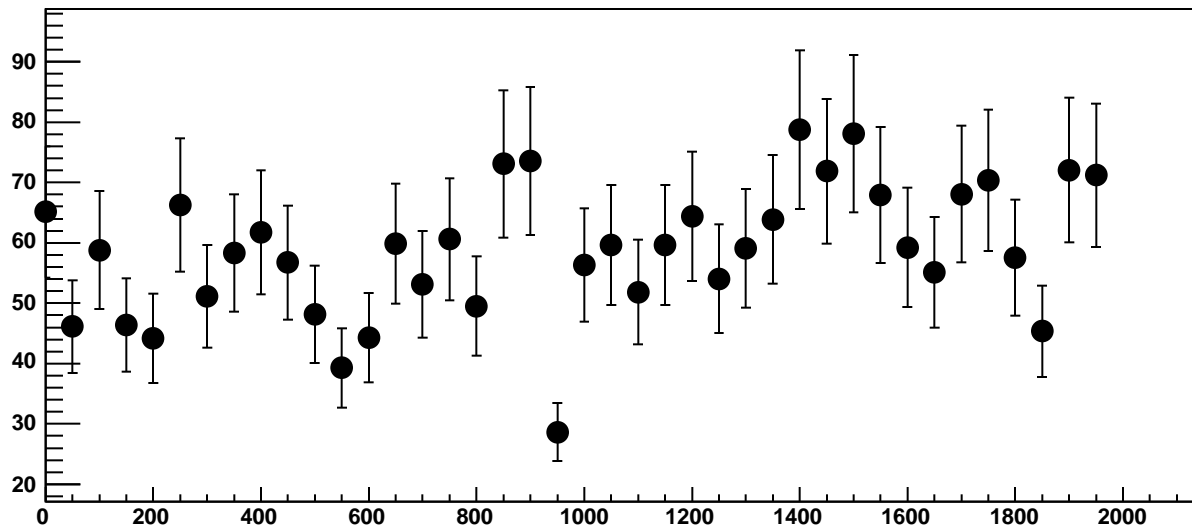
Chip 5, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC



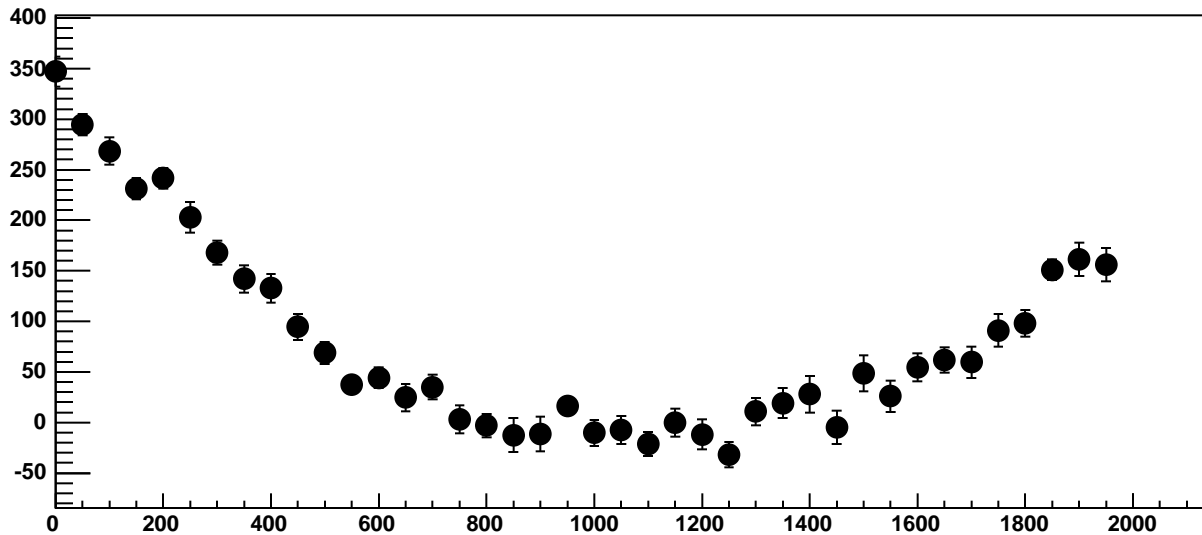
Chip 5, Channel 11, Enable 0, Hold=30, ADC Mean vs DAC



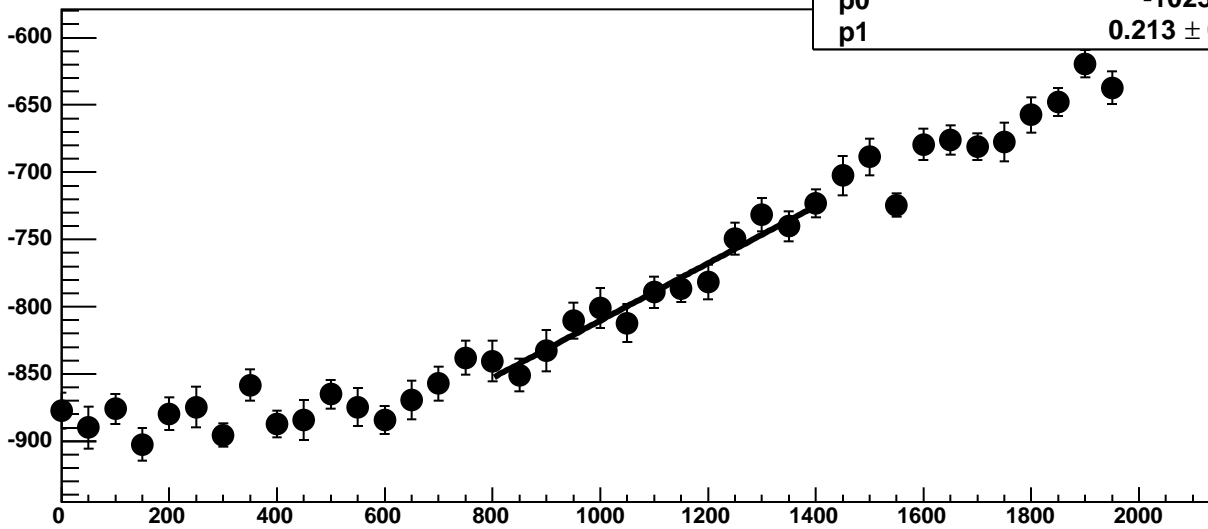
Chip 5, Channel 11, Enable 0, Hold=30, ADC Noise vs DAC



Chip 5, Channel 11, Enable 0, Hold=30, ADC Residuals vs DAC

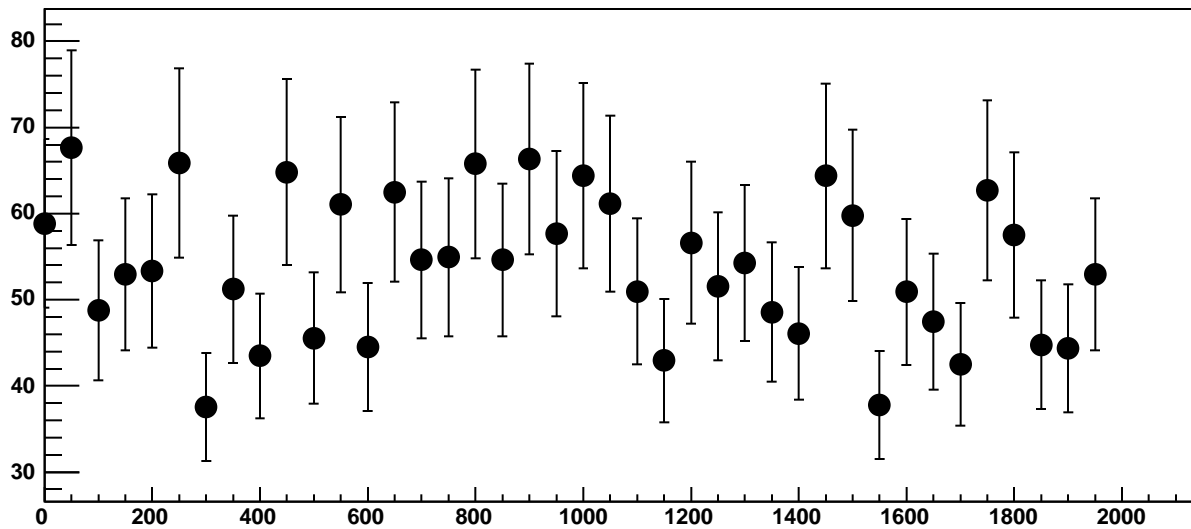


Chip 5, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC

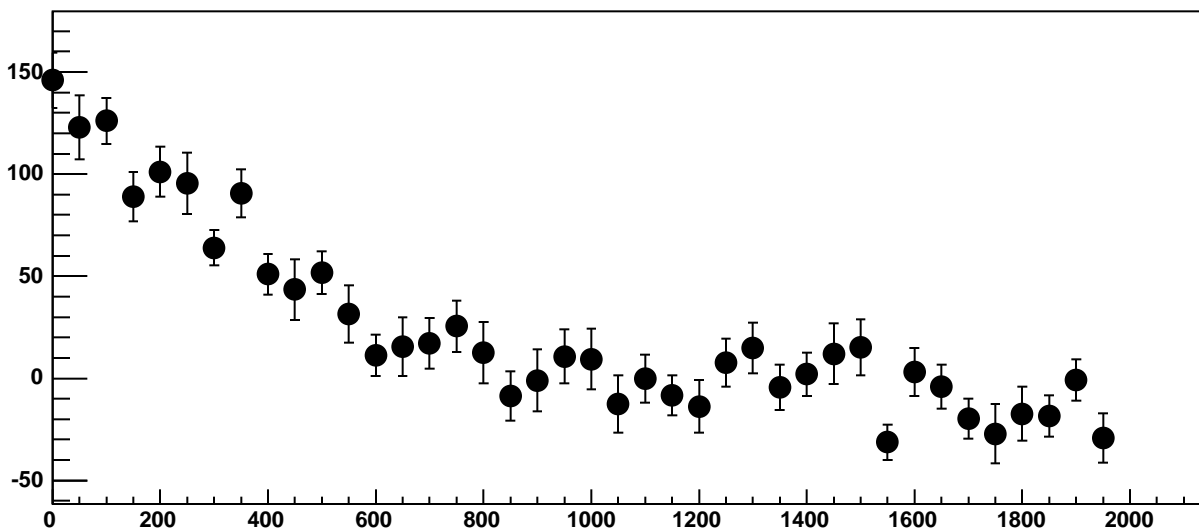


$\chi^2 / \text{ndf}$  6.878 / 11  
p0  $-1023 \pm 21.31$   
p1  $0.213 \pm 0.01858$

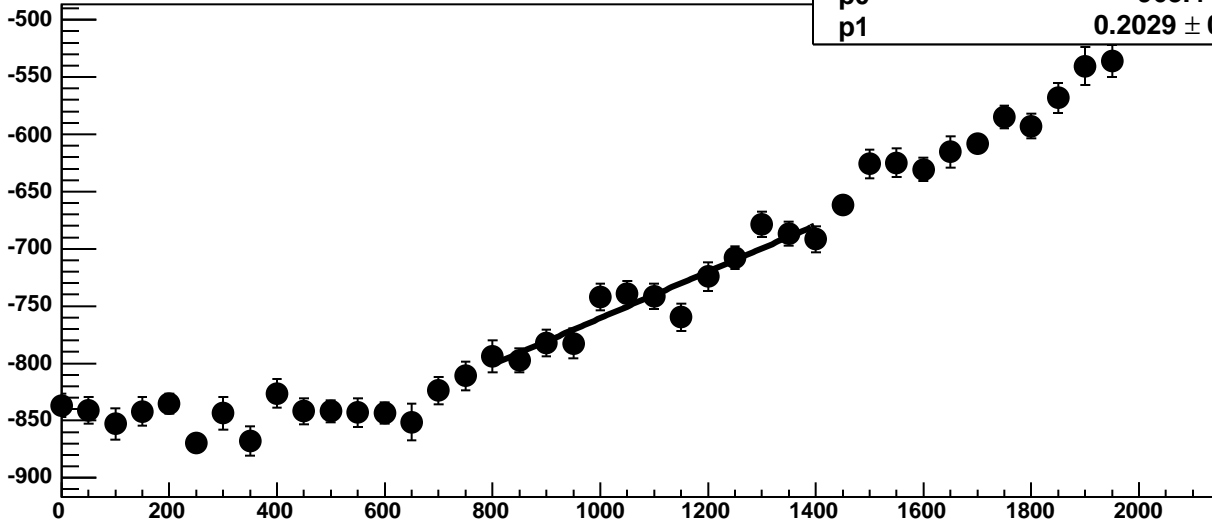
Chip 5, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC



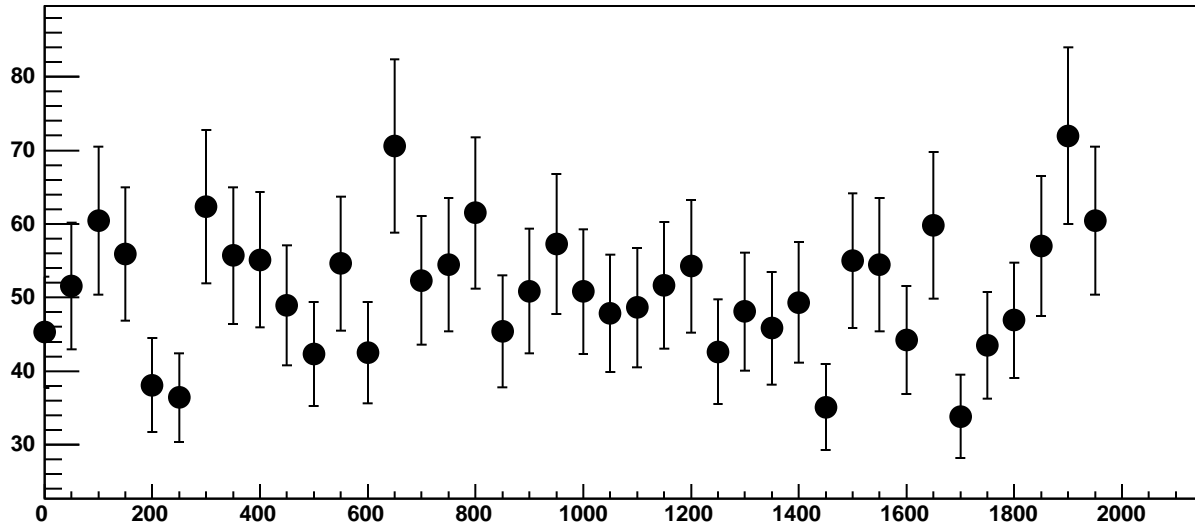
Chip 5, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC



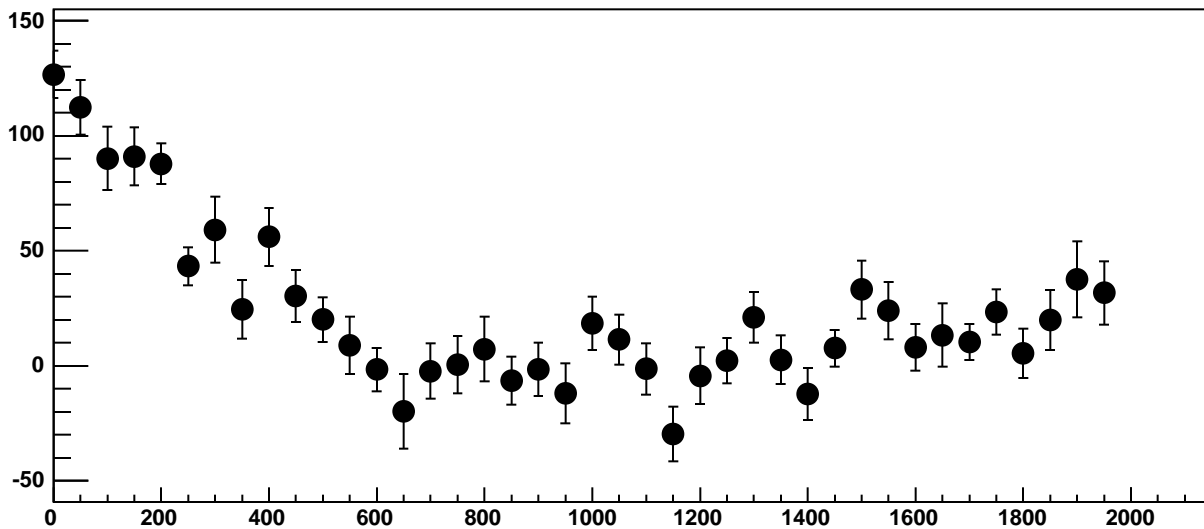
Chip 5, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC



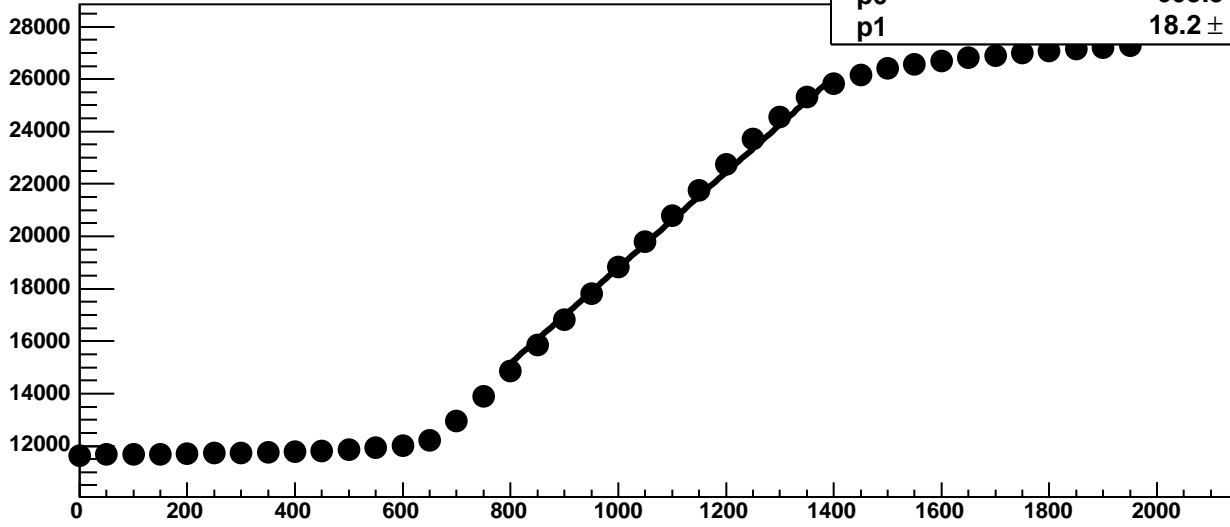
Chip 5, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC



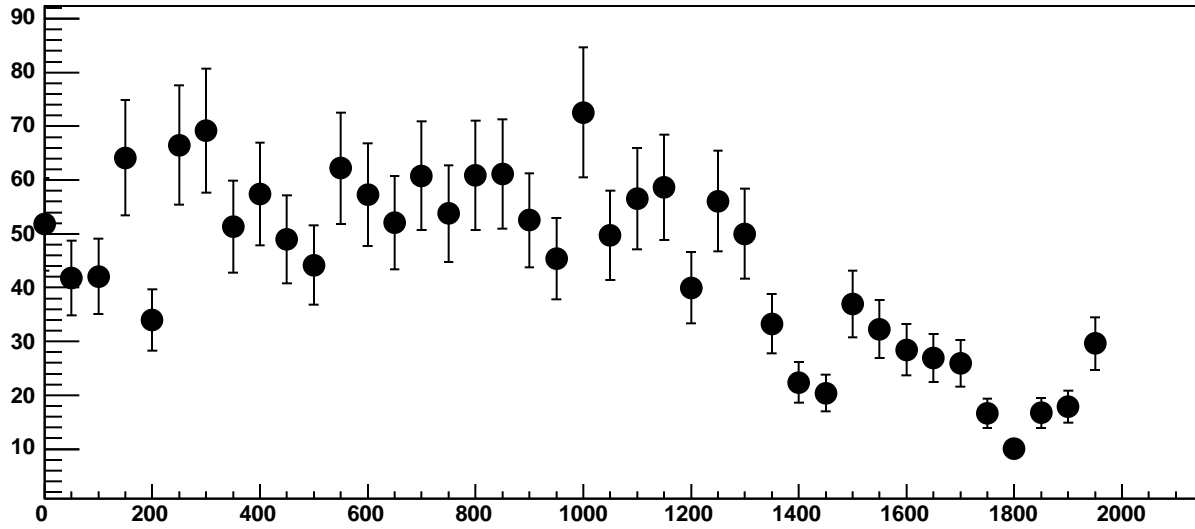
Chip 5, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC



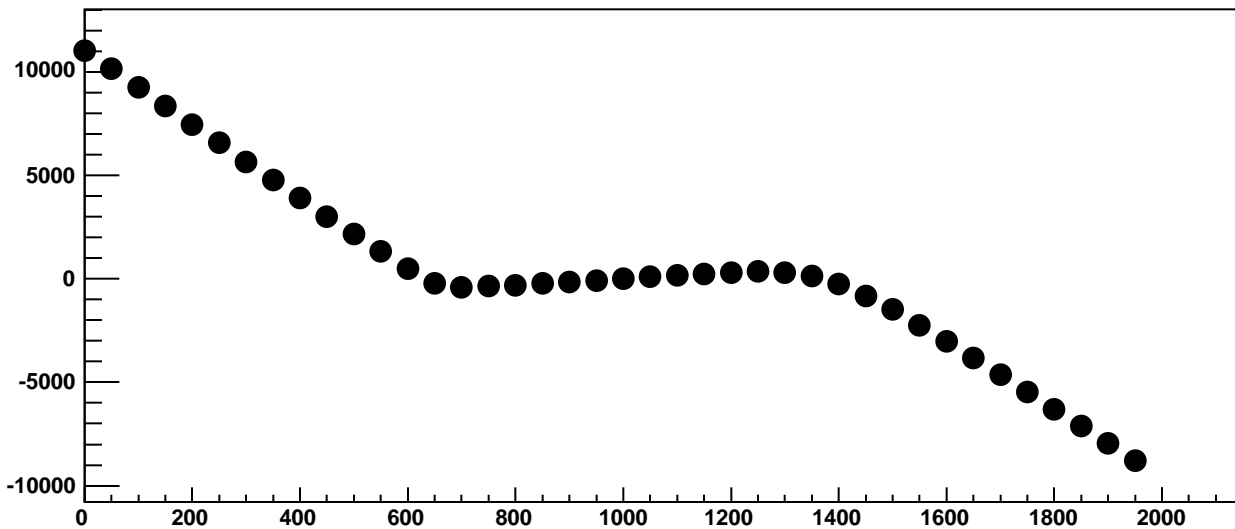
Chip 5, Channel 11, Enable 3!, Hold=30, ADC Mean vs DAC



Chip 5, Channel 11, Enable 3!, Hold=30, ADC Noise vs DAC

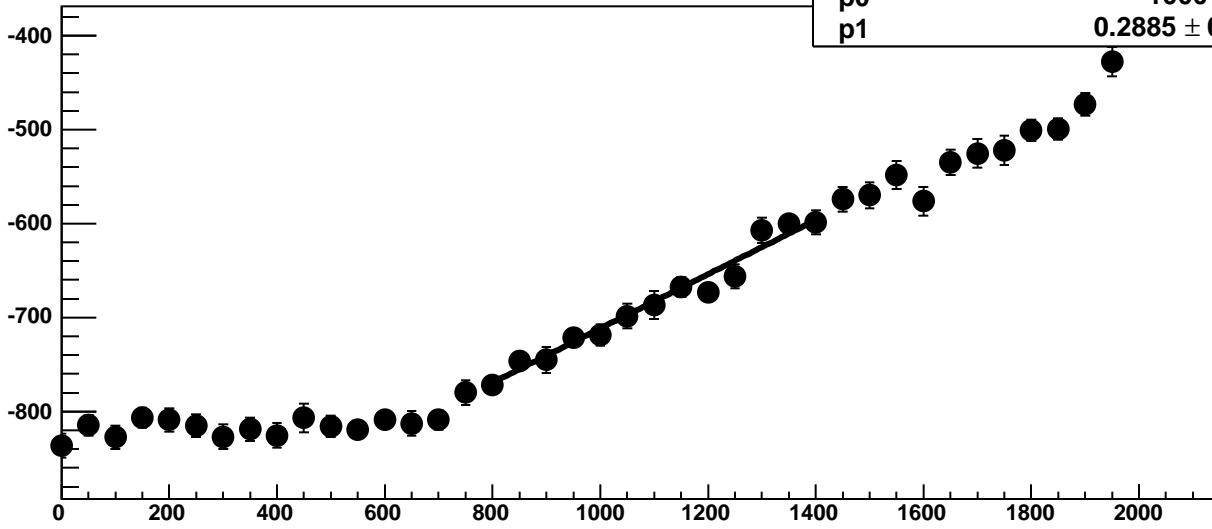


Chip 5, Channel 11, Enable 3!, Hold=30, ADC Residuals vs DAC

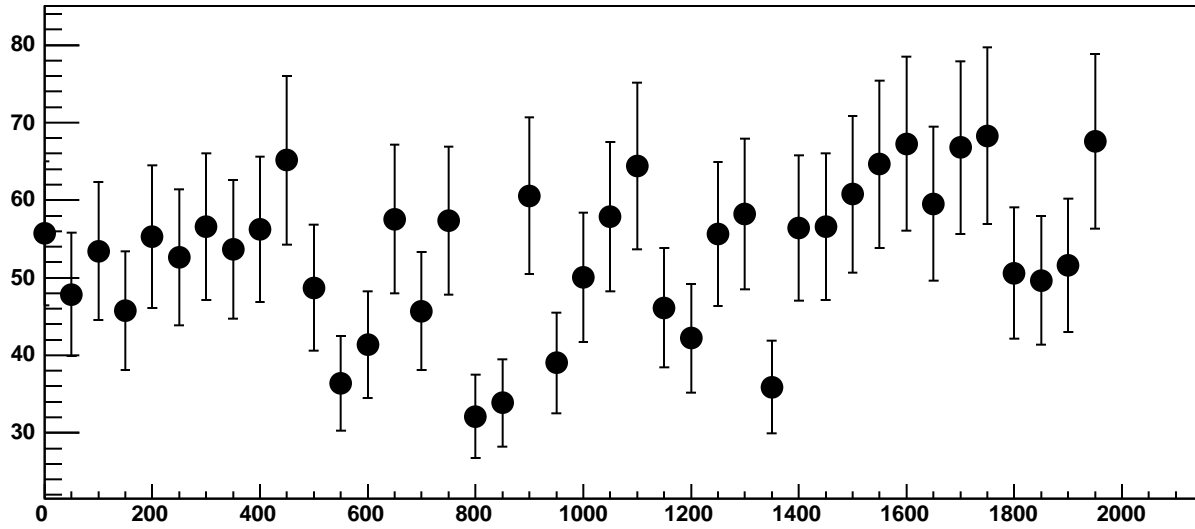




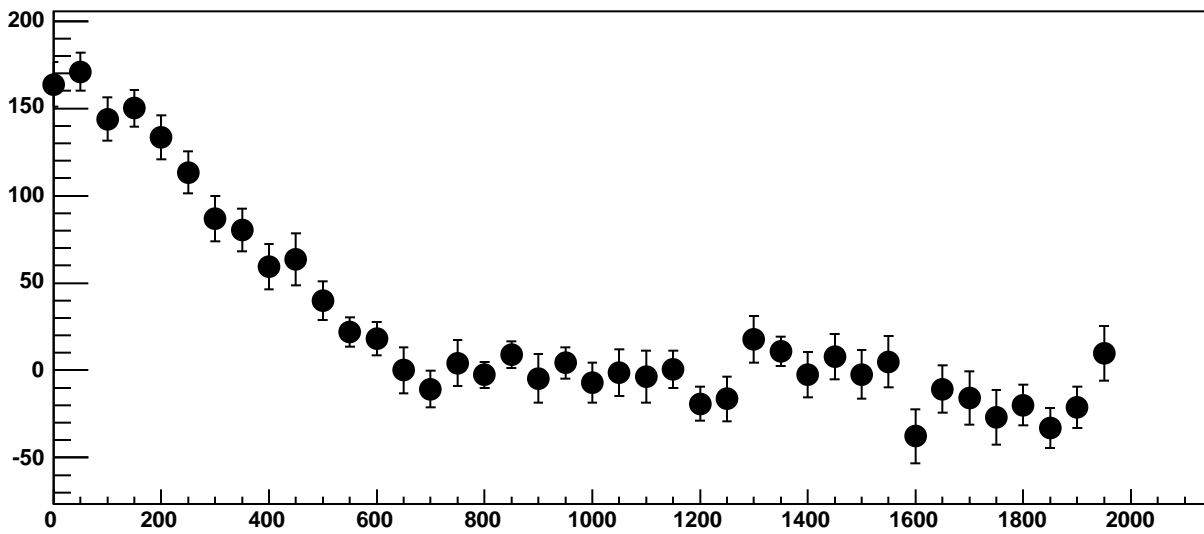
Chip 5, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC



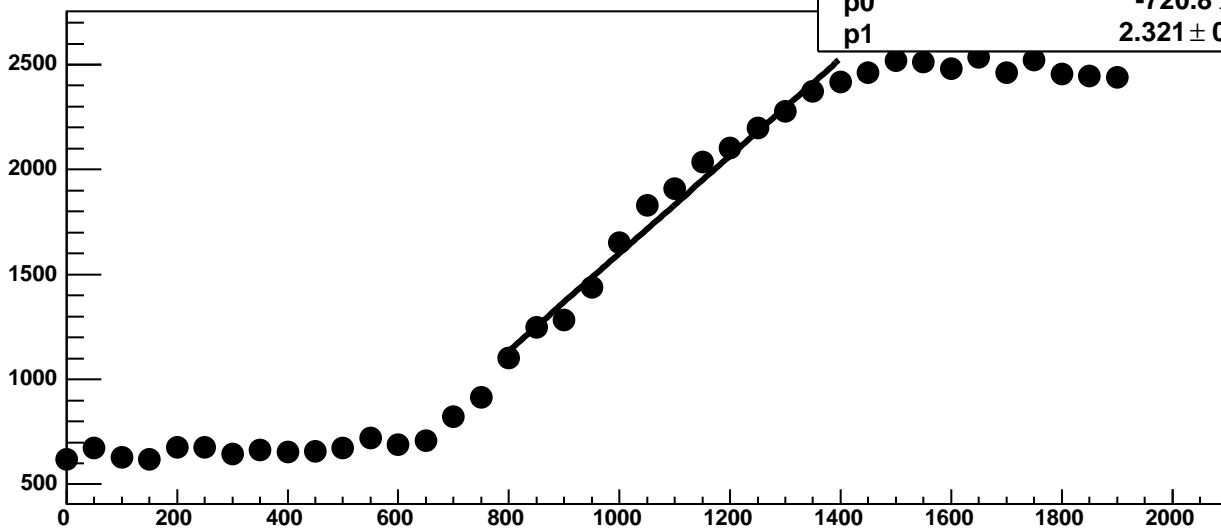
Chip 5, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC



Chip 5, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC

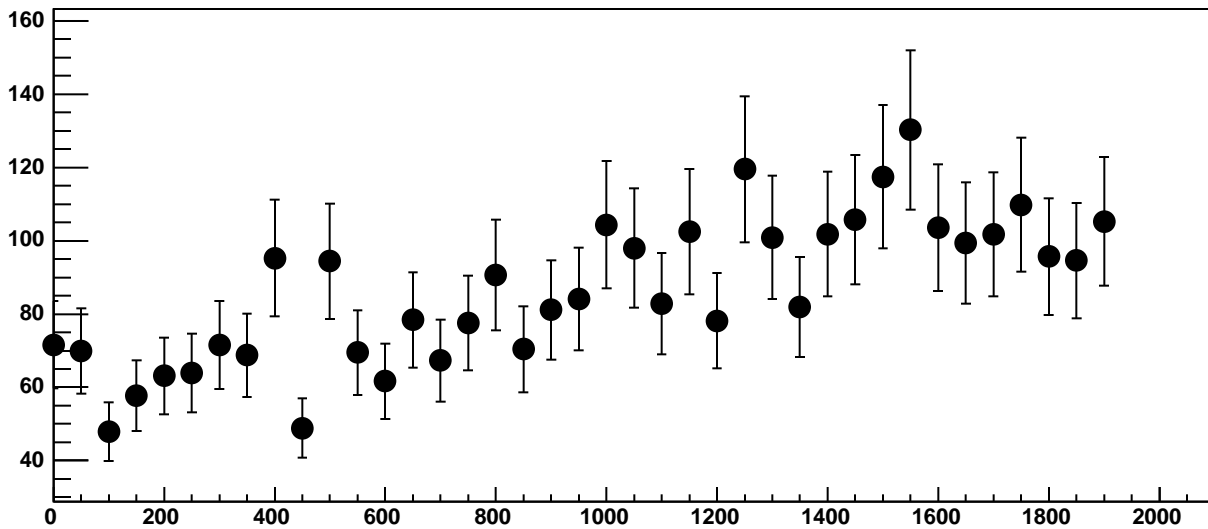


Chip 5, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC

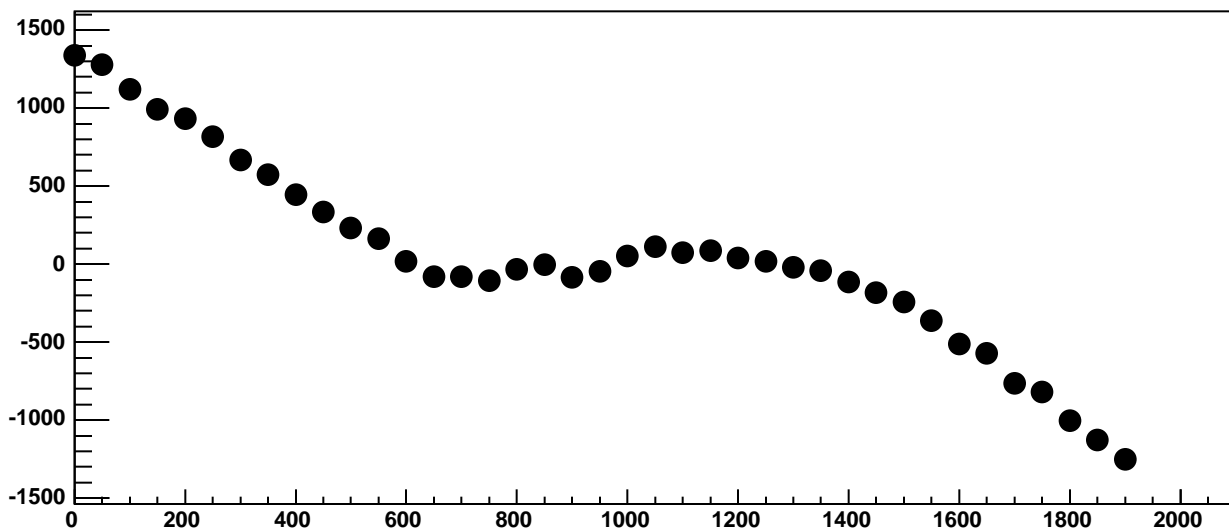


$\chi^2 / \text{ndf}$  121.9 / 11  
p0  $-720.8 \pm 32.82$   
p1  $2.321 \pm 0.02998$

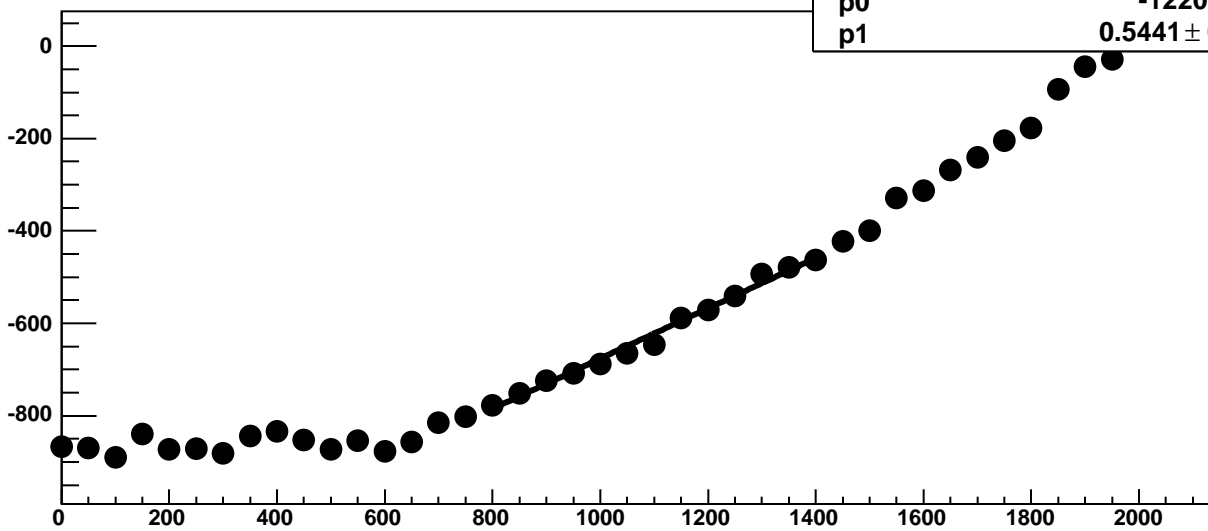
Chip 5, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC



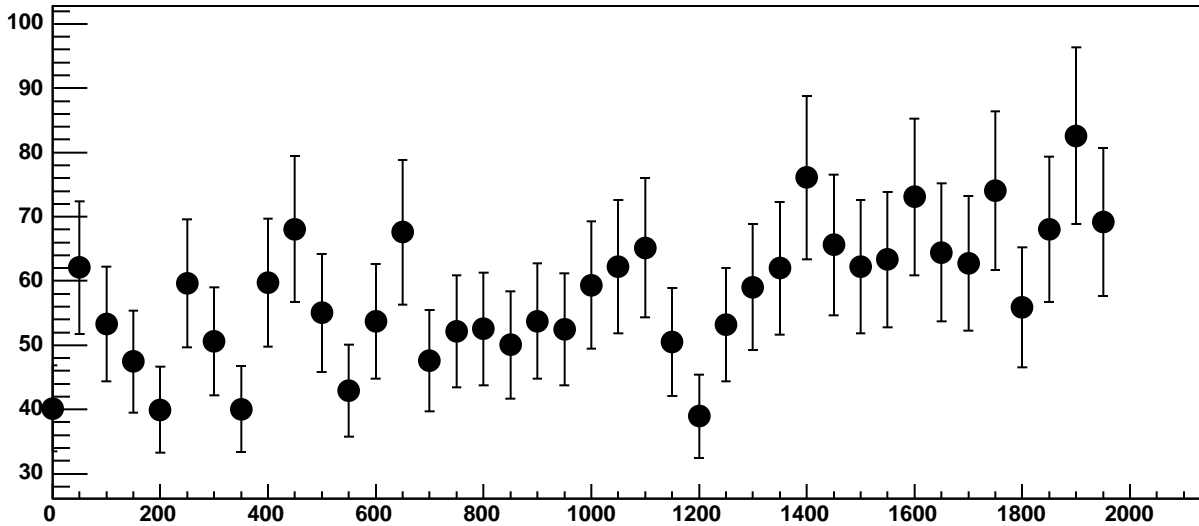
Chip 5, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC



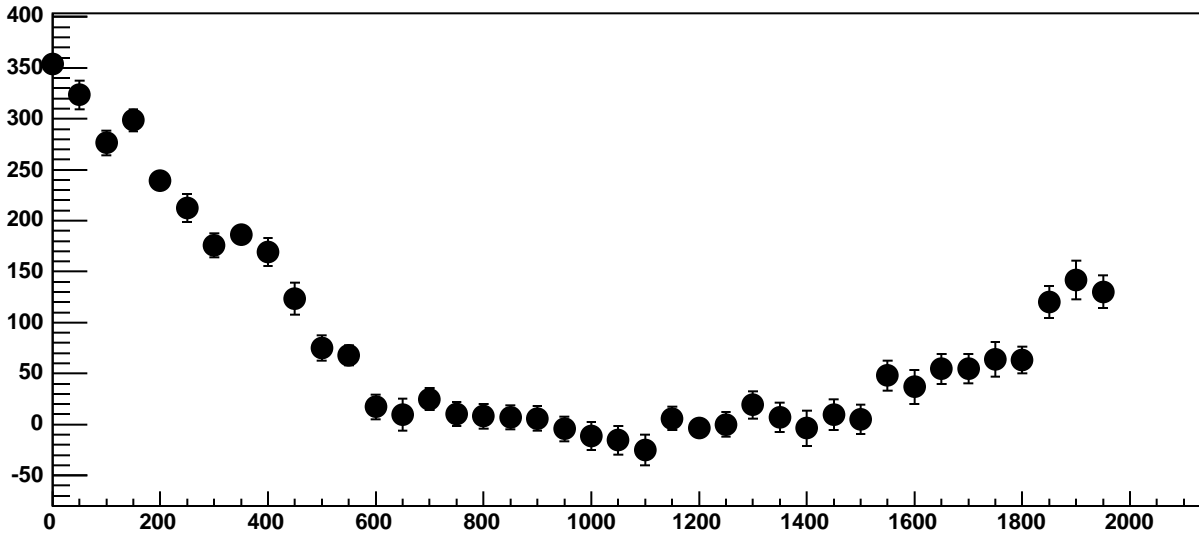
Chip 5, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC



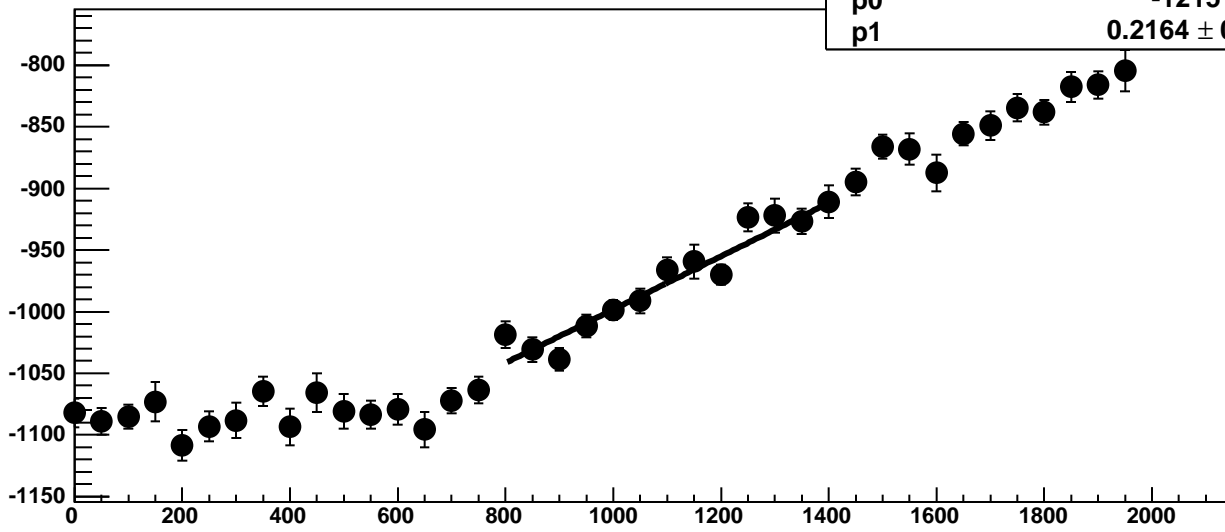
Chip 5, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC



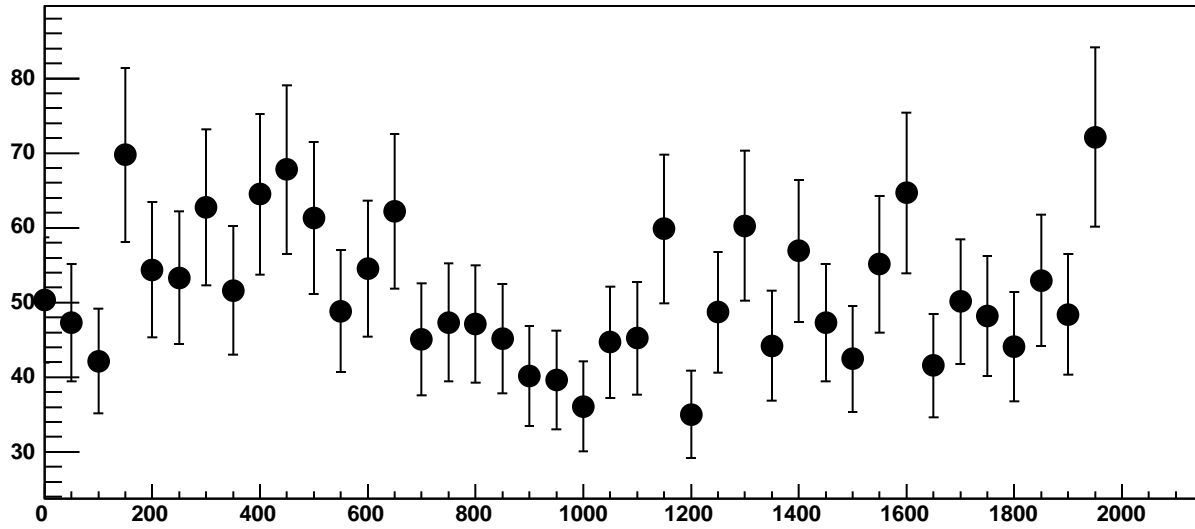
Chip 5, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC



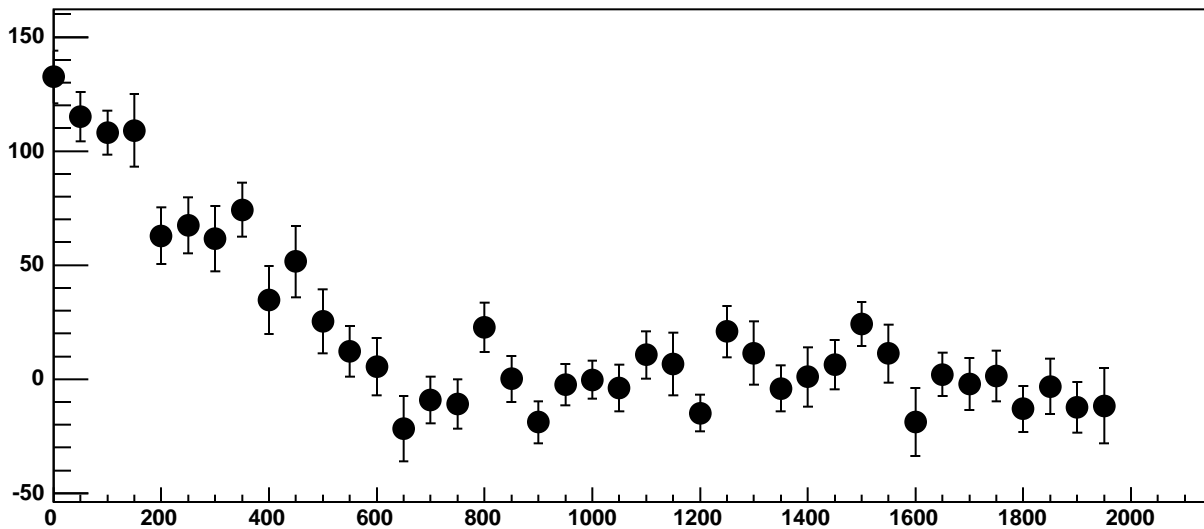
Chip 5, Channel 12, Enable 1, Hold=30, ADC Mean vs DAC



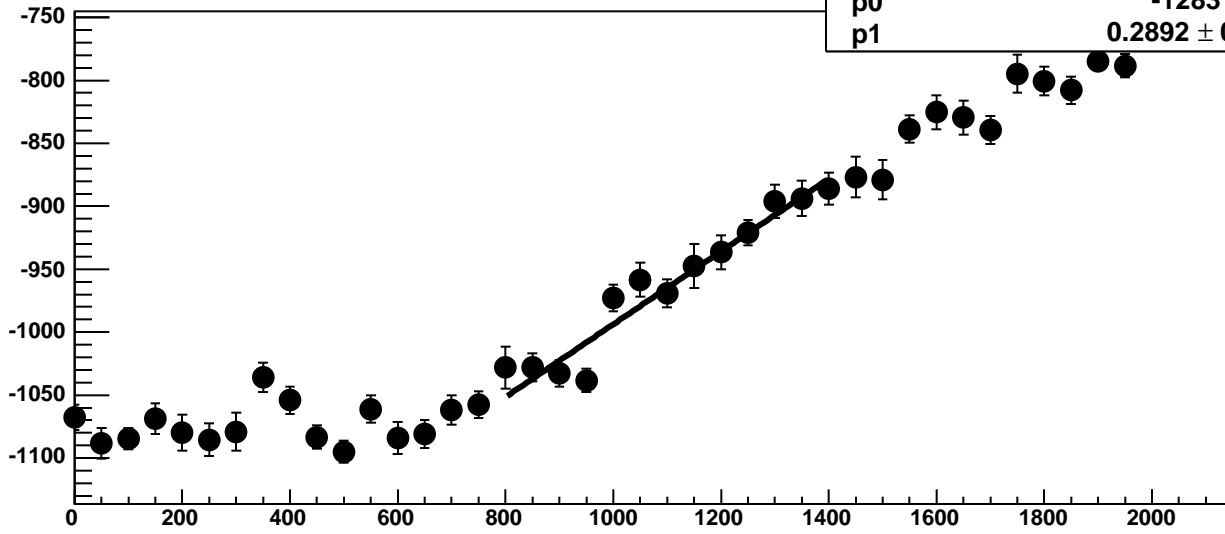
Chip 5, Channel 12, Enable 1, Hold=30, ADC Noise vs DAC



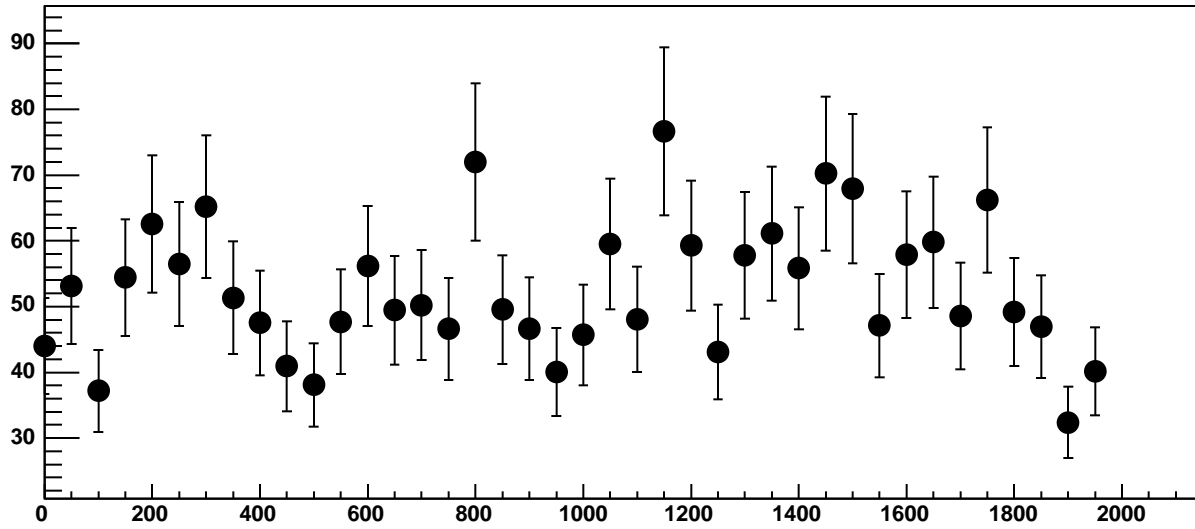
Chip 5, Channel 12, Enable 1, Hold=30, ADC Residuals vs DAC



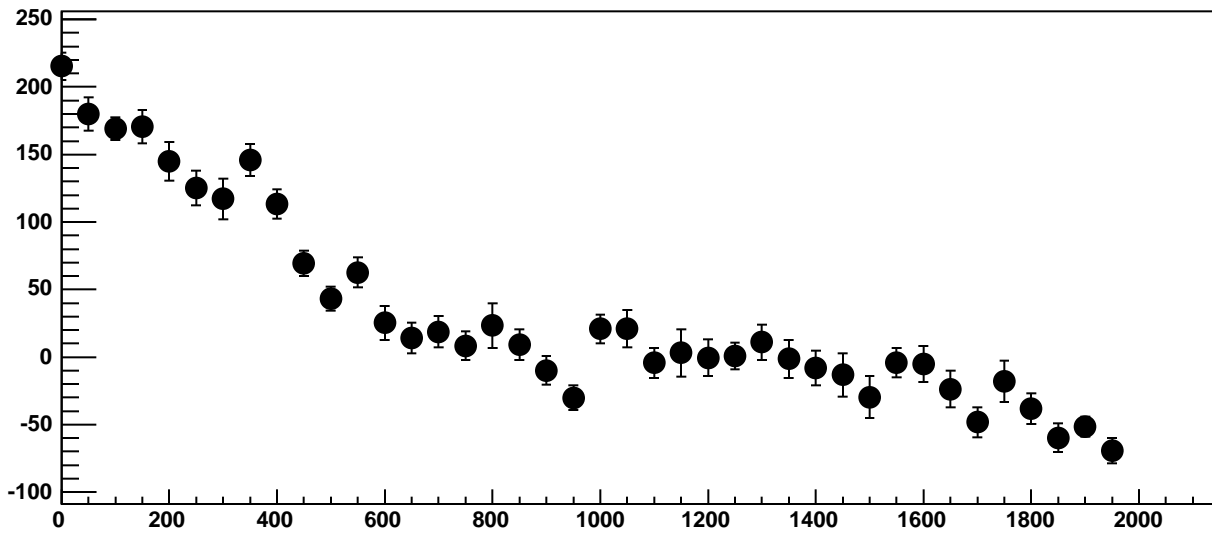
Chip 5, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC



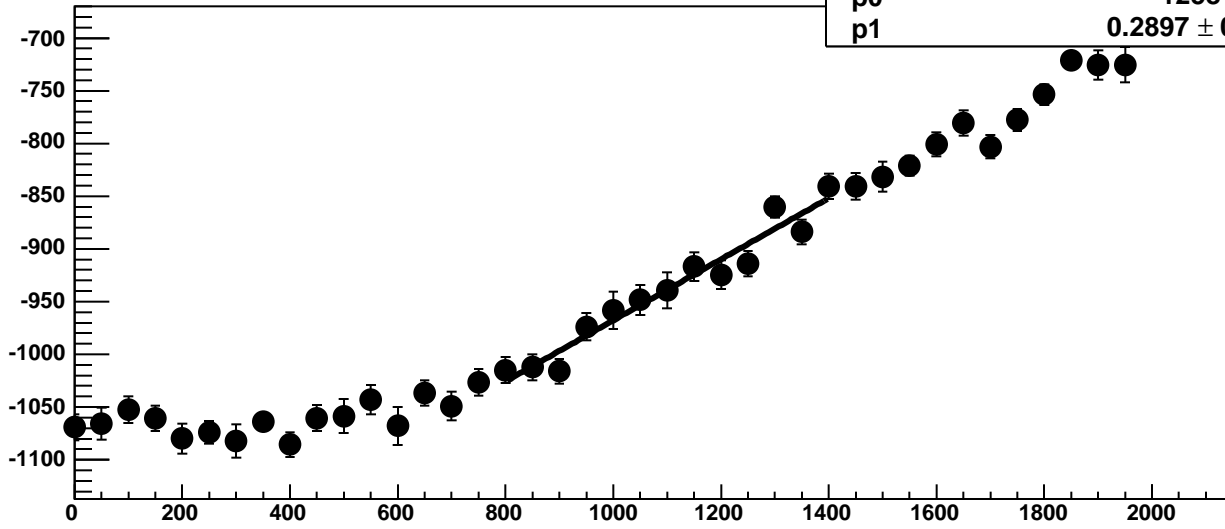
Chip 5, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC



Chip 5, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC

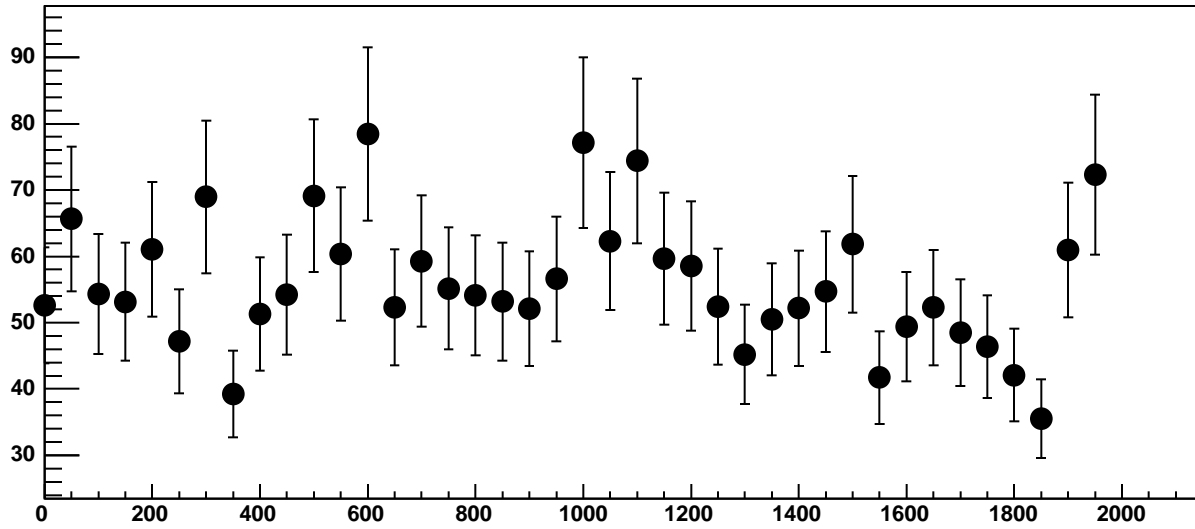


Chip 5, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC

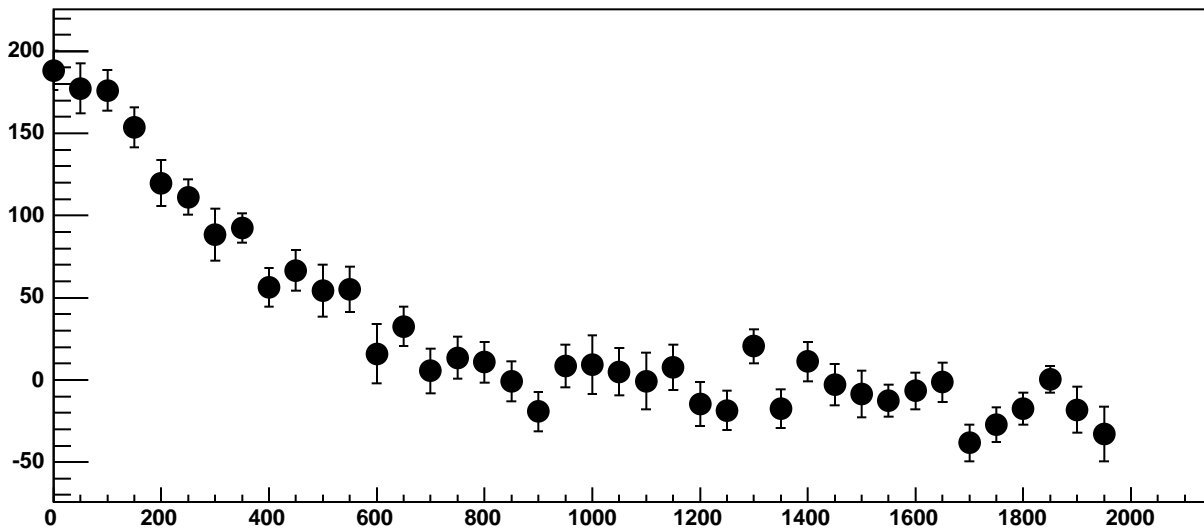


$\chi^2 / \text{ndf}$  15.15 / 11  
p0  $-1258 \pm 20.24$   
p1  $0.2897 \pm 0.01787$

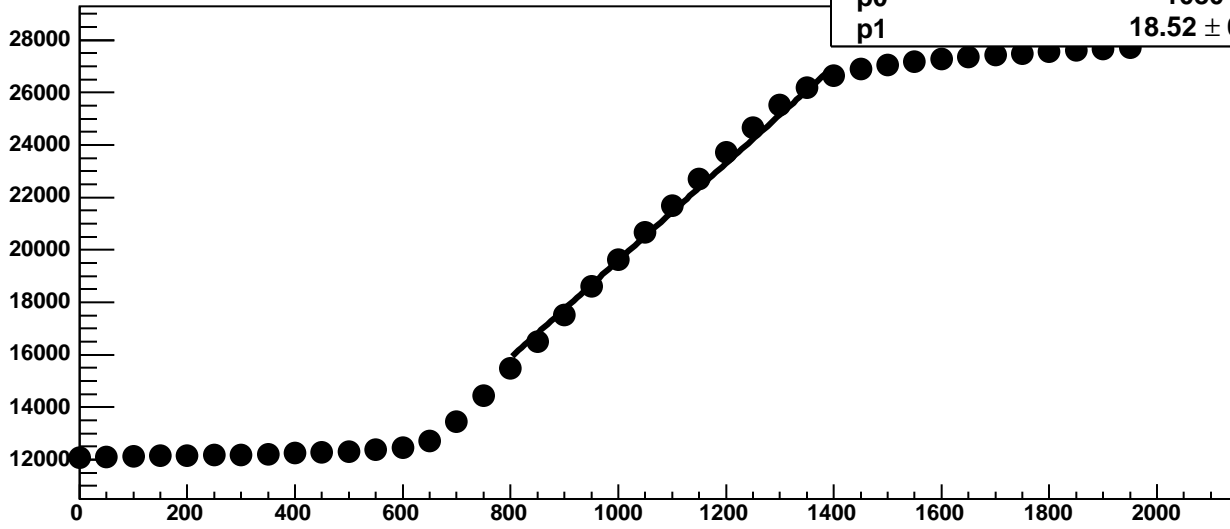
Chip 5, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC



Chip 5, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC

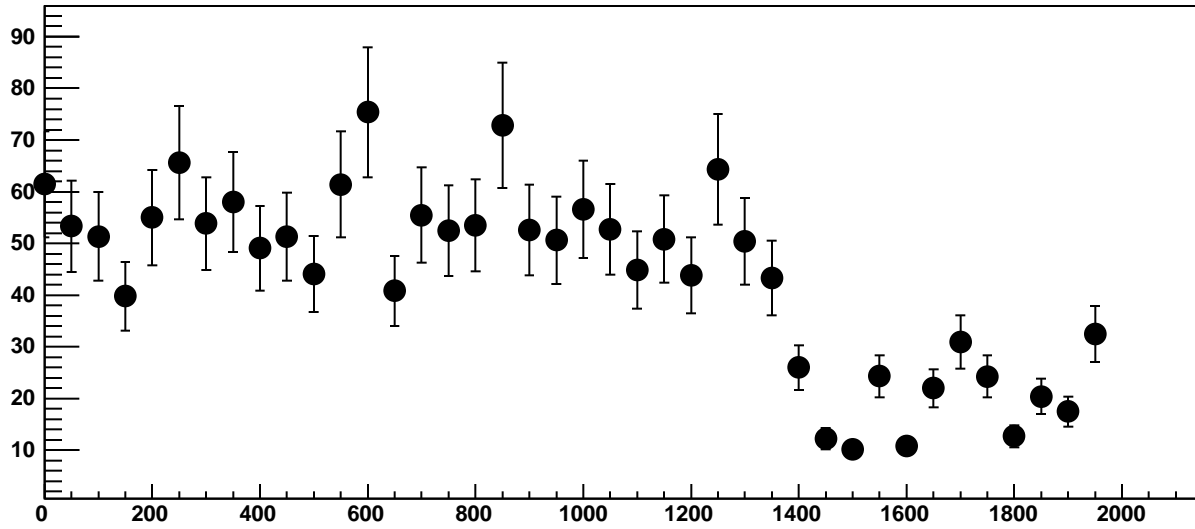


Chip 5, Channel 12, Enable 4!, Hold=30, ADC Mean vs DAC

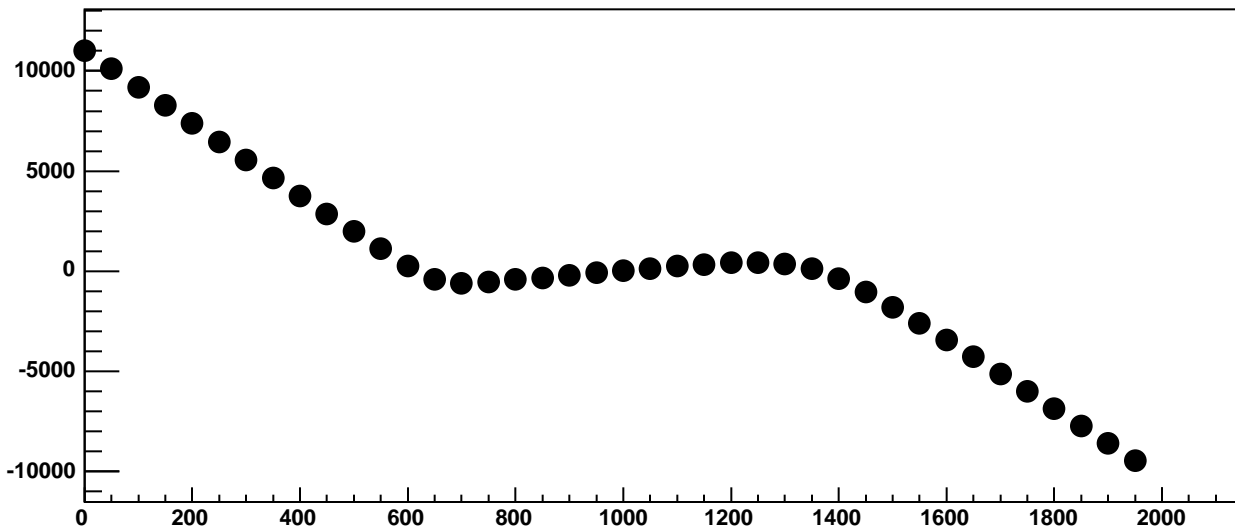


$\chi^2 / \text{ndf}$	1.093e+04 / 11
p0	1080 ± 17.84
p1	18.52 ± 0.01501

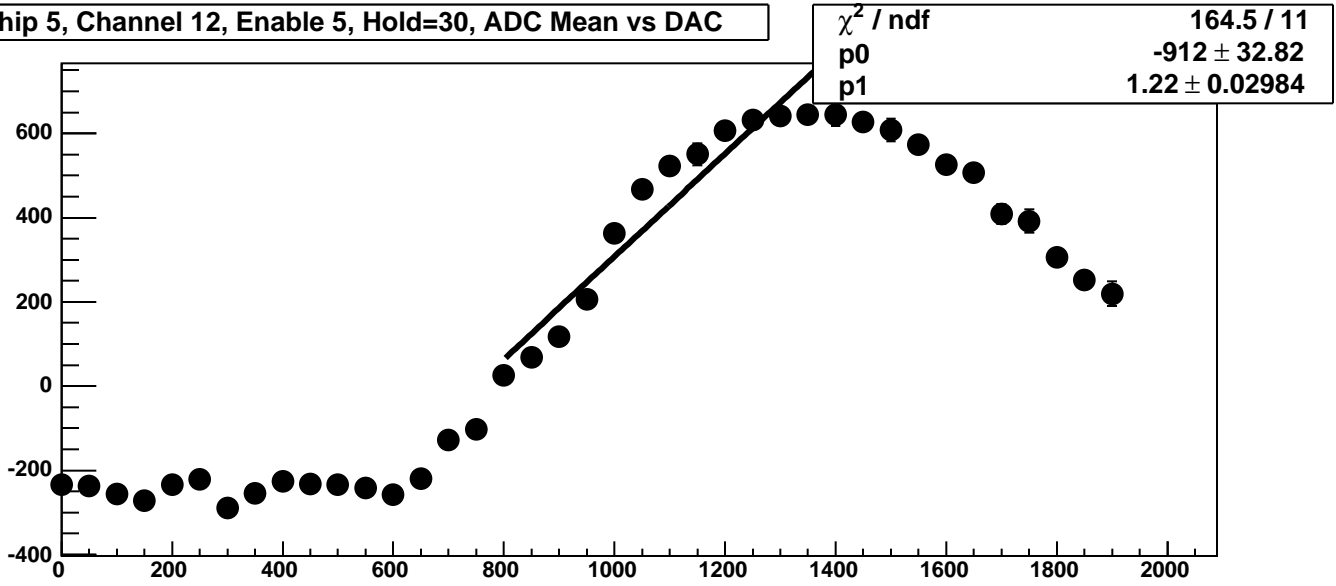
Chip 5, Channel 12, Enable 4!, Hold=30, ADC Noise vs DAC



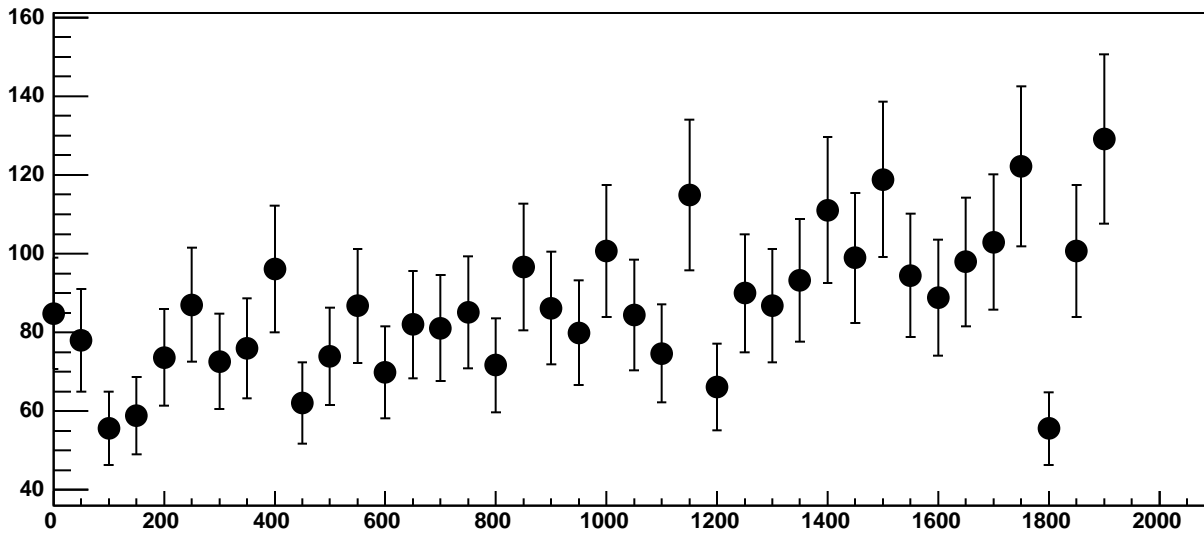
Chip 5, Channel 12, Enable 4!, Hold=30, ADC Residuals vs DAC



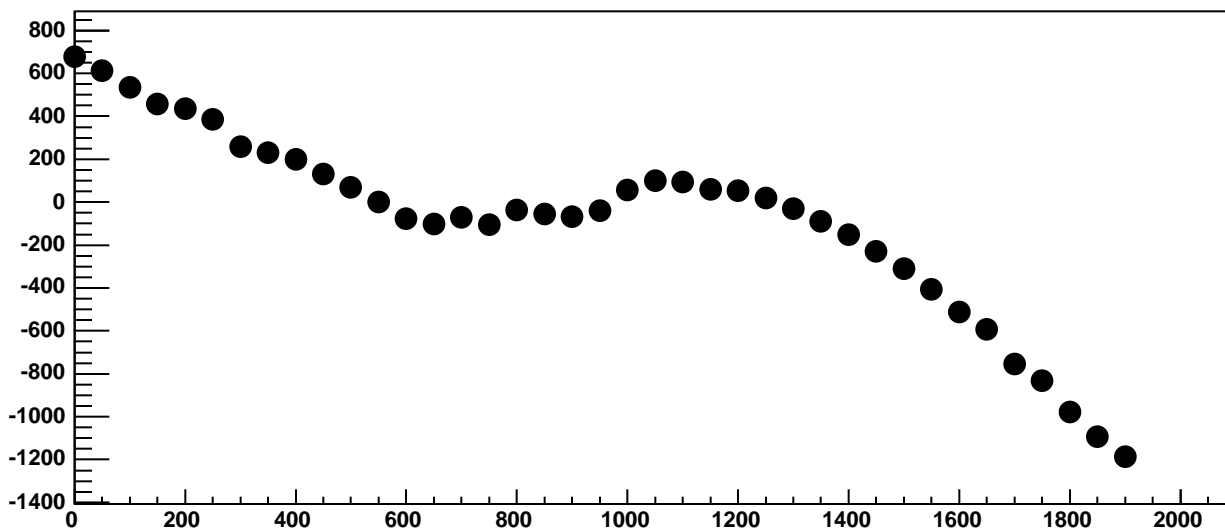
Chip 5, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC



Chip 5, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC

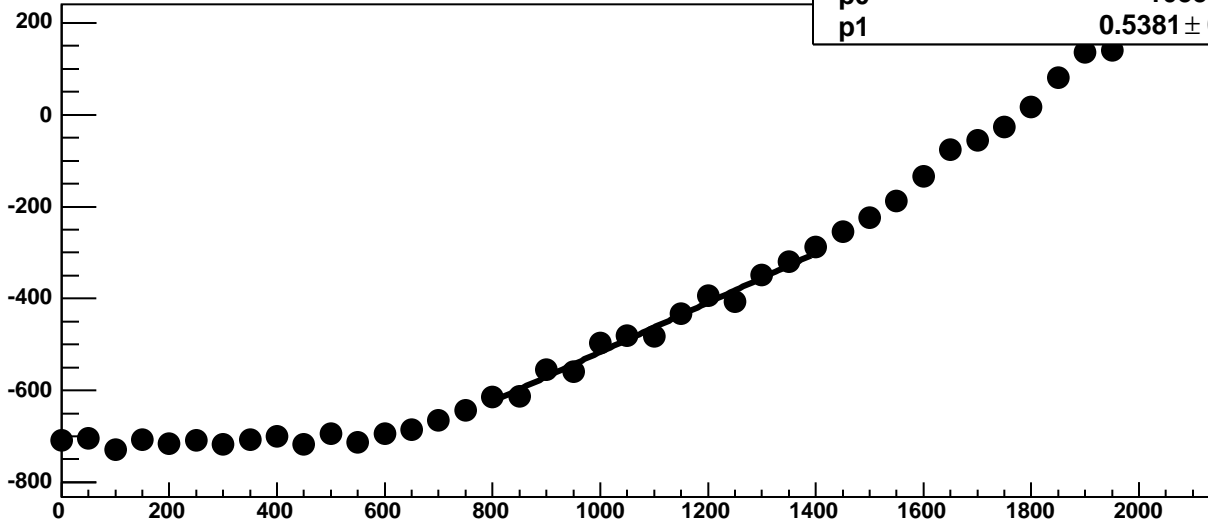


Chip 5, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC

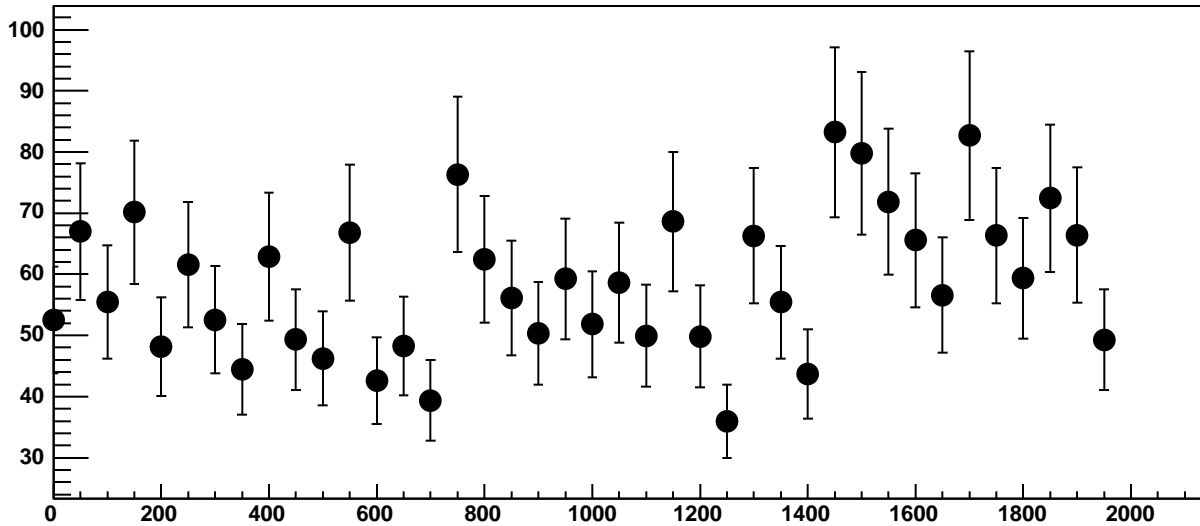




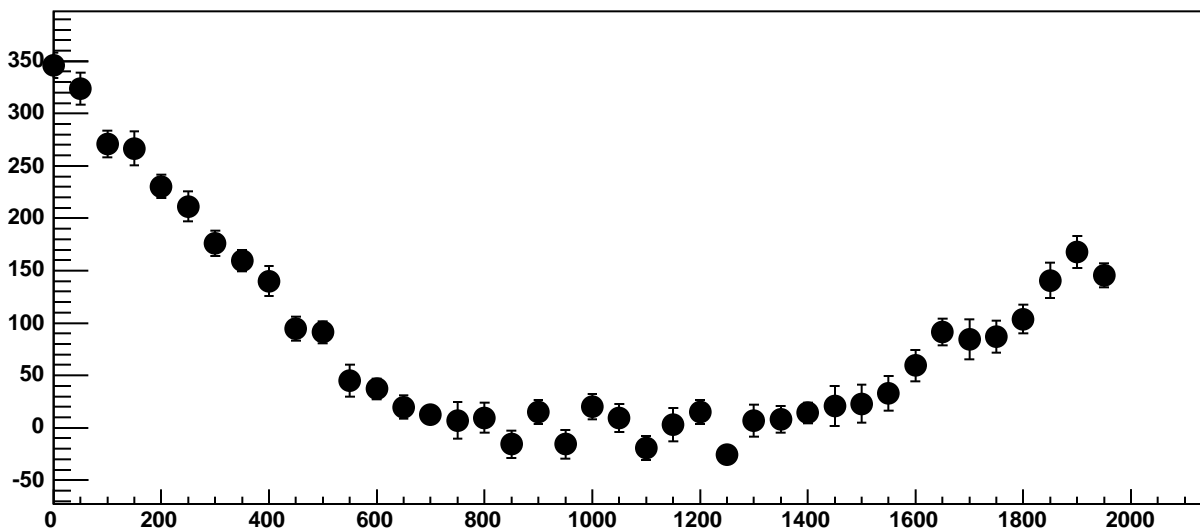
Chip 5, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC



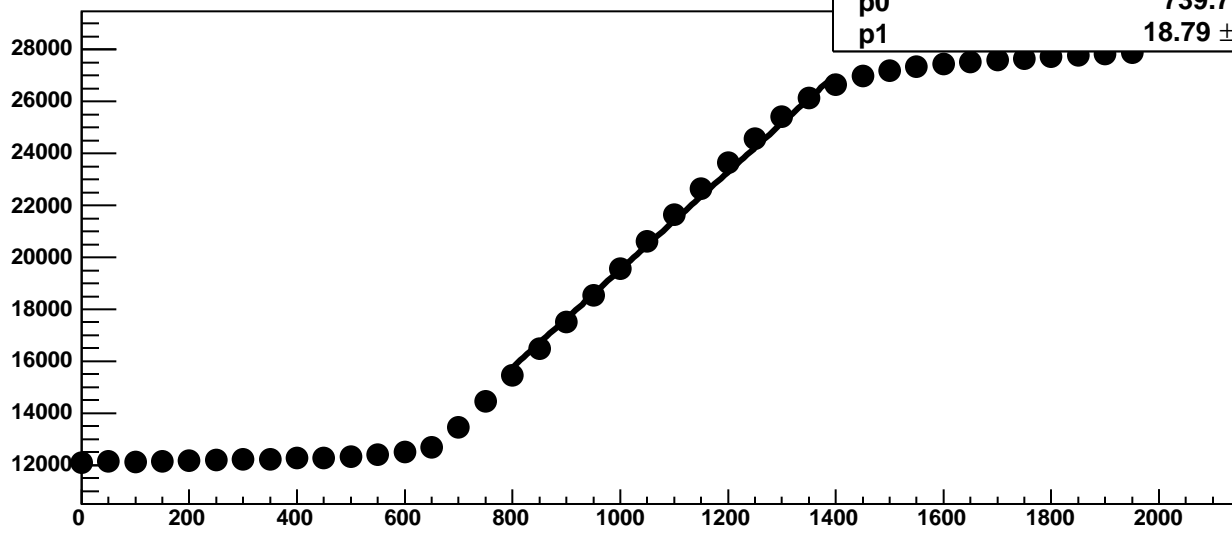
Chip 5, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC



Chip 5, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC

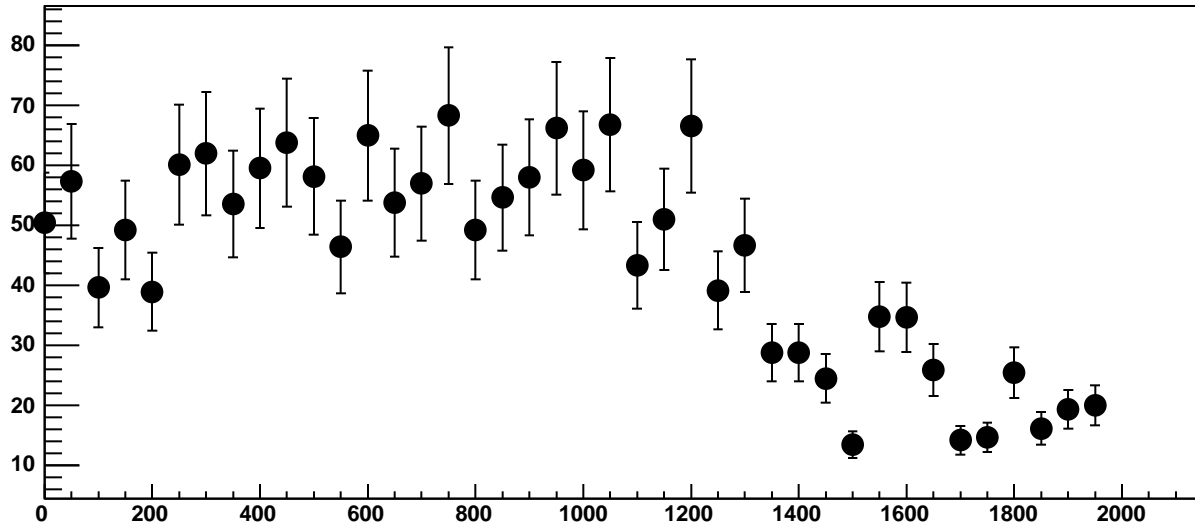


Chip 5, Channel 13, Enable 1!, Hold=30, ADC Mean vs DAC

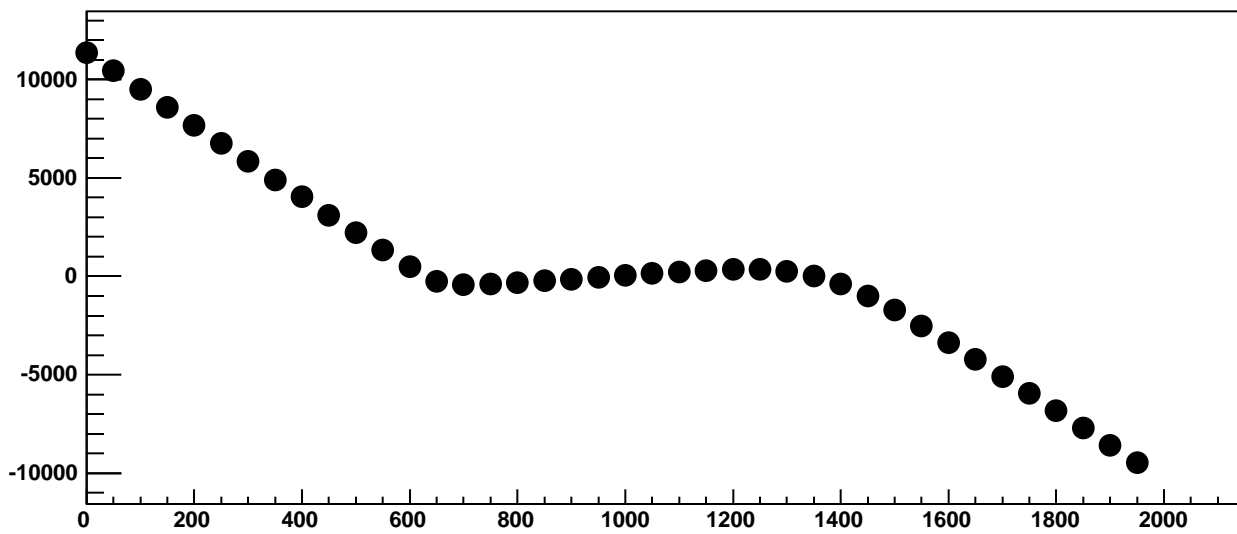


$\chi^2 / \text{ndf}$	8467 / 11
p0	$739.7 \pm 17.31$
p1	$18.79 \pm 0.0144$

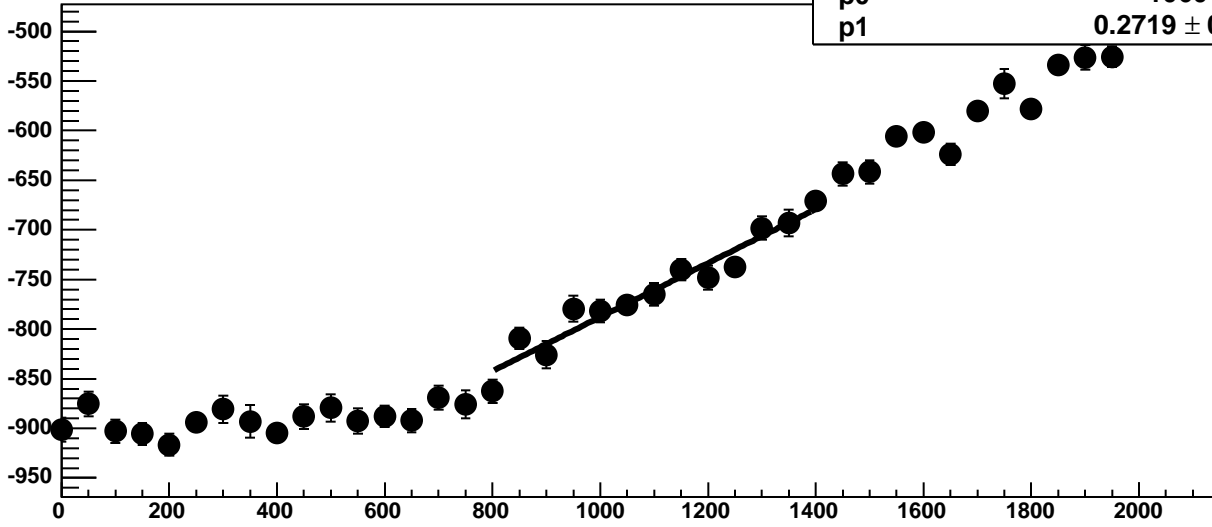
Chip 5, Channel 13, Enable 1!, Hold=30, ADC Noise vs DAC



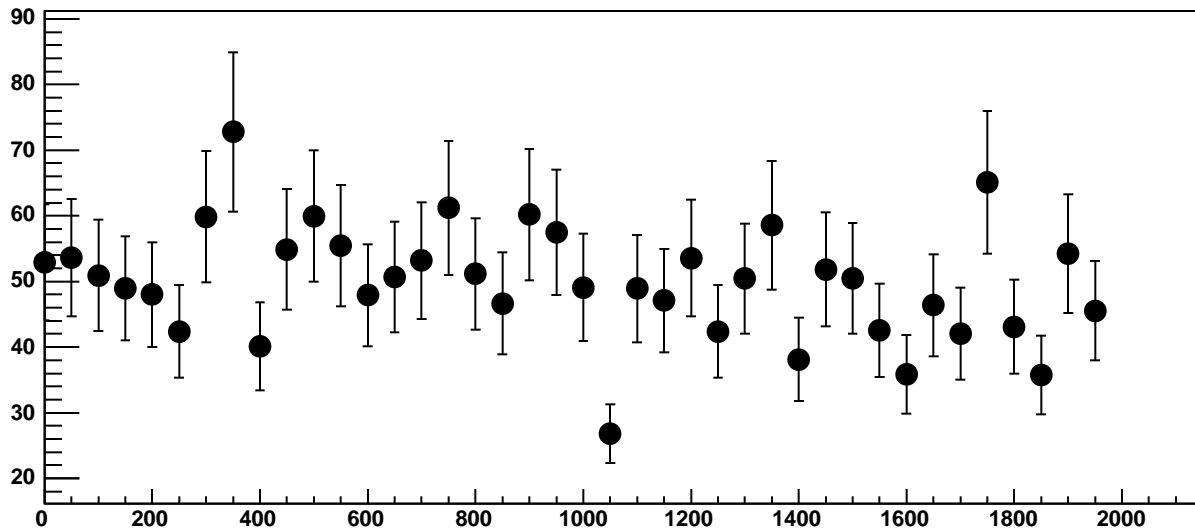
Chip 5, Channel 13, Enable 1!, Hold=30, ADC Residuals vs DAC



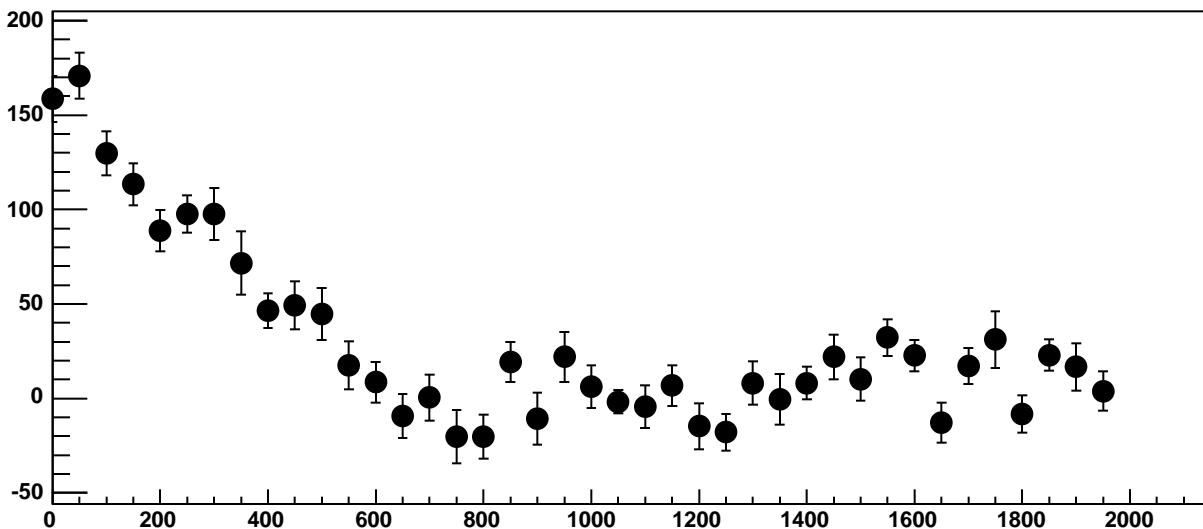
Chip 5, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC



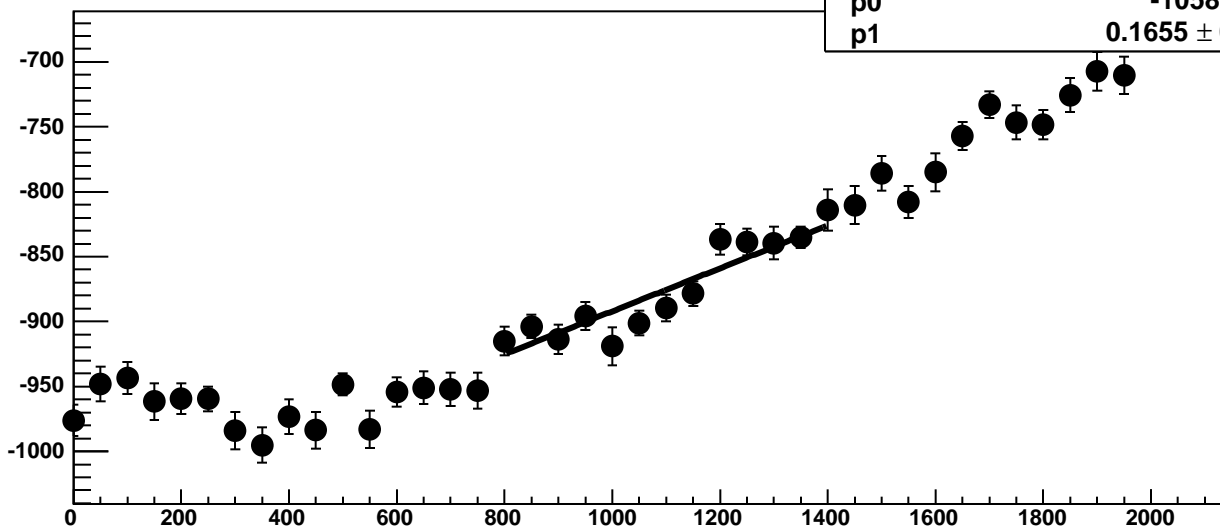
Chip 5, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC



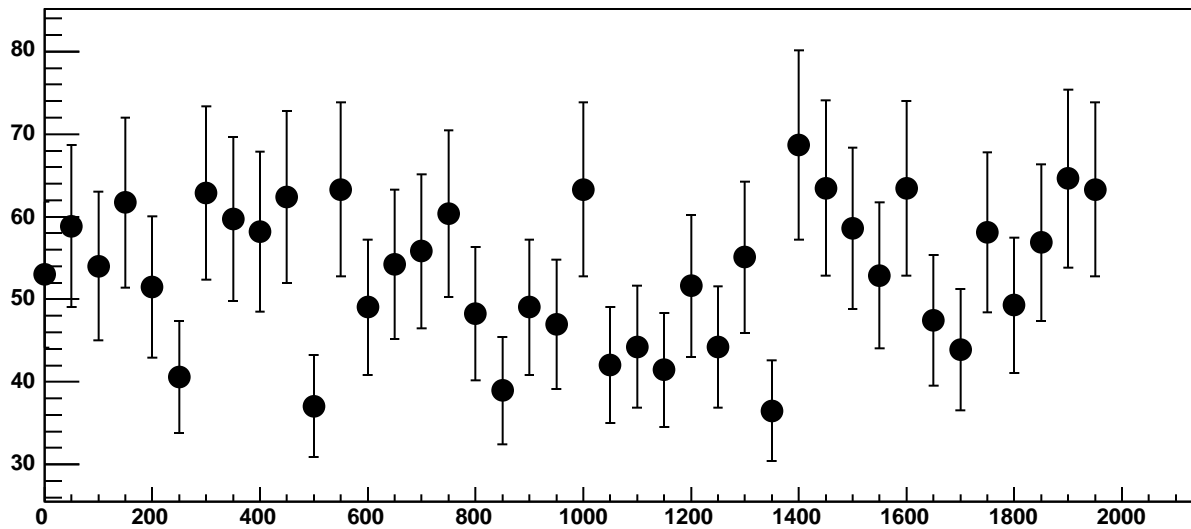
Chip 5, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC



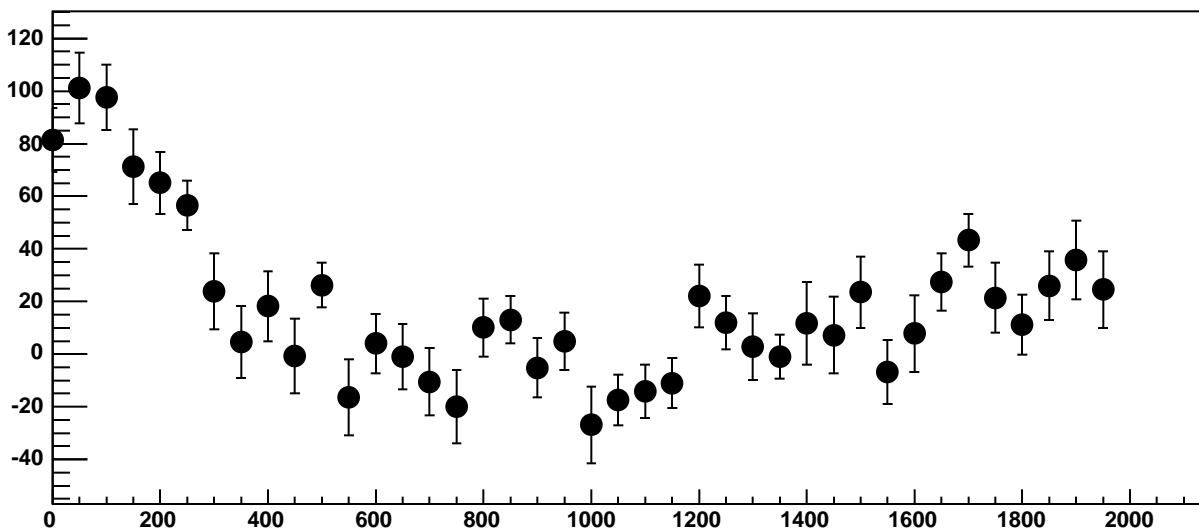
Chip 5, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC



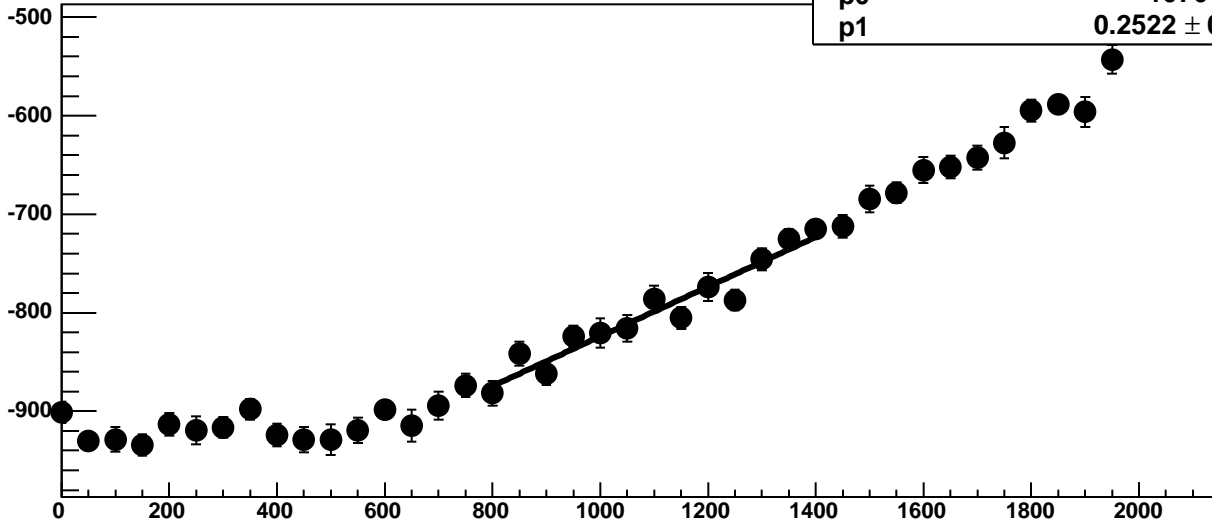
Chip 5, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC



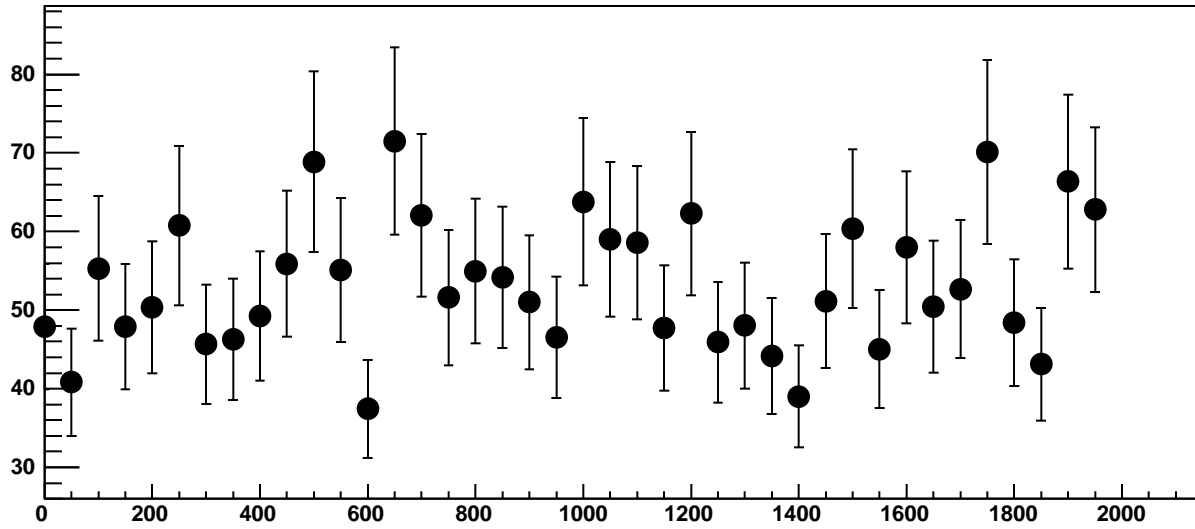
Chip 5, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC



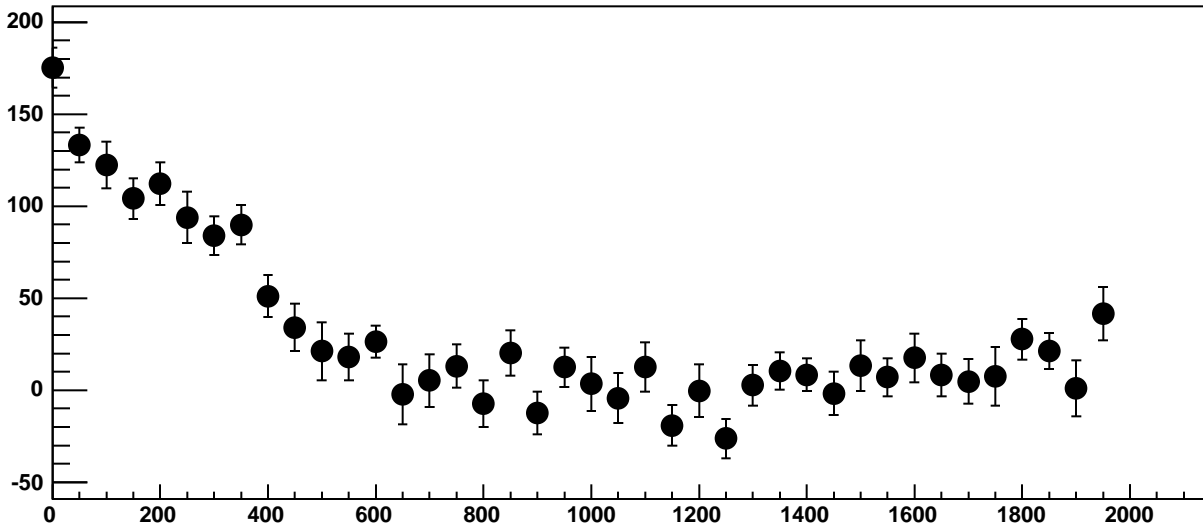
Chip 5, Channel 13, Enable 4, Hold=30, ADC Mean vs DAC



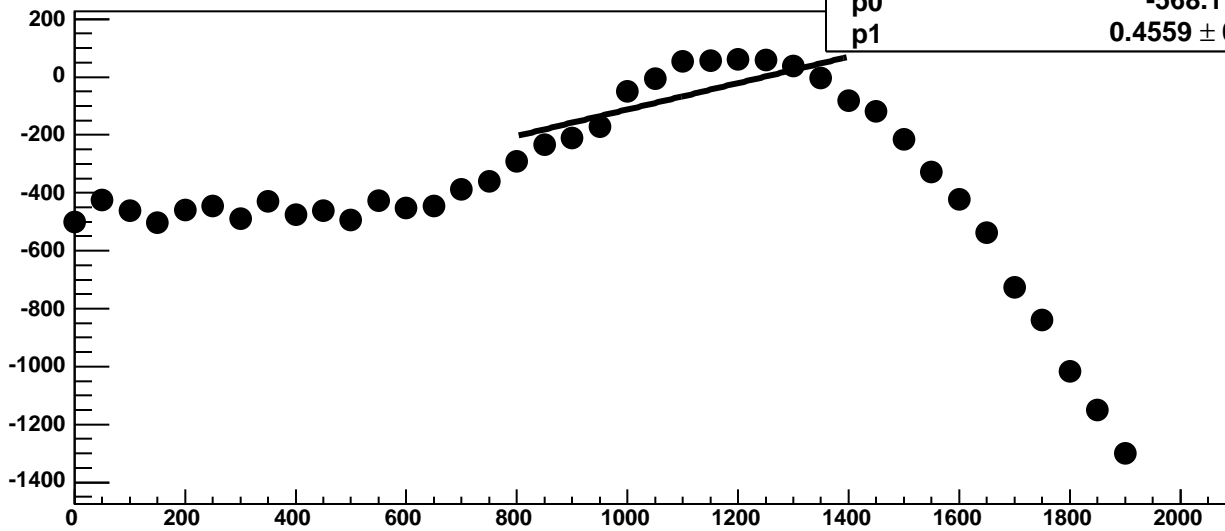
Chip 5, Channel 13, Enable 4, Hold=30, ADC Noise vs DAC



Chip 5, Channel 13, Enable 4, Hold=30, ADC Residuals vs DAC

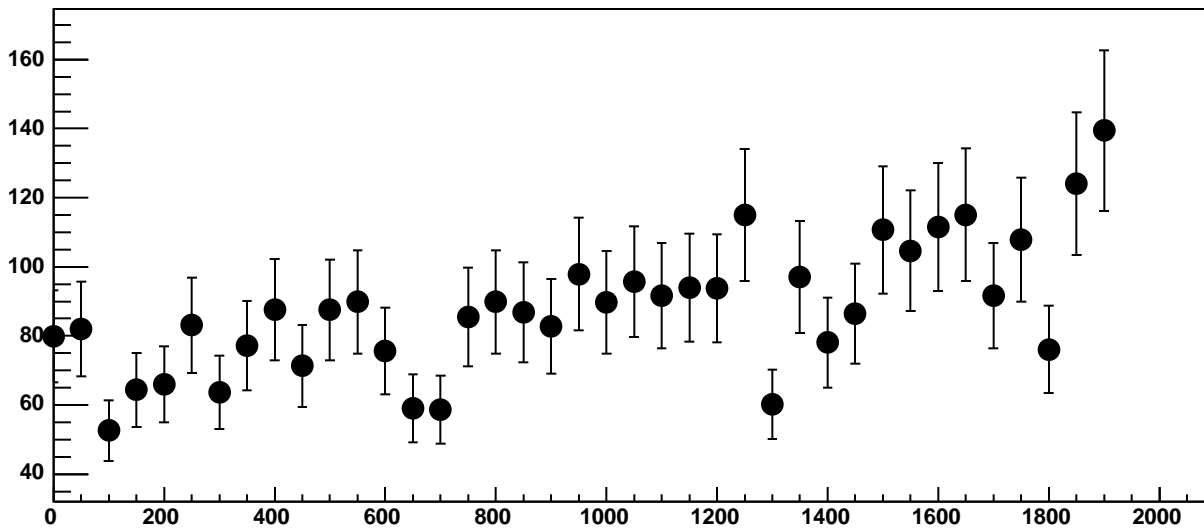


Chip 5, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC

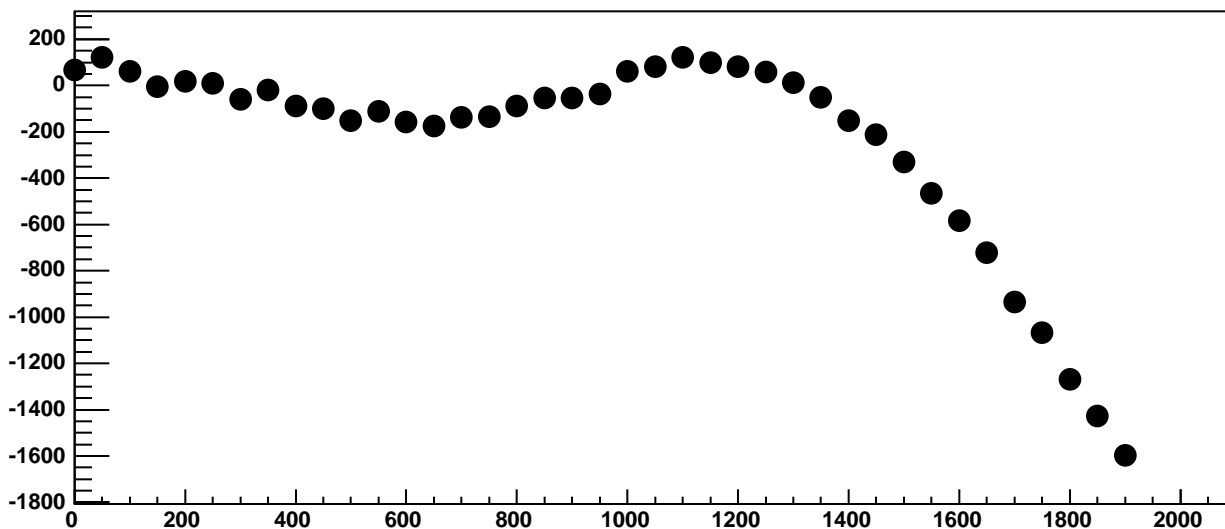


$\chi^2 / \text{ndf}$  211.5 / 11  
p0  $-568.1 \pm 32.39$   
p1  $0.4559 \pm 0.02861$

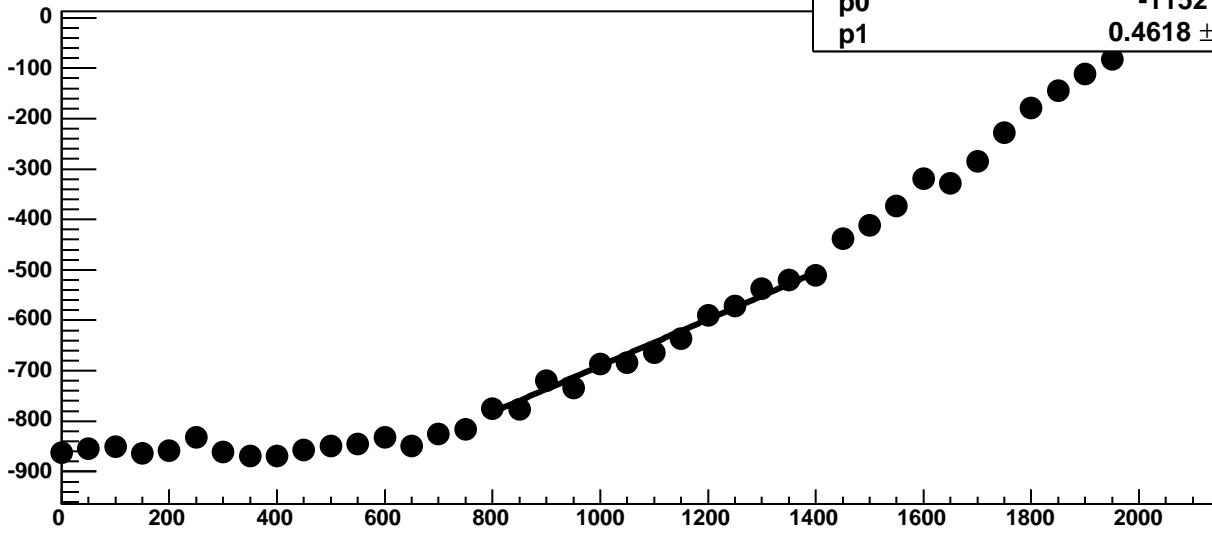
Chip 5, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC



Chip 5, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC

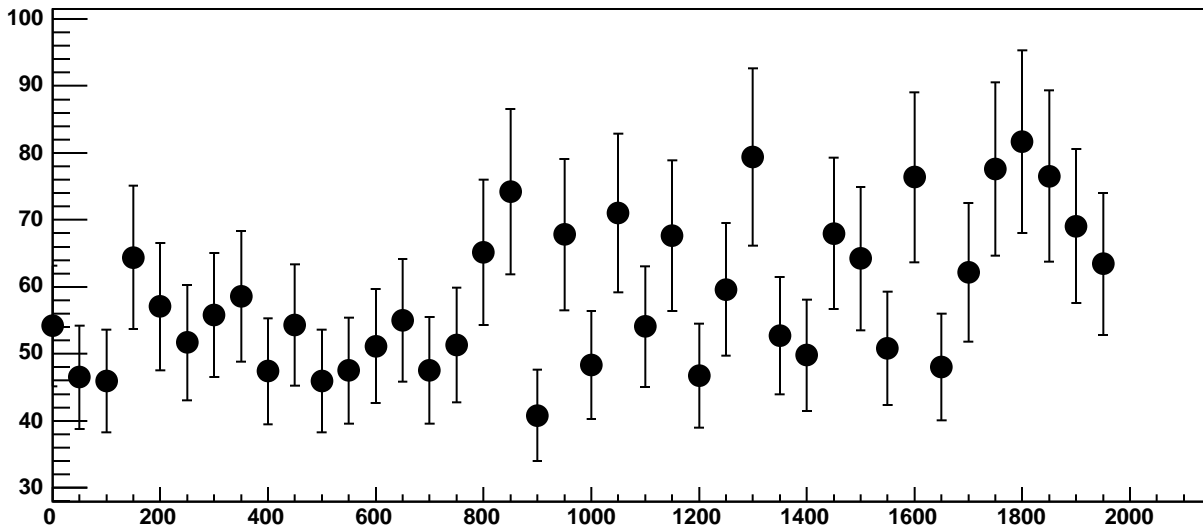


Chip 5, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC

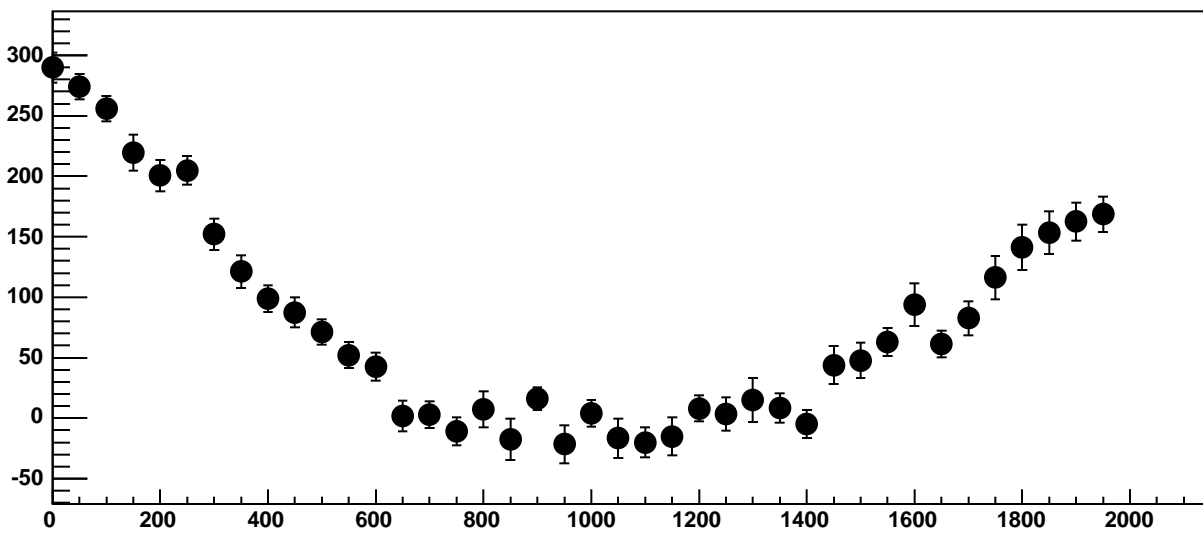


$\chi^2 / \text{ndf}$  12.96 / 11  
p0  $-1152 \pm 21.52$   
p1  $0.4618 \pm 0.0192$

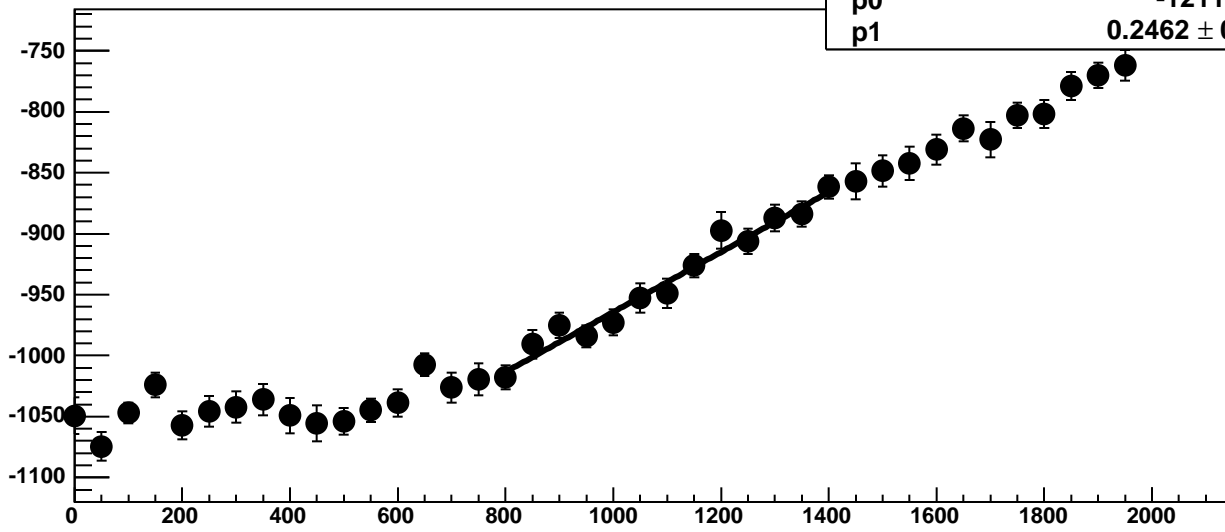
Chip 5, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC



Chip 5, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC

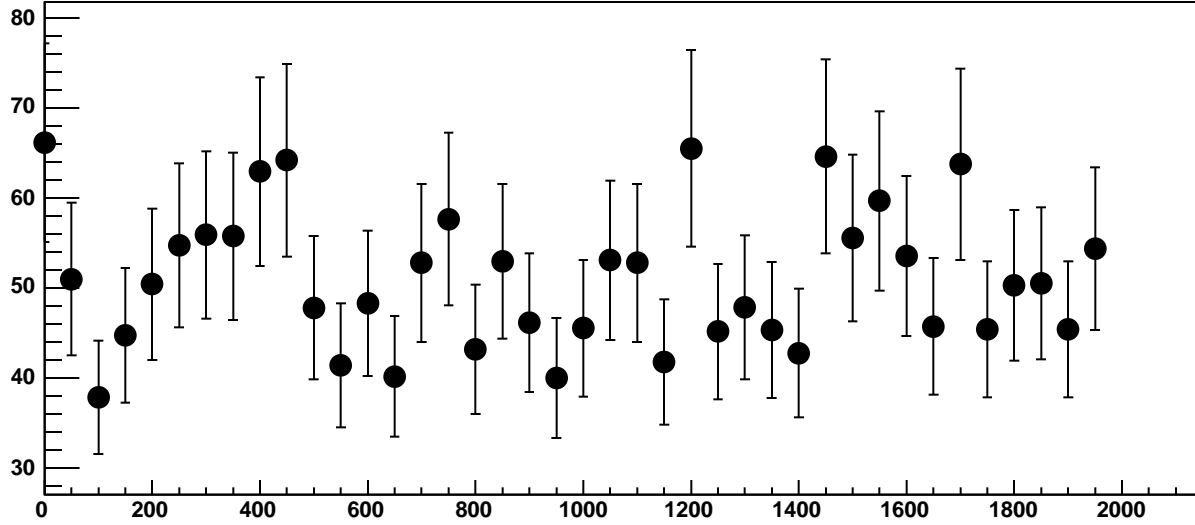


Chip 5, Channel 14, Enable 1, Hold=30, ADC Mean vs DAC

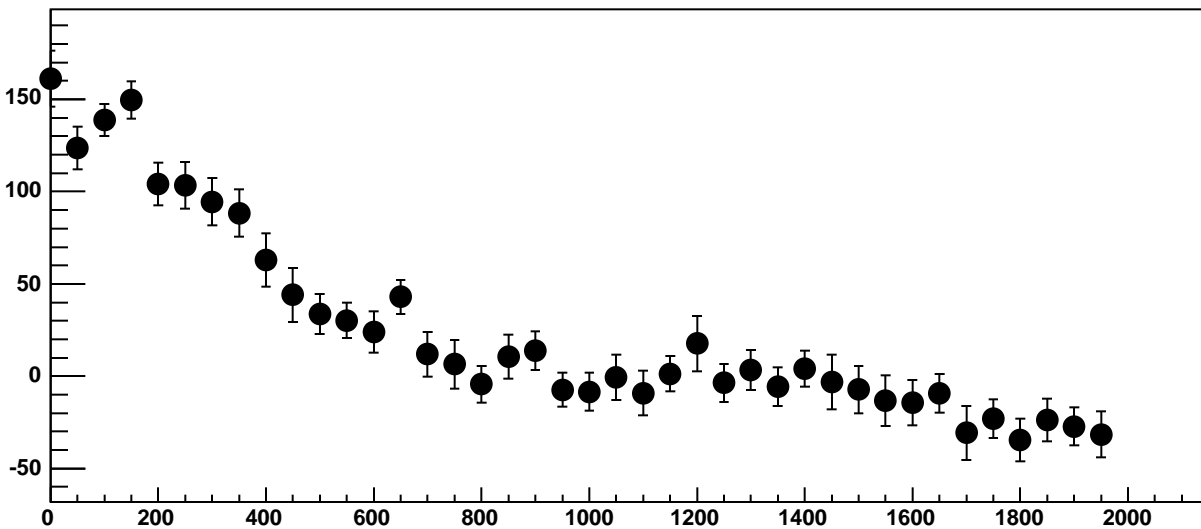


$\chi^2 / \text{ndf}$  6.658 / 11  
p0 -1211 ± 17.16  
p1 0.2462 ± 0.01539

Chip 5, Channel 14, Enable 1, Hold=30, ADC Noise vs DAC

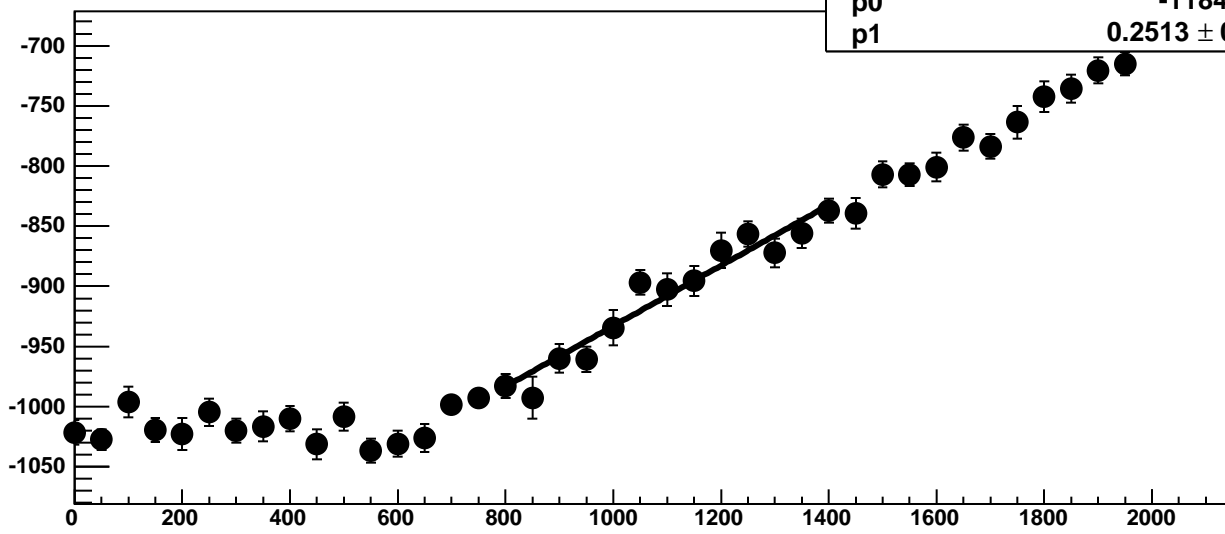


Chip 5, Channel 14, Enable 1, Hold=30, ADC Residuals vs DAC

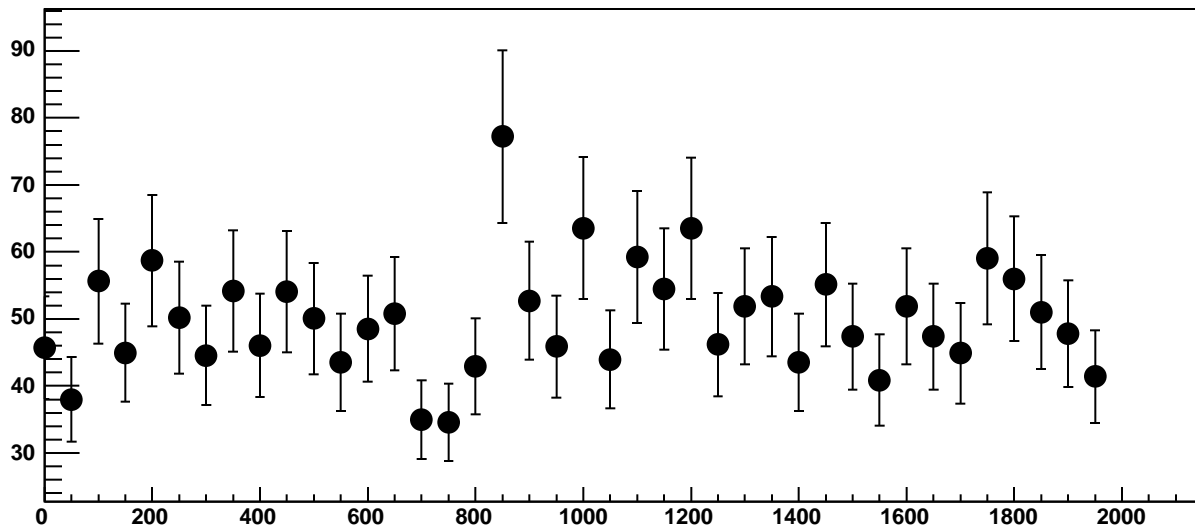




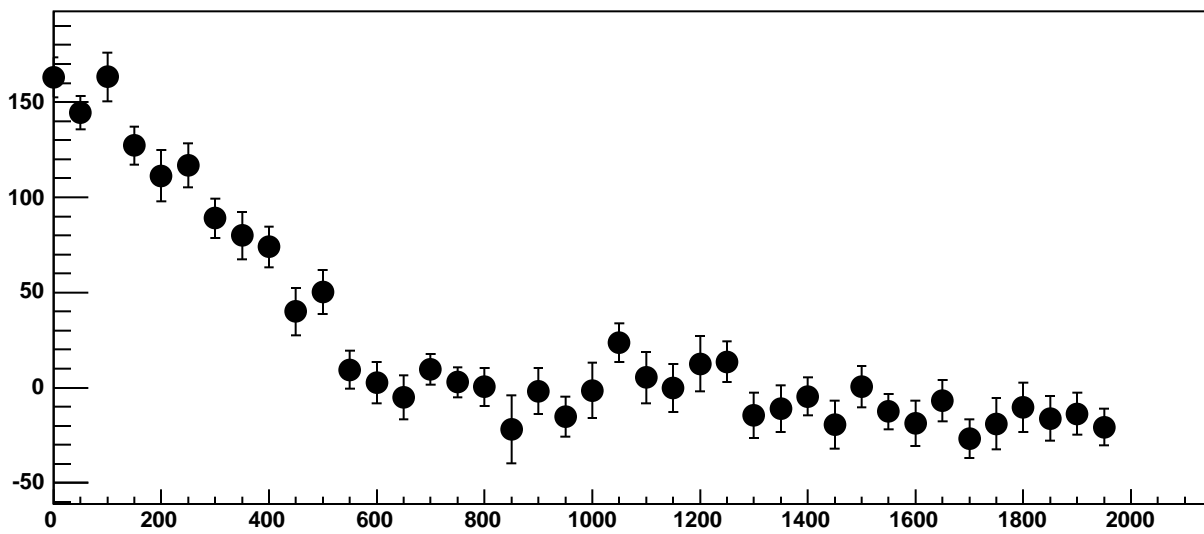
Chip 5, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC



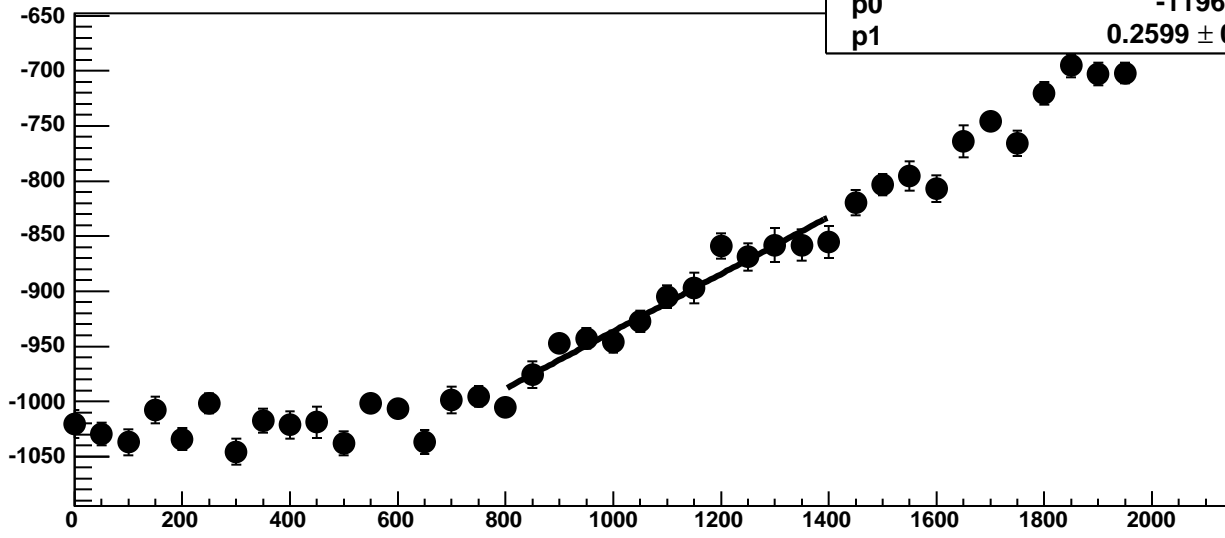
Chip 5, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC



Chip 5, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC

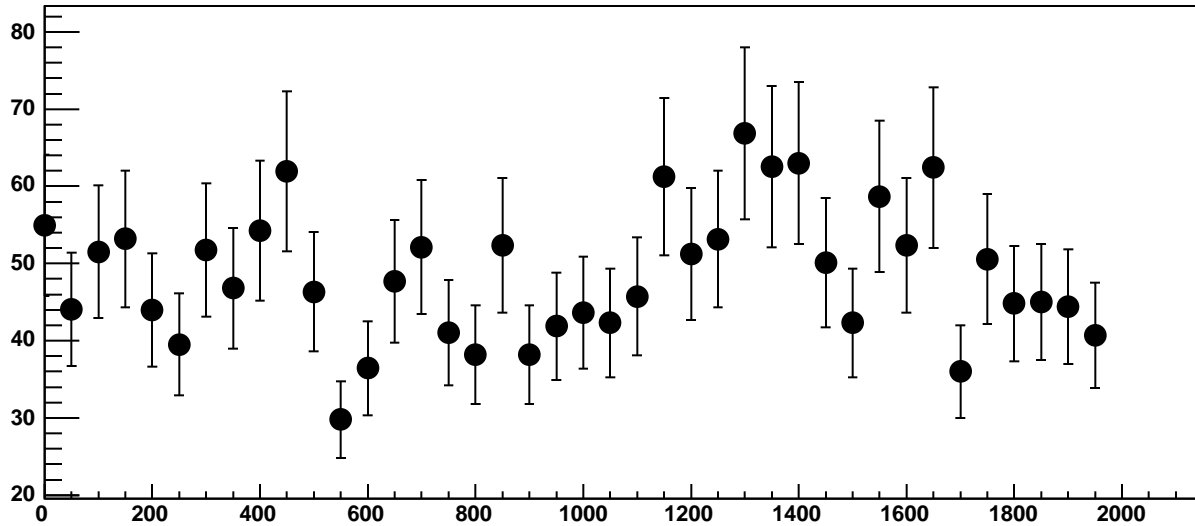


Chip 5, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC

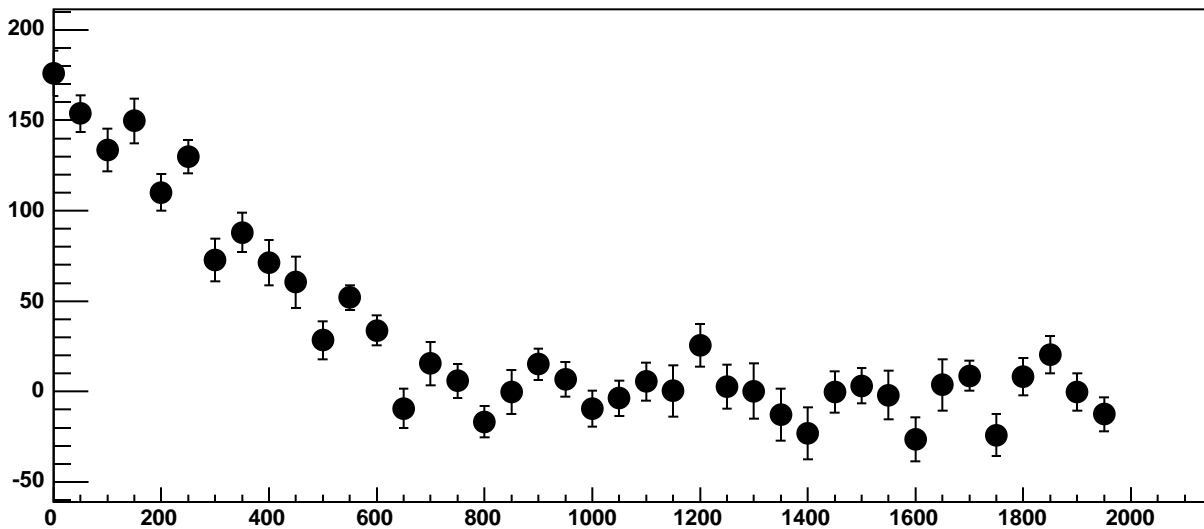


$\chi^2 / \text{ndf}$  16.44 / 11  
p0  $-1196 \pm 18.41$   
p1  $0.2599 \pm 0.01738$

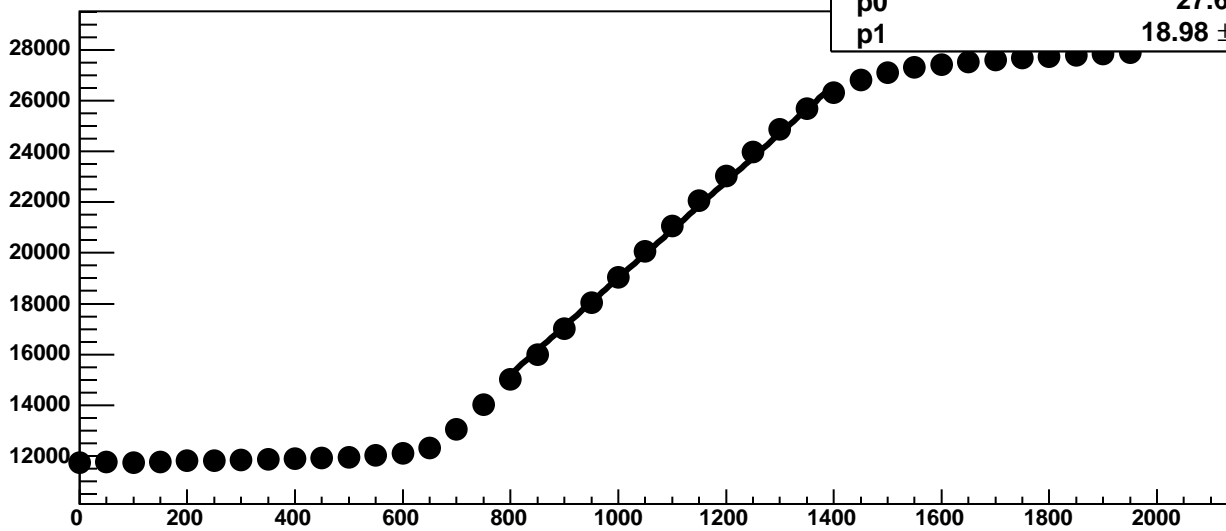
Chip 5, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC



Chip 5, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC

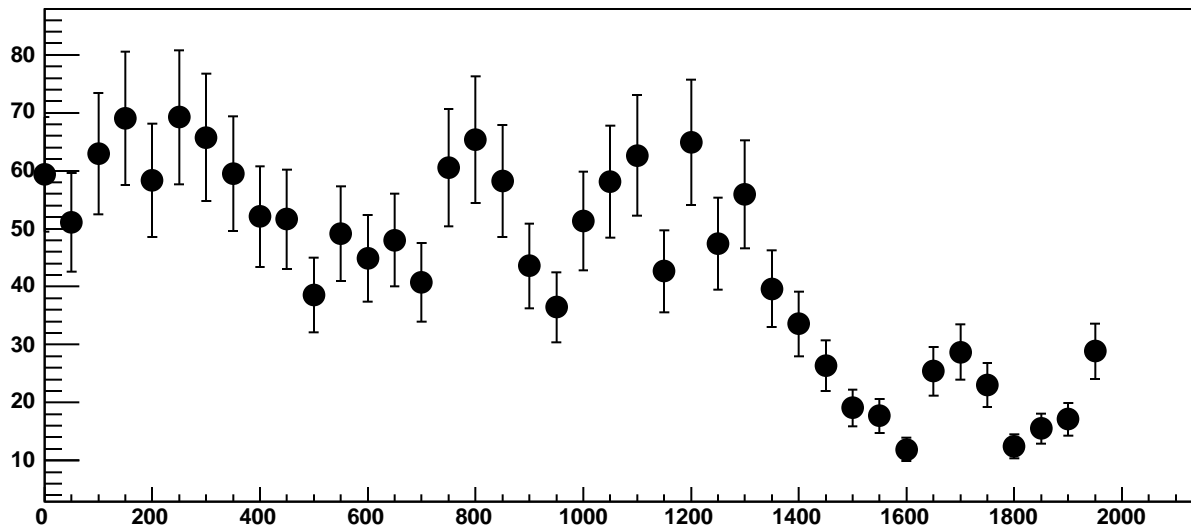


Chip 5, Channel 14, Enable 4!, Hold=30, ADC Mean vs DAC

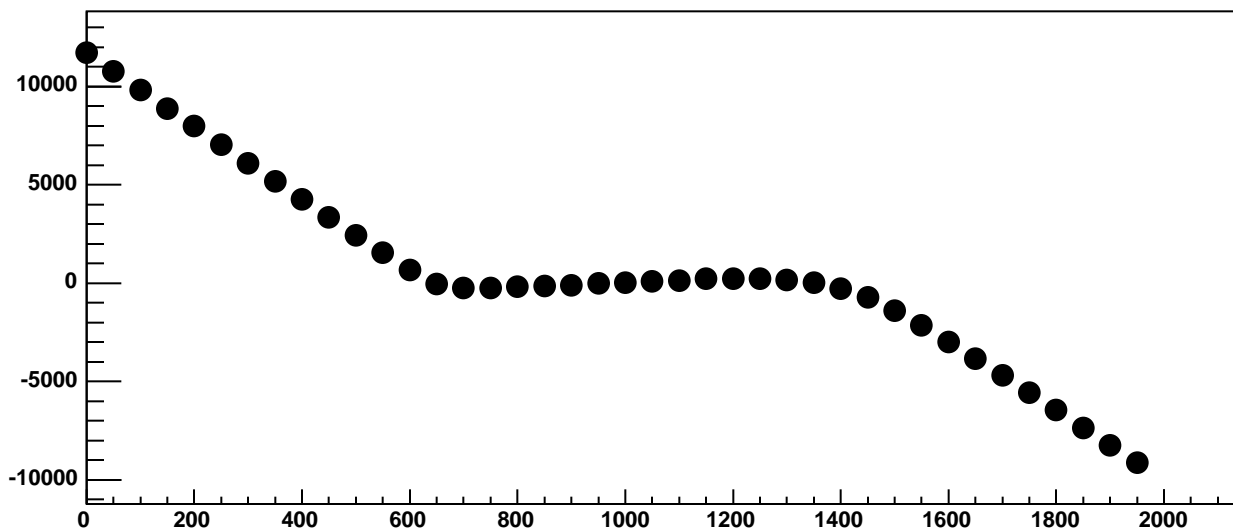


$\chi^2 / \text{ndf}$	3151 / 11
p0	$27.6 \pm 17.71$
p1	$18.98 \pm 0.0154$

Chip 5, Channel 14, Enable 4!, Hold=30, ADC Noise vs DAC

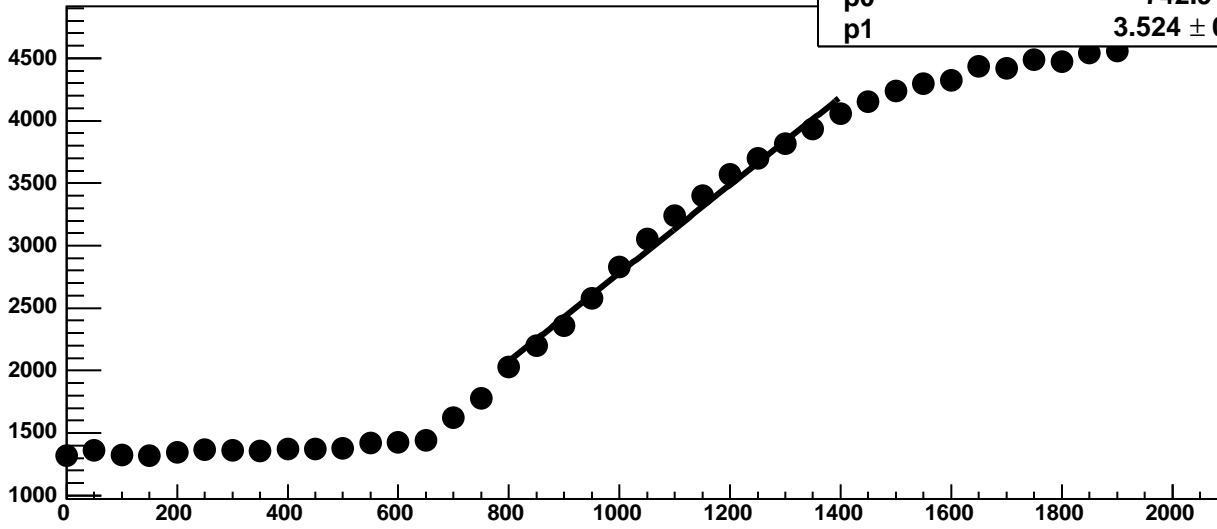


Chip 5, Channel 14, Enable 4!, Hold=30, ADC Residuals vs DAC

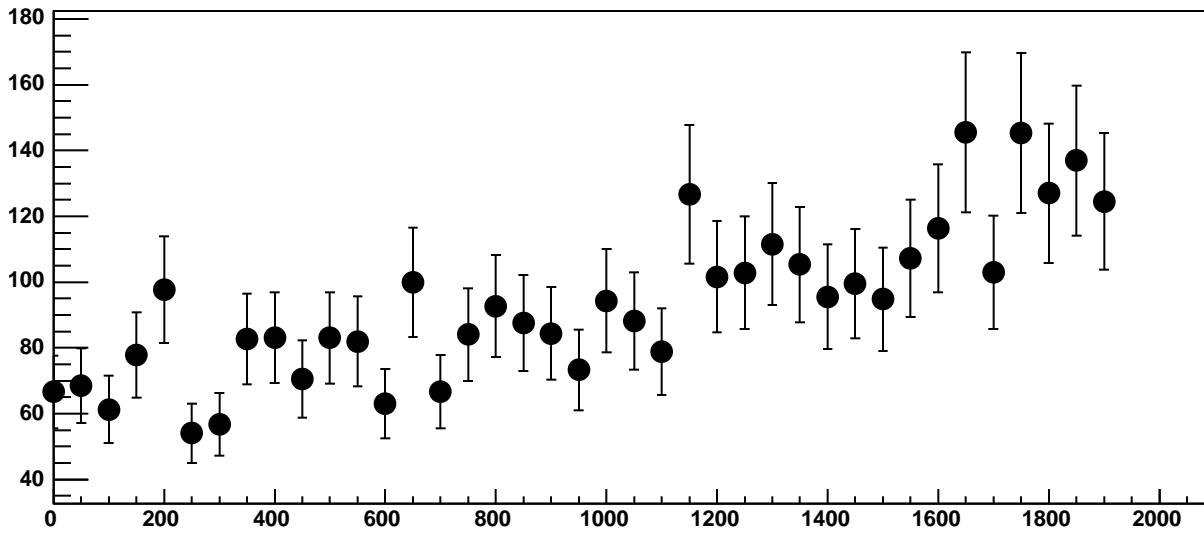


Chip 5, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC

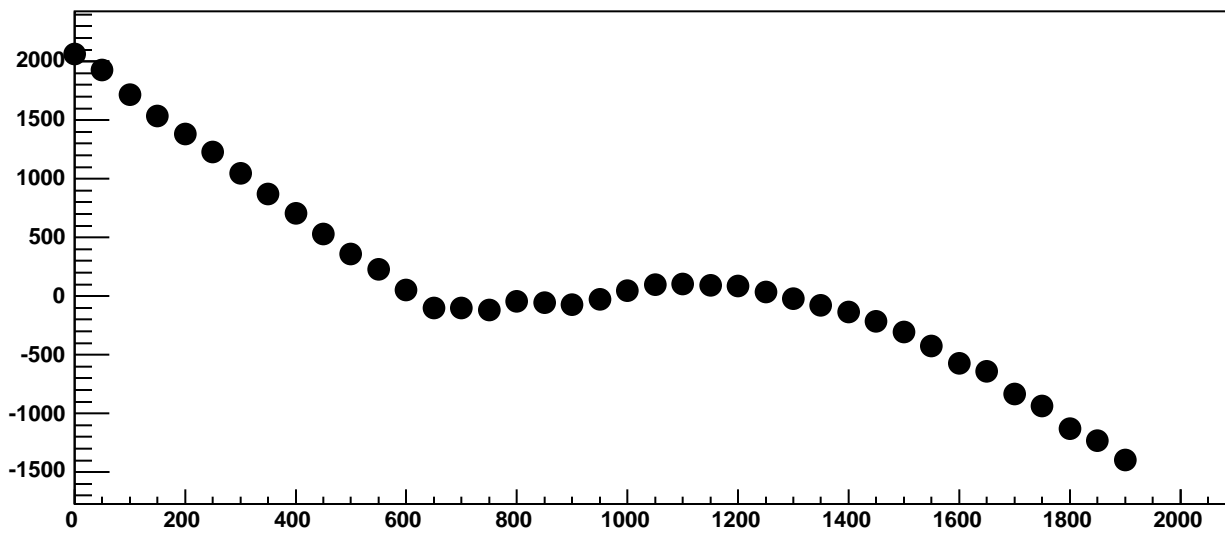
$\chi^2 / \text{ndf}$  163.6 / 11  
p0  $-742.9 \pm 35.06$   
p1  $3.524 \pm 0.03225$



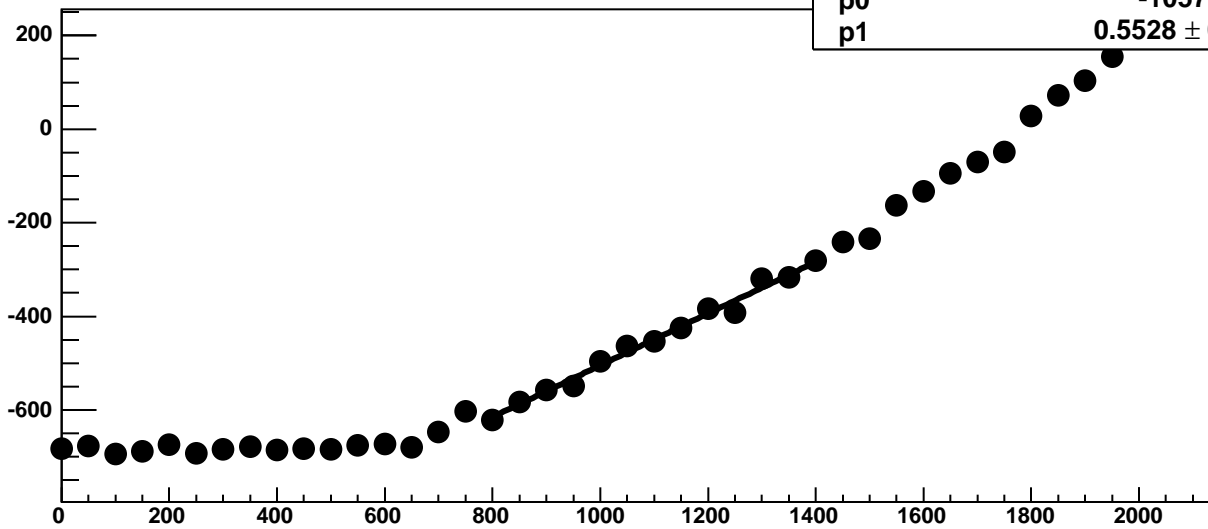
Chip 5, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC



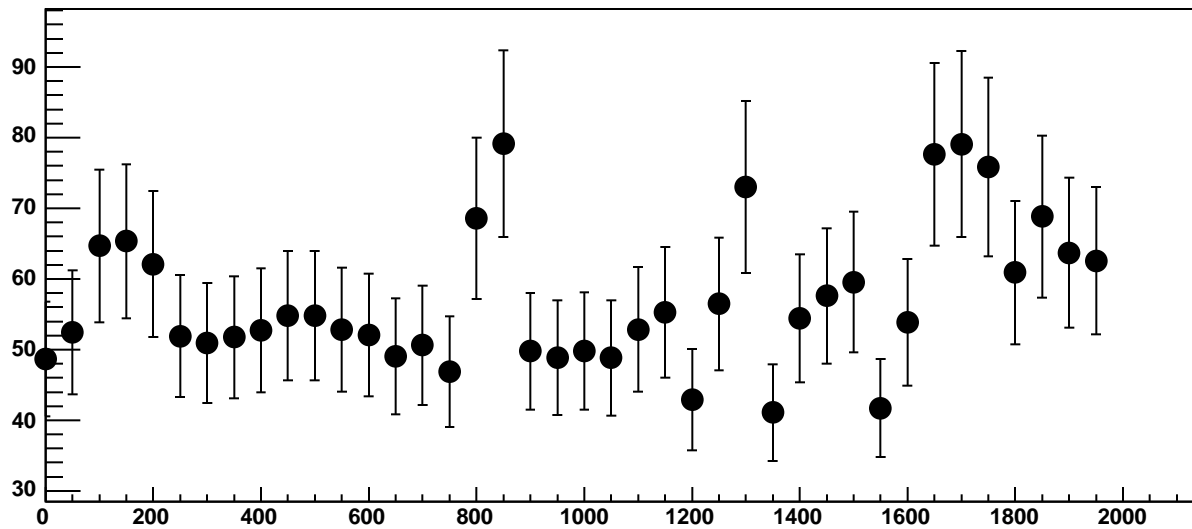
Chip 5, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC



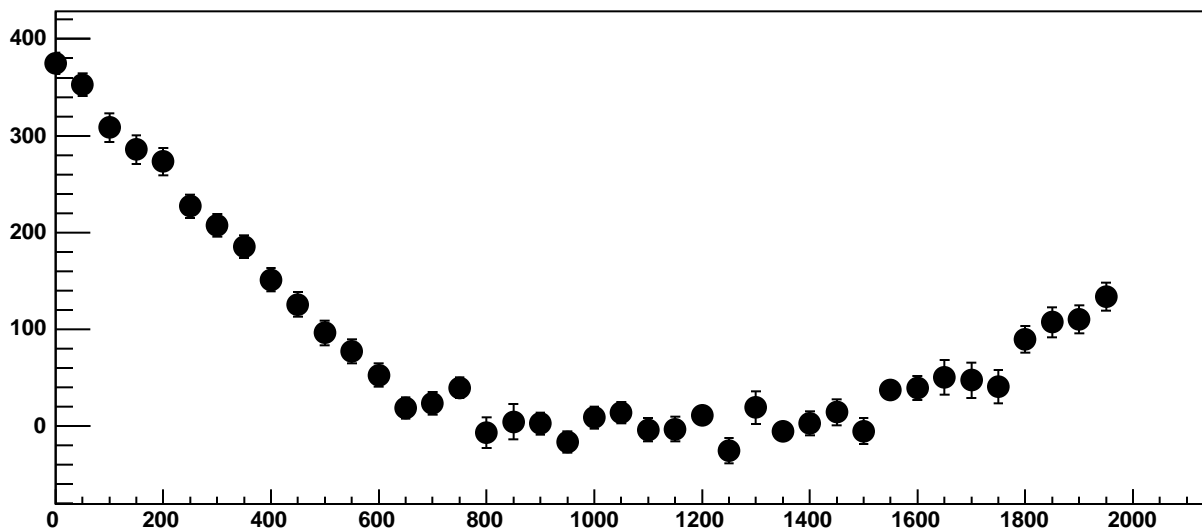
Chip 5, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC



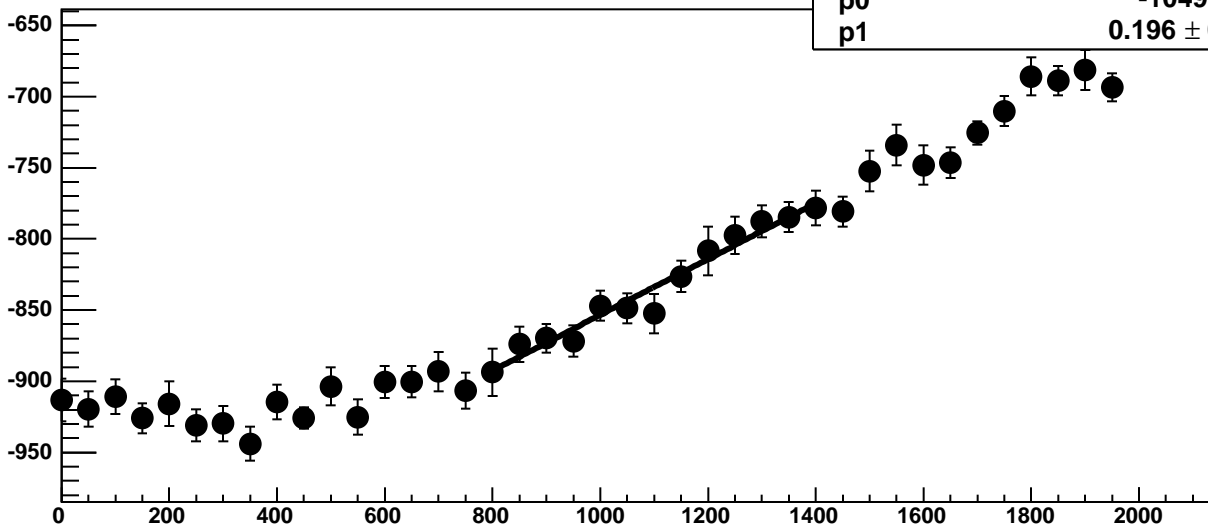
Chip 5, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC



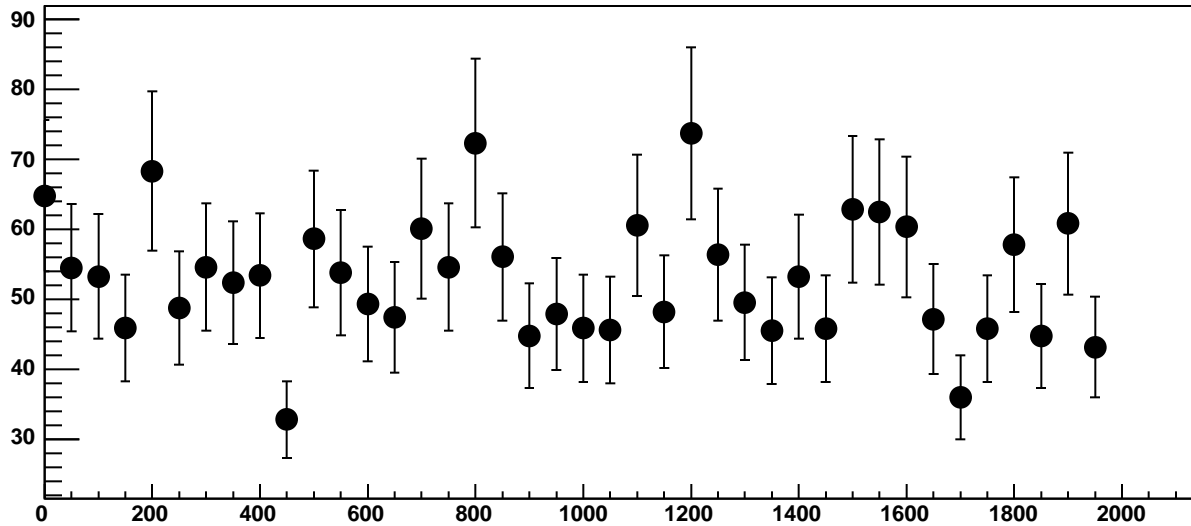
Chip 5, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC



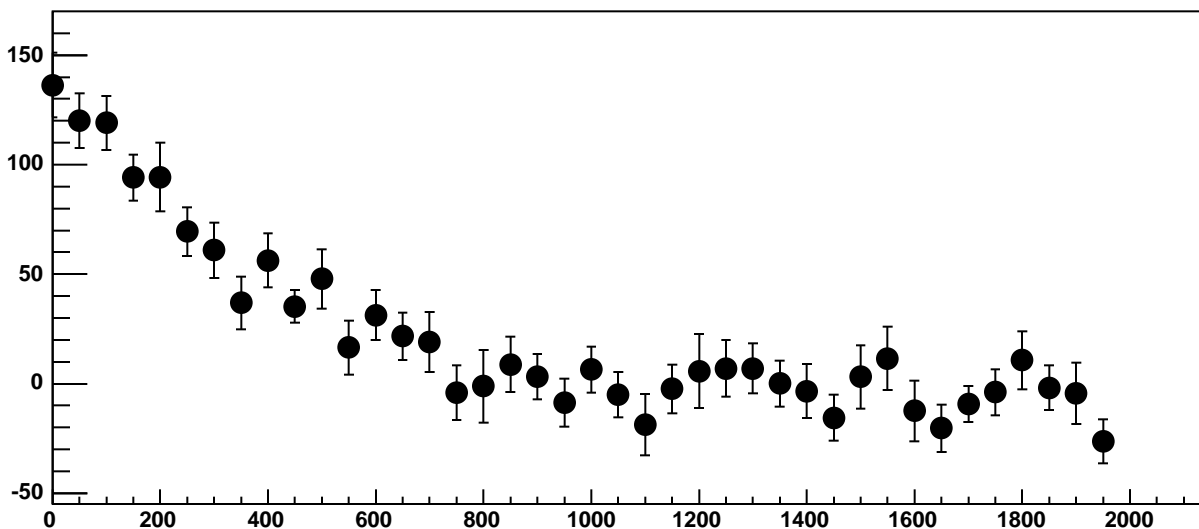
Chip 5, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC



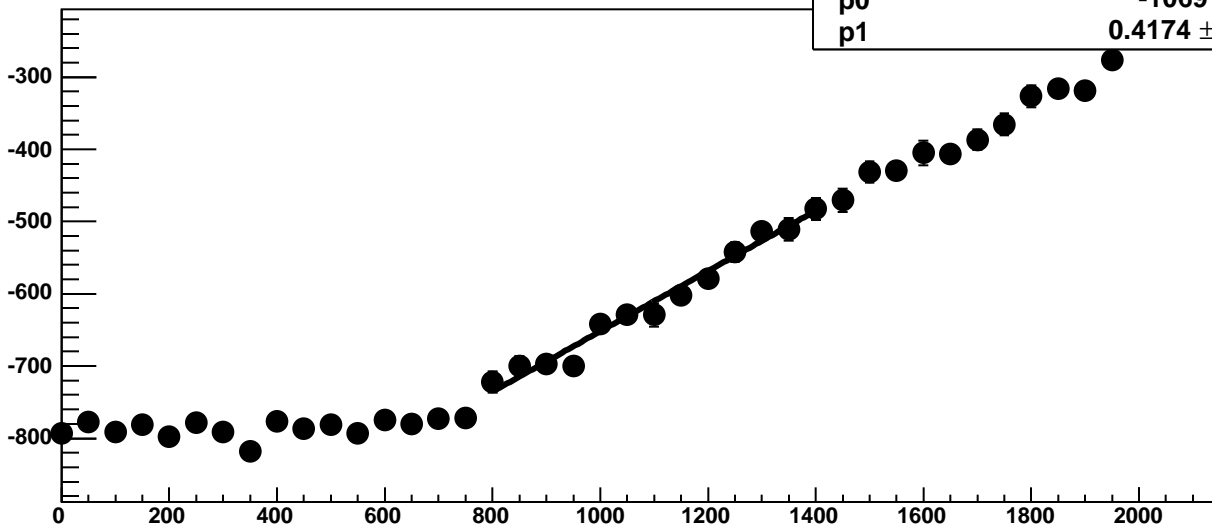
Chip 5, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC



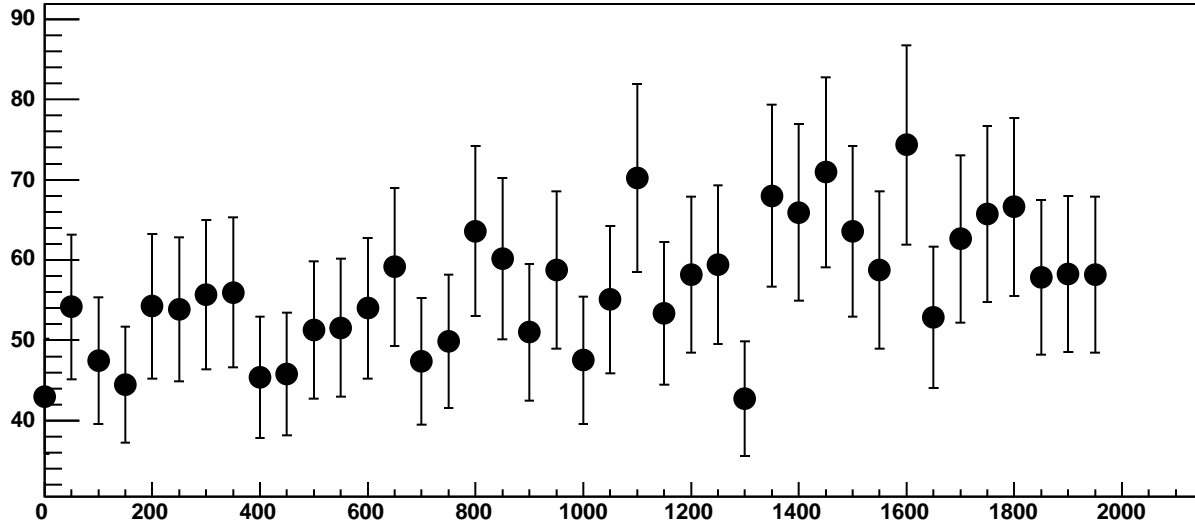
Chip 5, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC



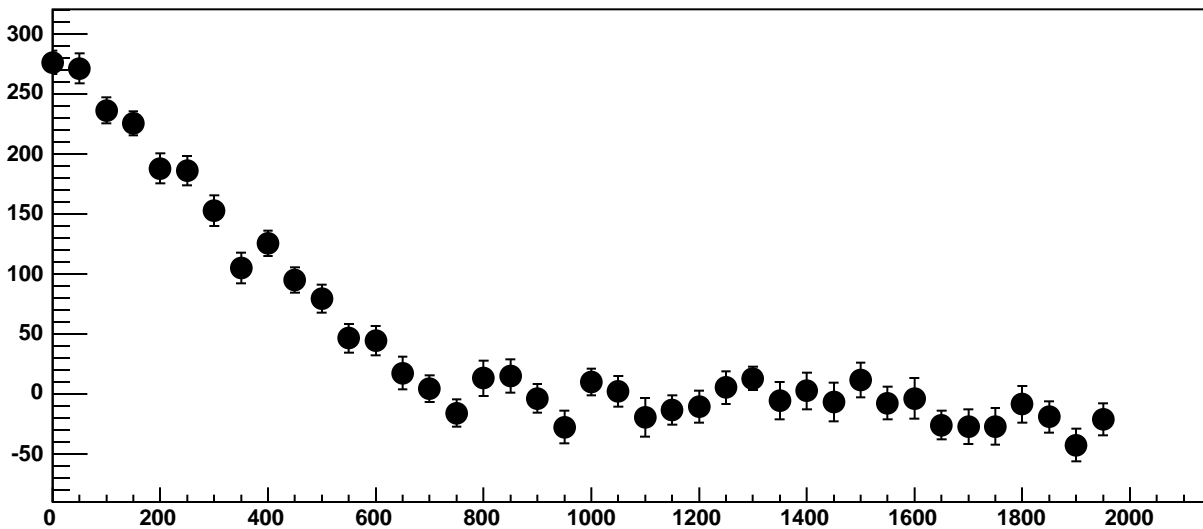
Chip 5, Channel 15, Enable 2, Hold=30, ADC Mean vs DAC



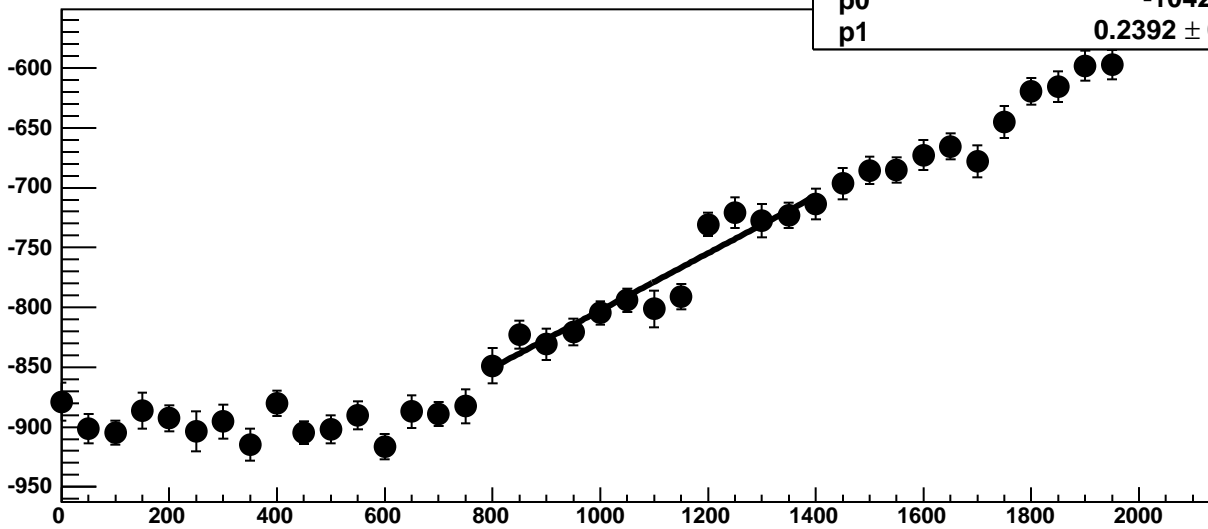
Chip 5, Channel 15, Enable 2, Hold=30, ADC Noise vs DAC



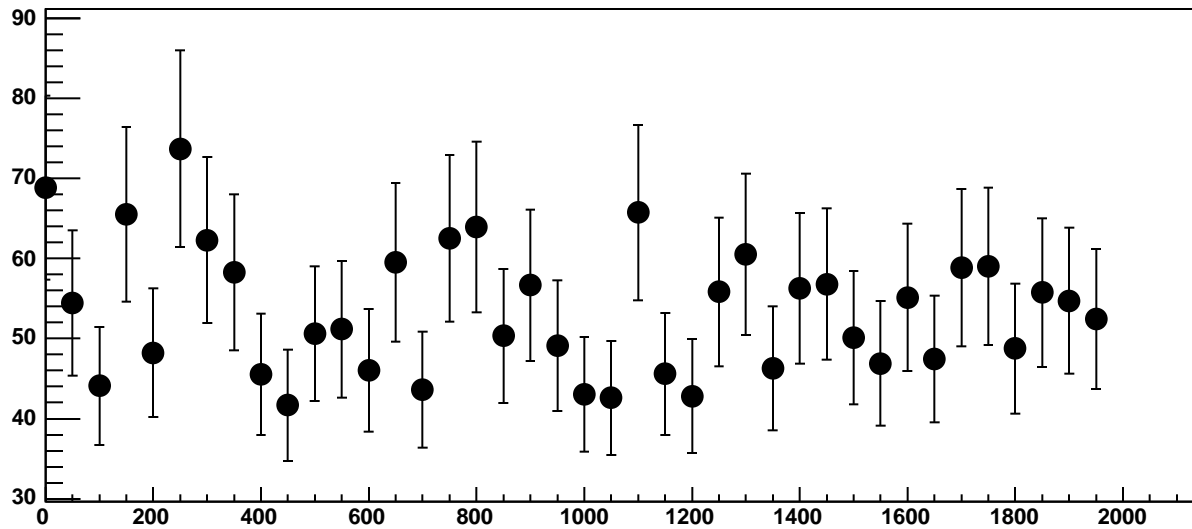
Chip 5, Channel 15, Enable 2, Hold=30, ADC Residuals vs DAC



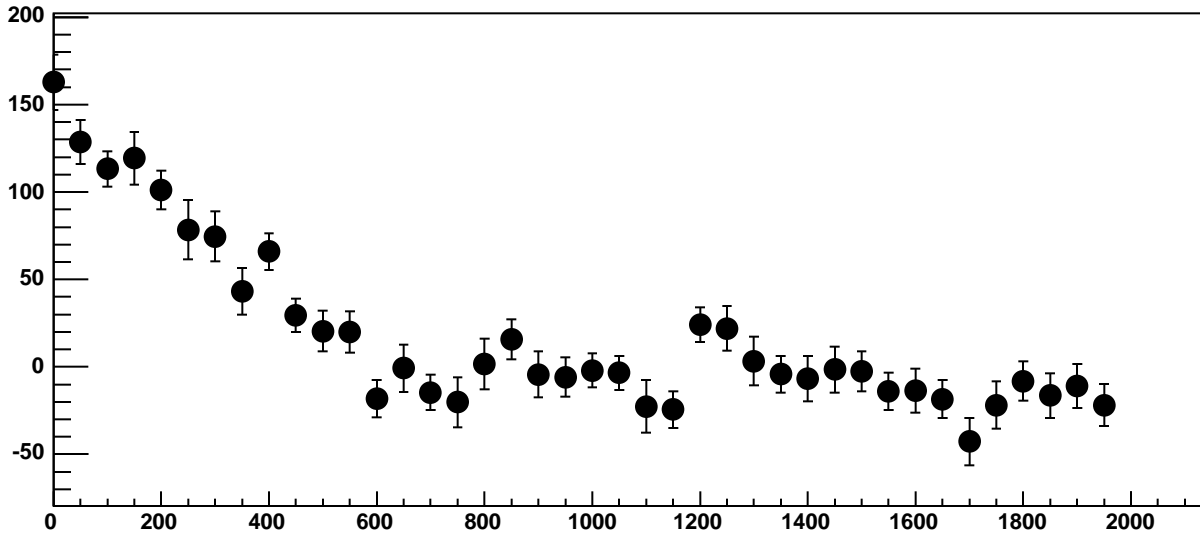
Chip 5, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC



Chip 5, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC

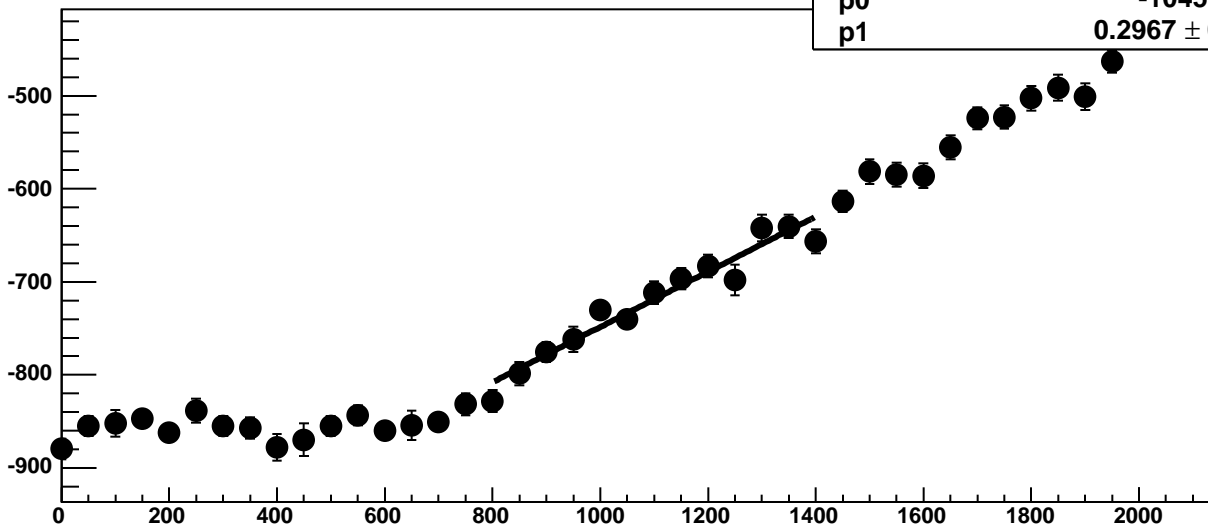


Chip 5, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC



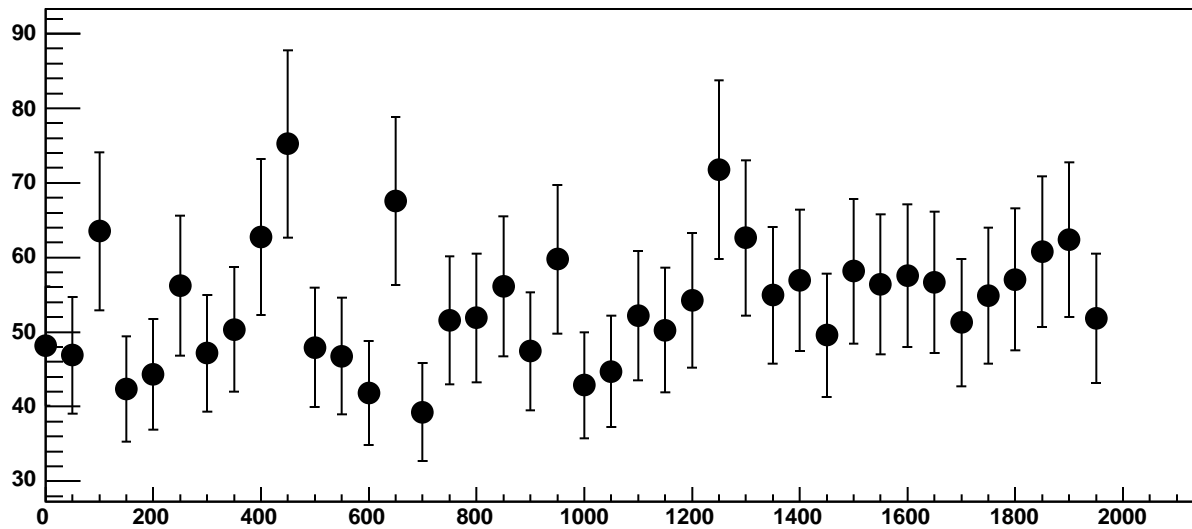


Chip 5, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC

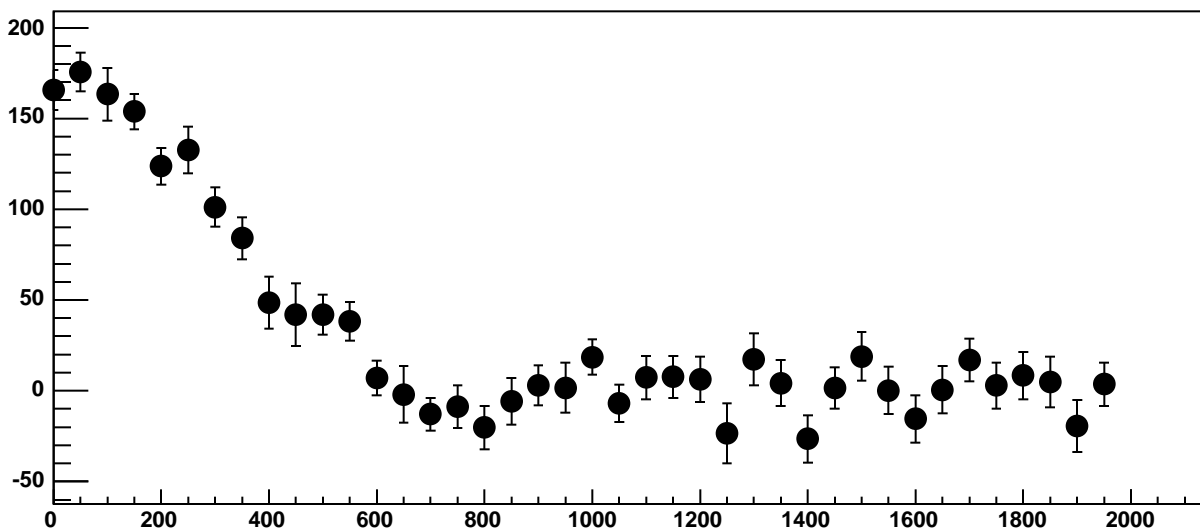


$\chi^2 / \text{ndf}$  15.93 / 11  
p0  $-1045 \pm 20.53$   
p1  $0.2967 \pm 0.01875$

Chip 5, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC



Chip 5, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC



Chip 5, Channel 15, Enable 5!, Hold=30, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

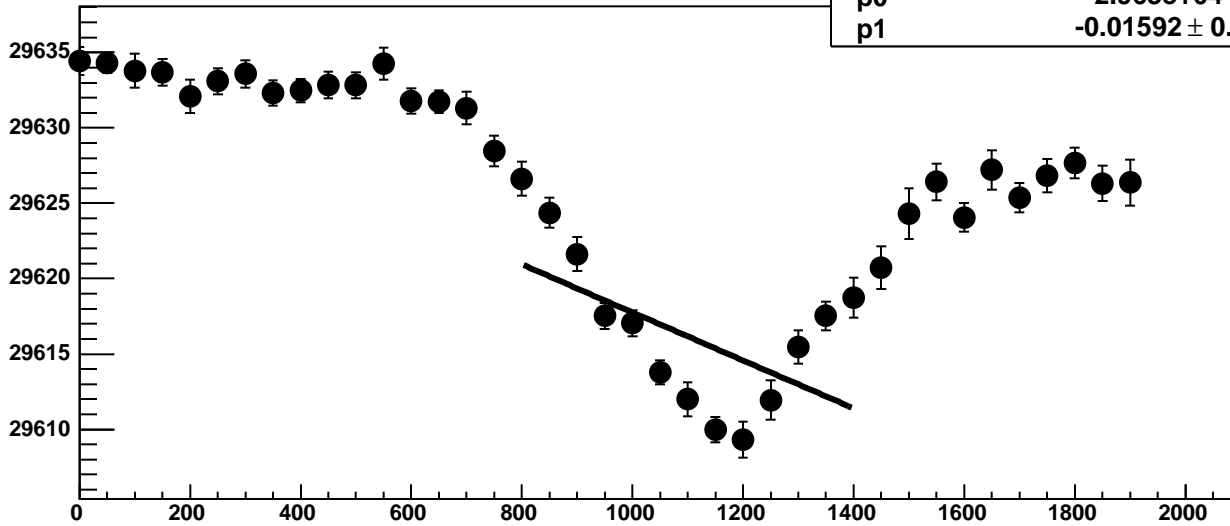
209.4 / 11

p0

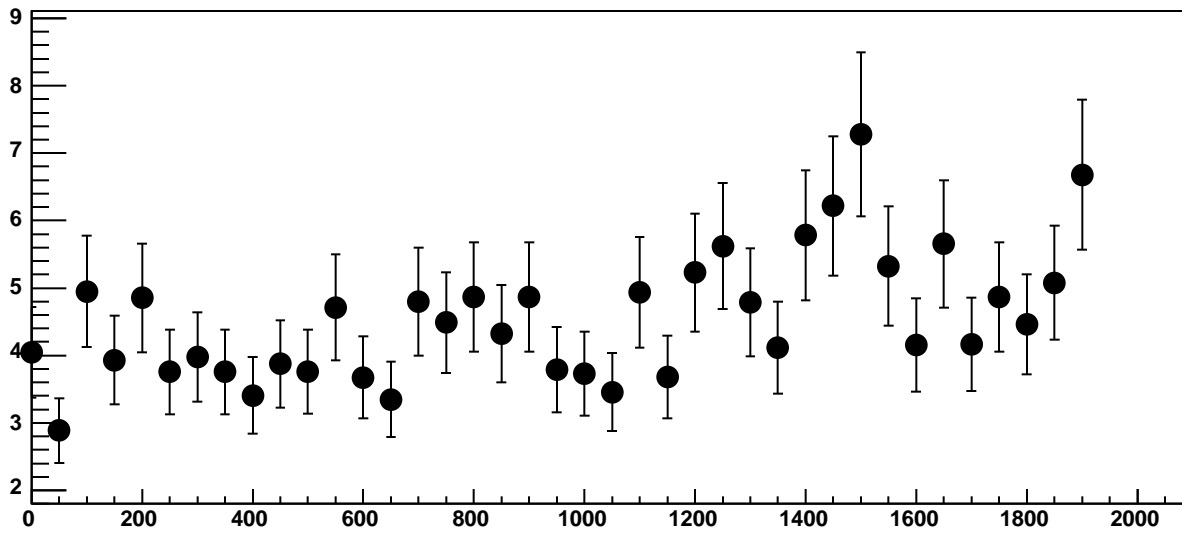
$2.963e+04 \pm 1.757$

p1

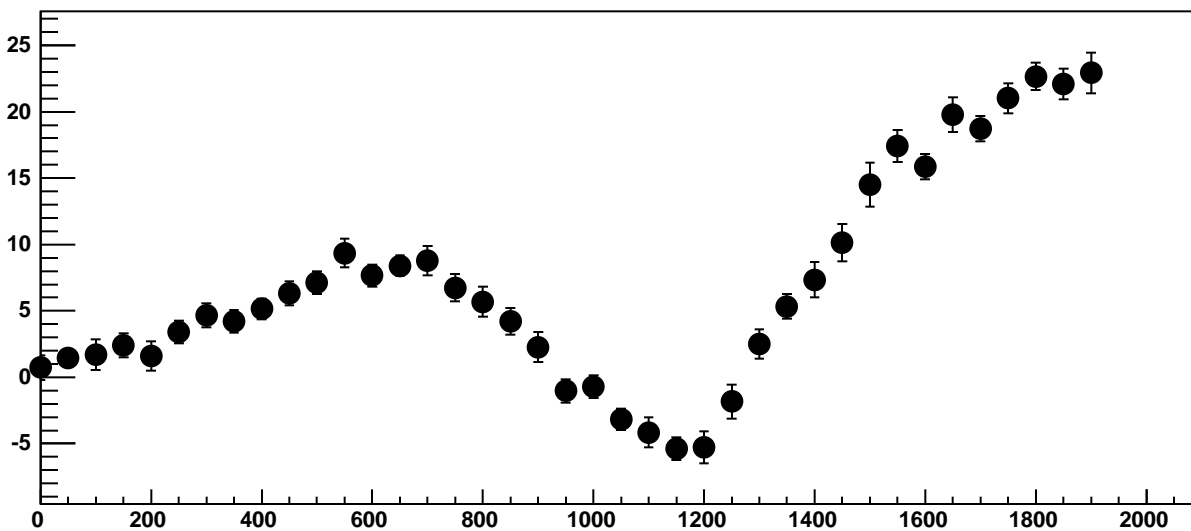
$-0.01592 \pm 0.001602$



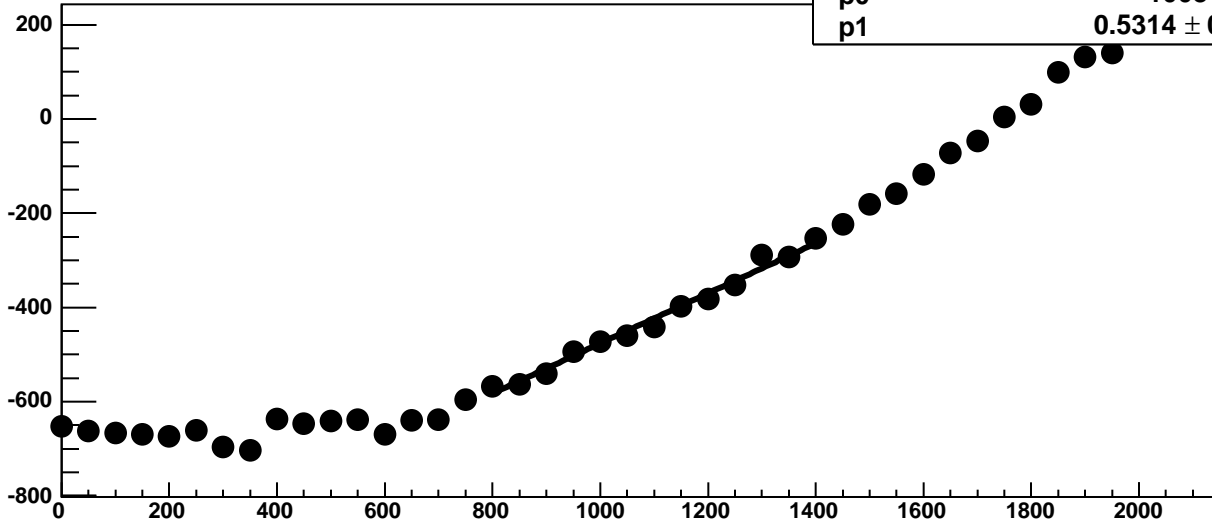
Chip 5, Channel 15, Enable 5!, Hold=30, ADC Noise vs DAC



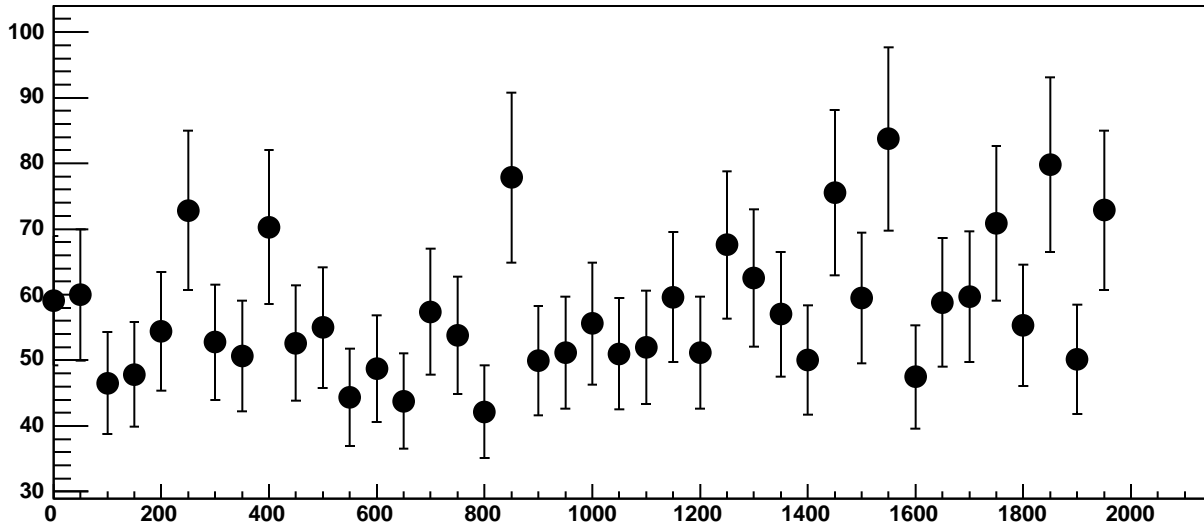
Chip 5, Channel 15, Enable 5!, Hold=30, ADC Residuals vs DAC



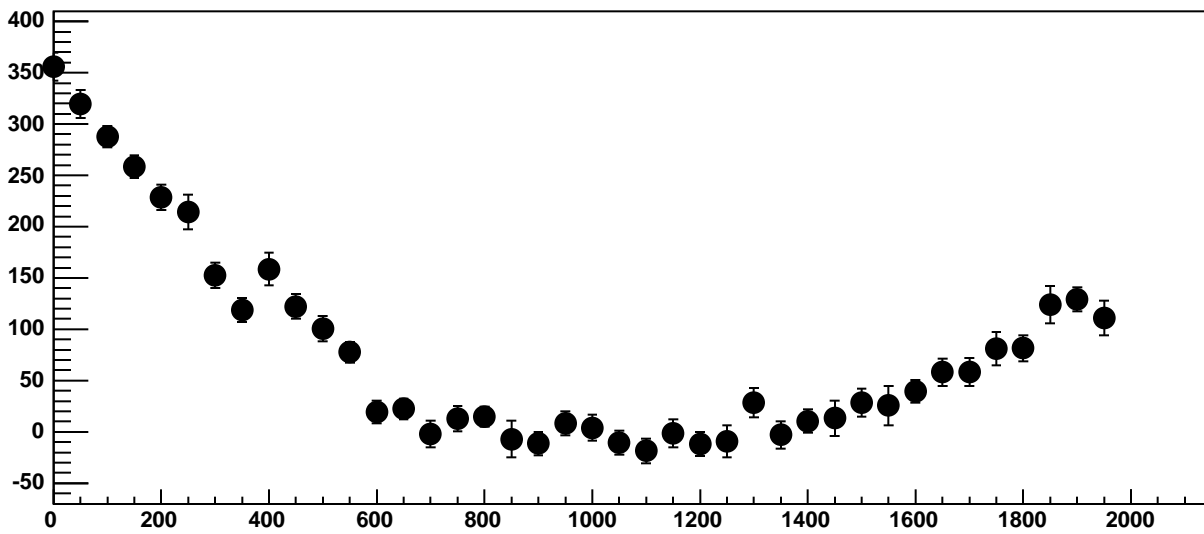
Chip 5, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC



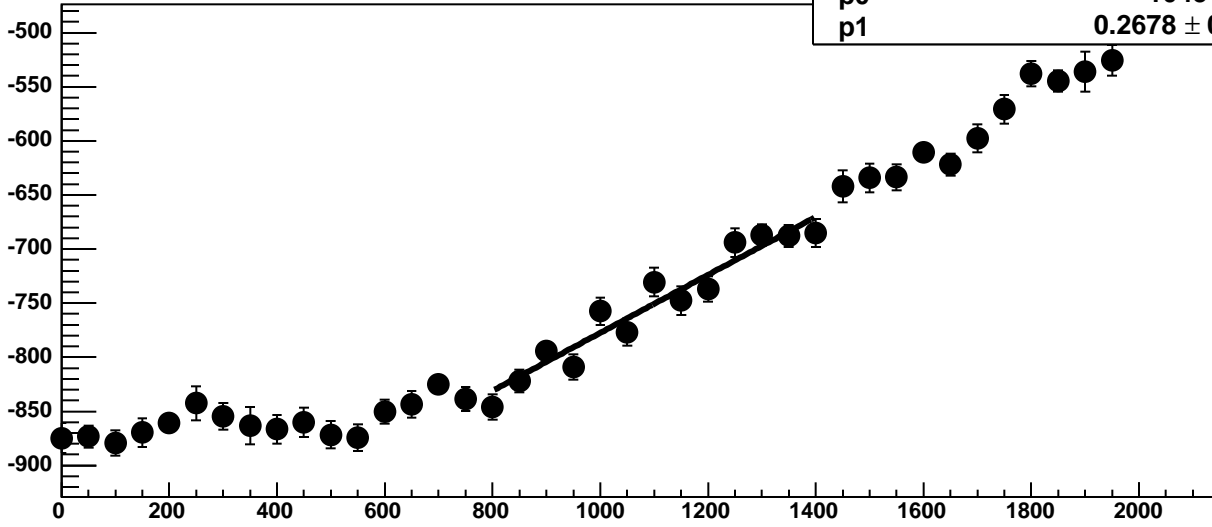
Chip 5, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC



Chip 5, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC

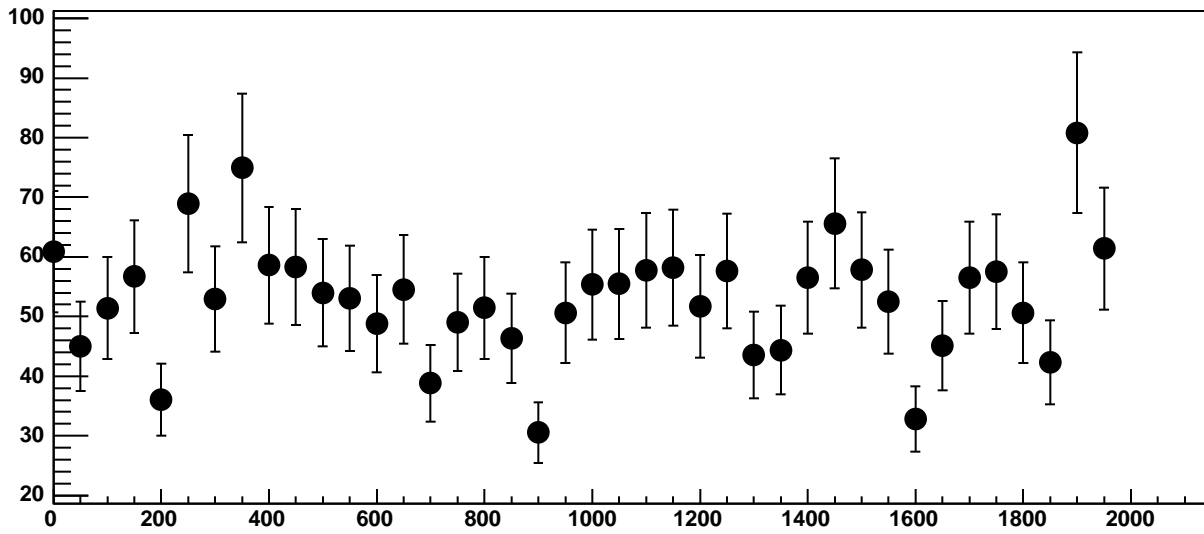


Chip 5, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC

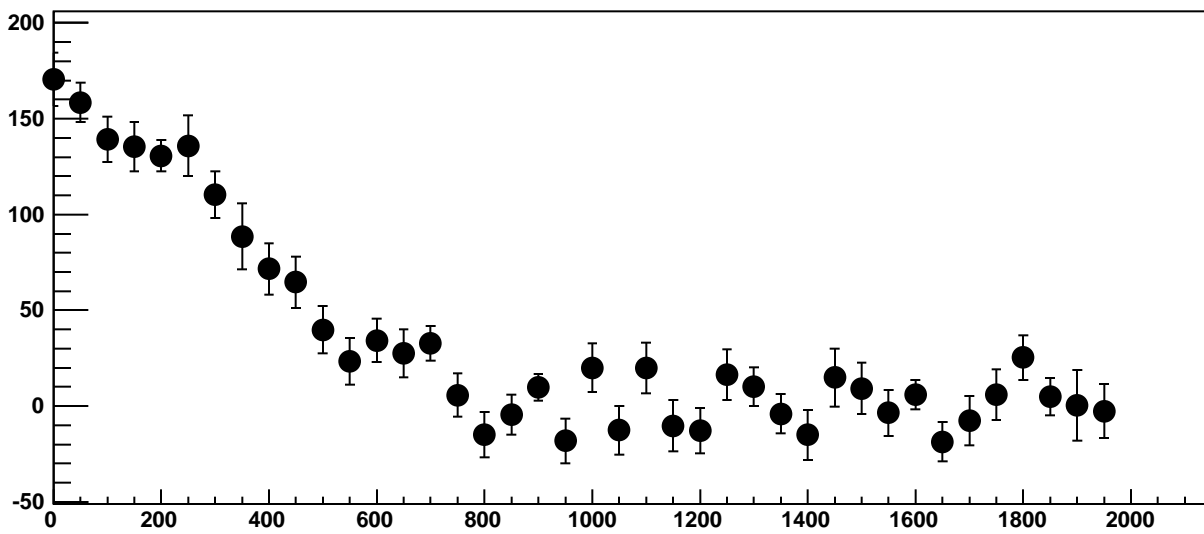


$\chi^2 / \text{ndf}$  17.85 / 11  
p0  $-1045 \pm 17.34$   
p1  $0.2678 \pm 0.01586$

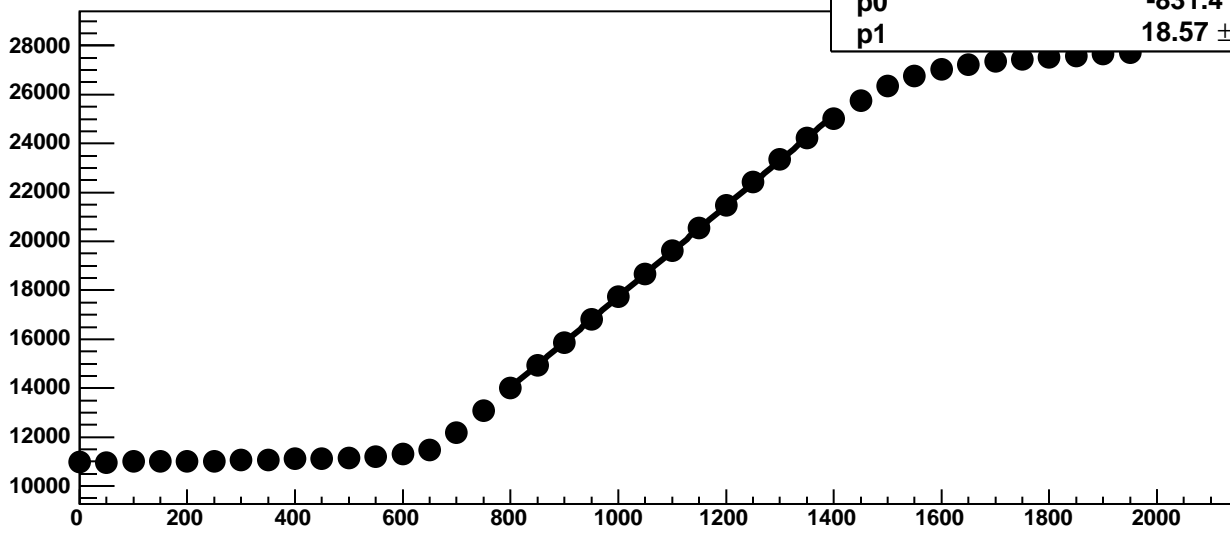
Chip 5, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC



Chip 5, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC

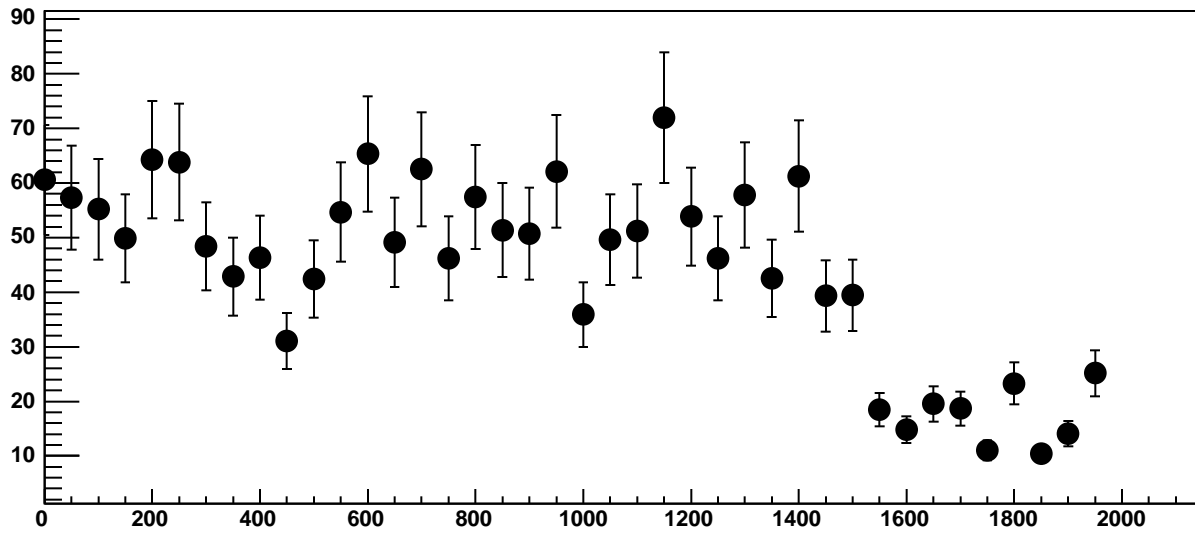


Chip 5, Channel 16, Enable 2!, Hold=30, ADC Mean vs DAC

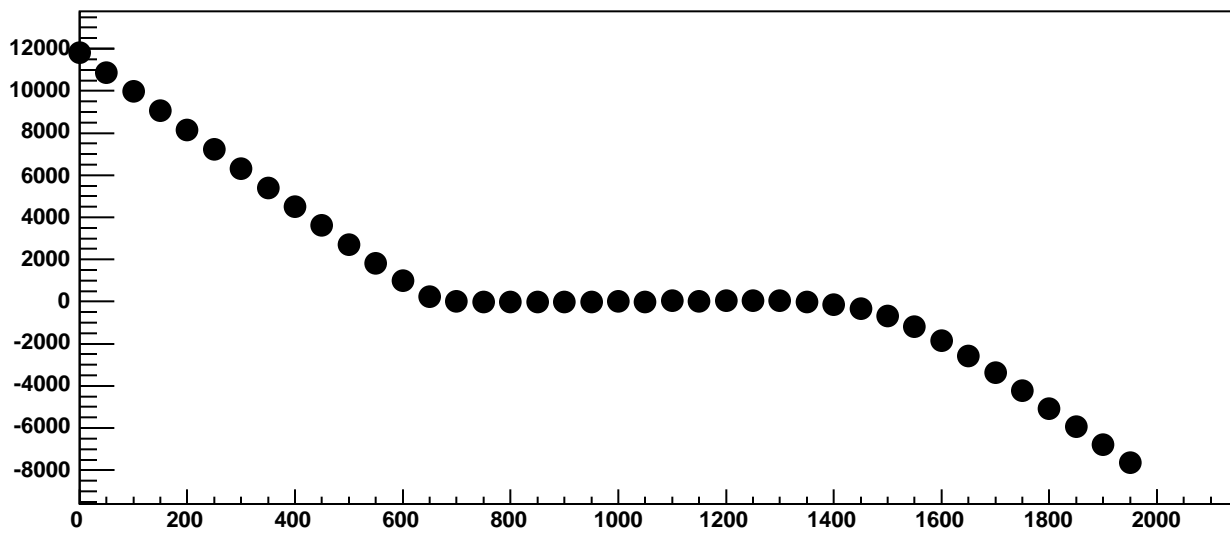


$\chi^2 / \text{ndf}$  164.5 / 11  
p0  $-831.4 \pm 19.82$   
p1  $18.57 \pm 0.0178$

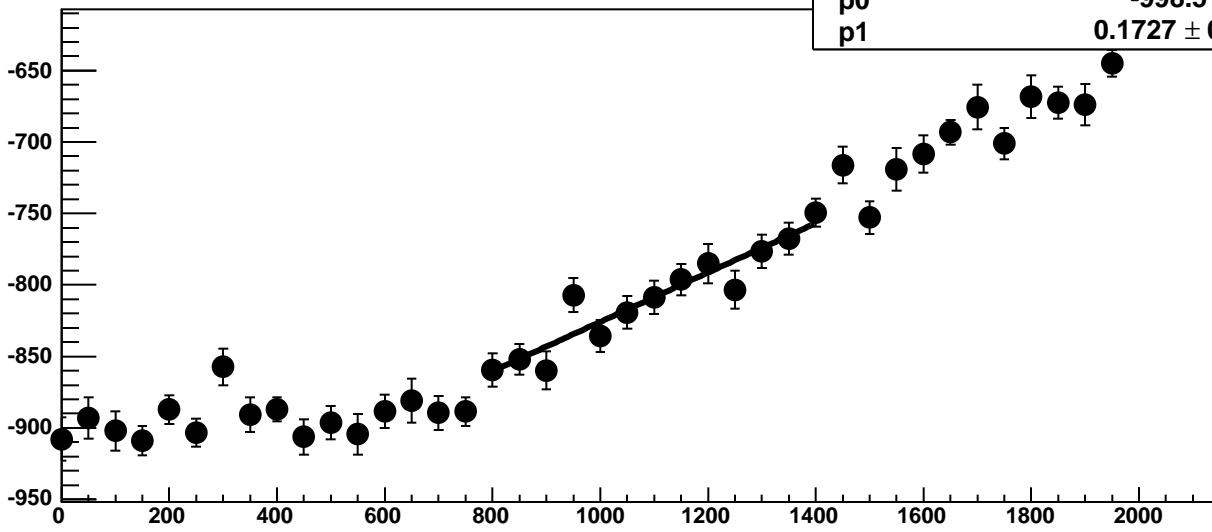
Chip 5, Channel 16, Enable 2!, Hold=30, ADC Noise vs DAC



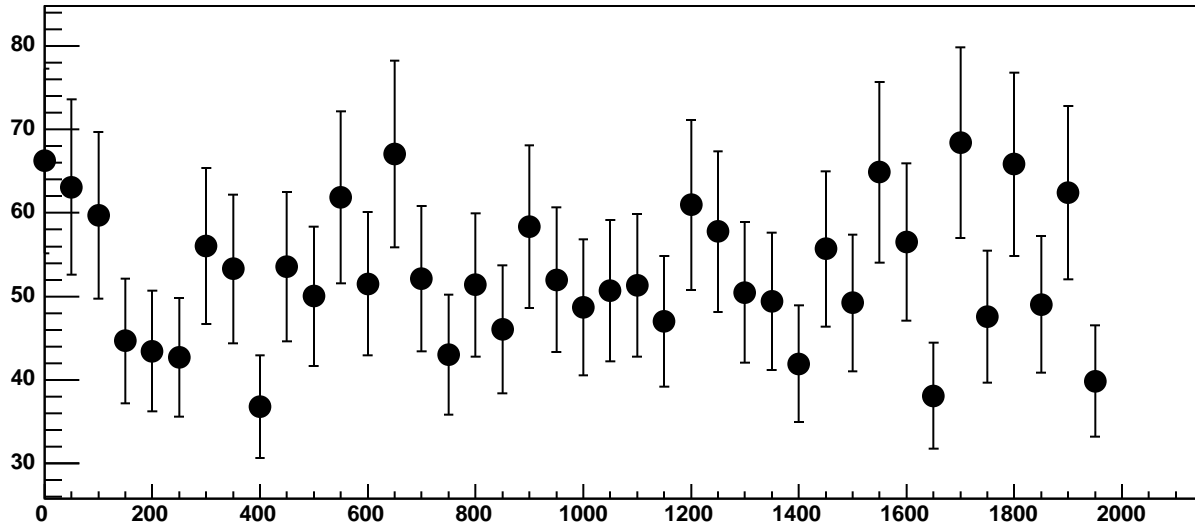
Chip 5, Channel 16, Enable 2!, Hold=30, ADC Residuals vs DAC



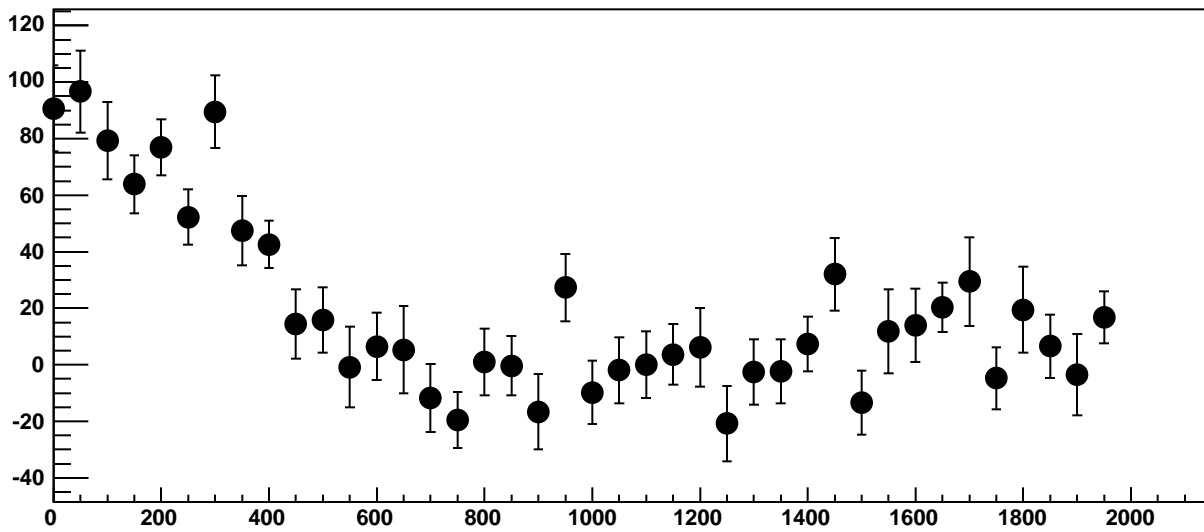
Chip 5, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC



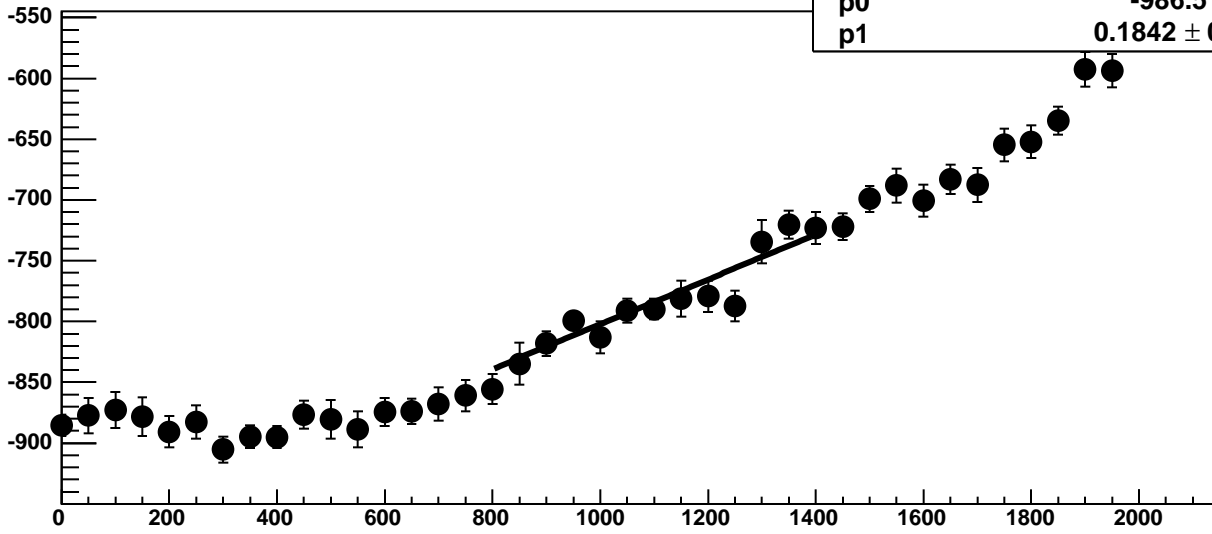
Chip 5, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC



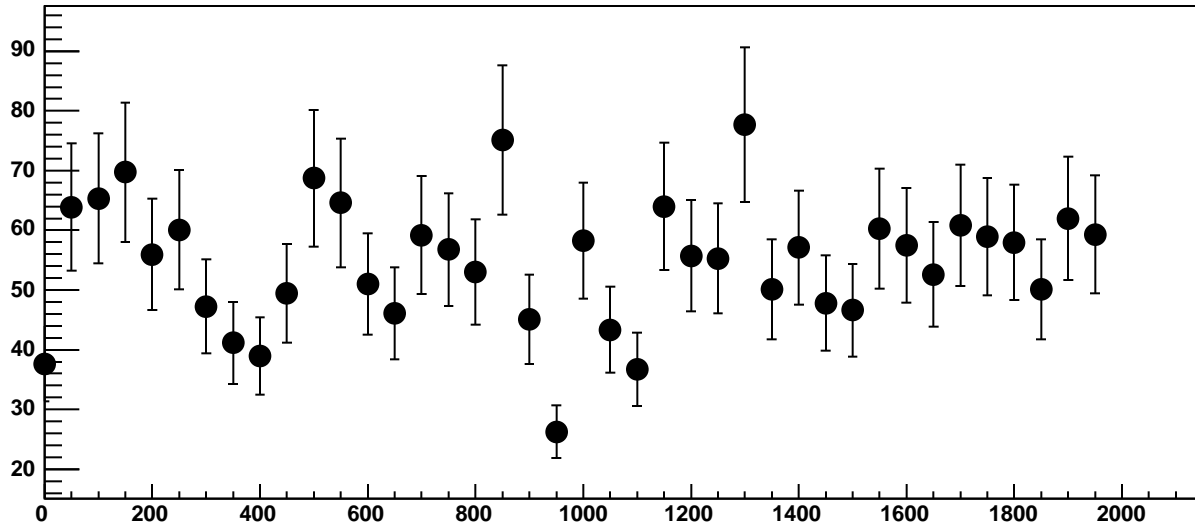
Chip 5, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC



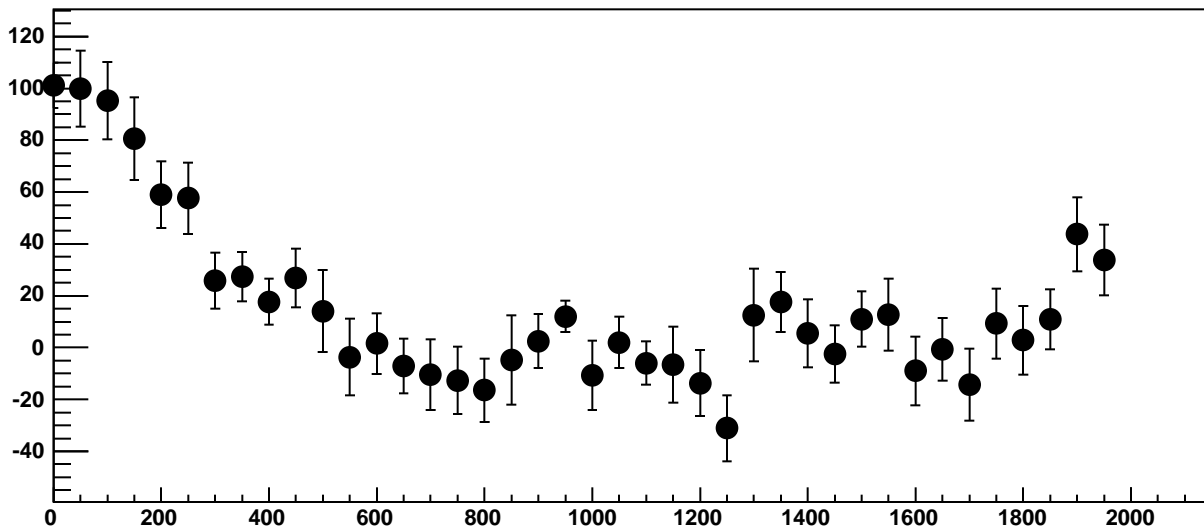
Chip 5, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC



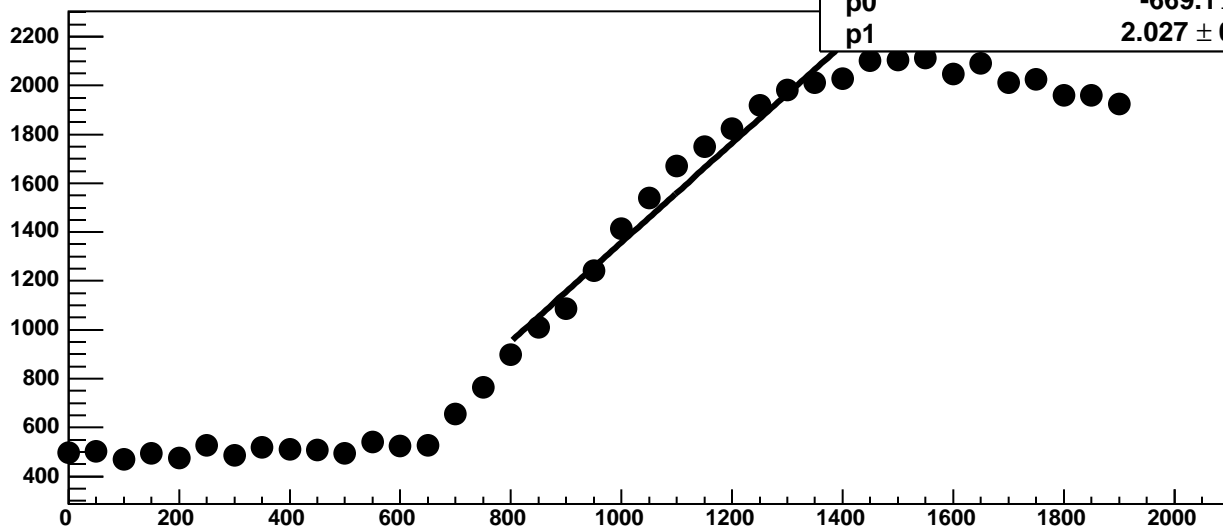
Chip 5, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC



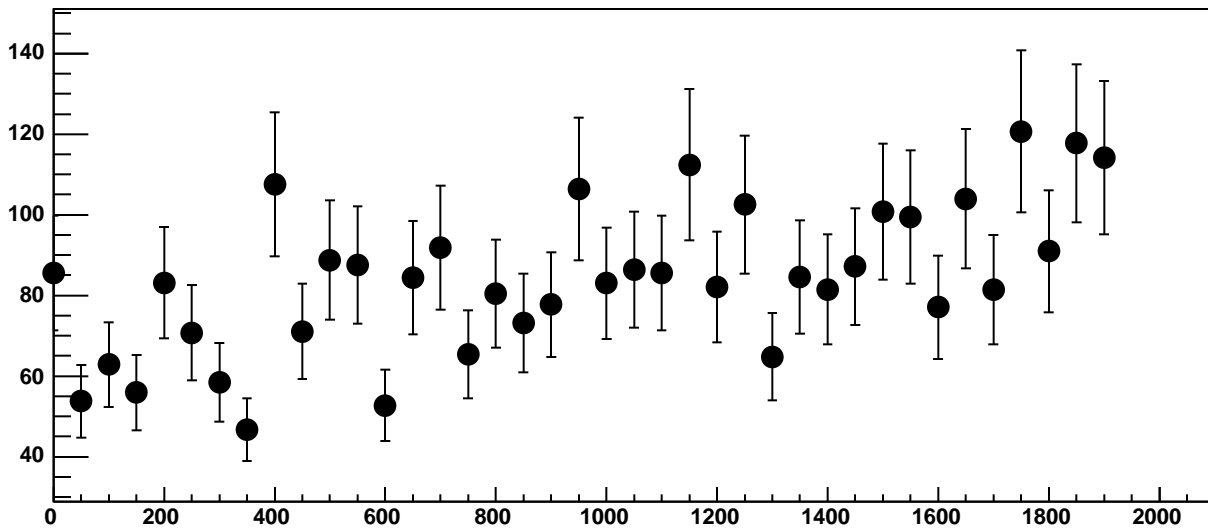
Chip 5, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC



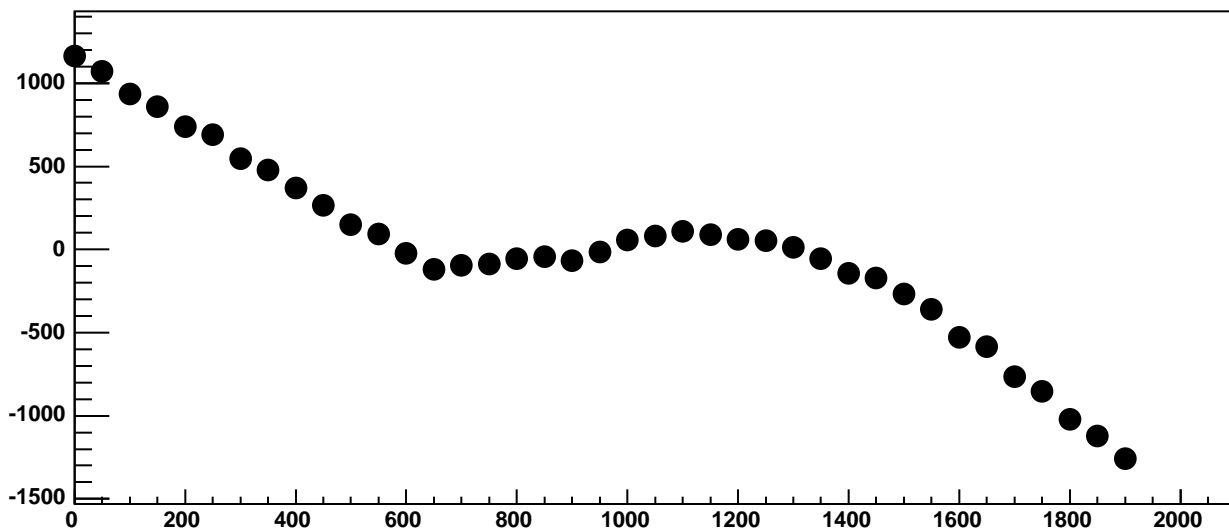
Chip 5, Channel 16, Enable 5, Hold=30, ADC Mean vs DAC



Chip 5, Channel 16, Enable 5, Hold=30, ADC Noise vs DAC

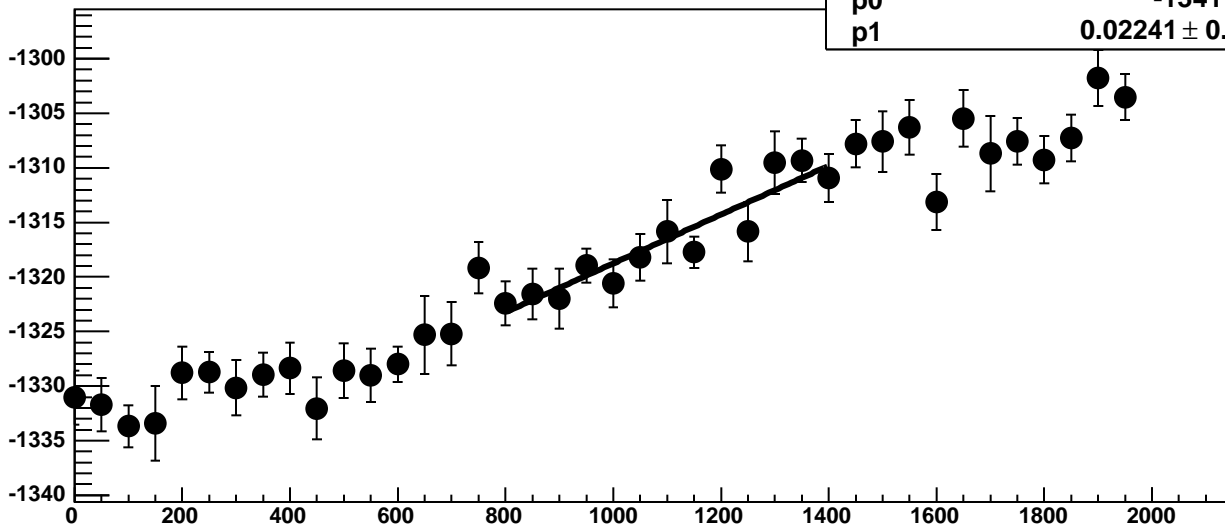


Chip 5, Channel 16, Enable 5, Hold=30, ADC Residuals vs DAC



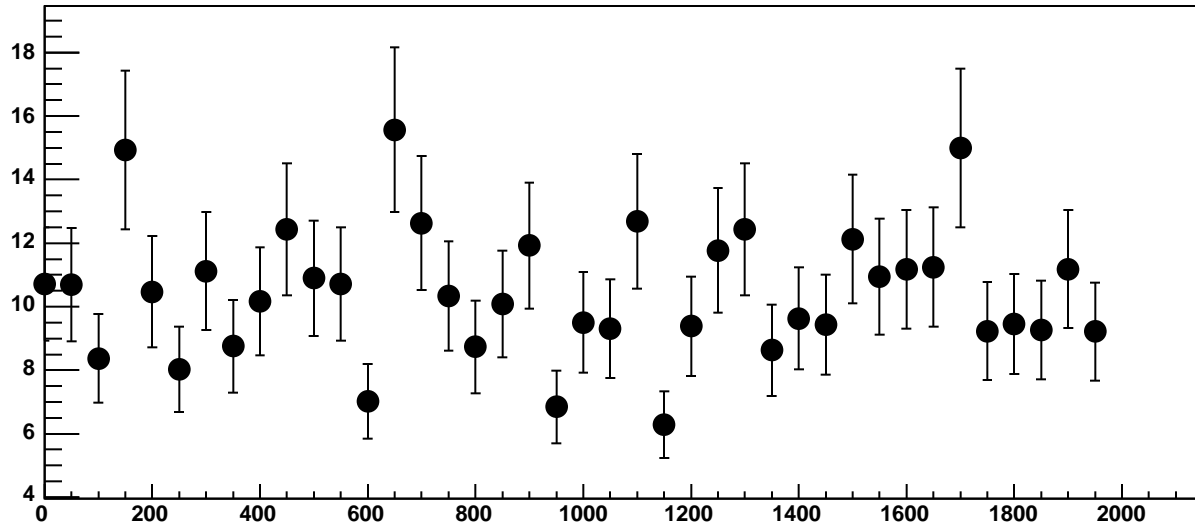


Chip 5, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC

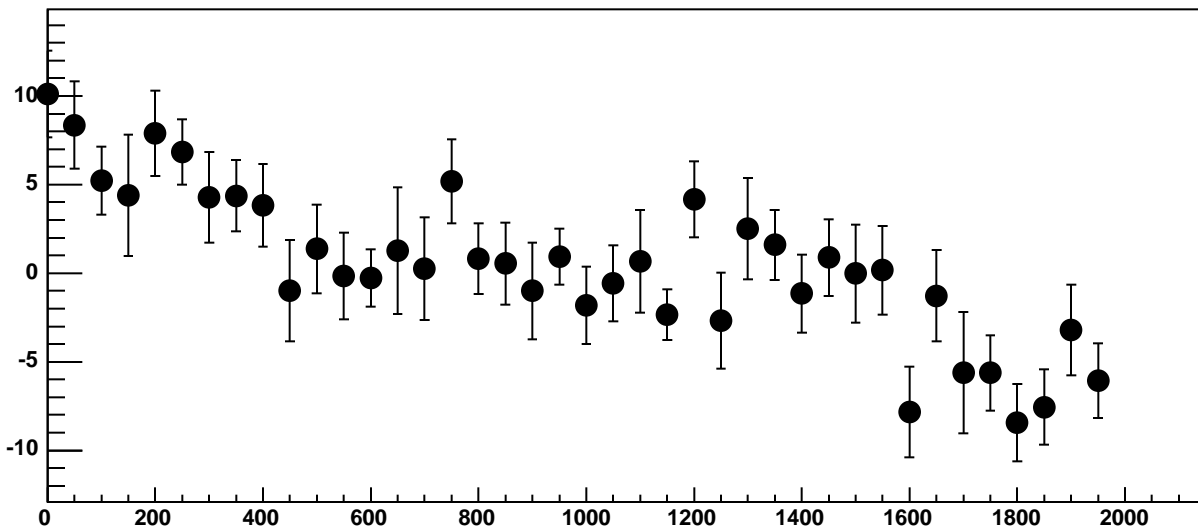


$\chi^2 / \text{ndf}$  10.6 / 11  
p0  $-1341 \pm 3.558$   
p1  $0.02241 \pm 0.003215$

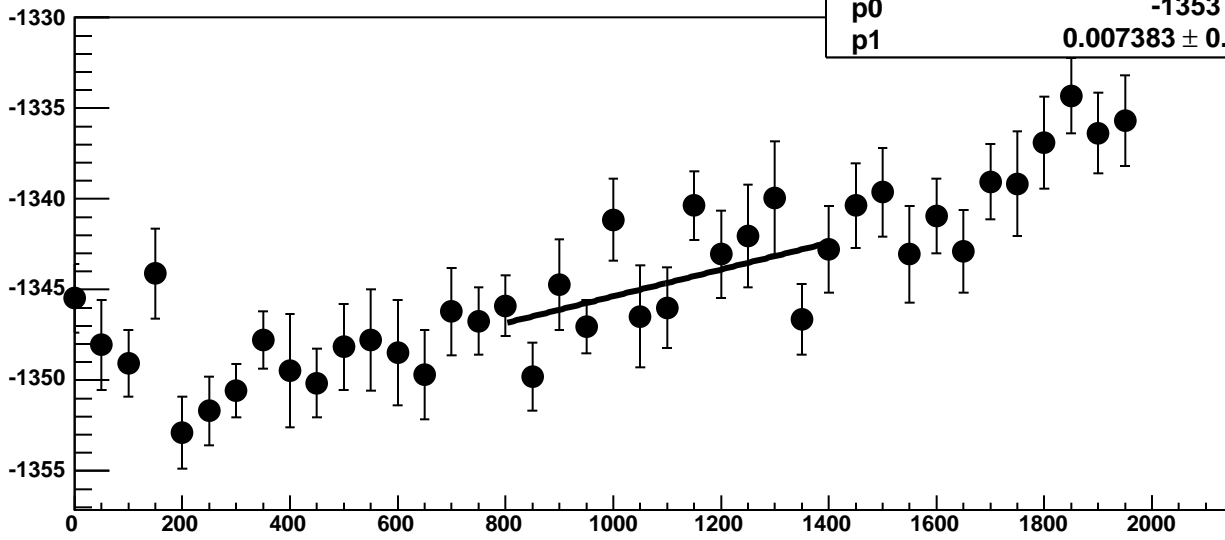
Chip 5, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC



Chip 5, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC

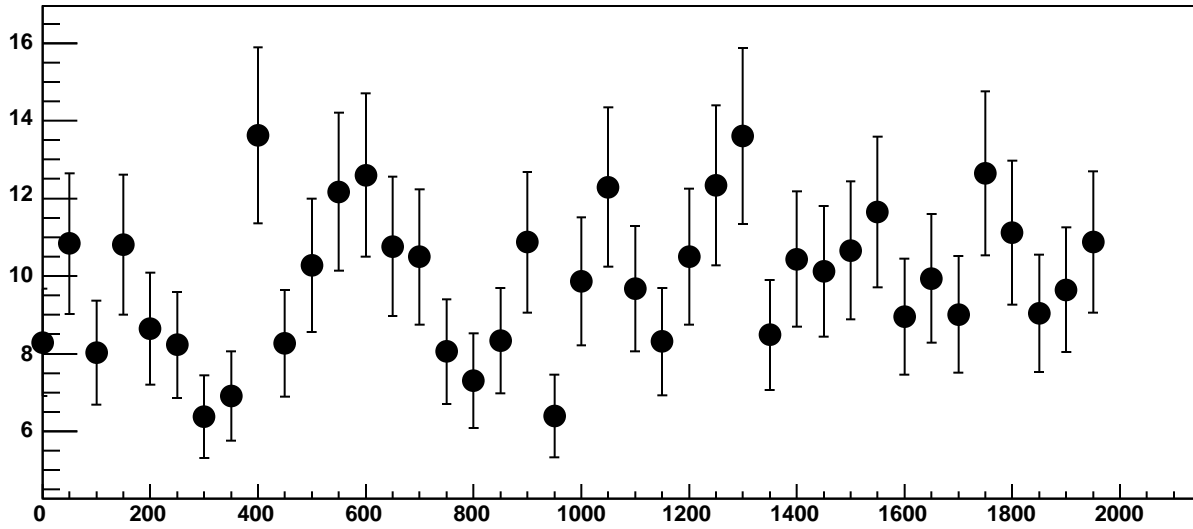


Chip 5, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC

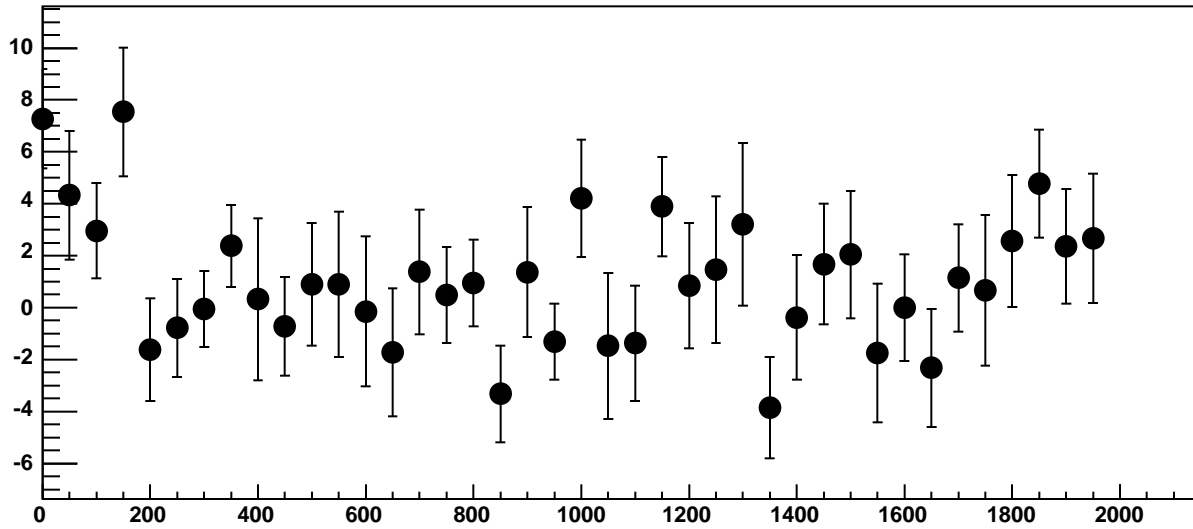


$\chi^2 / \text{ndf}$  18.26 / 11  
p0  $-1353 \pm 3.309$   
p1  $0.007383 \pm 0.003074$

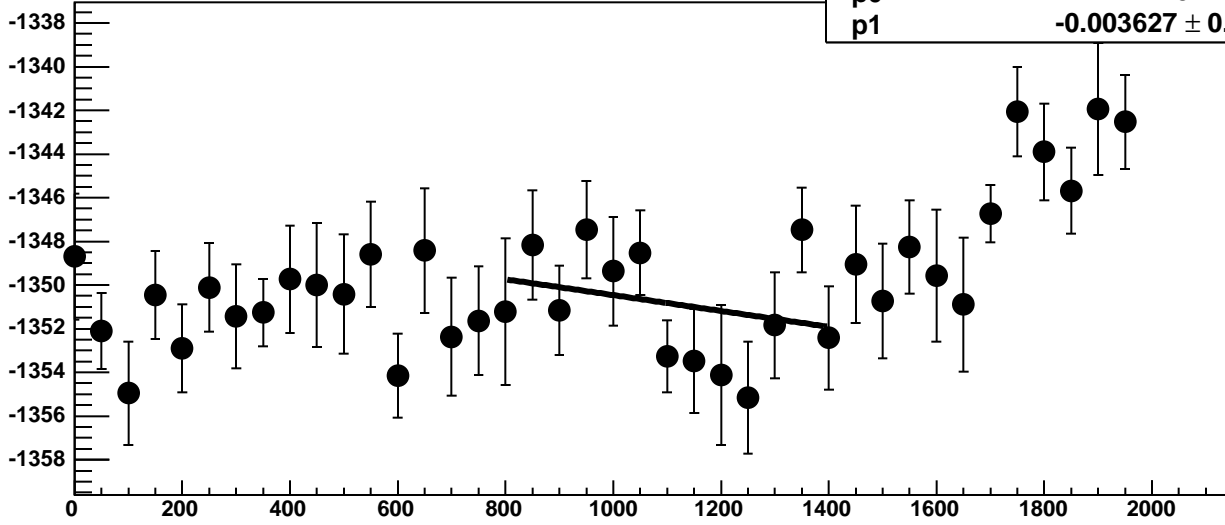
Chip 5, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC



Chip 5, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC

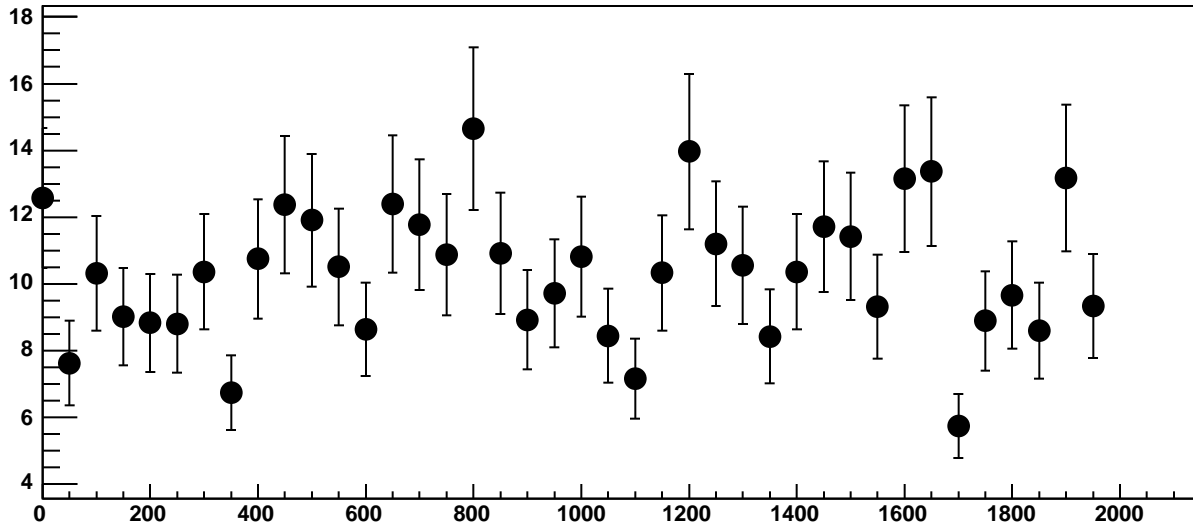


Chip 5, Channel 17, Enable 2, Hold=30, ADC Mean vs DAC

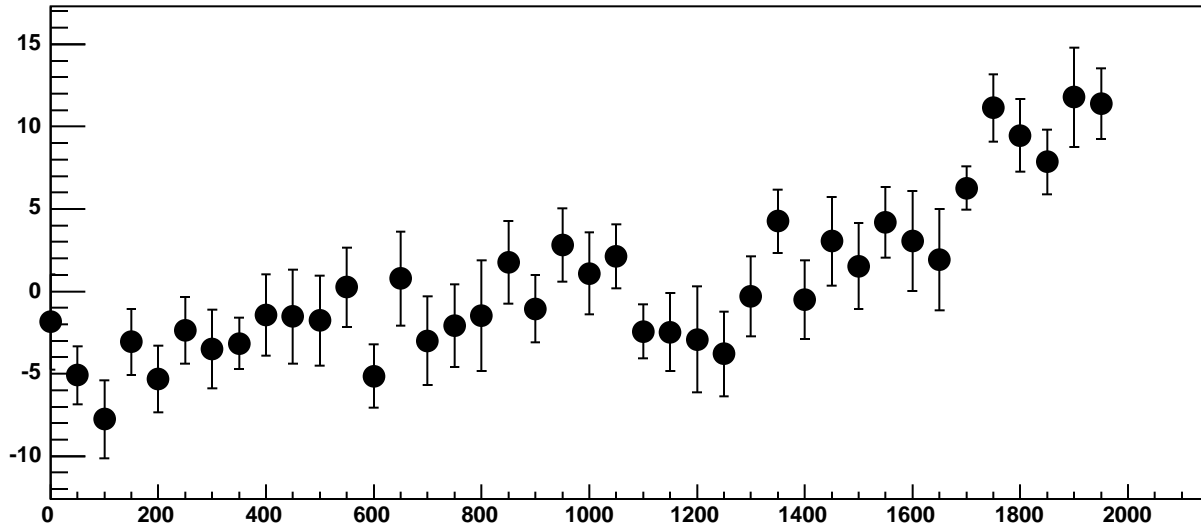


$\chi^2 / \text{ndf}$  15.13 / 11  
p0  $-1347 \pm 4.009$   
p1  $-0.003627 \pm 0.003571$

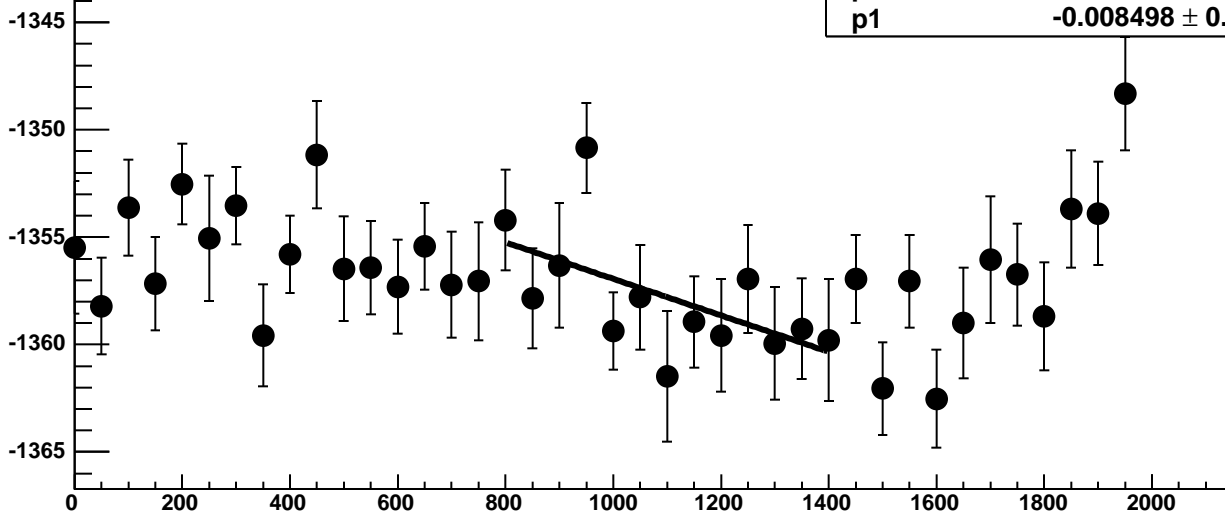
Chip 5, Channel 17, Enable 2, Hold=30, ADC Noise vs DAC



Chip 5, Channel 17, Enable 2, Hold=30, ADC Residuals vs DAC

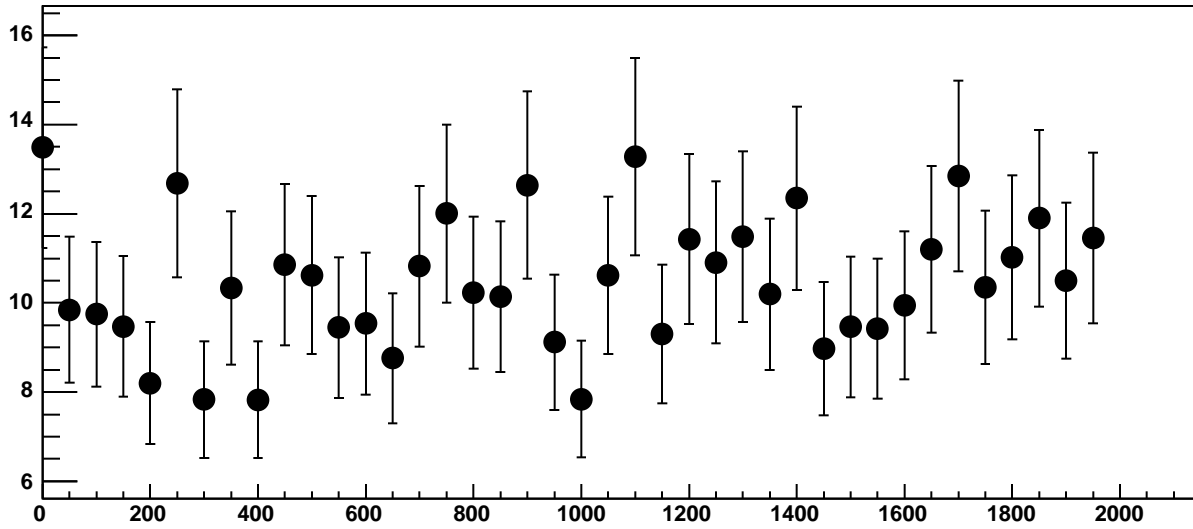


Chip 5, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC

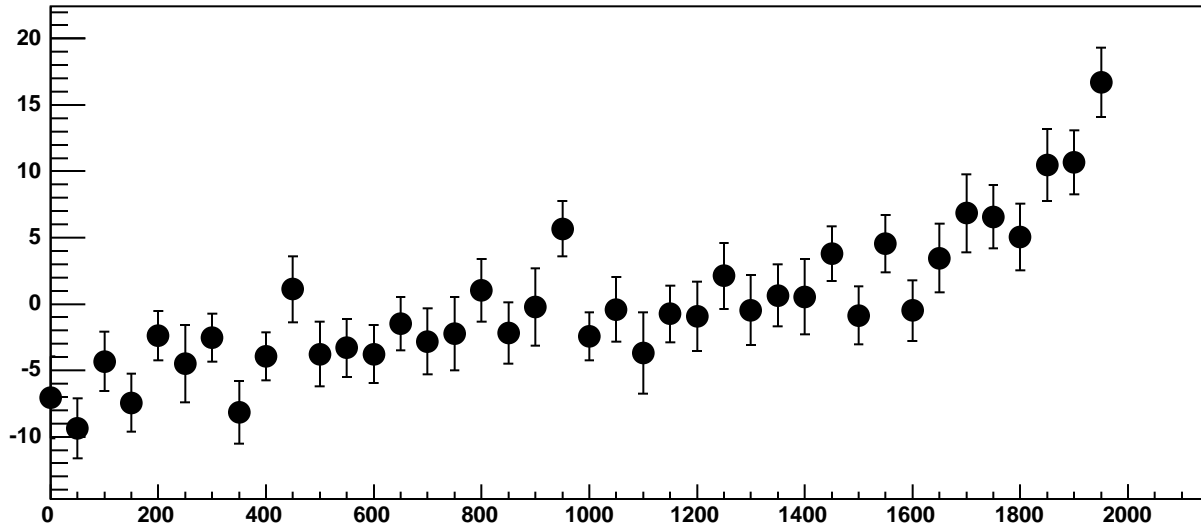


$\chi^2 / \text{ndf}$  12.85 / 11  
p0 -1348 ± 4.021  
p1 -0.008498 ± 0.003659

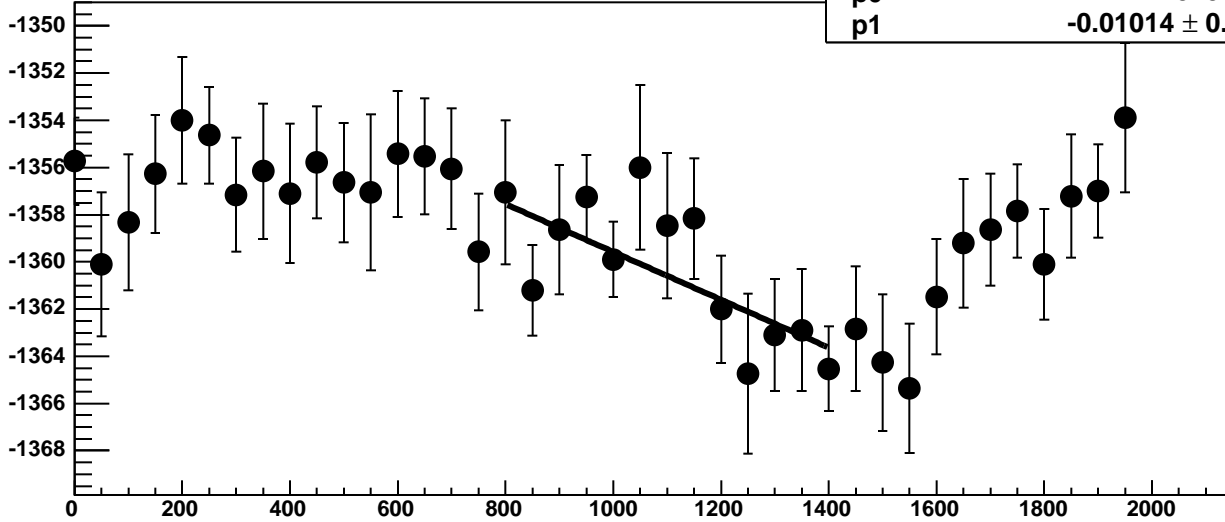
Chip 5, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC



Chip 5, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC

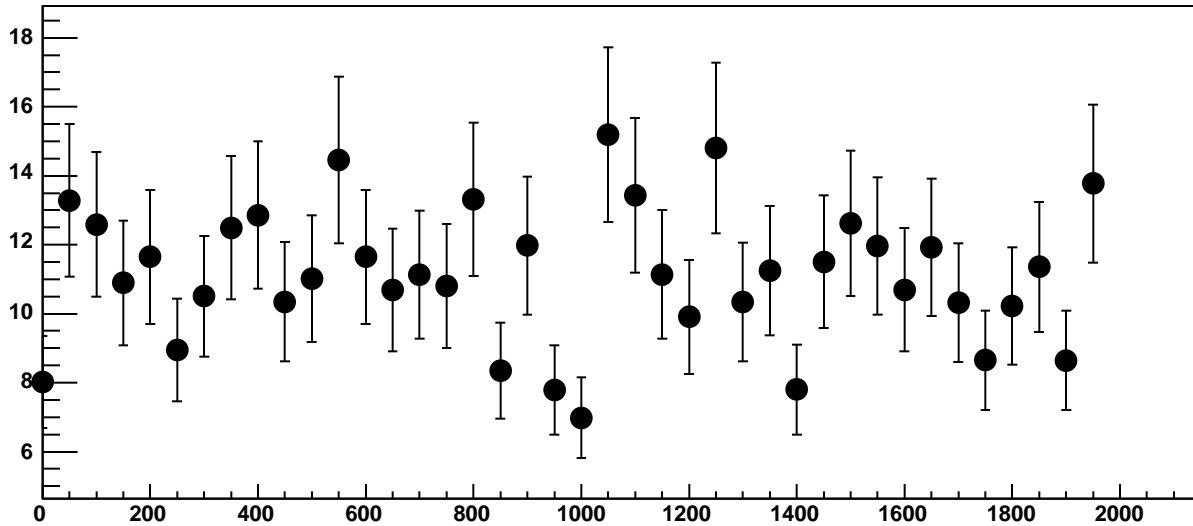


Chip 5, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC

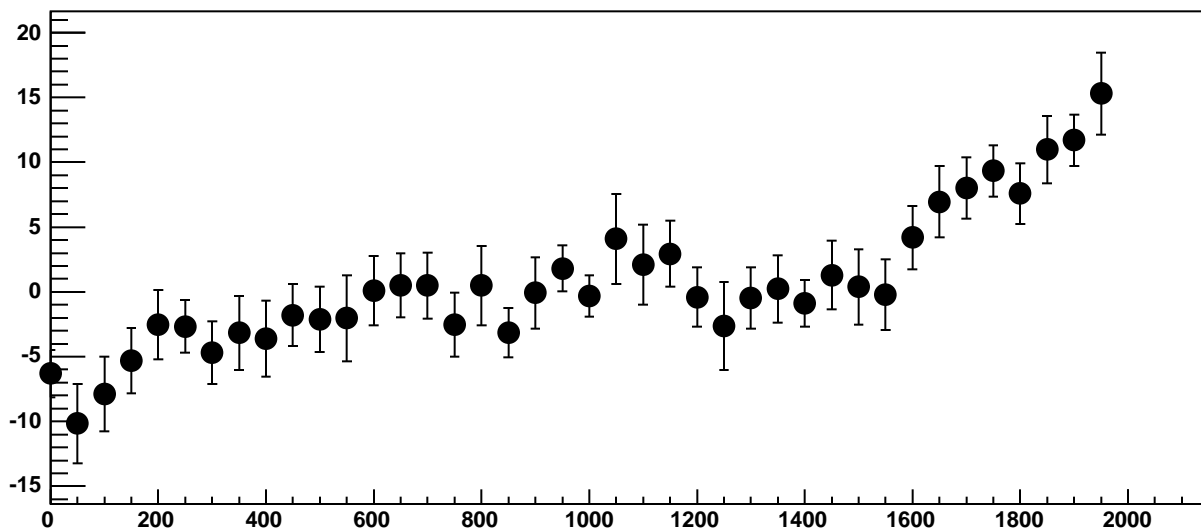


$\chi^2 / \text{ndf}$  7.899 / 11  
p0  $-1349 \pm 3.669$   
p1  $-0.01014 \pm 0.003297$

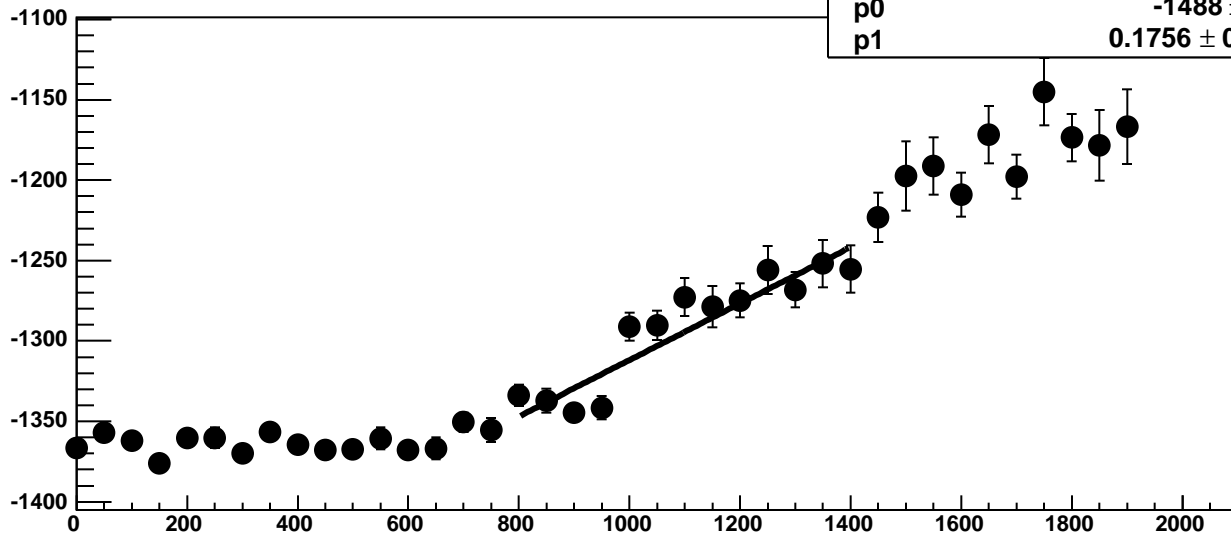
Chip 5, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC



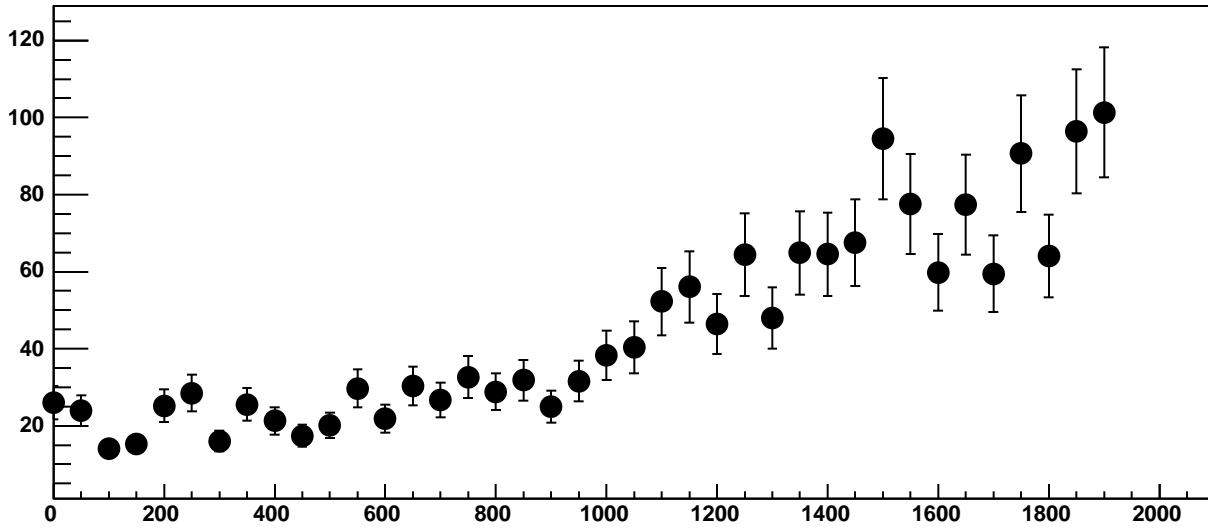
Chip 5, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC



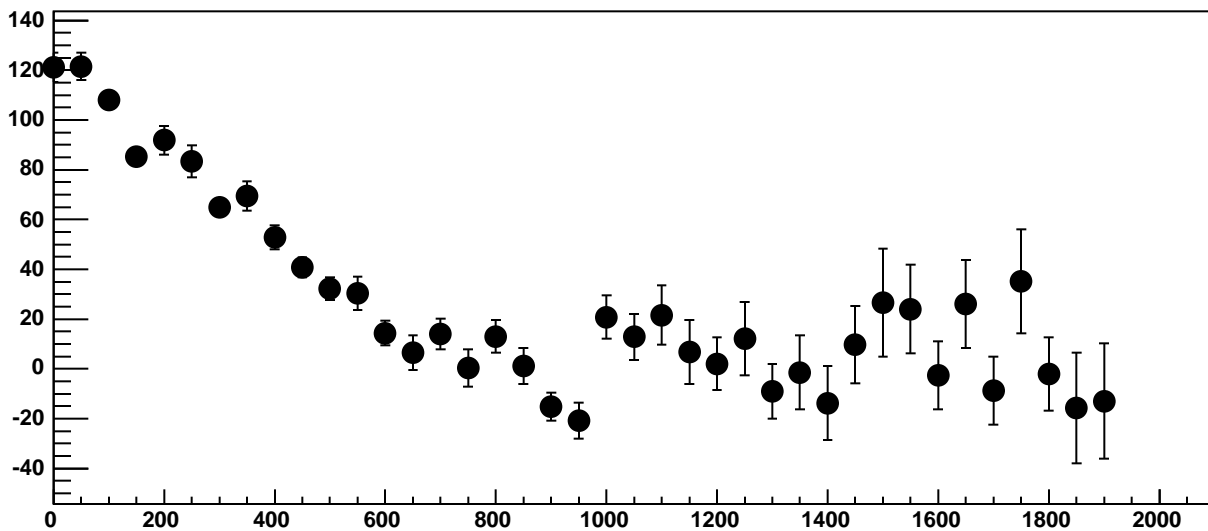
Chip 5, Channel 17, Enable 5!, Hold=30, ADC Mean vs DAC



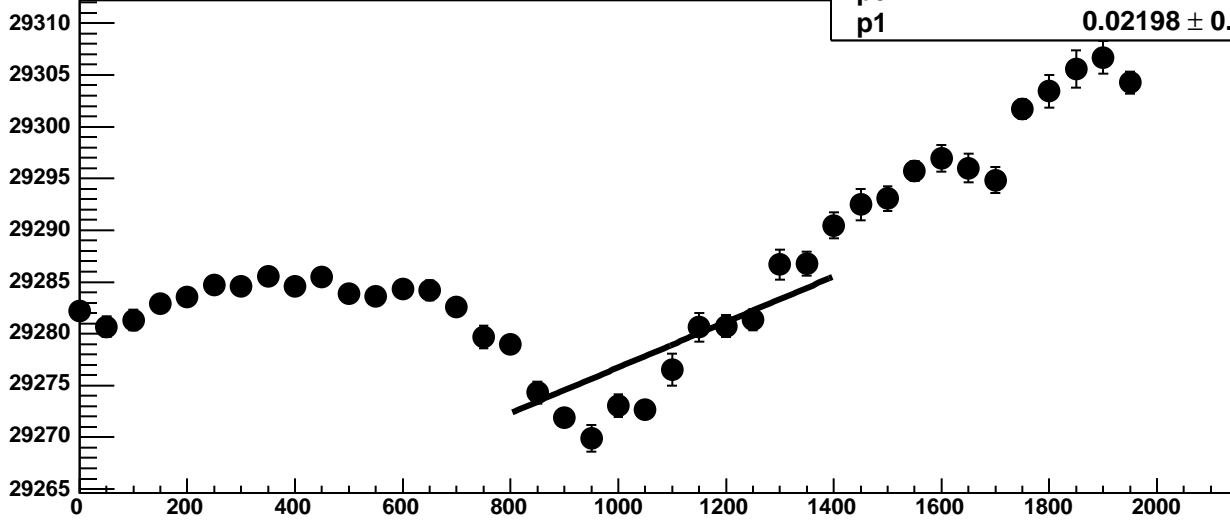
Chip 5, Channel 17, Enable 5!, Hold=30, ADC Noise vs DAC



Chip 5, Channel 17, Enable 5!, Hold=30, ADC Residuals vs DAC

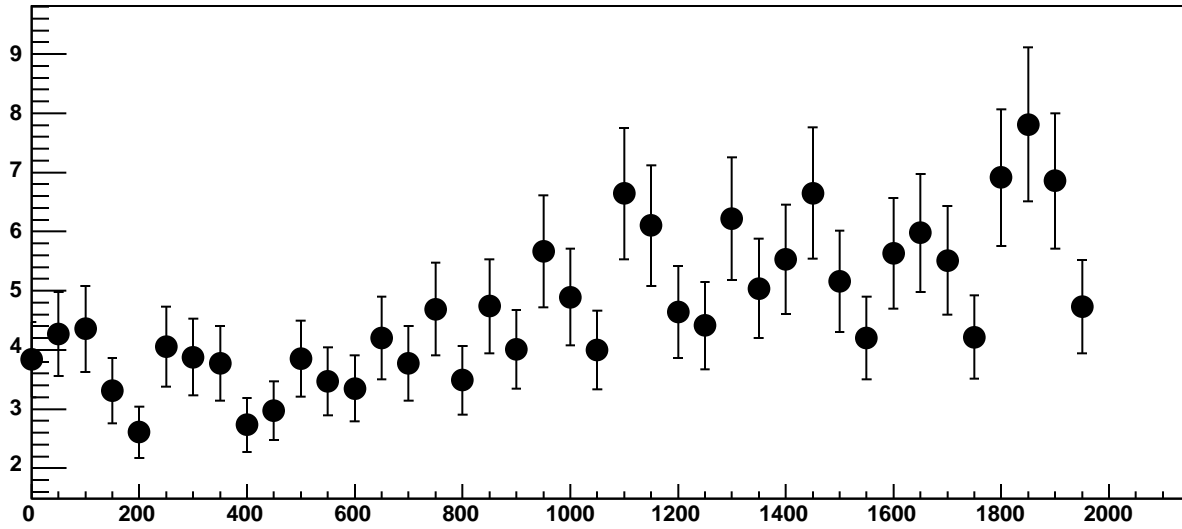


Chip 6, Channel 0, Enable 0!, Hold=30, ADC Mean vs DAC

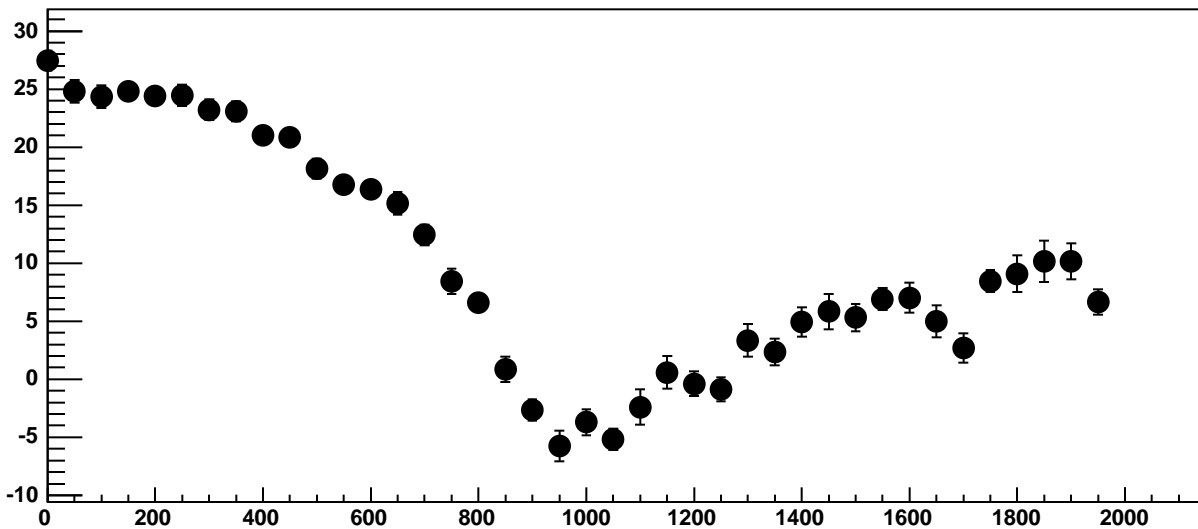


$\chi^2 / \text{ndf}$  167.3 / 11  
p0  $2.925\text{e}+04 \pm 1.699$   
p1  $0.02198 \pm 0.001574$

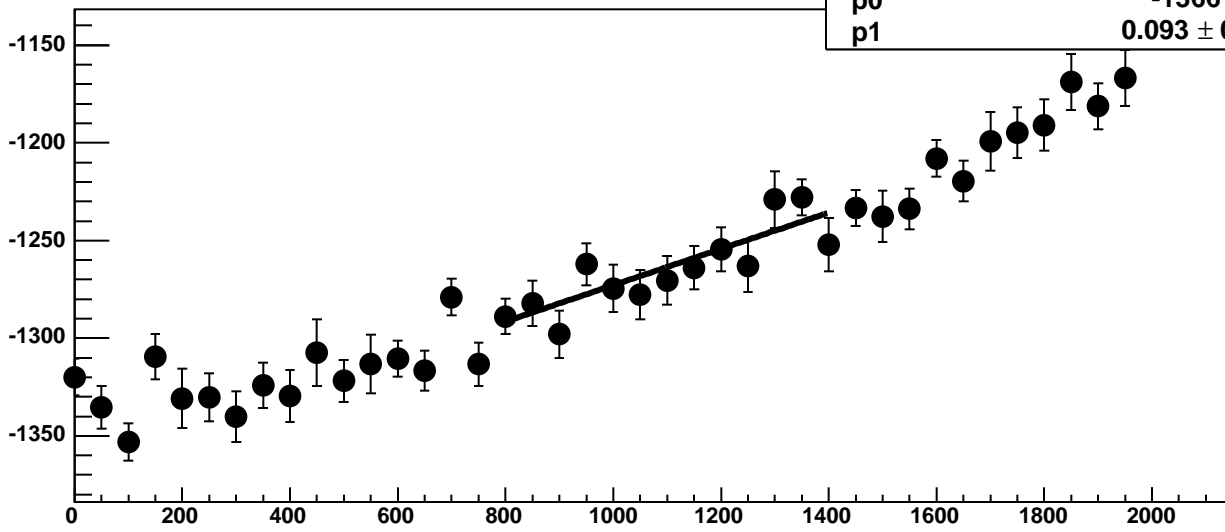
Chip 6, Channel 0, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 6, Channel 0, Enable 0!, Hold=30, ADC Residuals vs DAC

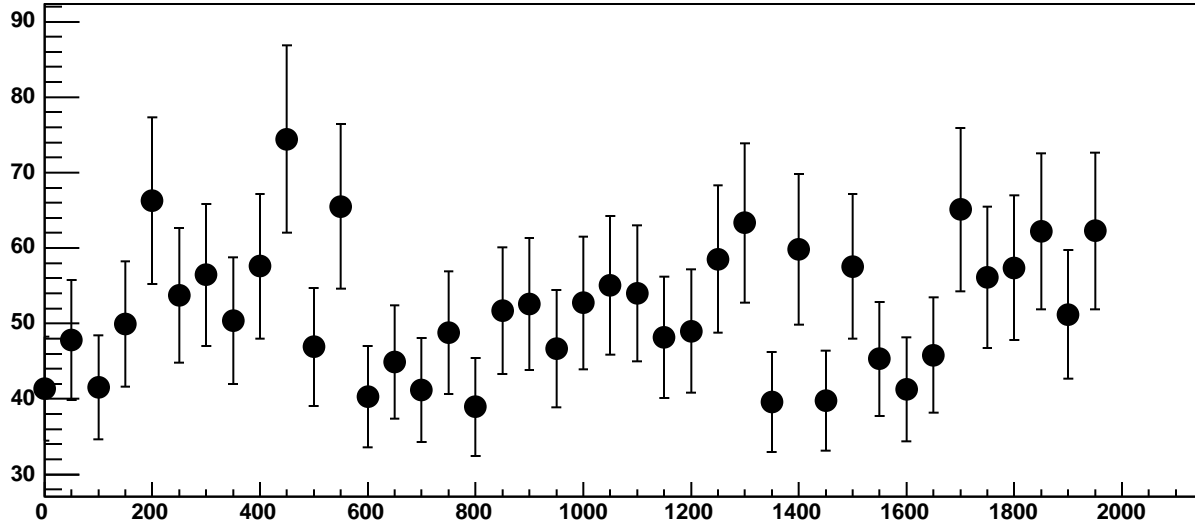


Chip 6, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC

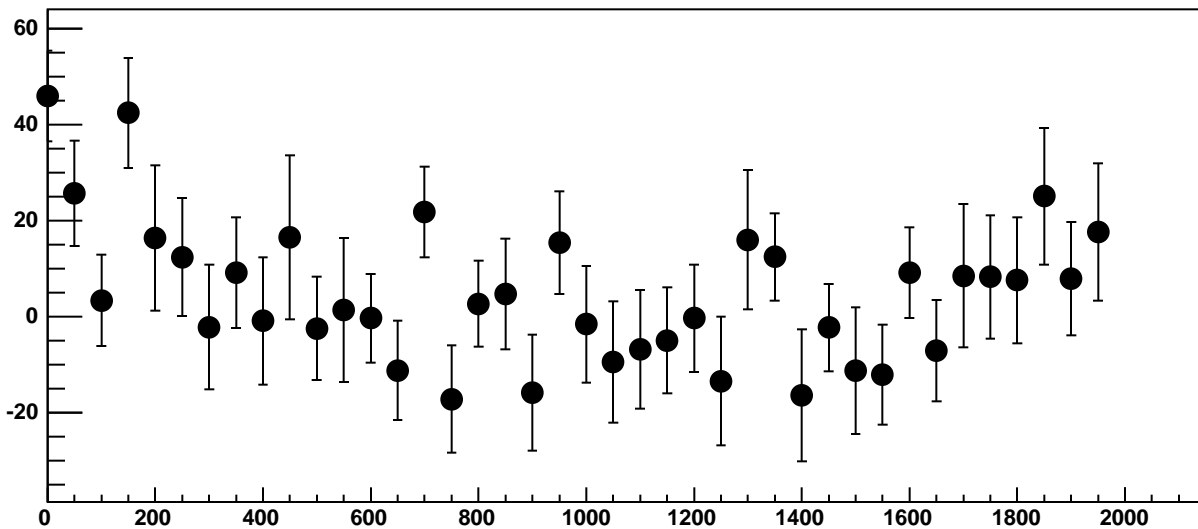


$\chi^2 / \text{ndf}$  10.64 / 11  
p0  $-1366 \pm 17.92$   
p1  $0.093 \pm 0.01629$

Chip 6, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC

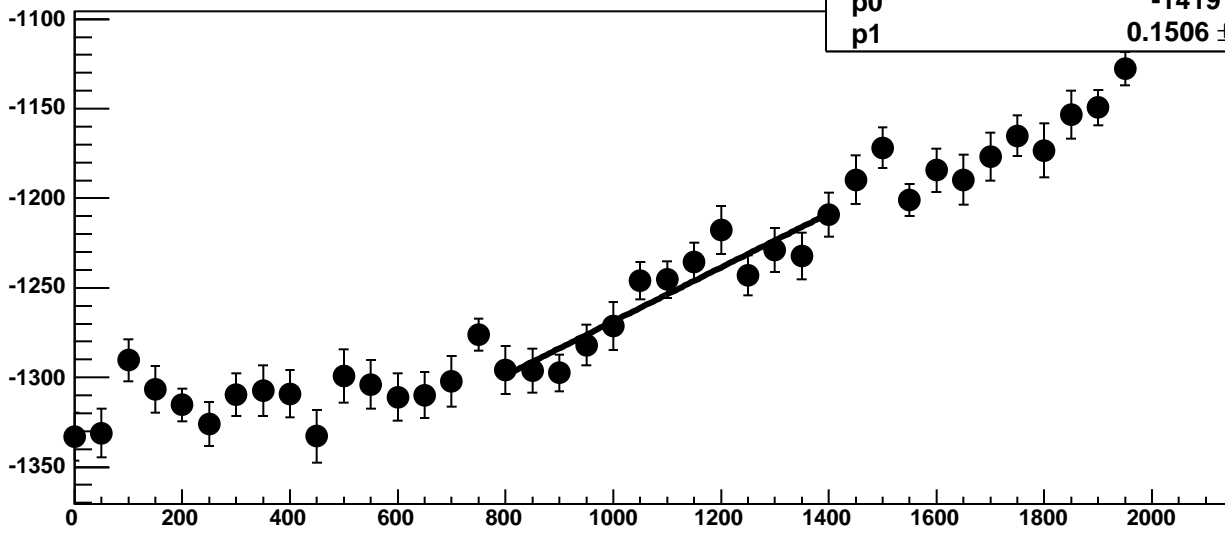


Chip 6, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC



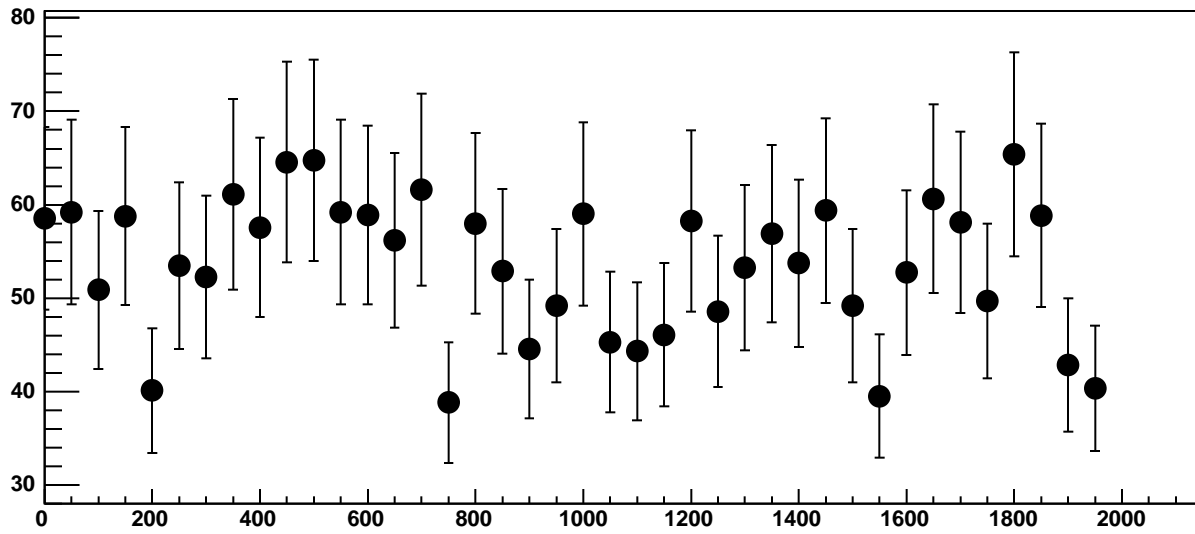


Chip 6, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC

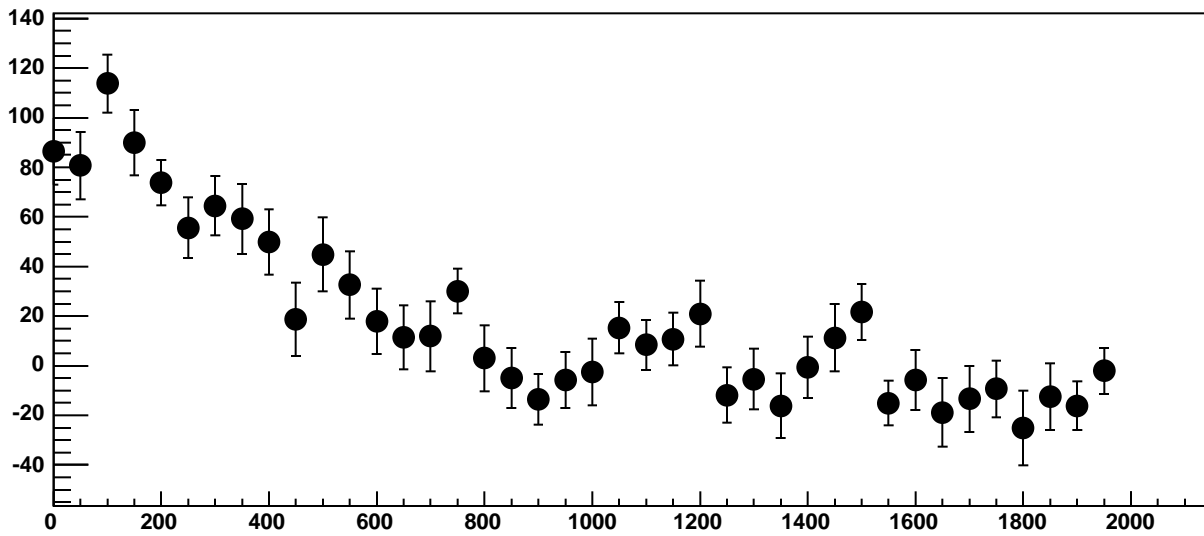


$\chi^2 / \text{ndf}$  11.46 / 11  
p0  $-1419 \pm 20.07$   
p1  $0.1506 \pm 0.0181$

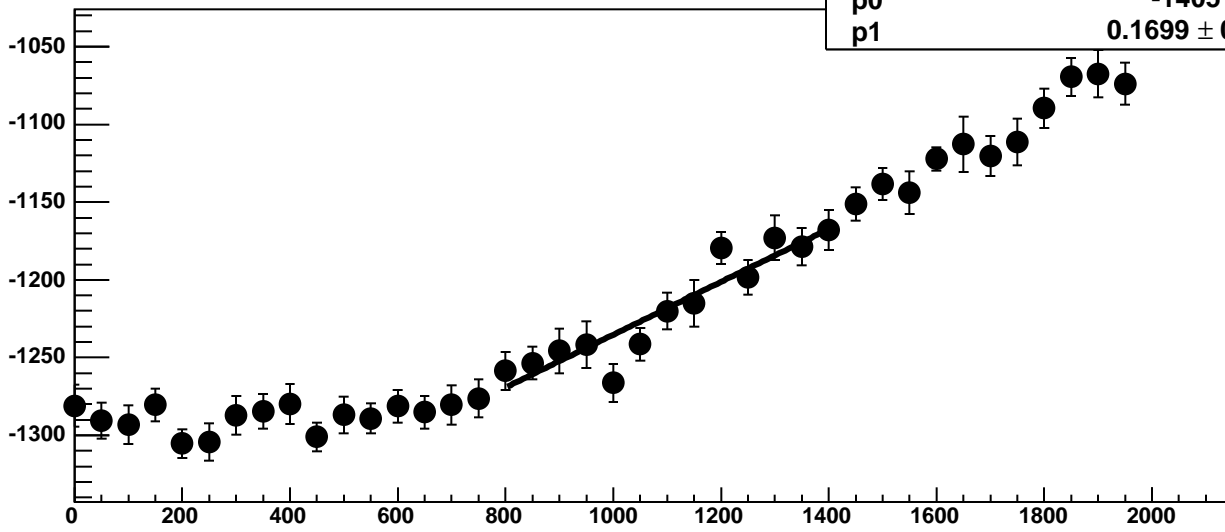
Chip 6, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC



Chip 6, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC

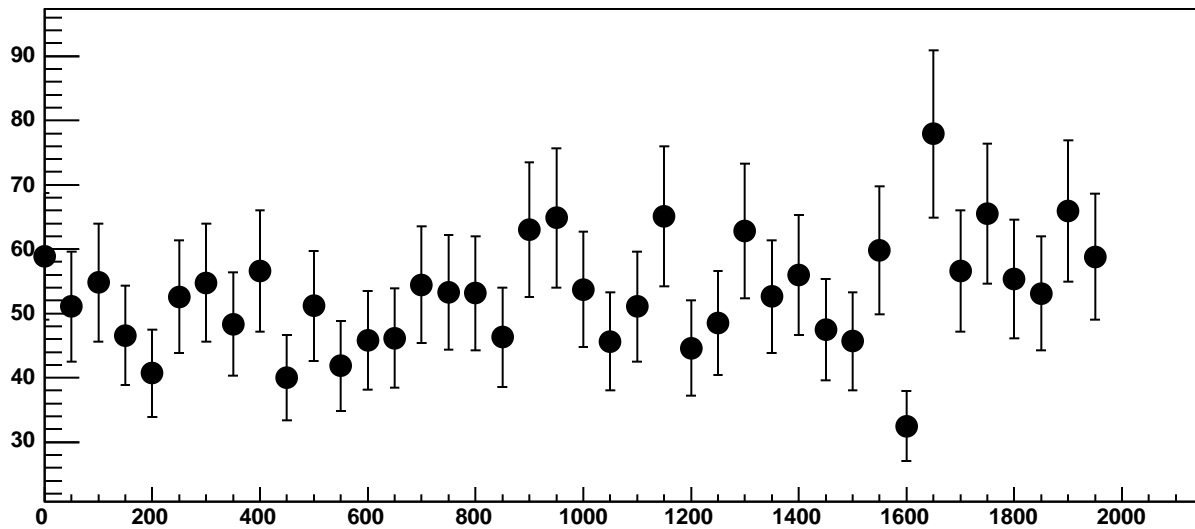


Chip 6, Channel 0, Enable 3, Hold=30, ADC Mean vs DAC

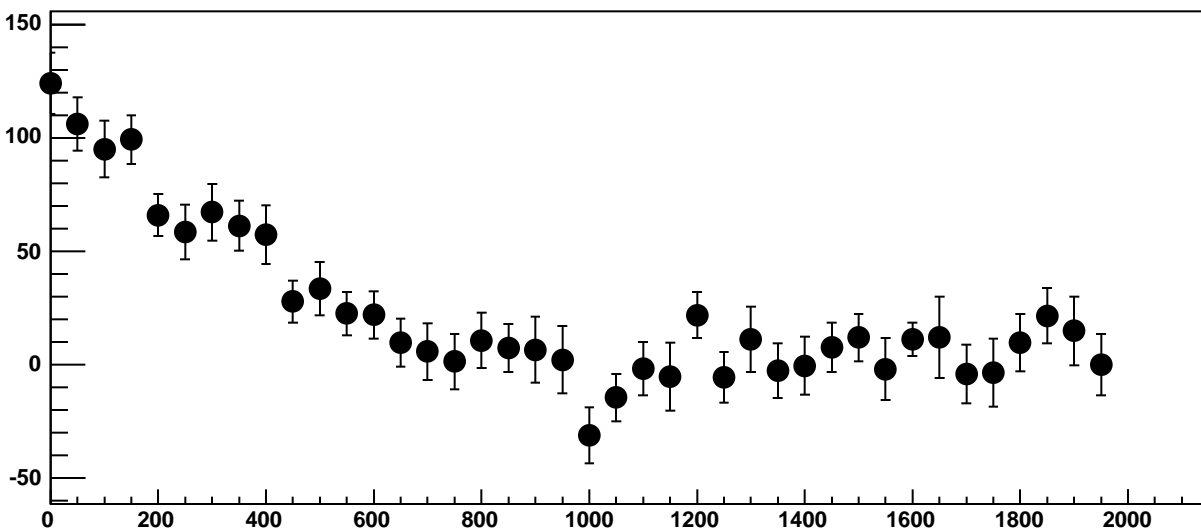


$\chi^2 / \text{ndf}$  15.36 / 11  
p0  $-1405 \pm 20.36$   
p1  $0.1699 \pm 0.01827$

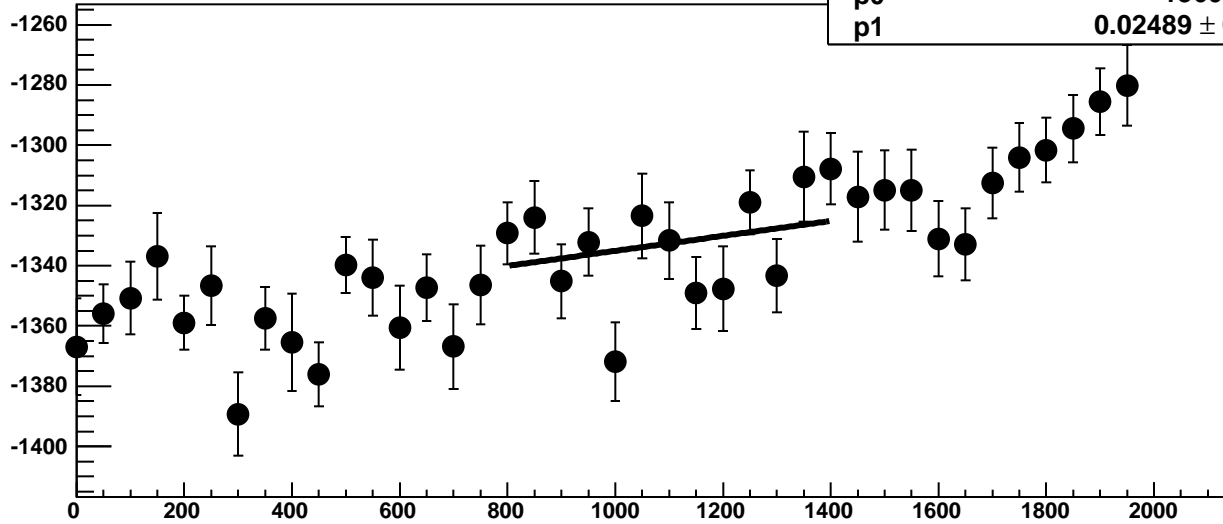
Chip 6, Channel 0, Enable 3, Hold=30, ADC Noise vs DAC



Chip 6, Channel 0, Enable 3, Hold=30, ADC Residuals vs DAC



Chip 6, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

21.1 / 11

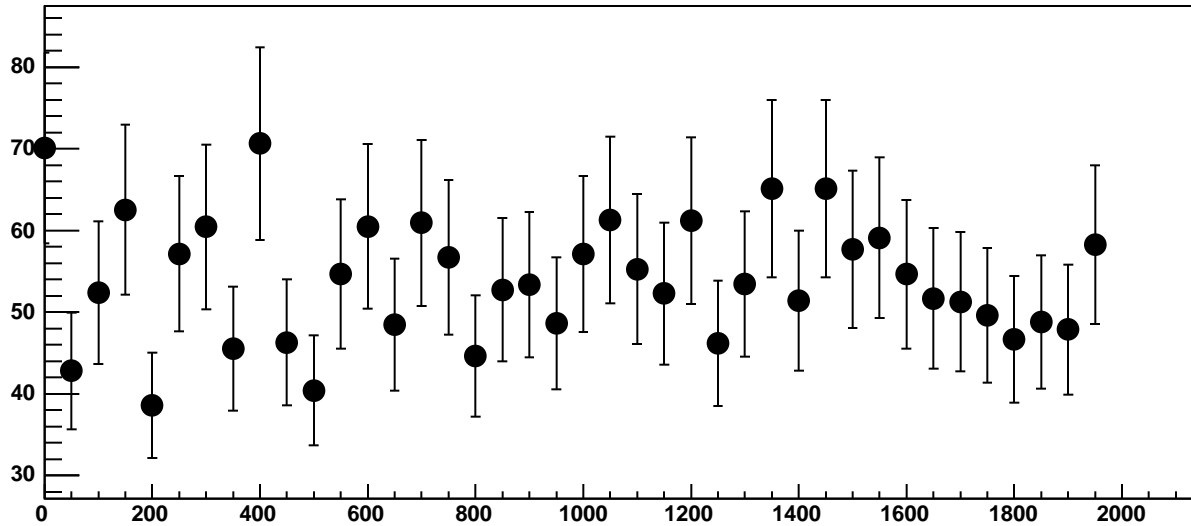
p0

$-1360 \pm 19.37$

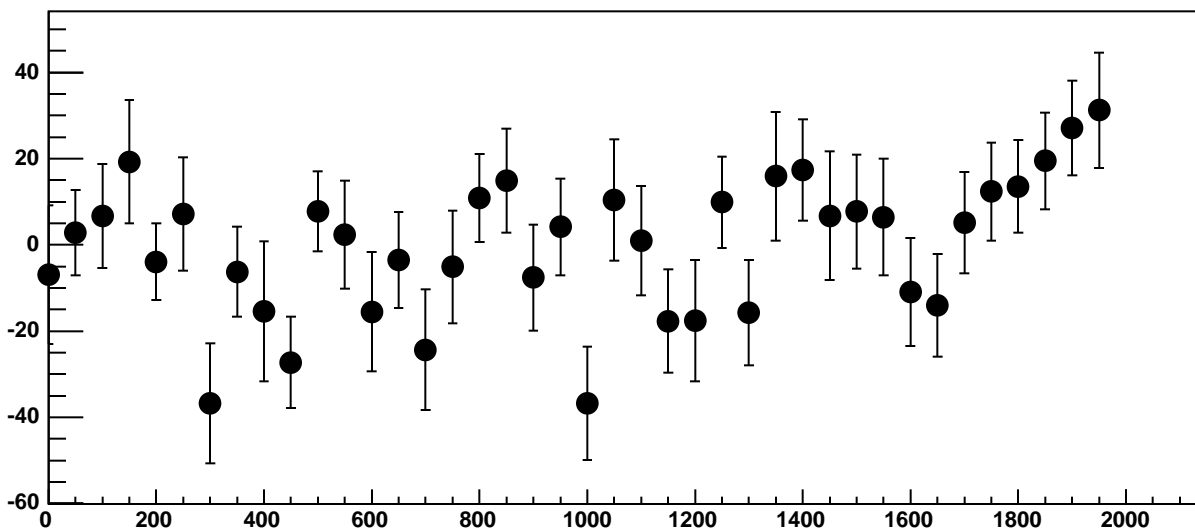
p1

$0.02489 \pm 0.01754$

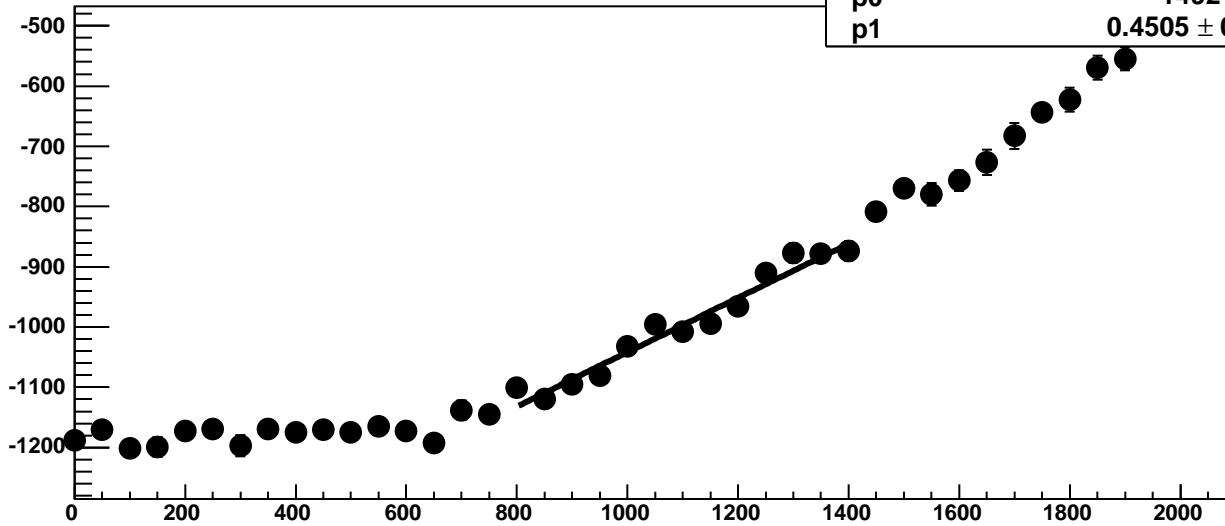
Chip 6, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC



Chip 6, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC

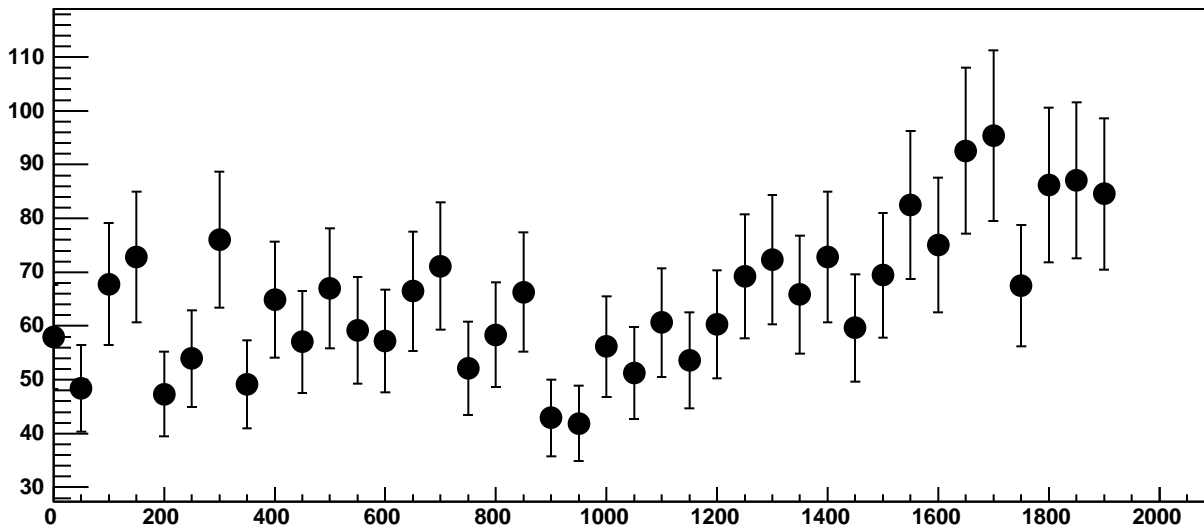


Chip 6, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC

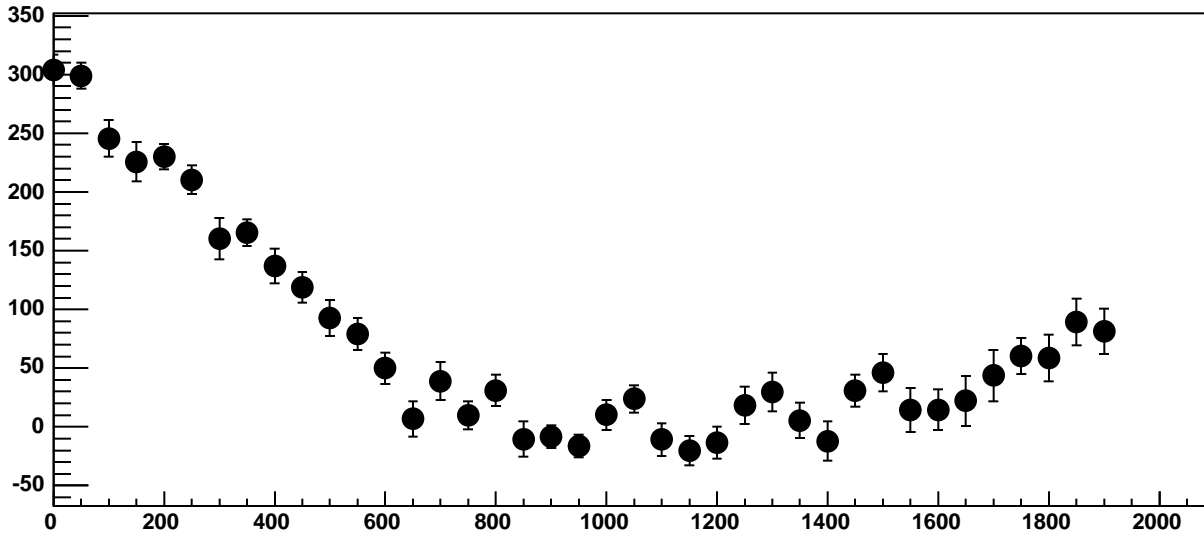


$\chi^2 / \text{ndf}$  23.51 / 11  
p0  $-1492 \pm 22.47$   
p1  $0.4505 \pm 0.02092$

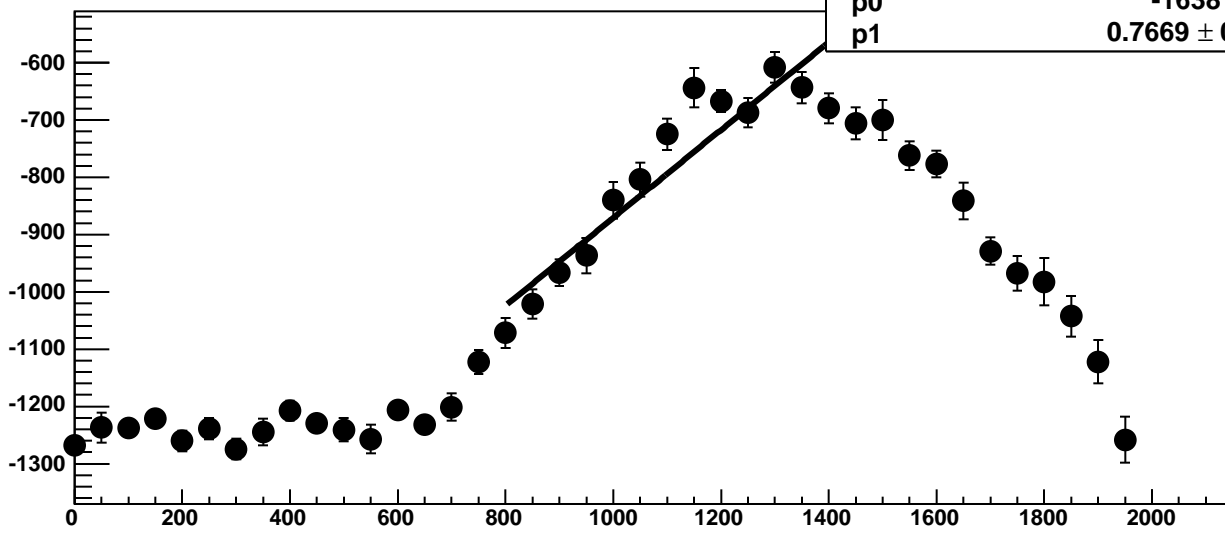
Chip 6, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC



Chip 6, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC

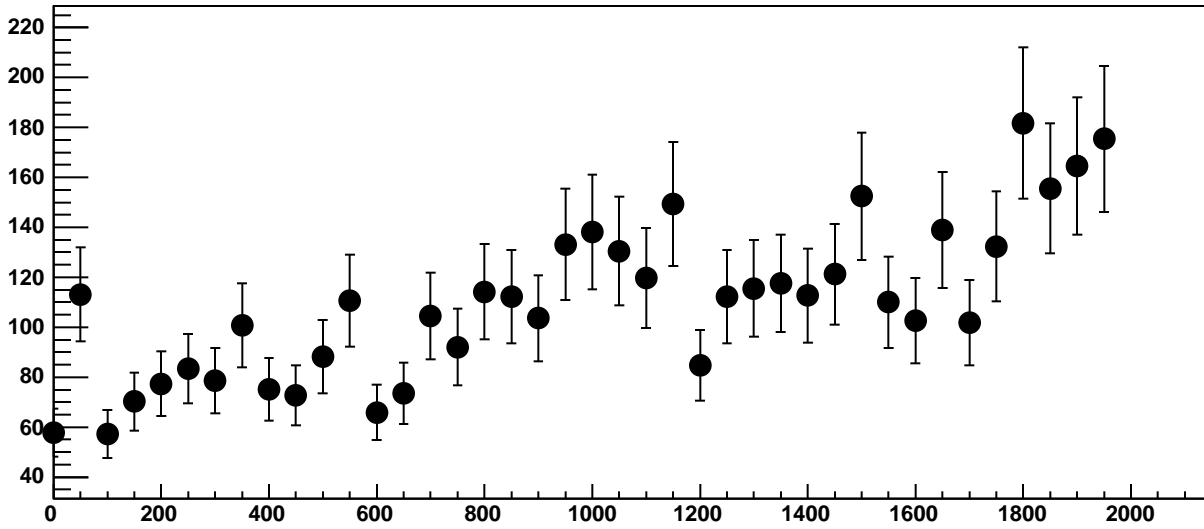


Chip 6, Channel 1, Enable 0, Hold=30, ADC Mean vs DAC

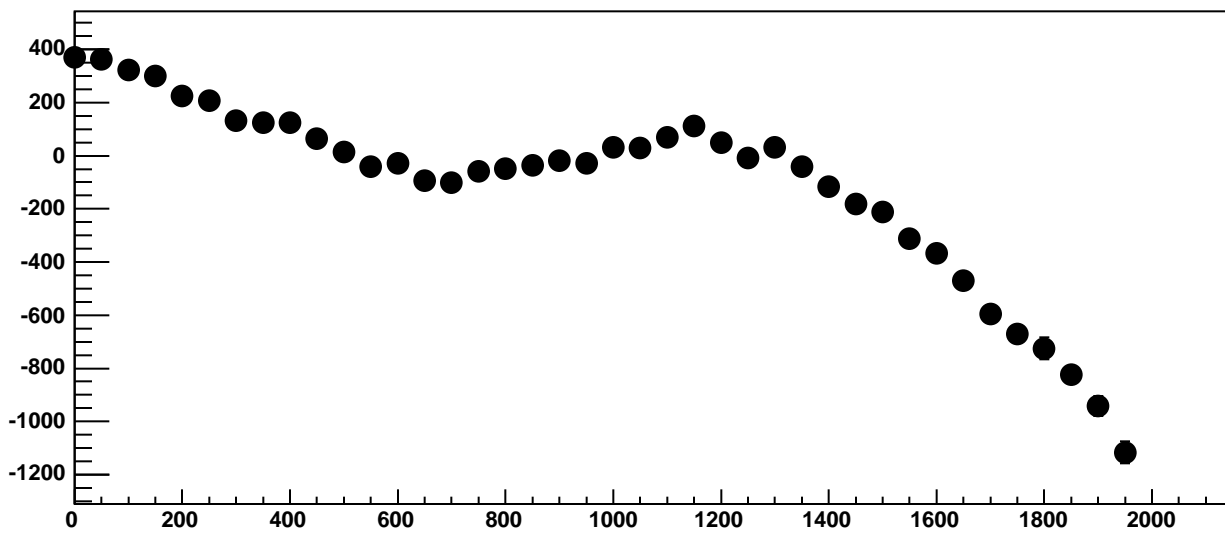


$\chi^2 / \text{ndf}$  56.08 / 11  
p0  $-1638 \pm 43.32$   
p1  $0.7669 \pm 0.03858$

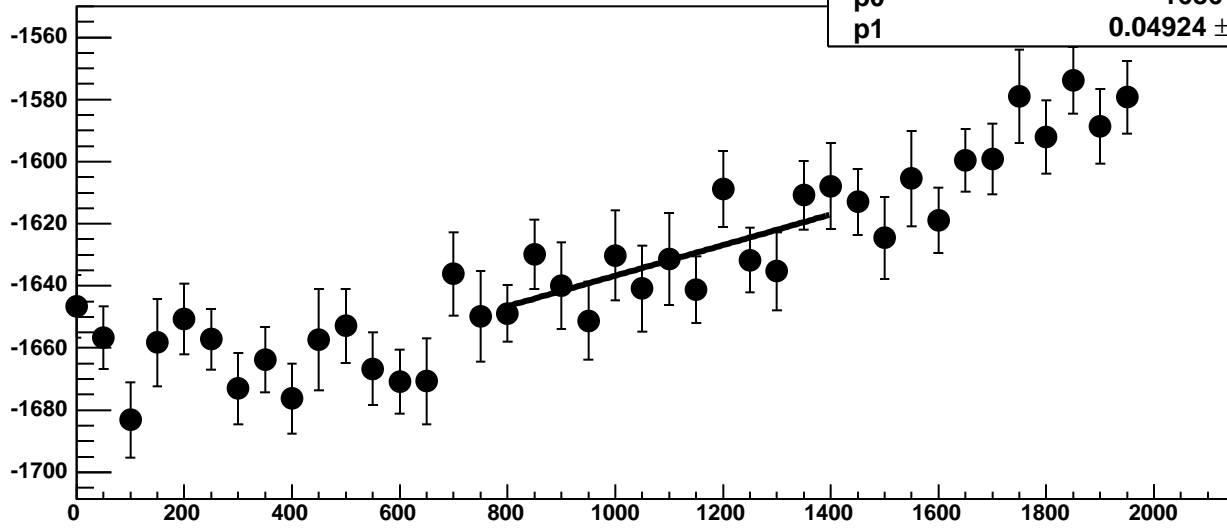
Chip 6, Channel 1, Enable 0, Hold=30, ADC Noise vs DAC



Chip 6, Channel 1, Enable 0, Hold=30, ADC Residuals vs DAC

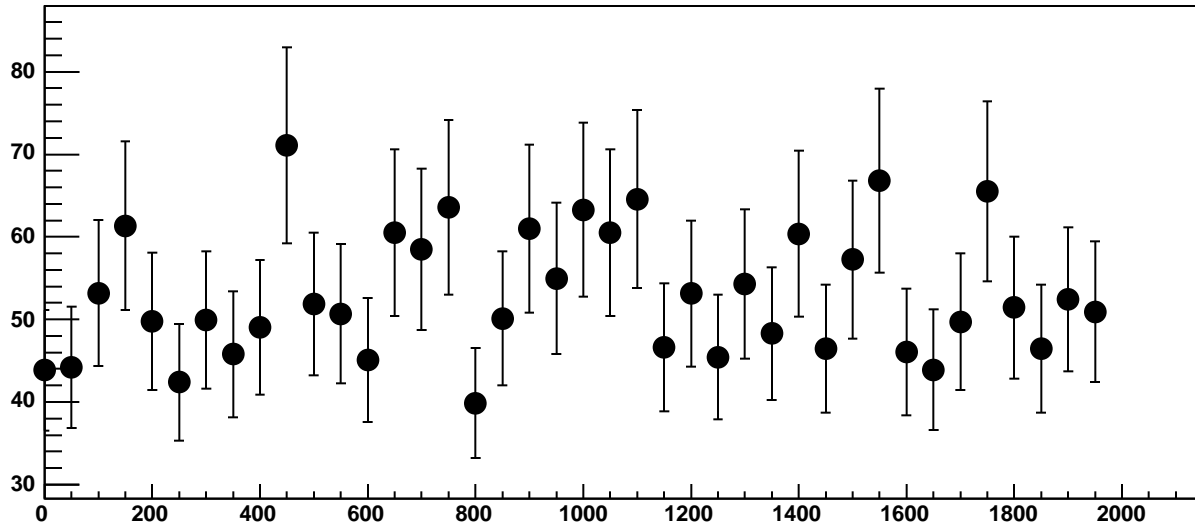


Chip 6, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC

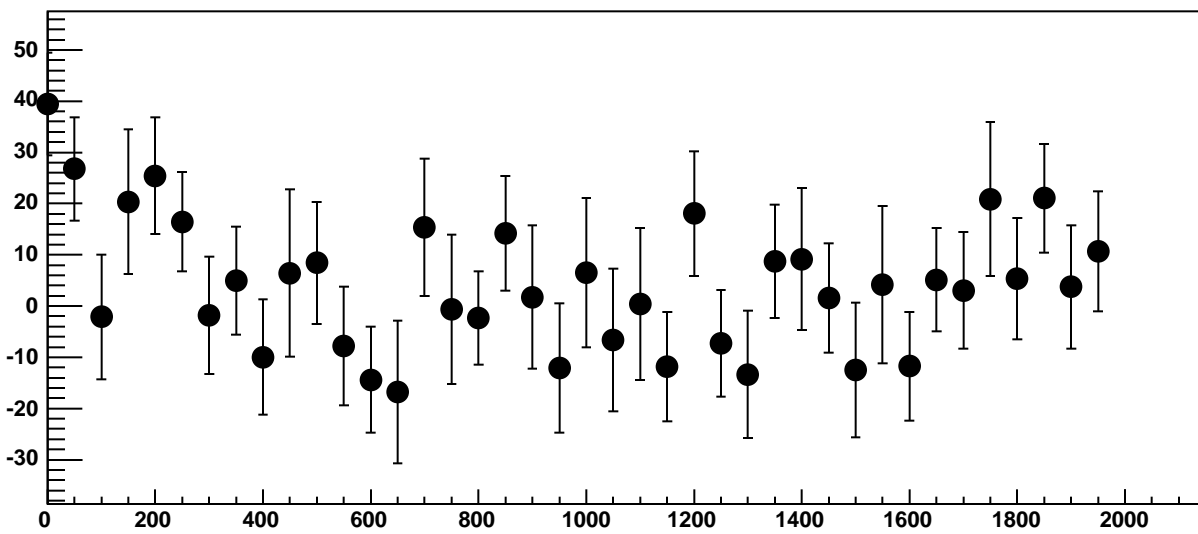


$\chi^2 / \text{ndf}$  9.14 / 11  
p0  $-1686 \pm 18.72$   
p1  $0.04924 \pm 0.0169$

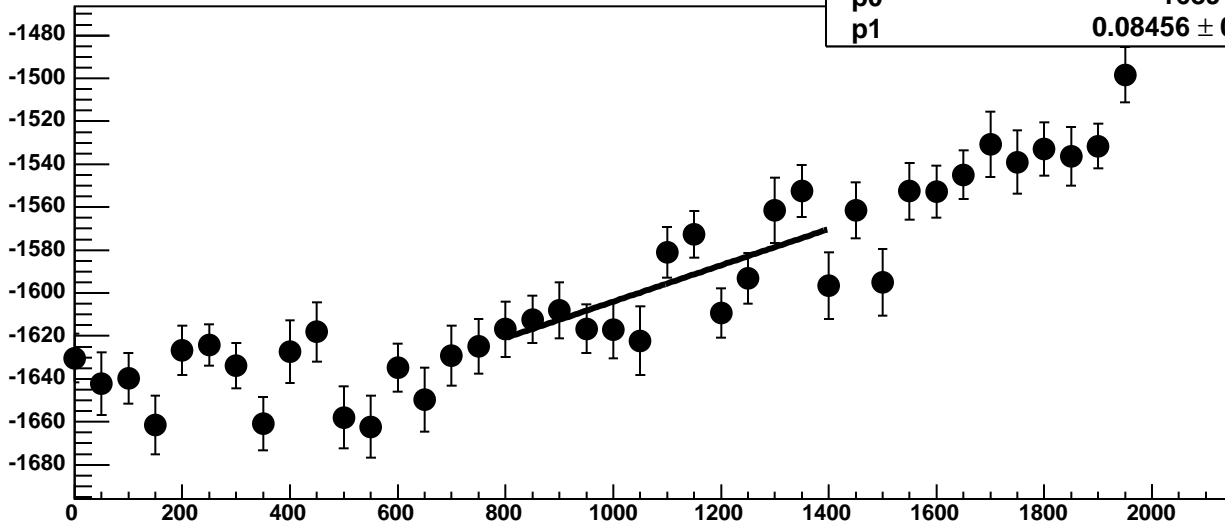
Chip 6, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC



Chip 6, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC

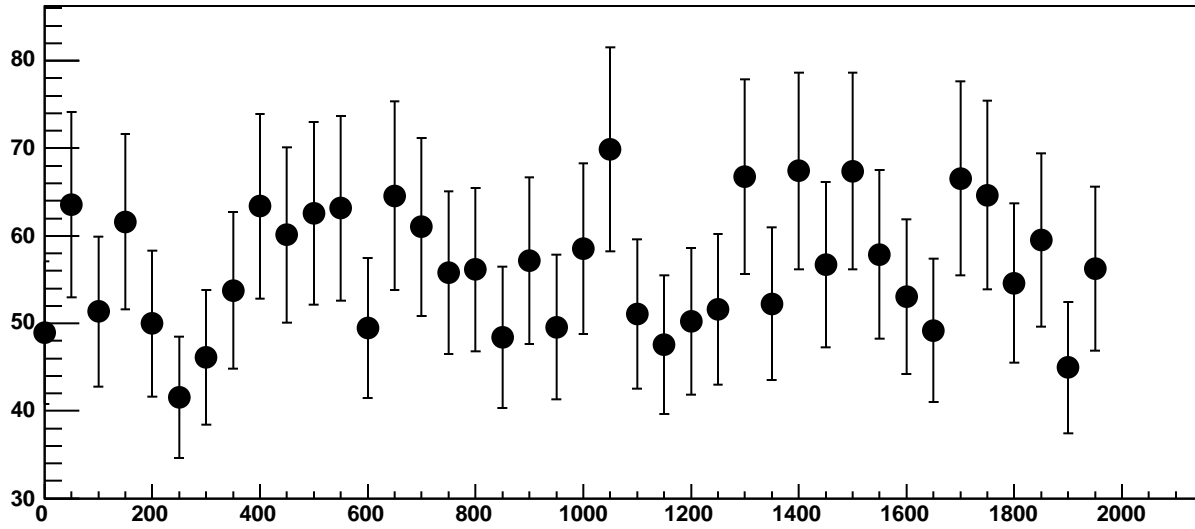


Chip 6, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC

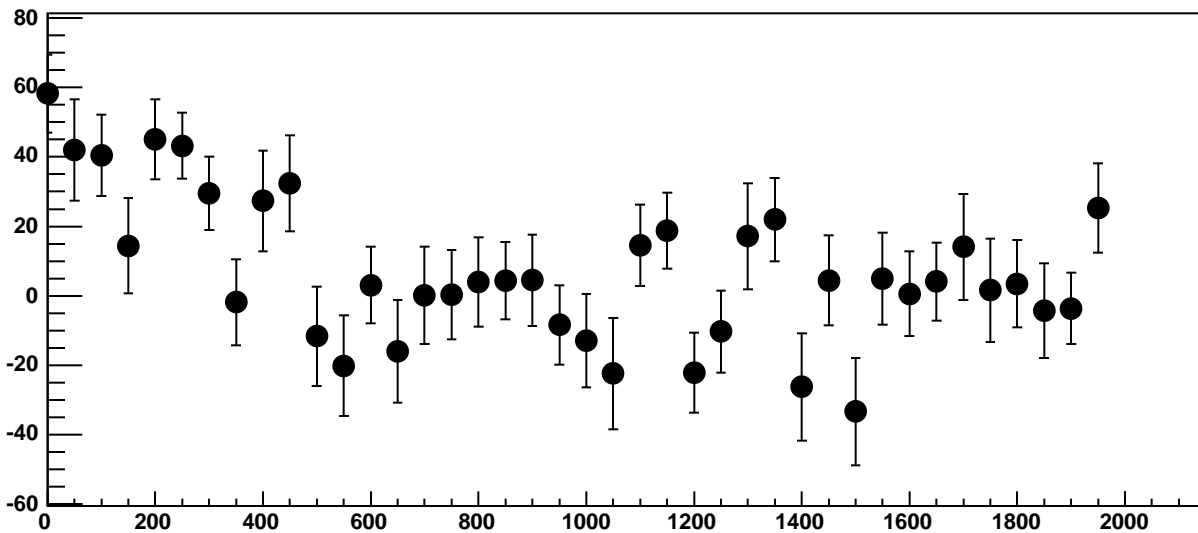


$\chi^2 / \text{ndf}$  20.23 / 11  
p0  $-1689 \pm 21.07$   
p1  $0.08456 \pm 0.01906$

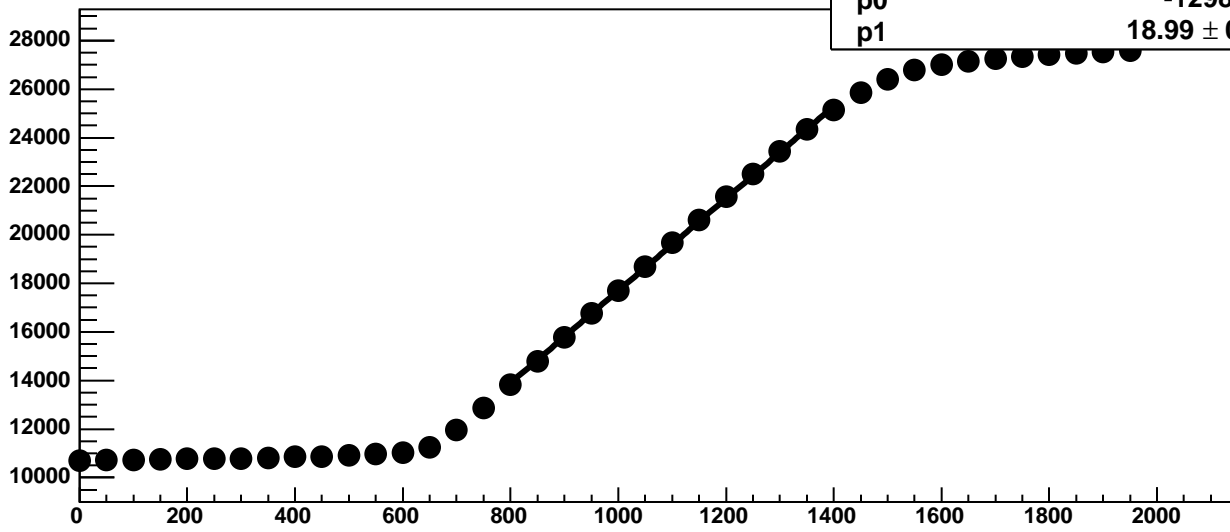
Chip 6, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC



Chip 6, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC

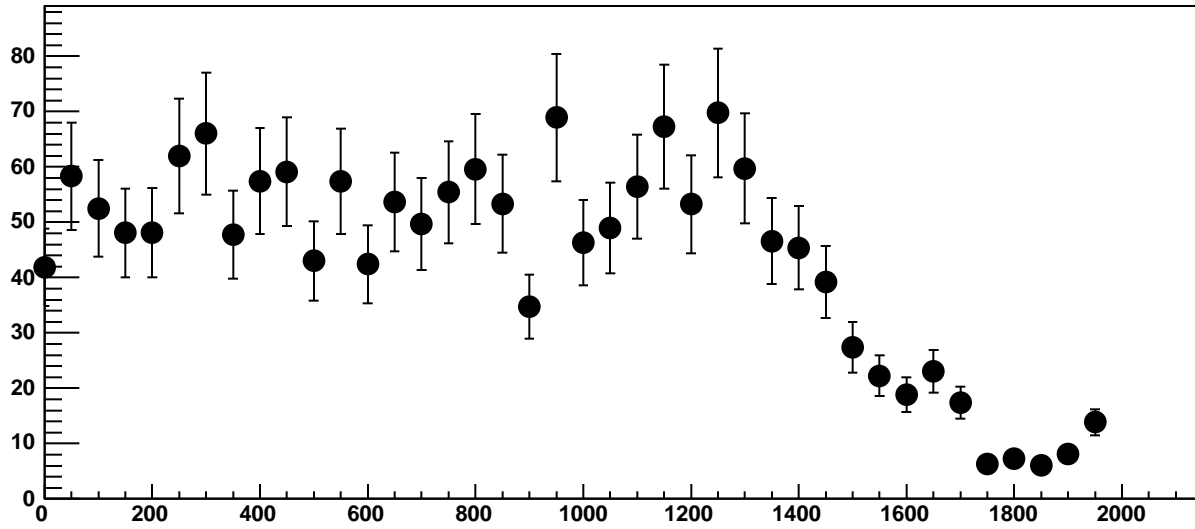


Chip 6, Channel 1, Enable 3!, Hold=30, ADC Mean vs DAC

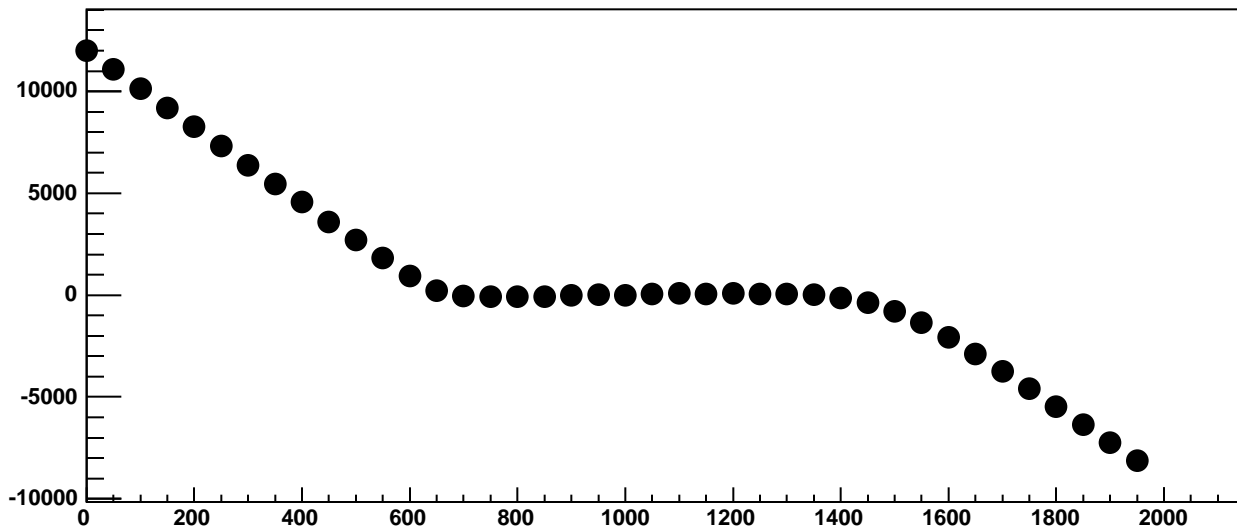


$\chi^2 / \text{ndf}$  380.9 / 11  
p0  $-1298 \pm 18.8$   
p1  $18.99 \pm 0.01697$

Chip 6, Channel 1, Enable 3!, Hold=30, ADC Noise vs DAC

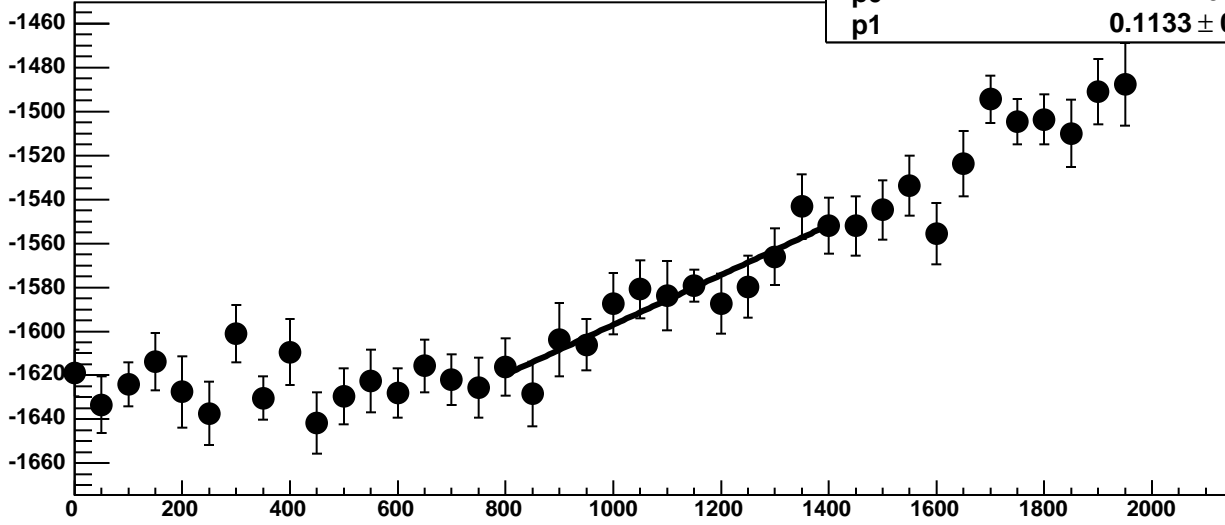


Chip 6, Channel 1, Enable 3!, Hold=30, ADC Residuals vs DAC



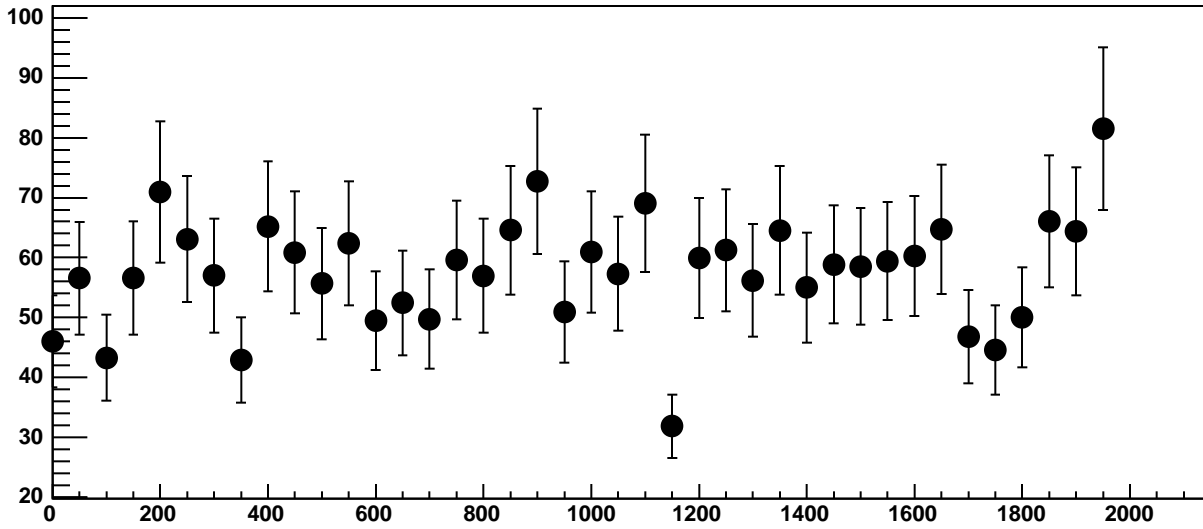


Chip 6, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC

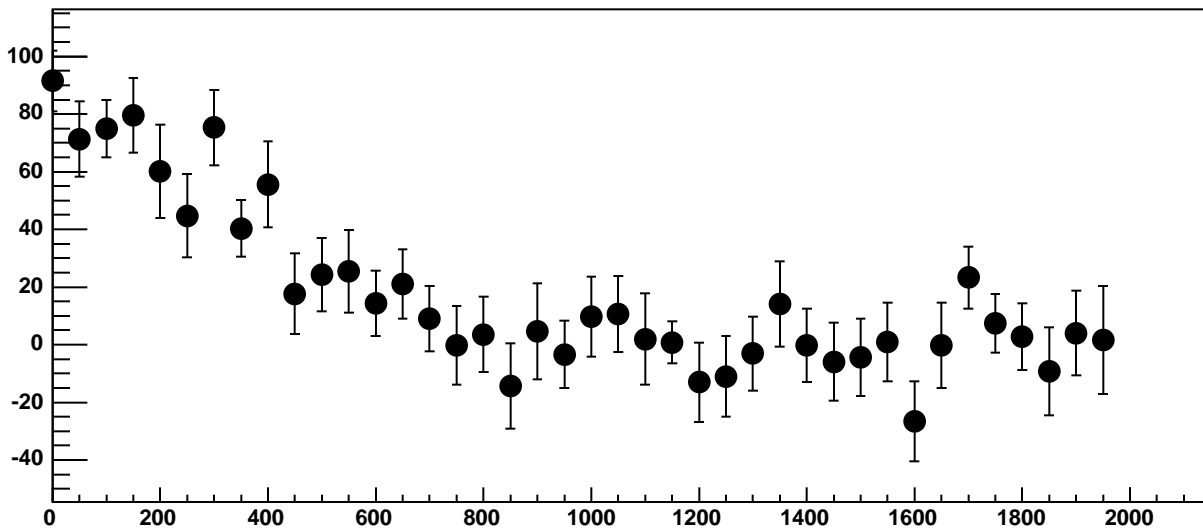


$\chi^2 / \text{ndf}$  4.824 / 11  
p0  $-1710 \pm 22.59$   
p1  $0.1133 \pm 0.02008$

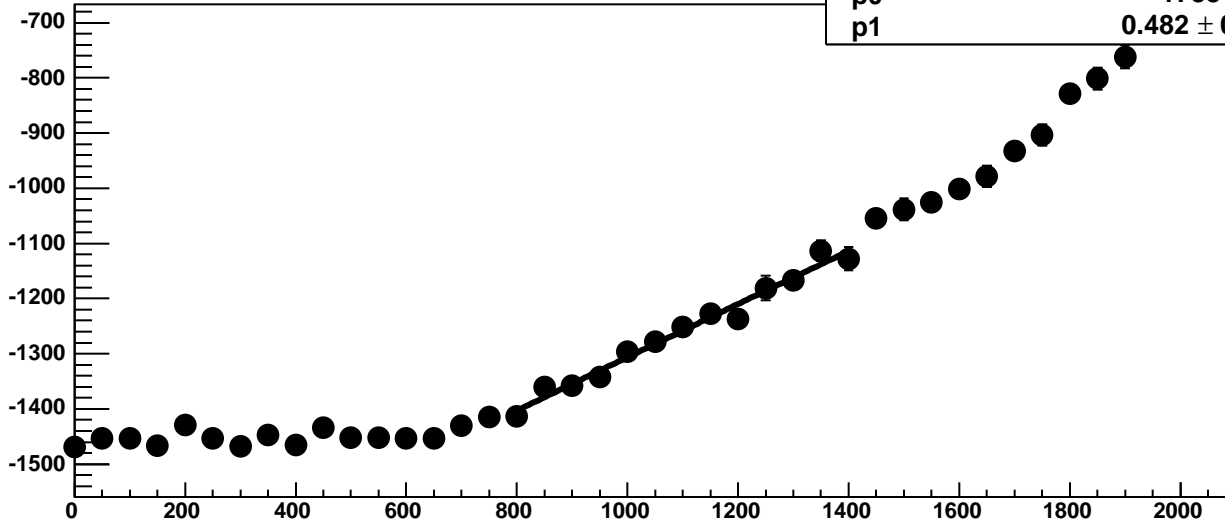
Chip 6, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC



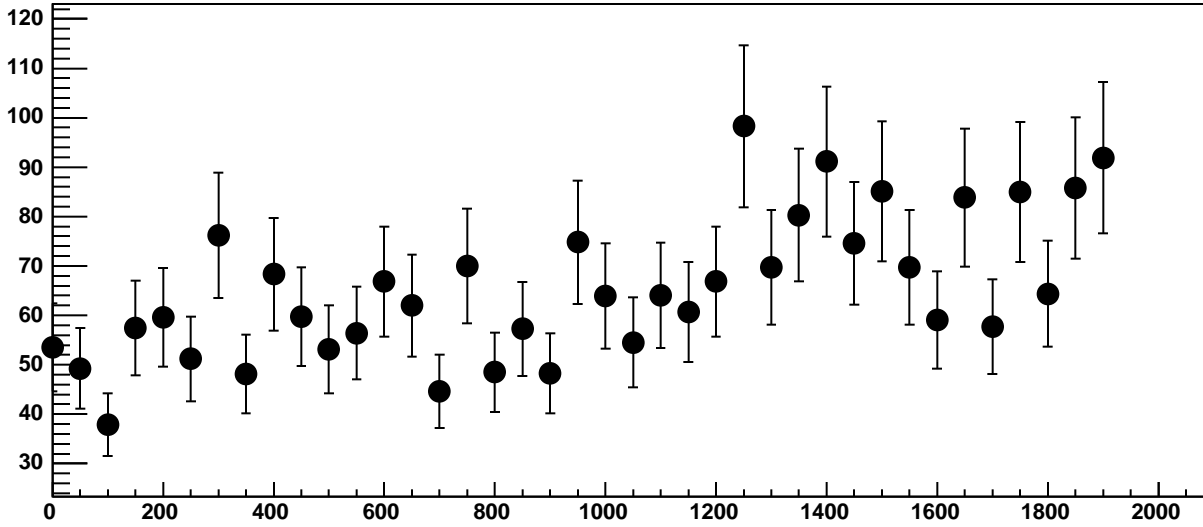
Chip 6, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC



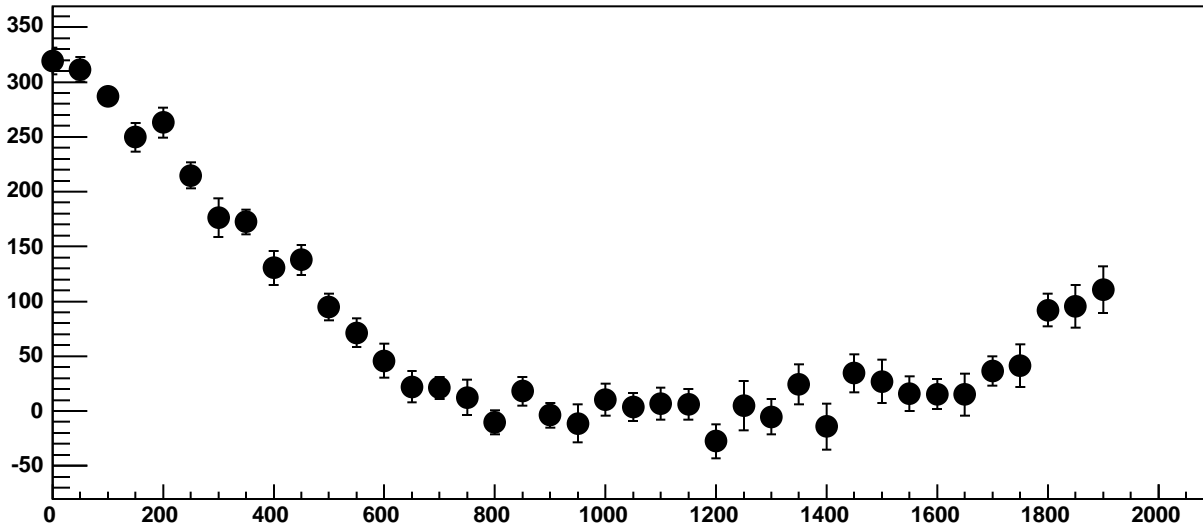
Chip 6, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC



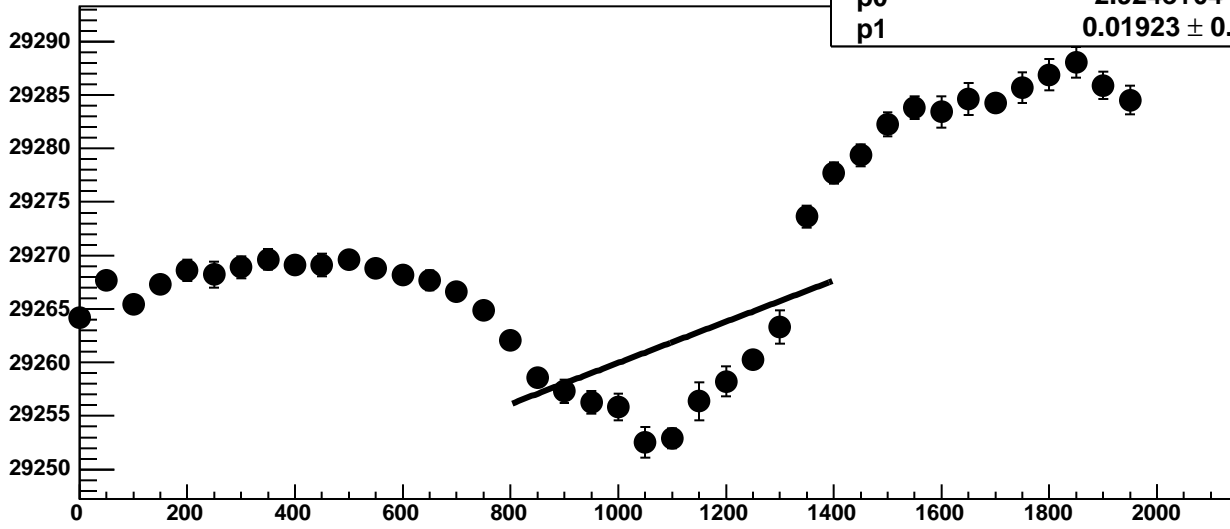
Chip 6, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC



Chip 6, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC

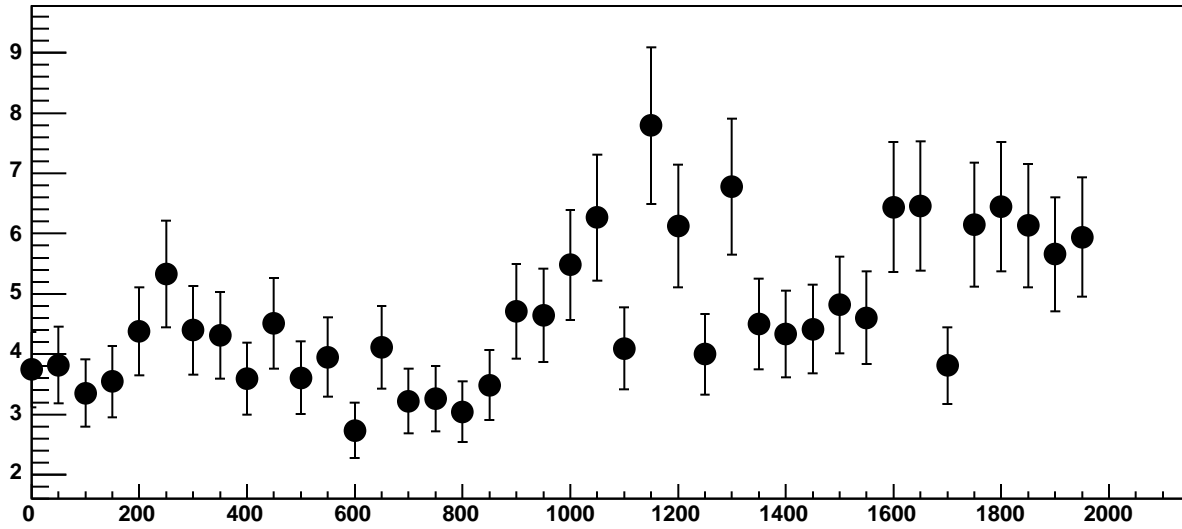


Chip 6, Channel 2, Enable 0!, Hold=30, ADC Mean vs DAC

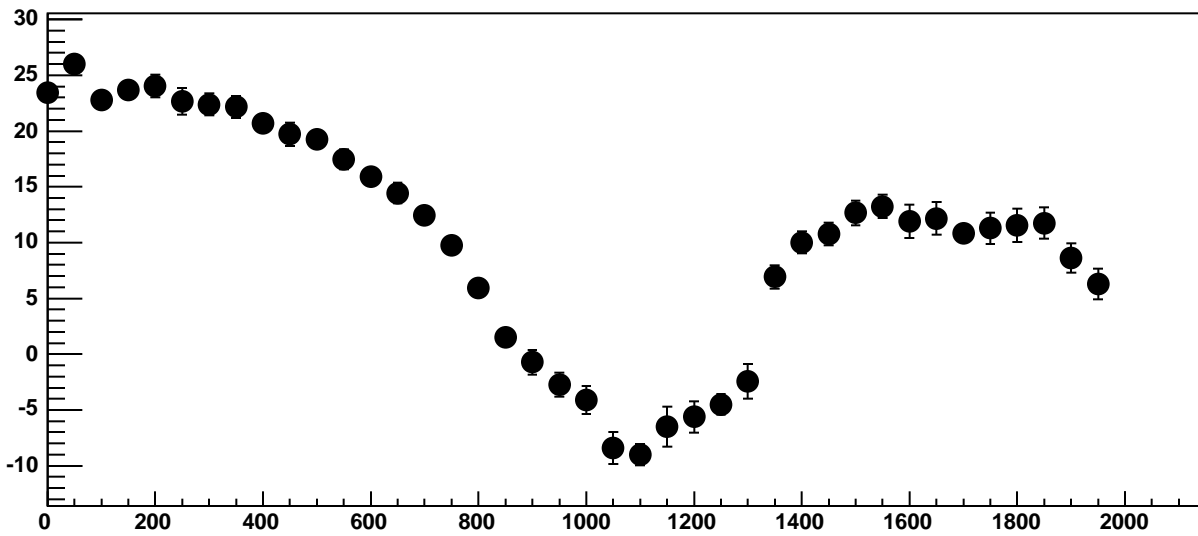


$\chi^2 / \text{ndf}$  421.8 / 11  
p0  $2.924\text{e}+04 \pm 1.494$   
p1  $0.01923 \pm 0.001388$

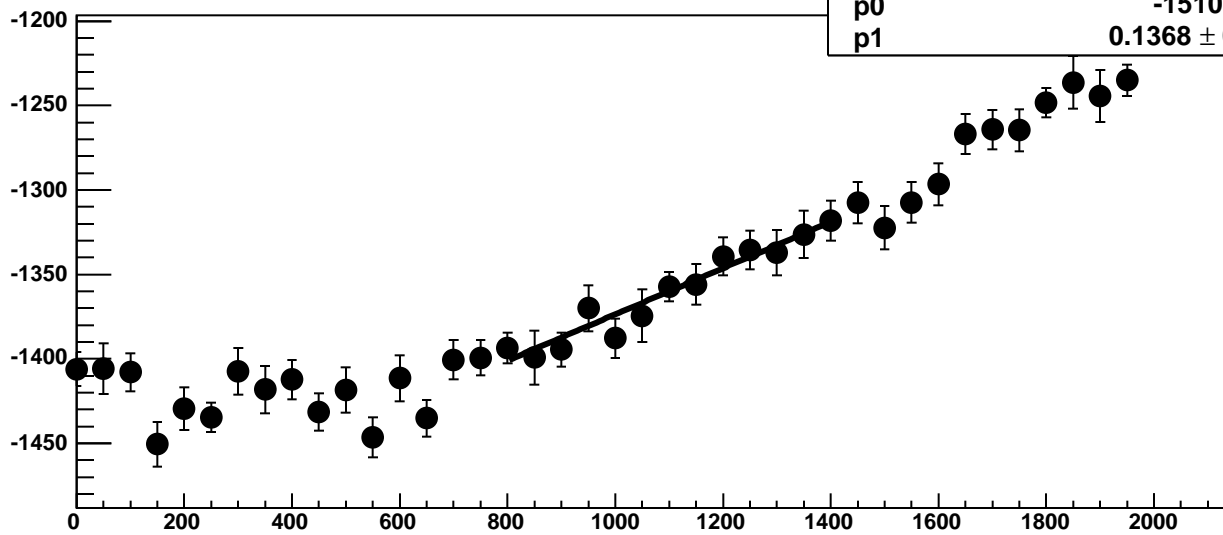
Chip 6, Channel 2, Enable 0!, Hold=30, ADC Noise vs DAC



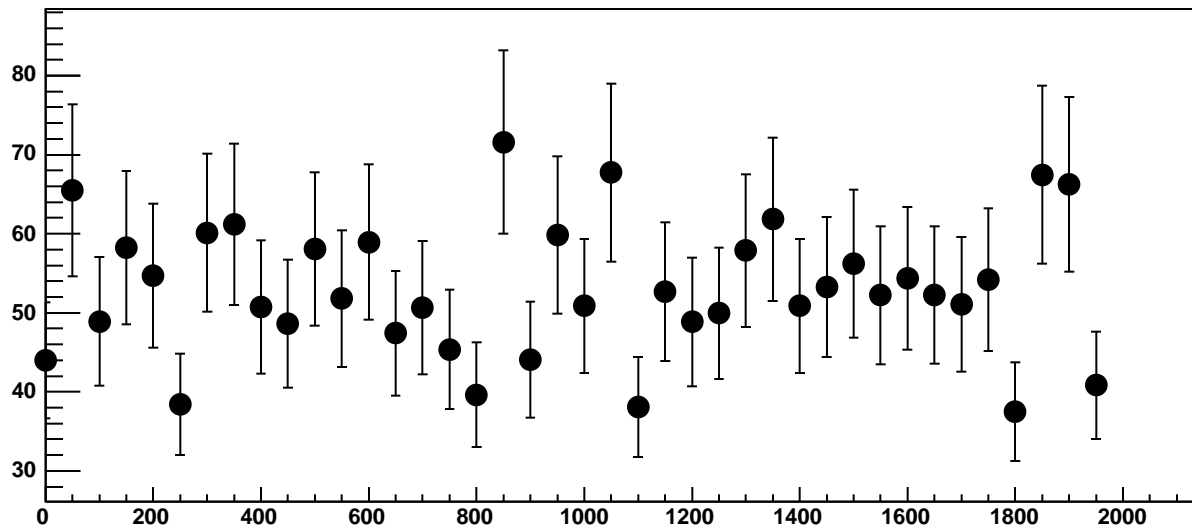
Chip 6, Channel 2, Enable 0!, Hold=30, ADC Residuals vs DAC



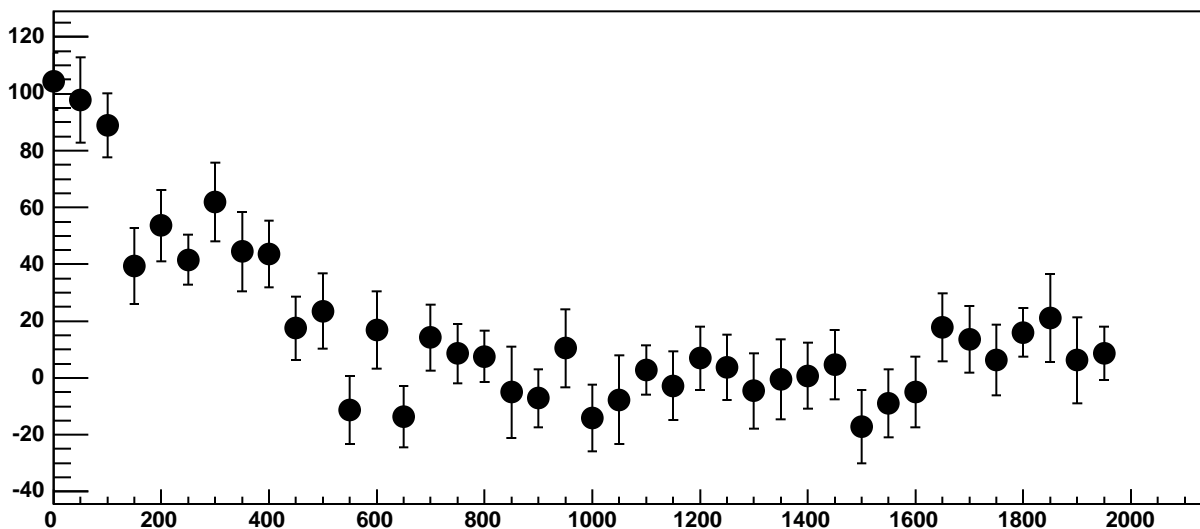
Chip 6, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC



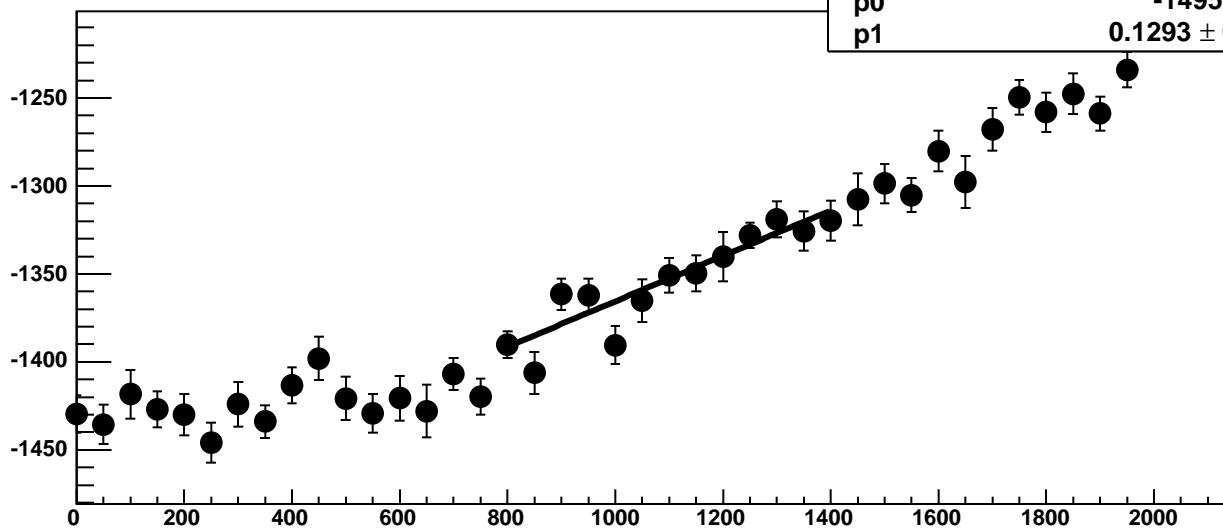
Chip 6, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC



Chip 6, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC

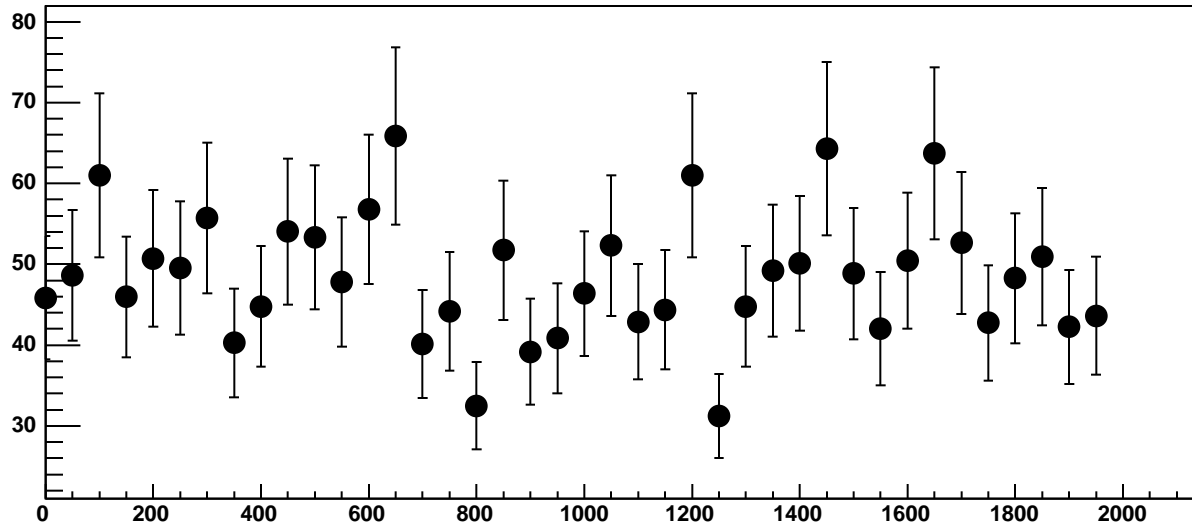


Chip 6, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC

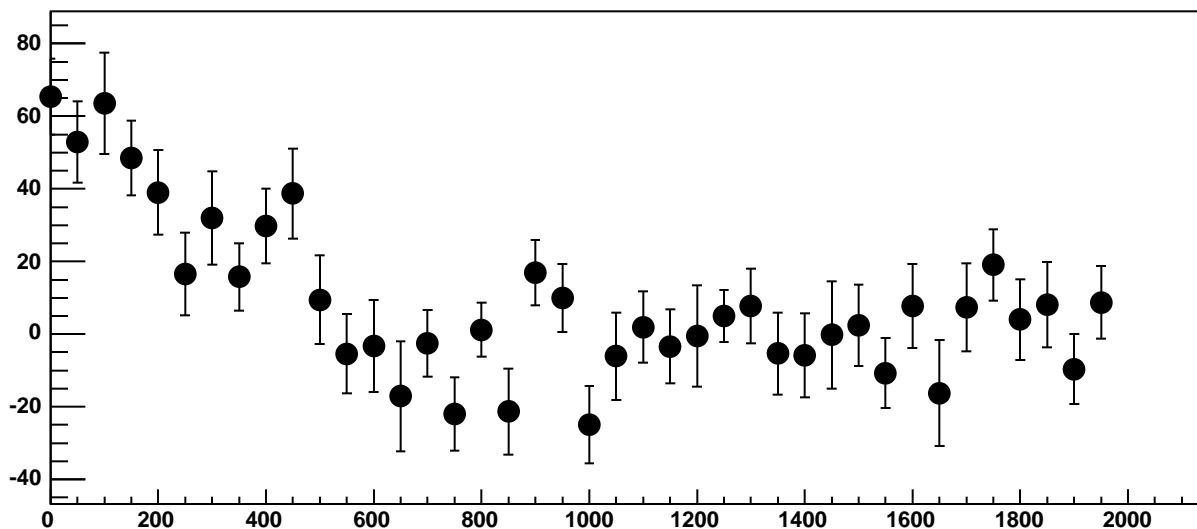


$\chi^2 / \text{ndf}$  15.29 / 11  
p0  $-1495 \pm 15.63$   
p1  $0.1293 \pm 0.01424$

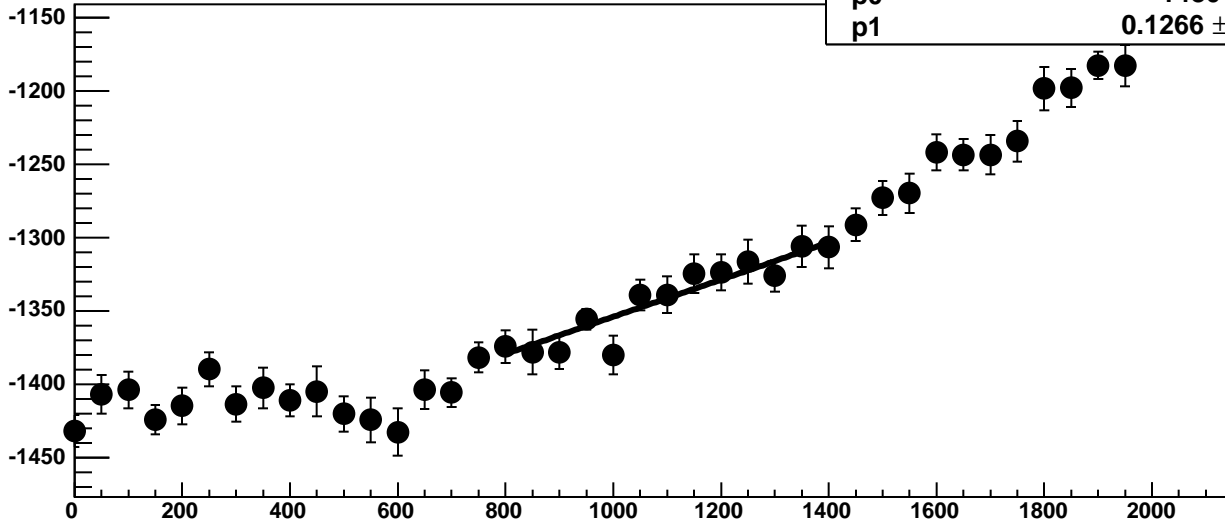
Chip 6, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC



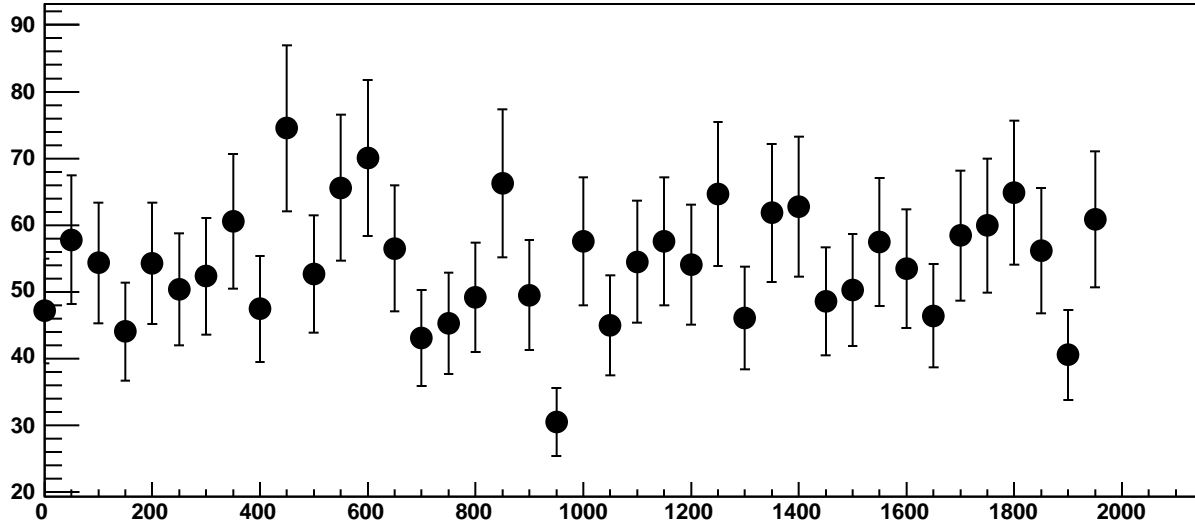
Chip 6, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC



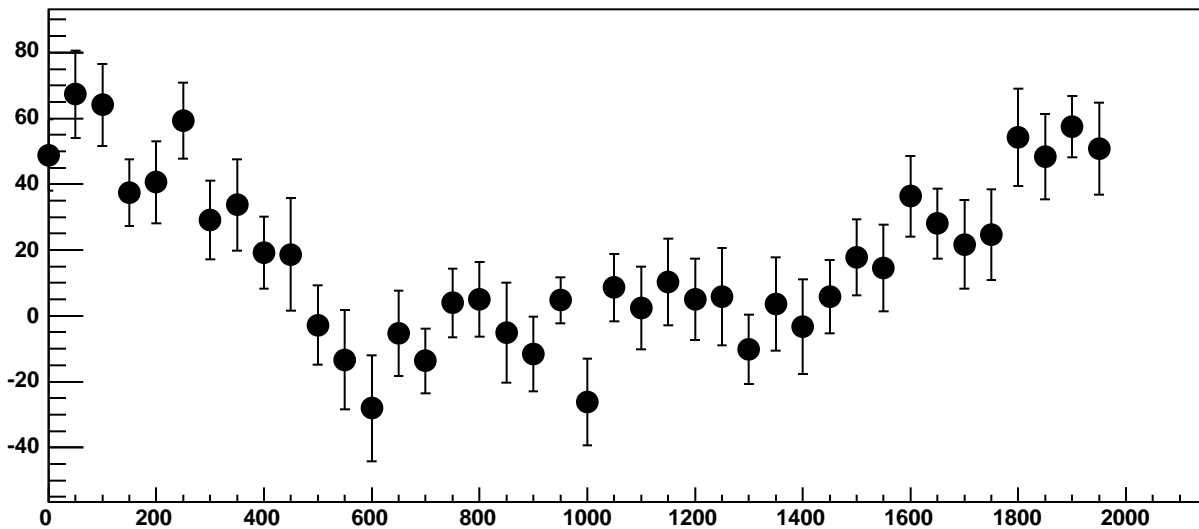
Chip 6, Channel 2, Enable 3, Hold=30, ADC Mean vs DAC



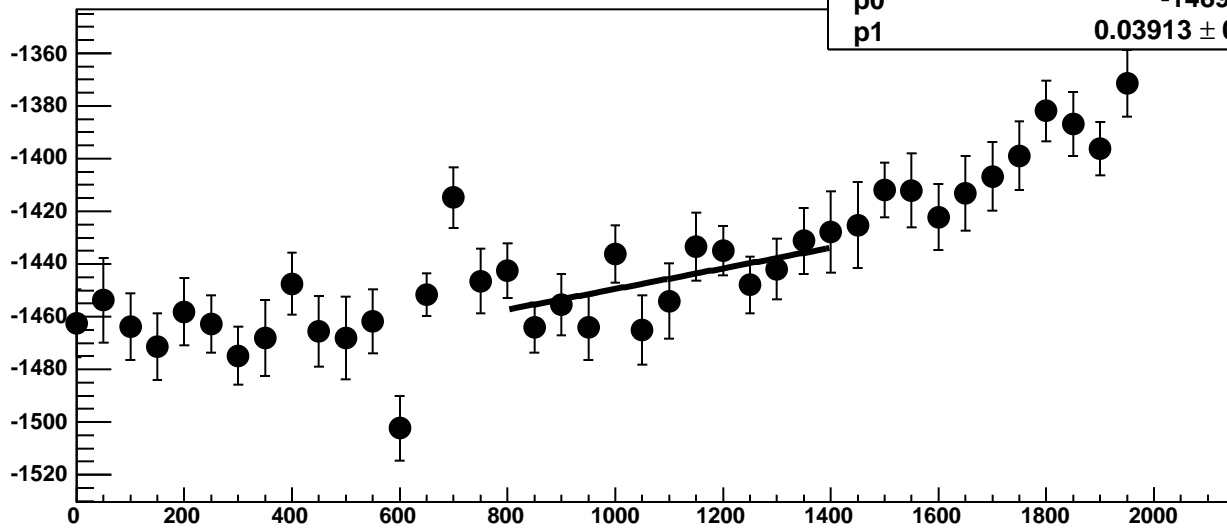
Chip 6, Channel 2, Enable 3, Hold=30, ADC Noise vs DAC



Chip 6, Channel 2, Enable 3, Hold=30, ADC Residuals vs DAC

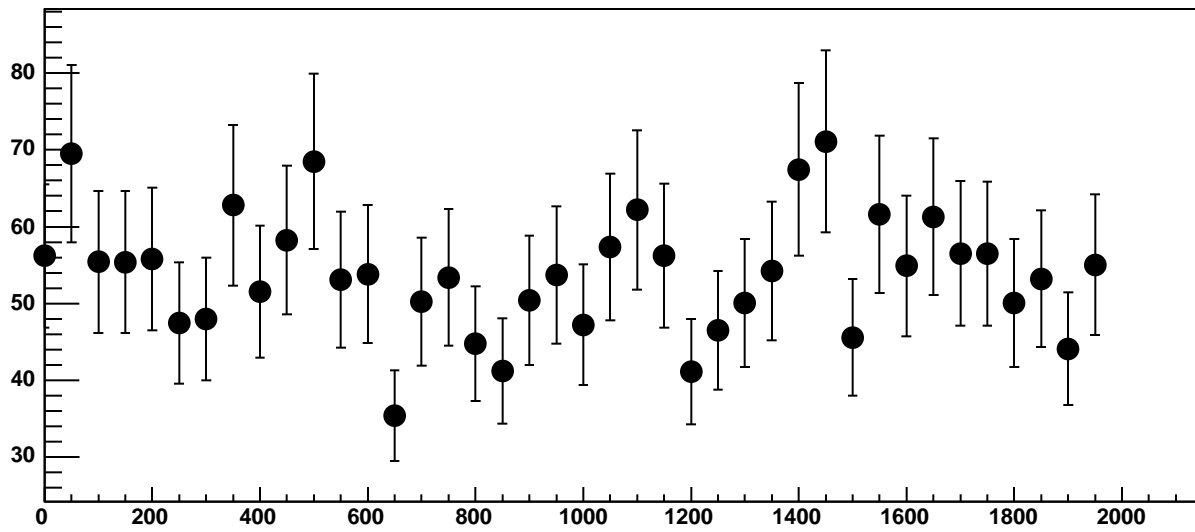


Chip 6, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC

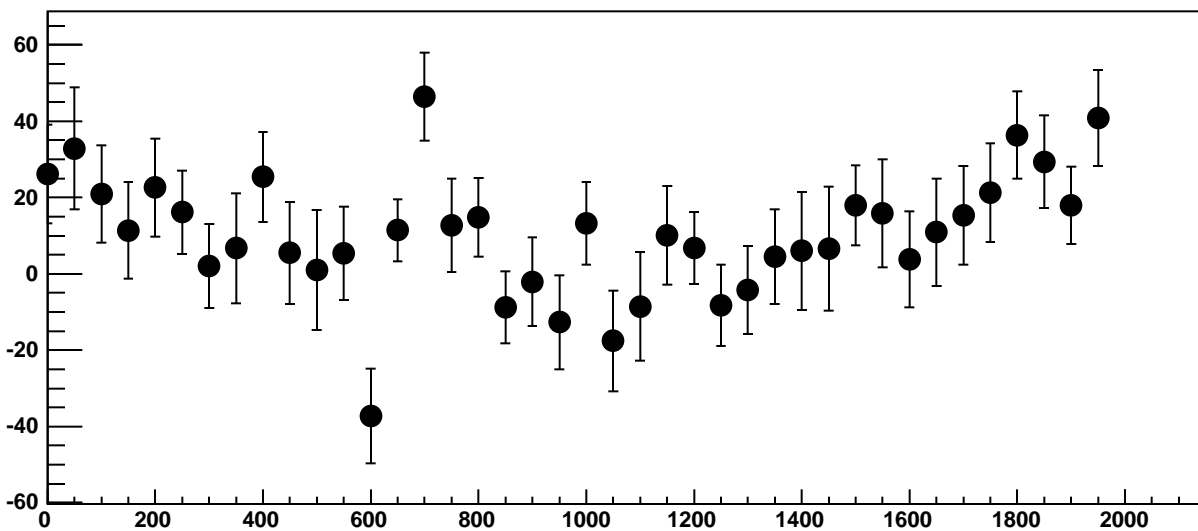


$\chi^2 / \text{ndf}$  9.807 / 11  
p0  $-1489 \pm 18.6$   
p1  $0.03913 \pm 0.01699$

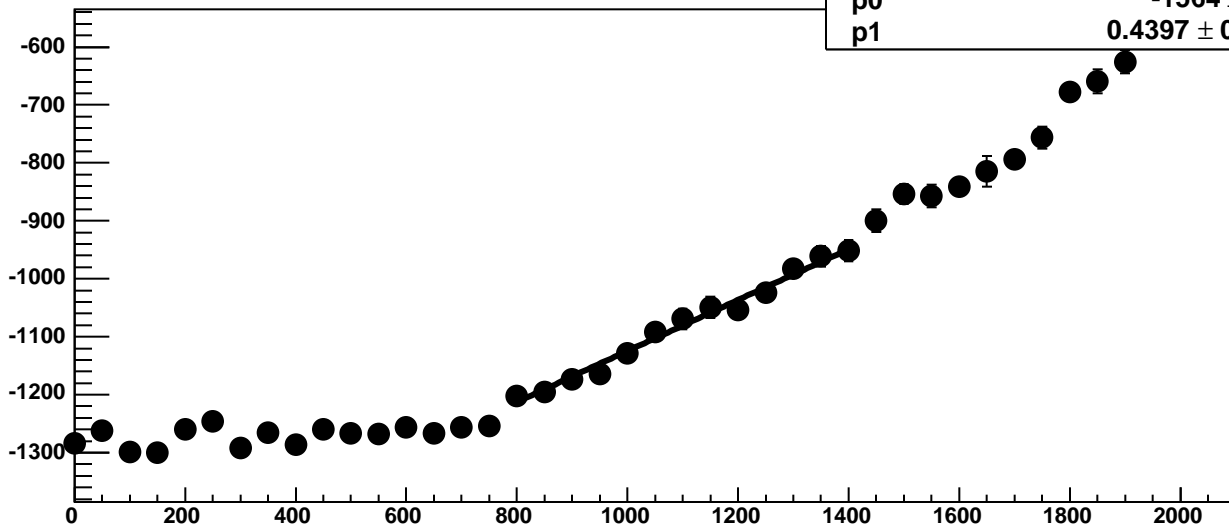
Chip 6, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC



Chip 6, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC

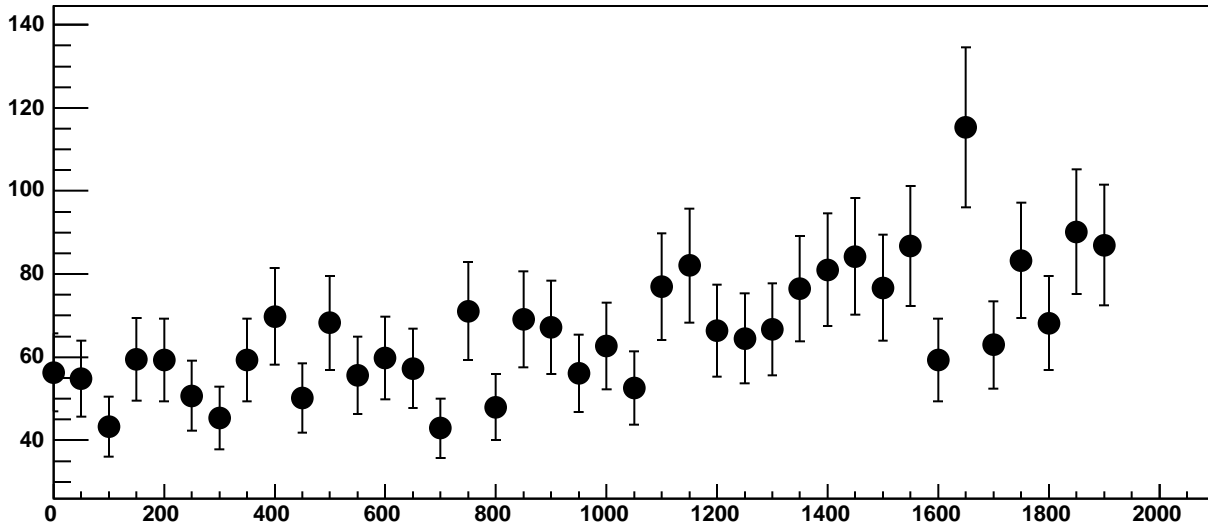


Chip 6, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC

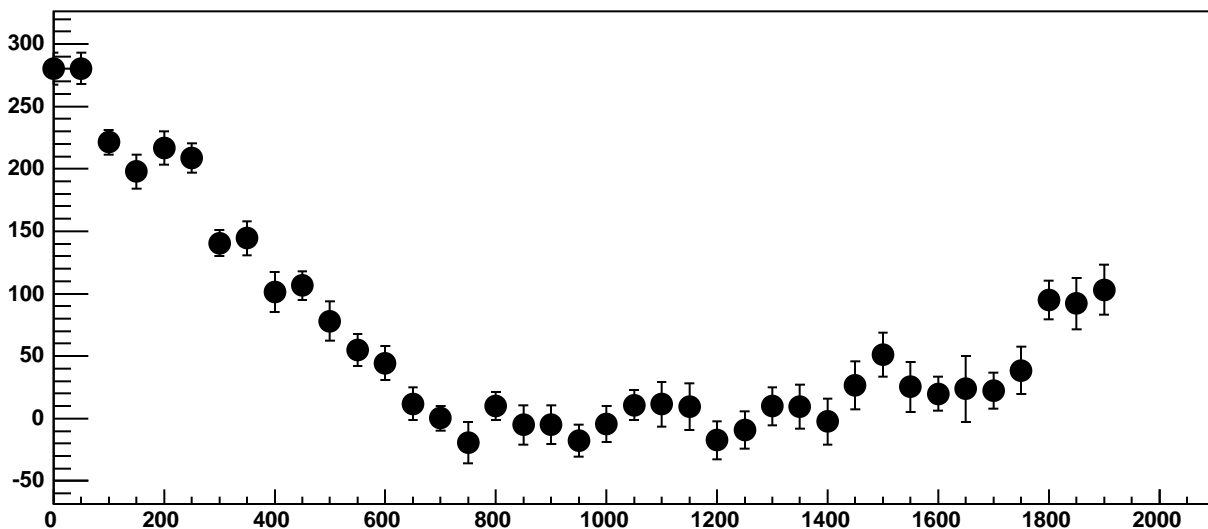


$\chi^2 / \text{ndf}$  6.884 / 11  
p0  $-1564 \pm 23.75$   
p1  $0.4397 \pm 0.02202$

Chip 6, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC

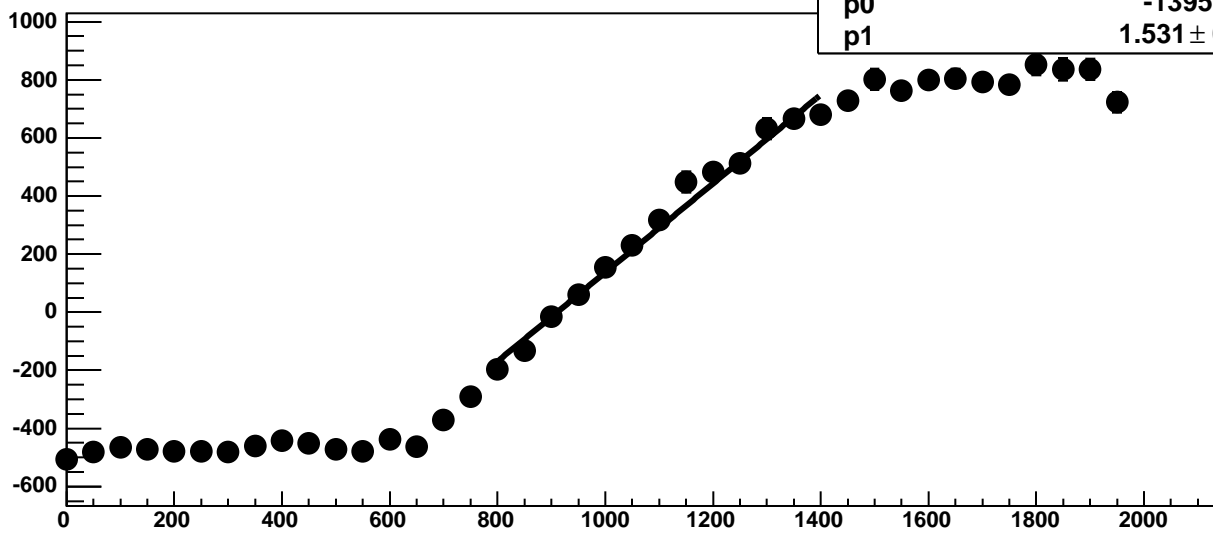


Chip 6, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC



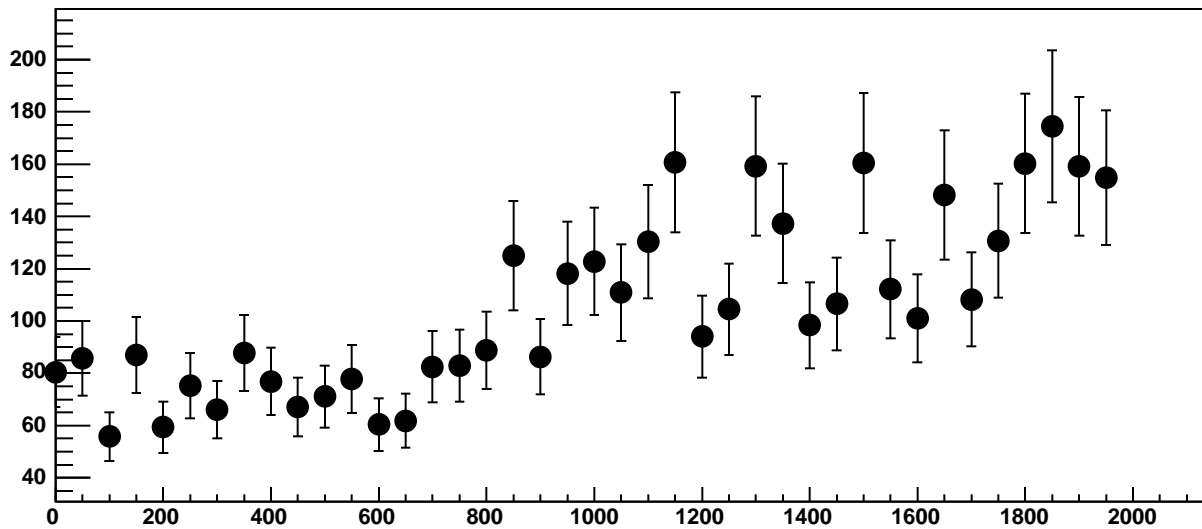


Chip 6, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC

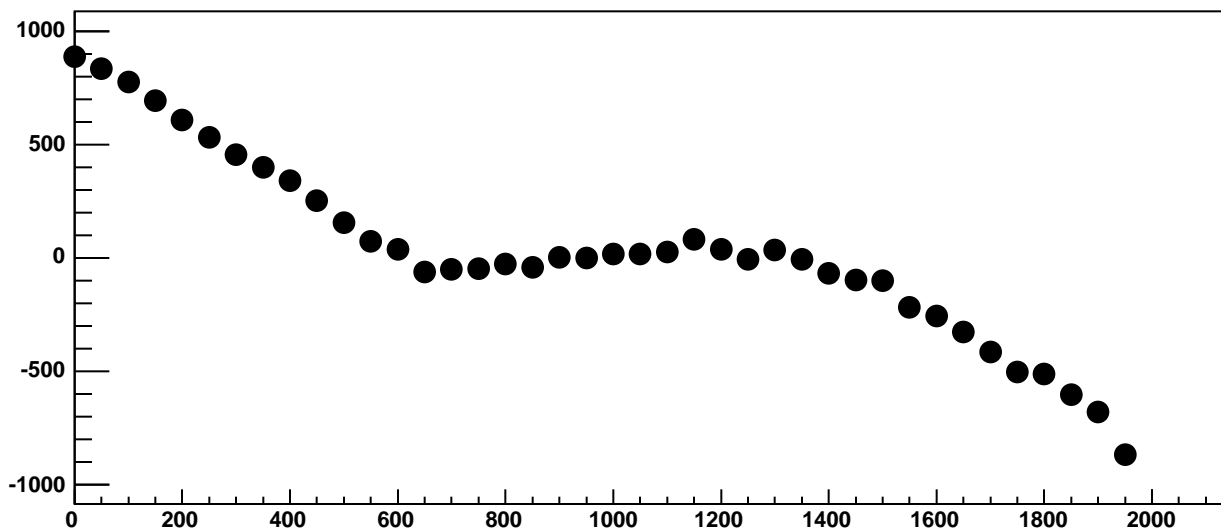


$\chi^2 / \text{ndf}$  23.86 / 11  
p0  $-1395 \pm 39.63$   
p1  $1.531 \pm 0.03617$

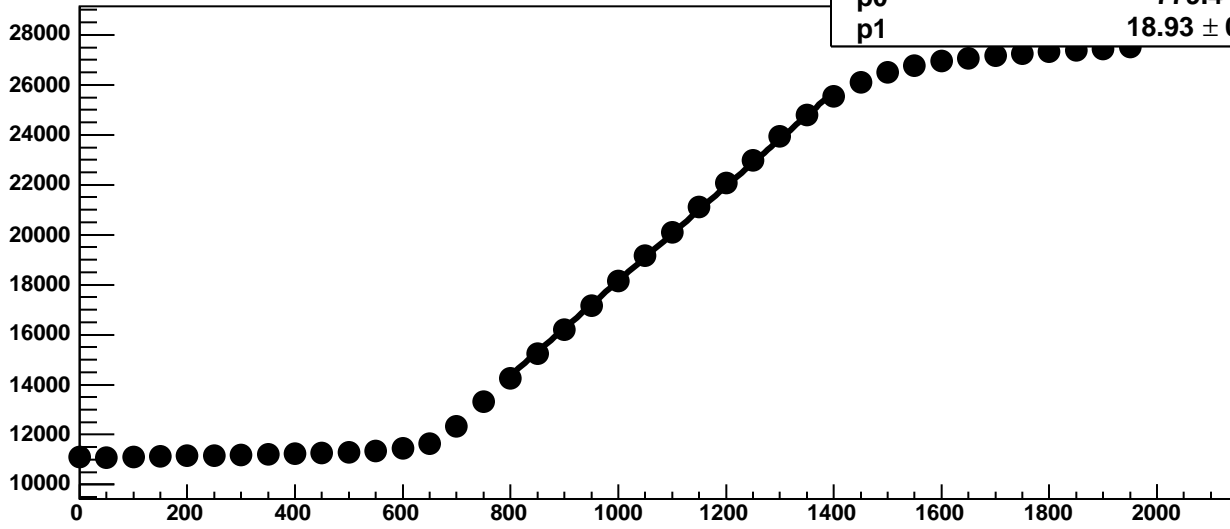
Chip 6, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC



Chip 6, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC

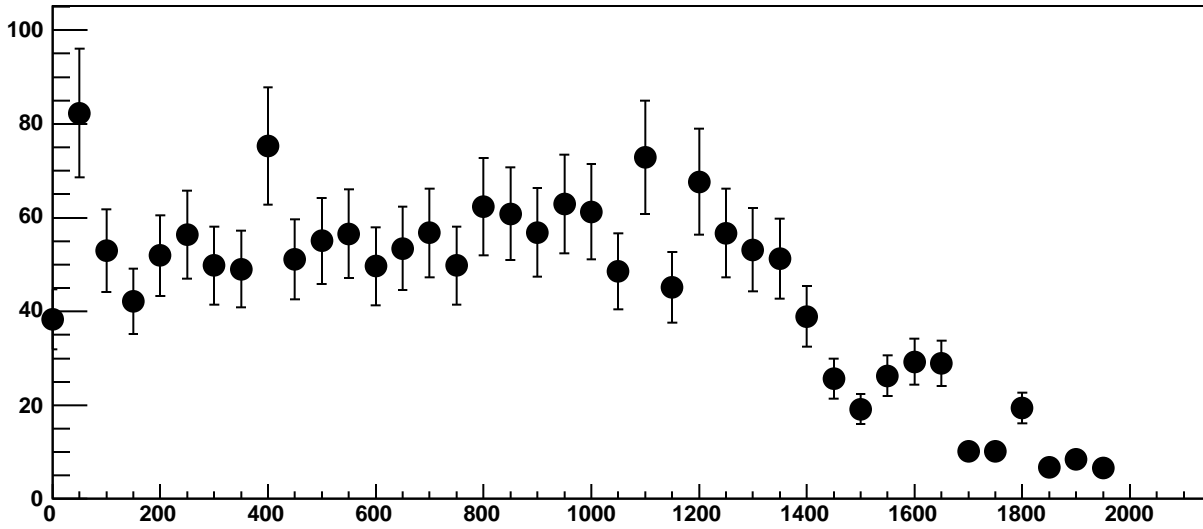


Chip 6, Channel 3, Enable 1!, Hold=30, ADC Mean vs DAC

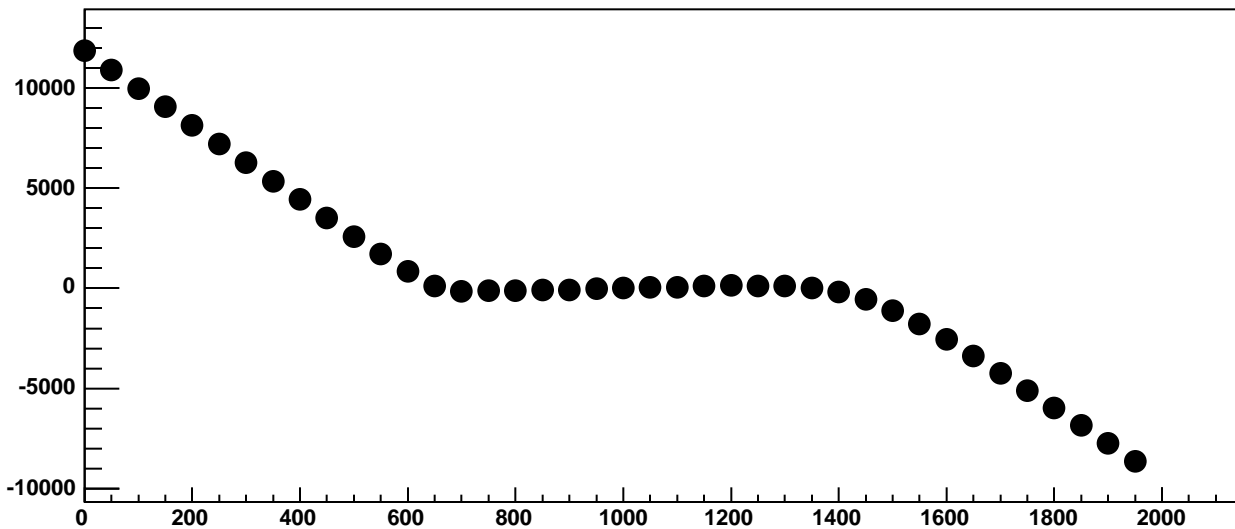


$\chi^2 / \text{ndf}$	966.5 / 11
p0	-779.4 ± 20.62
p1	18.93 ± 0.01788

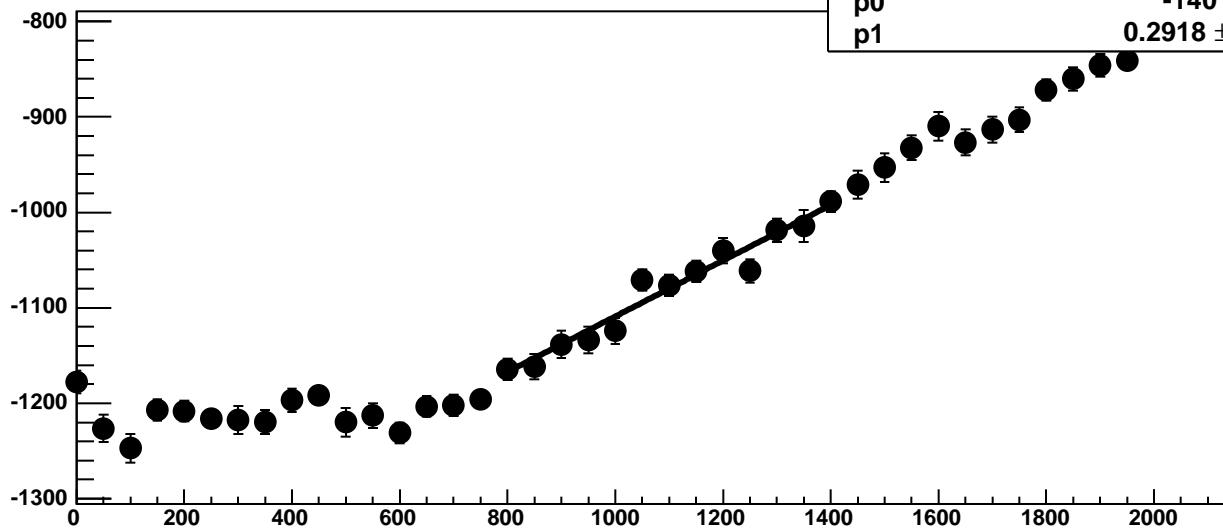
Chip 6, Channel 3, Enable 1!, Hold=30, ADC Noise vs DAC



Chip 6, Channel 3, Enable 1!, Hold=30, ADC Residuals vs DAC

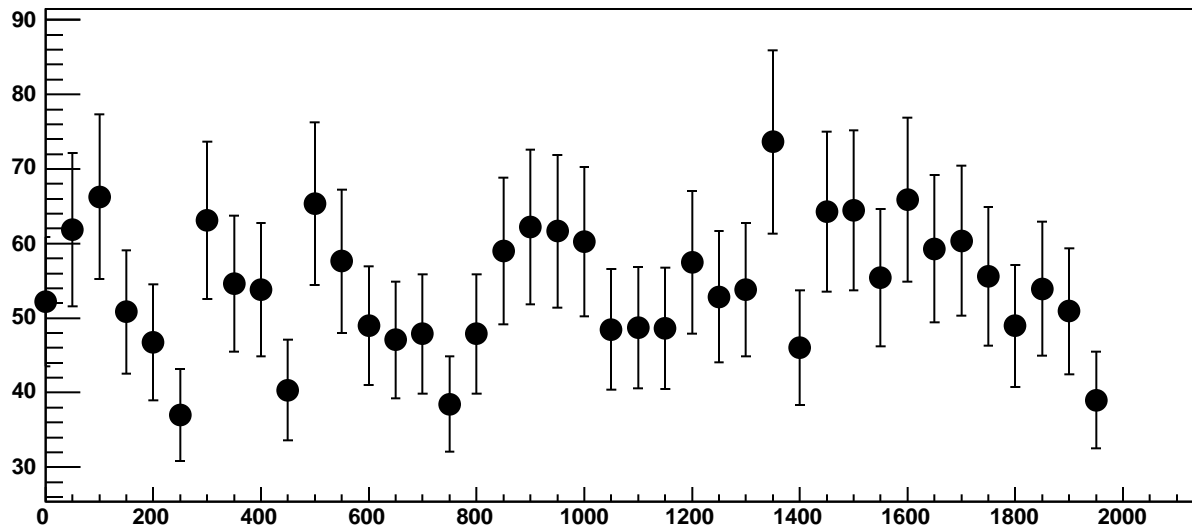


Chip 6, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC

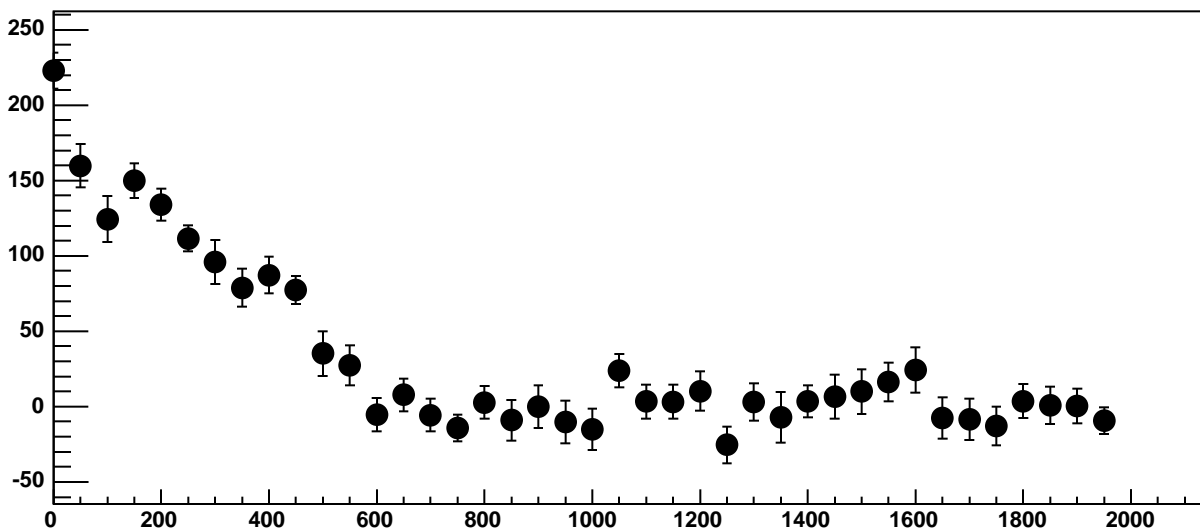


$\chi^2 / \text{ndf}$  12.27 / 11  
p0  $-1401 \pm 20.5$   
p1  $0.2918 \pm 0.0183$

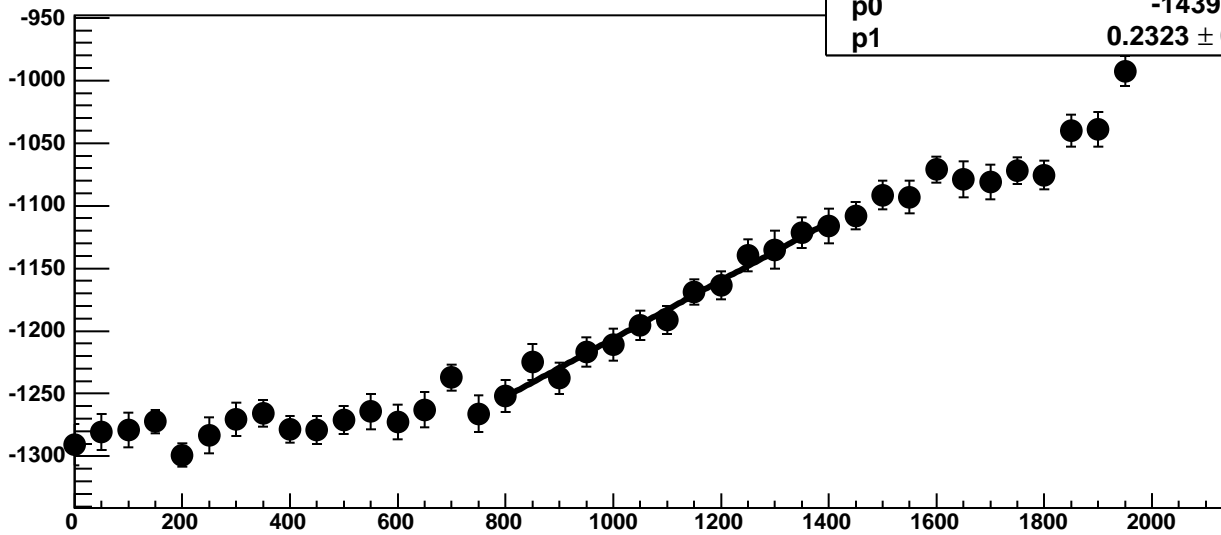
Chip 6, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC



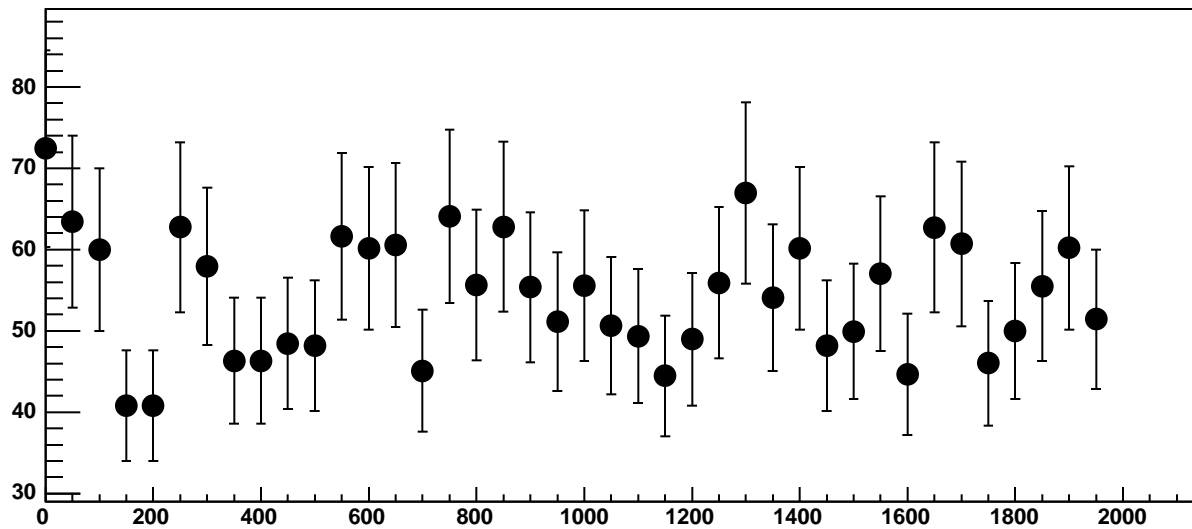
Chip 6, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC



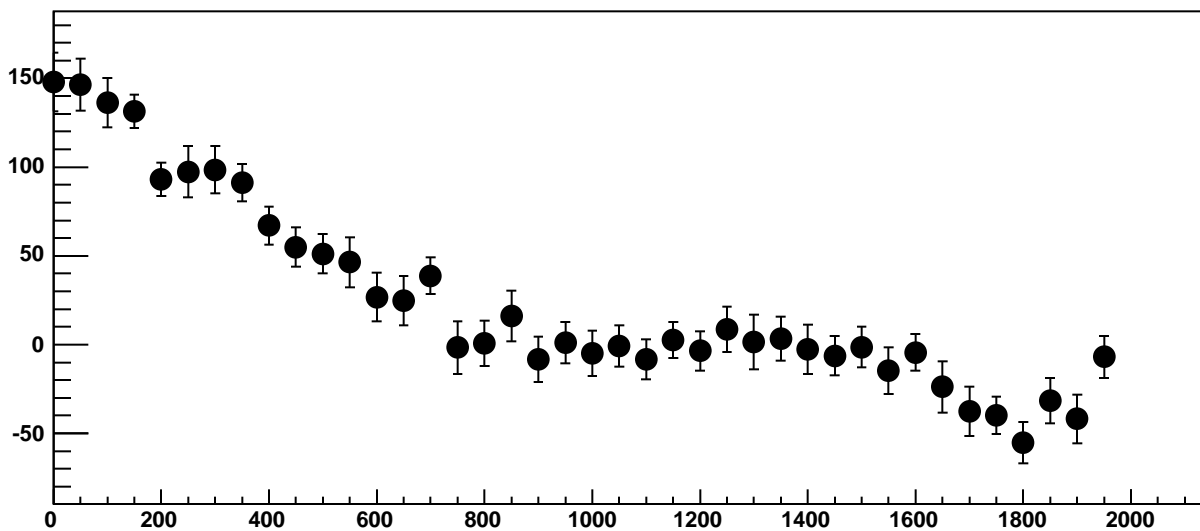
Chip 6, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC



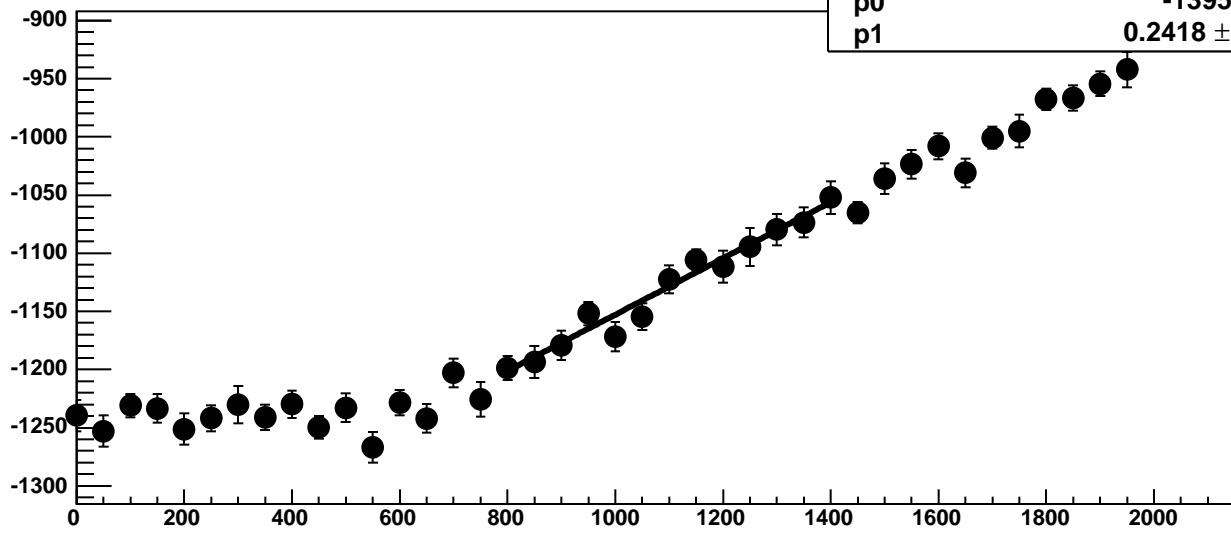
Chip 6, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC



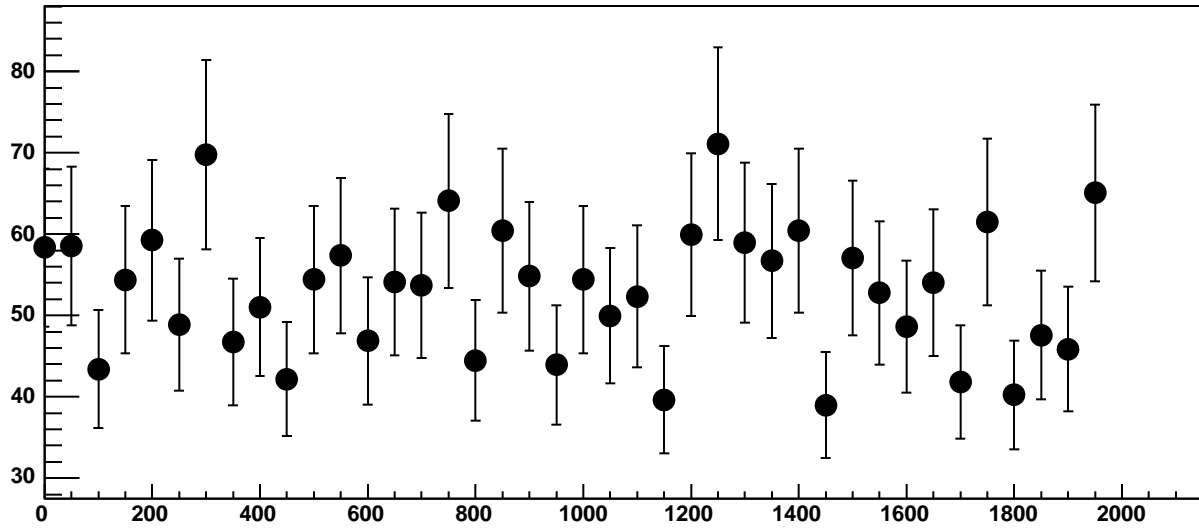
Chip 6, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC



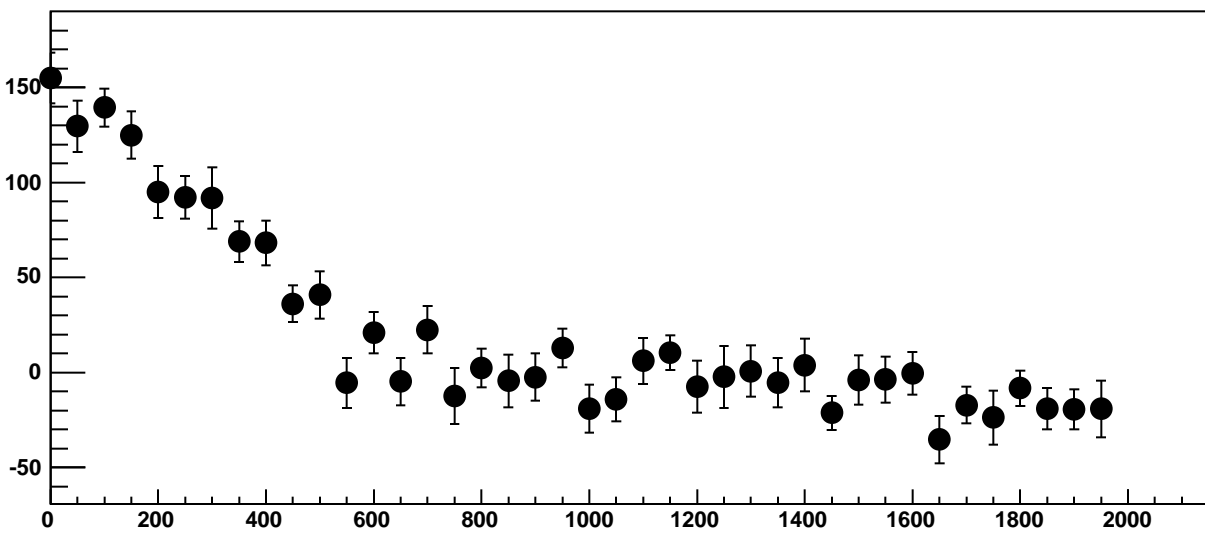
Chip 6, Channel 3, Enable 4, Hold=30, ADC Mean vs DAC



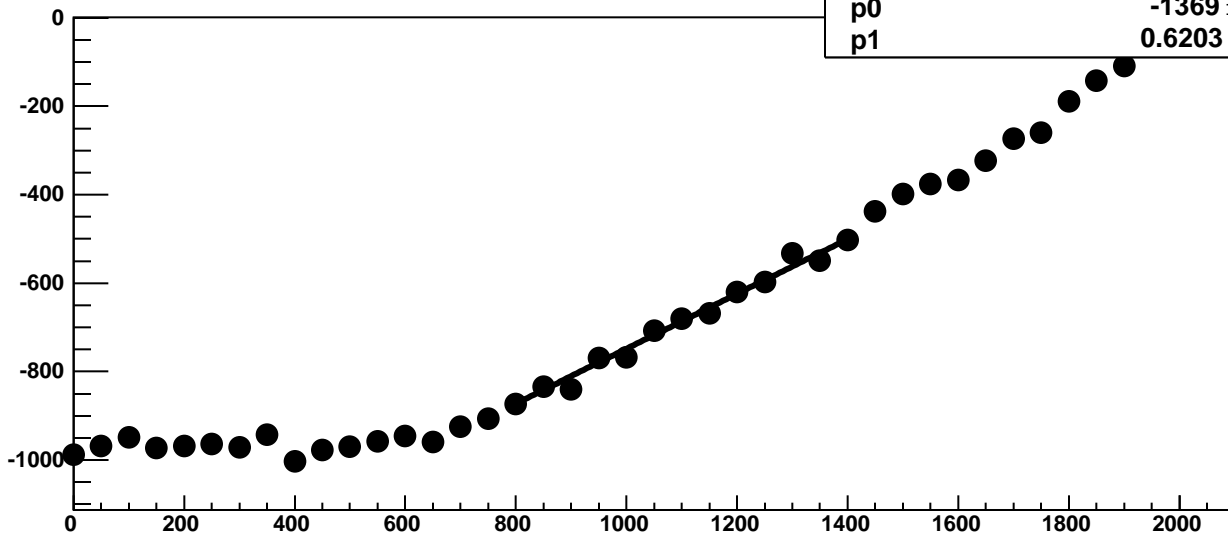
Chip 6, Channel 3, Enable 4, Hold=30, ADC Noise vs DAC



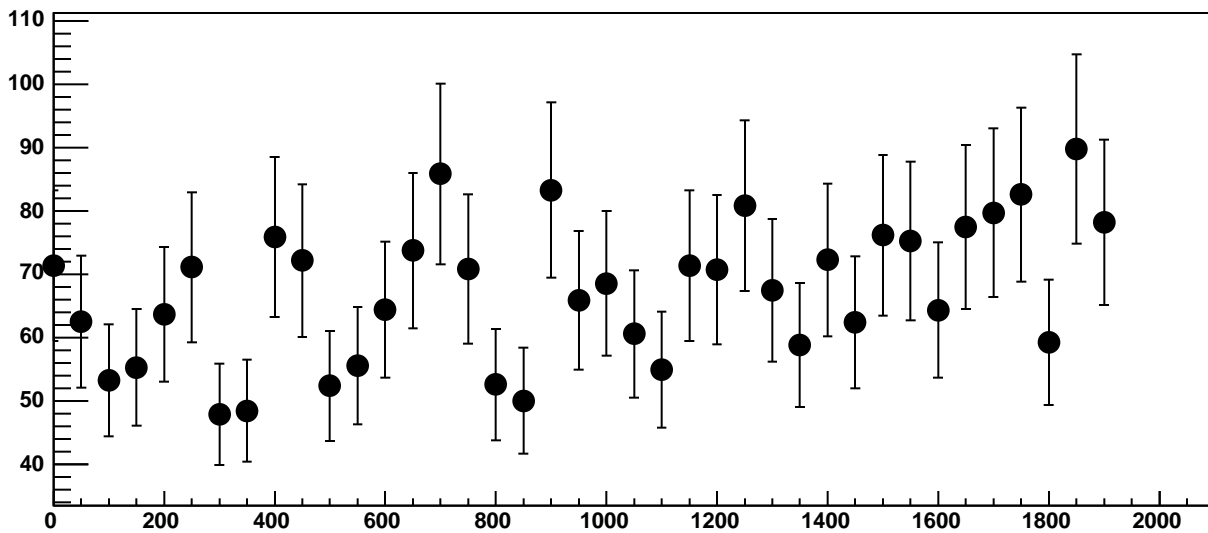
Chip 6, Channel 3, Enable 4, Hold=30, ADC Residuals vs DAC



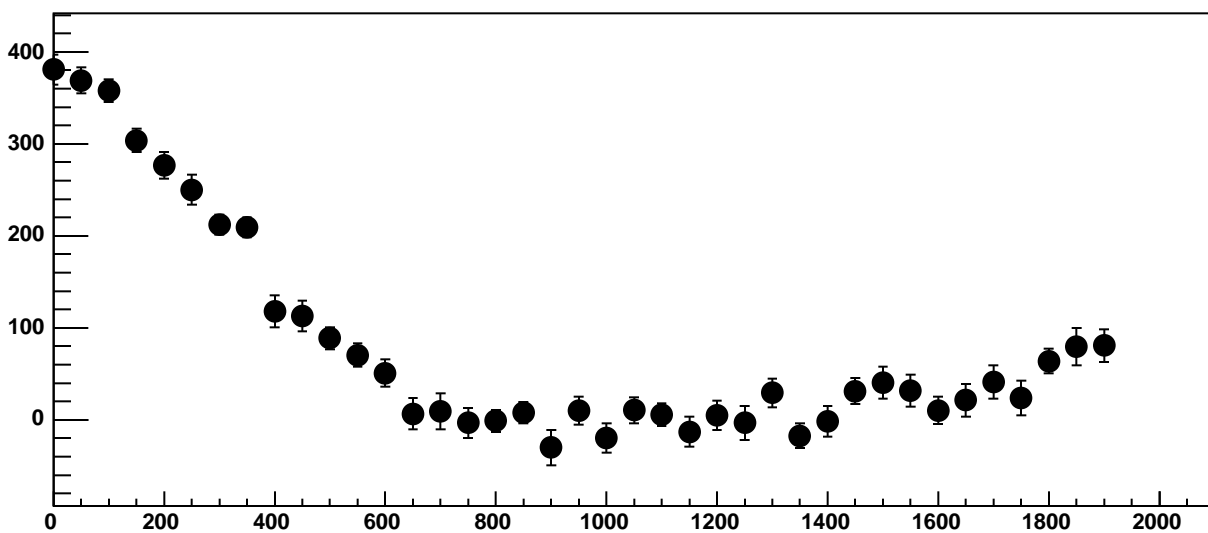
Chip 6, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC



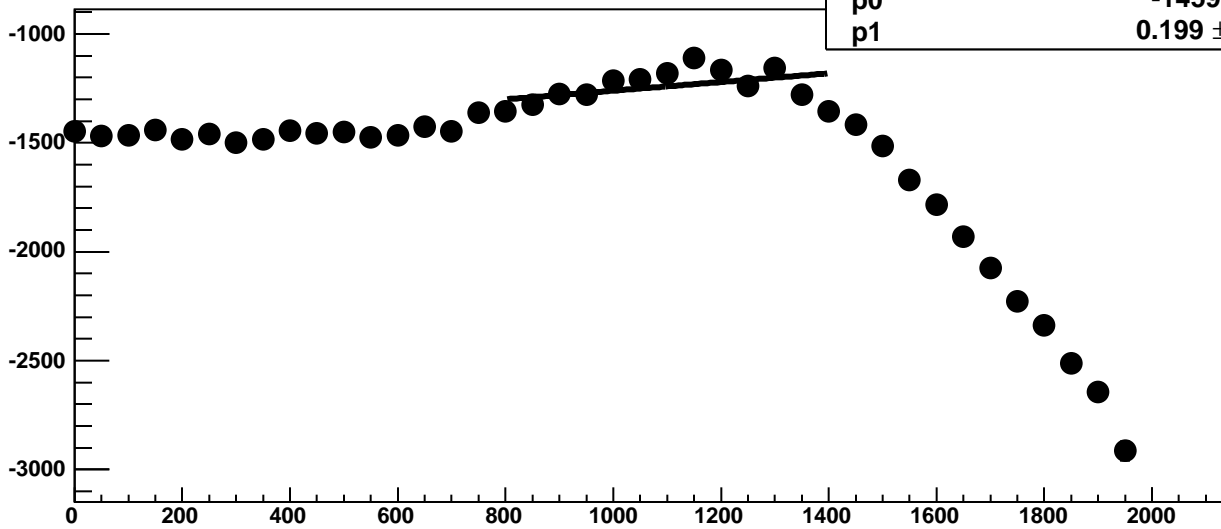
Chip 6, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC



Chip 6, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 6, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

69.09 / 11

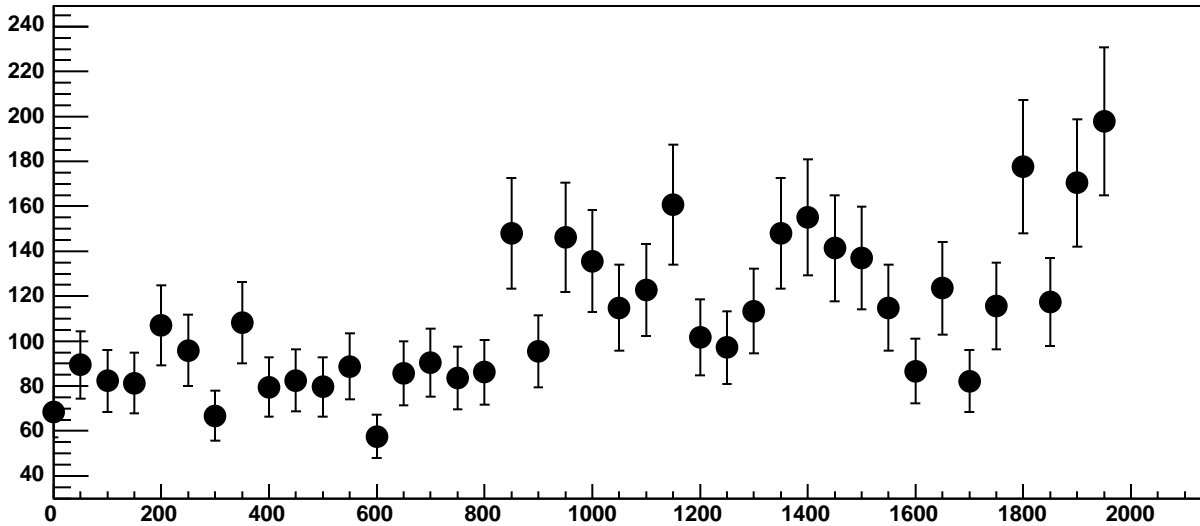
p0

$-1459 \pm 43.14$

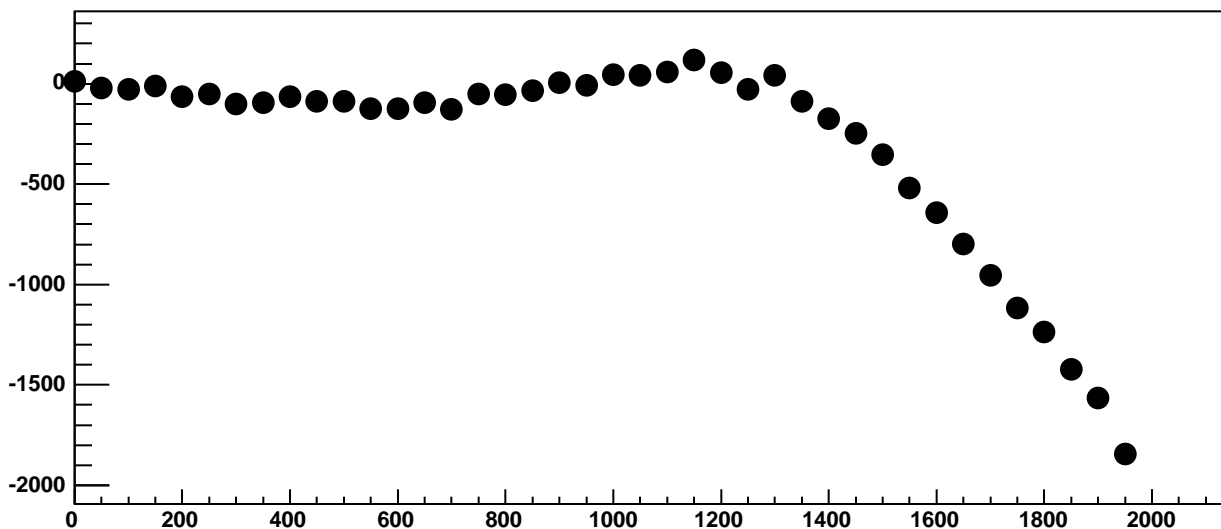
p1

$0.199 \pm 0.0395$

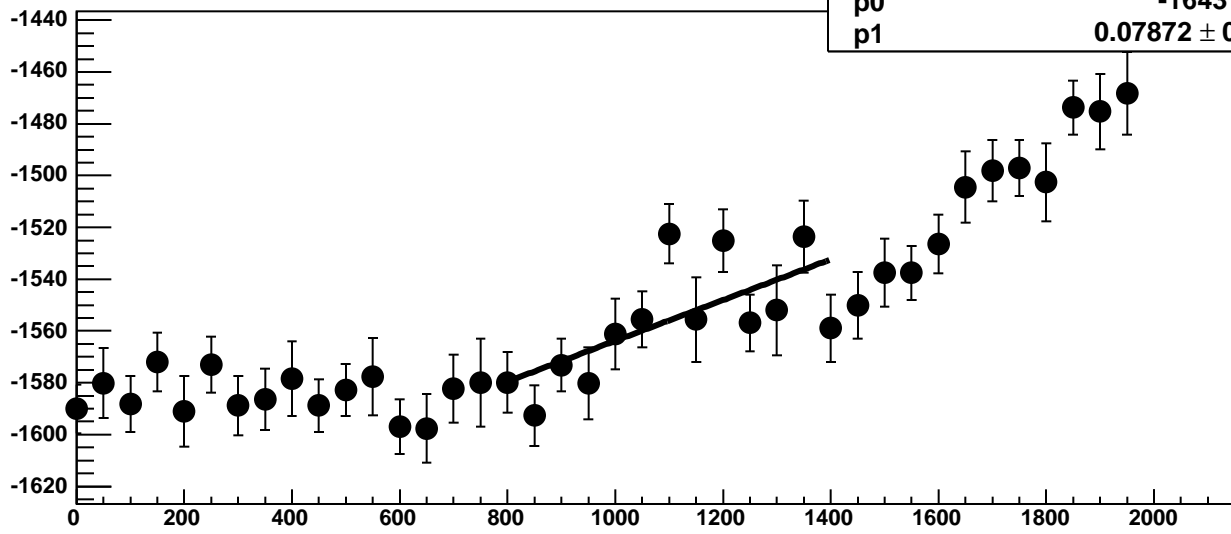
Chip 6, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC



Chip 6, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC

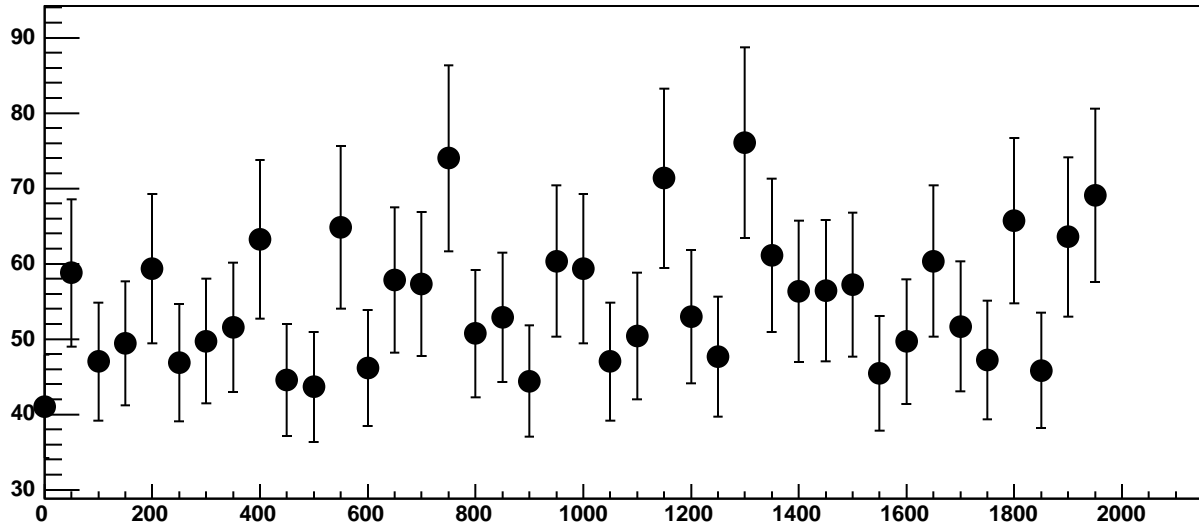


Chip 6, Channel 4, Enable 1, Hold=30, ADC Mean vs DAC

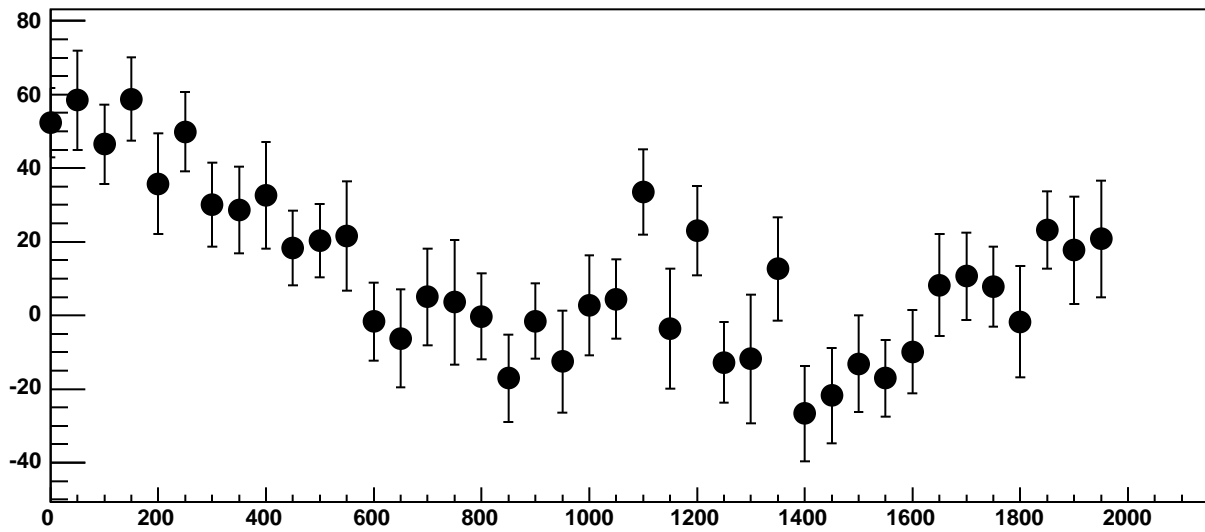


$\chi^2 / \text{ndf}$  22.04 / 11  
p0  $-1643 \pm 20.31$   
p1  $0.07872 \pm 0.01856$

Chip 6, Channel 4, Enable 1, Hold=30, ADC Noise vs DAC

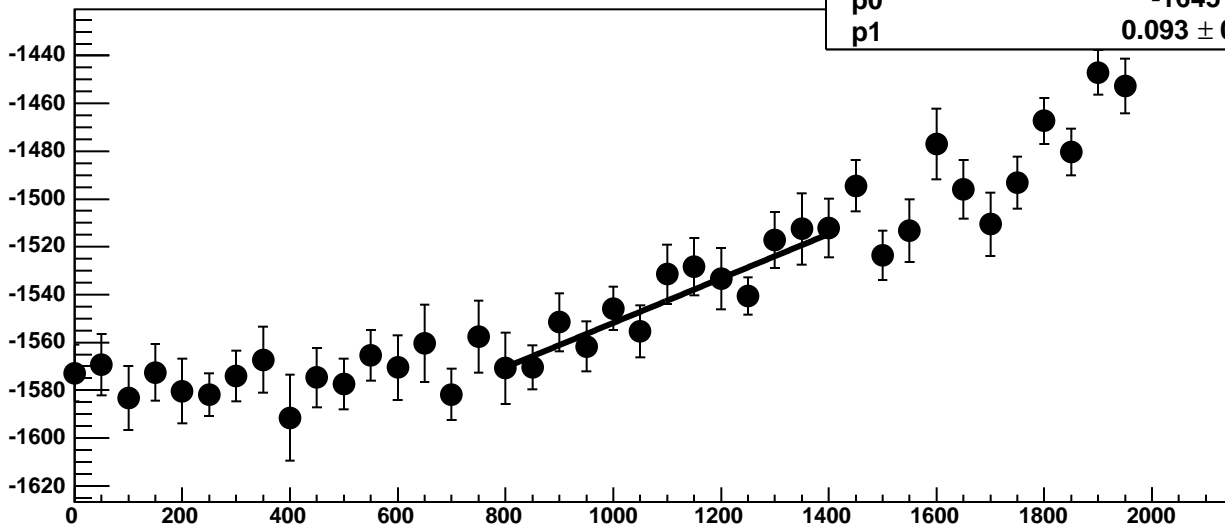


Chip 6, Channel 4, Enable 1, Hold=30, ADC Residuals vs DAC



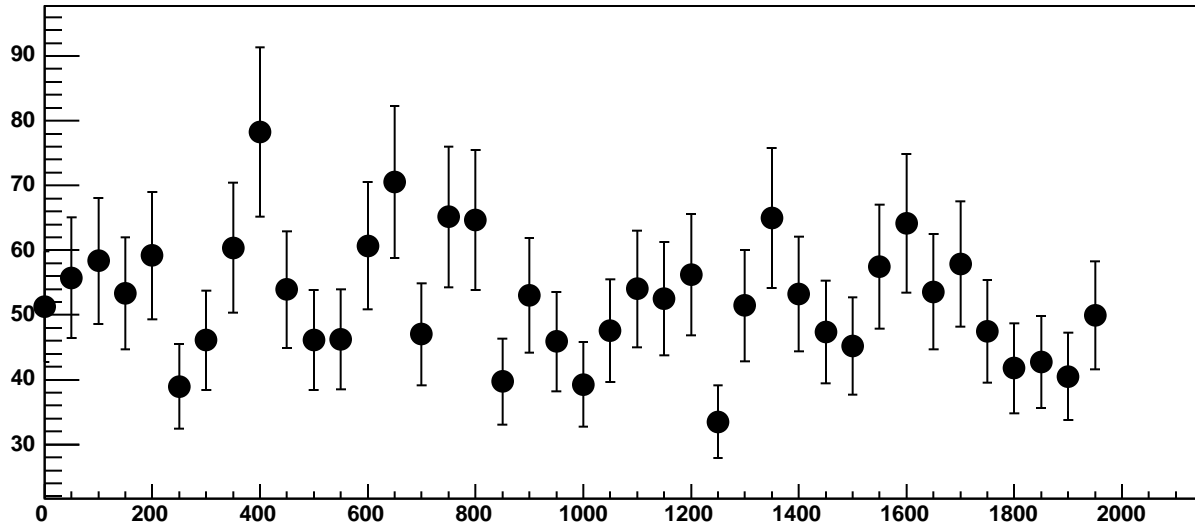


Chip 6, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC

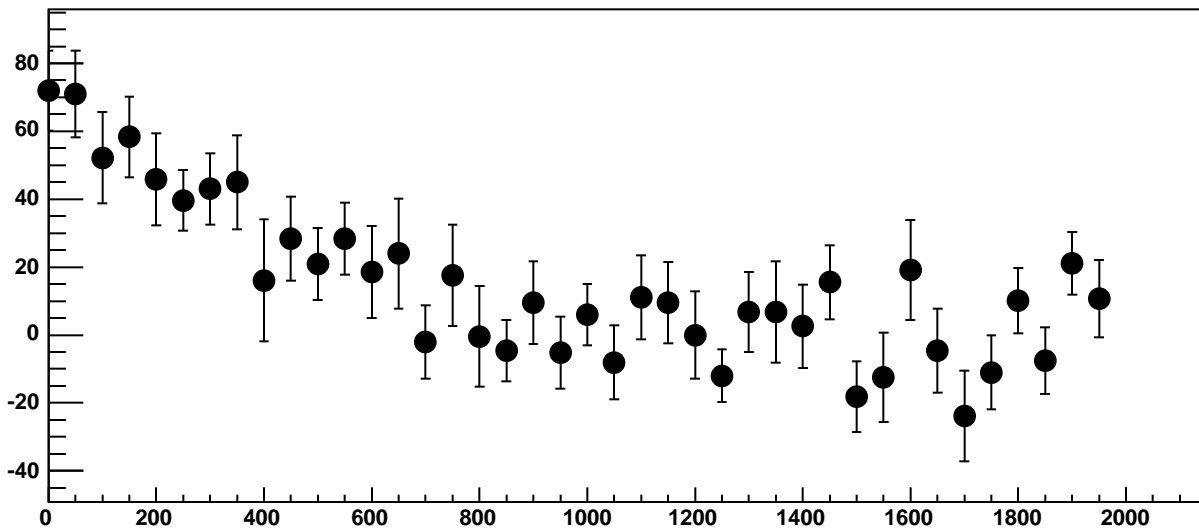


$\chi^2 / \text{ndf}$  6.574 / 11  
p0 -1645 ± 18.99  
p1 0.093 ± 0.01714

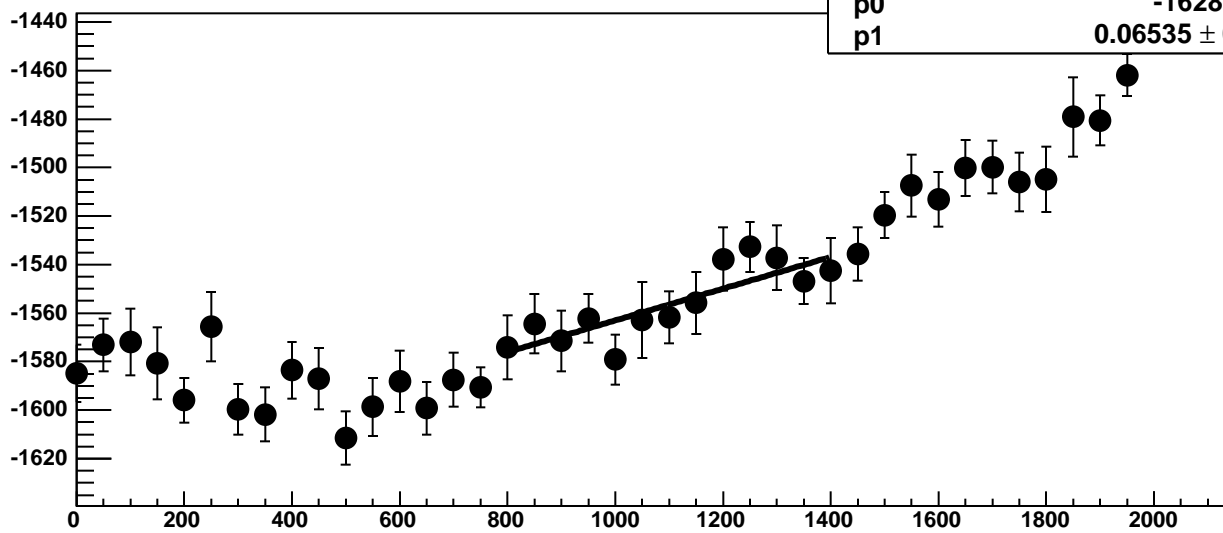
Chip 6, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC



Chip 6, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 6, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

7.099 / 11

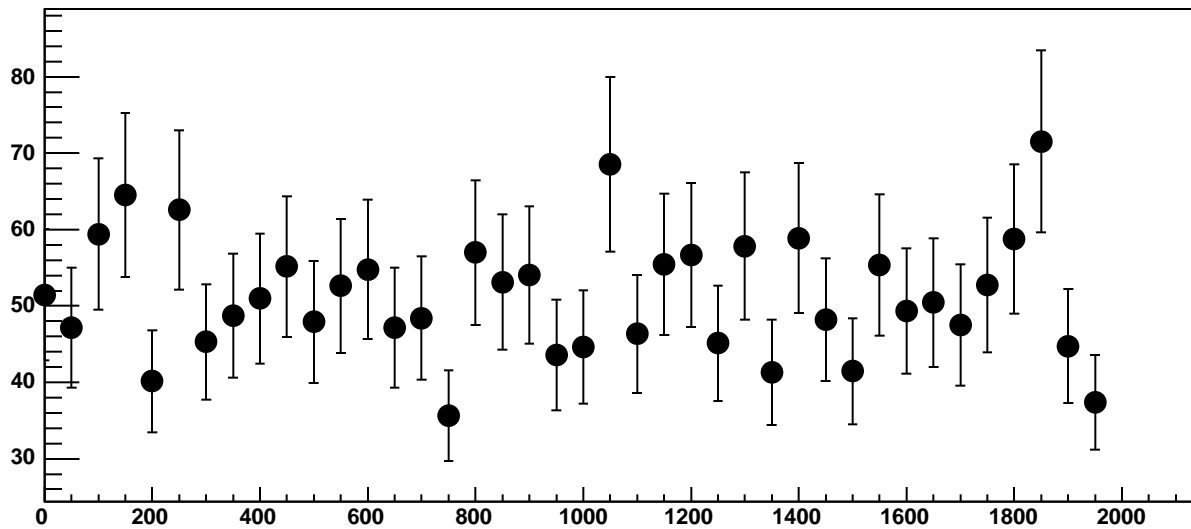
p0

$-1628 \pm 19.69$

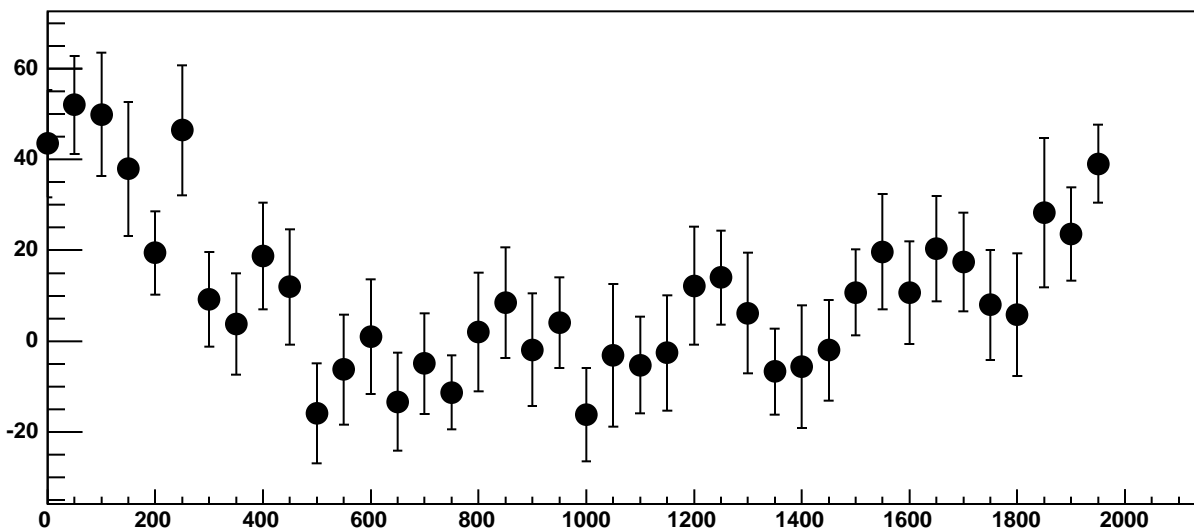
p1

$0.06535 \pm 0.01758$

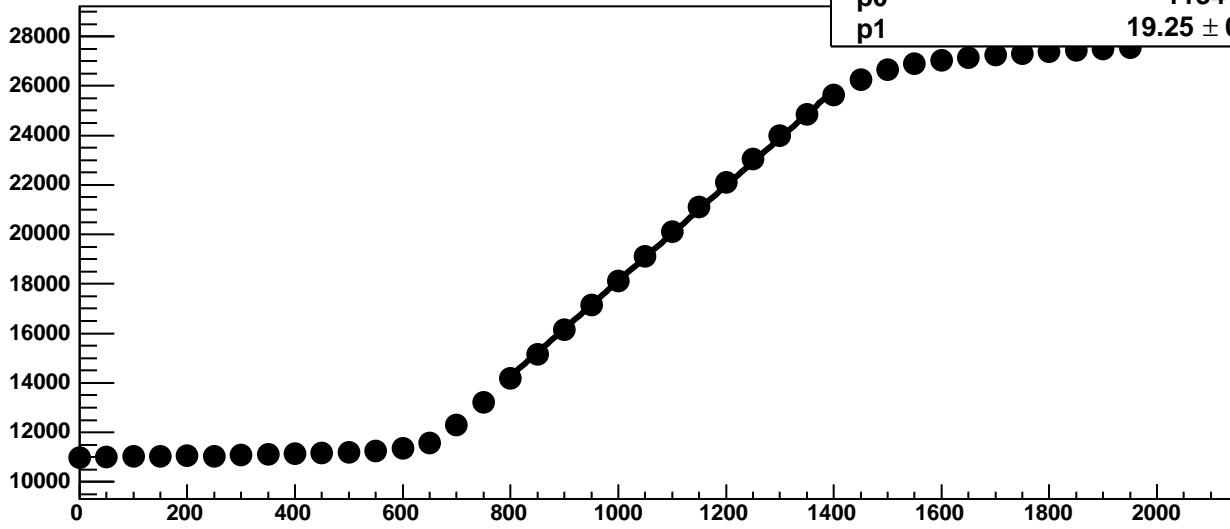
Chip 6, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC



Chip 6, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC

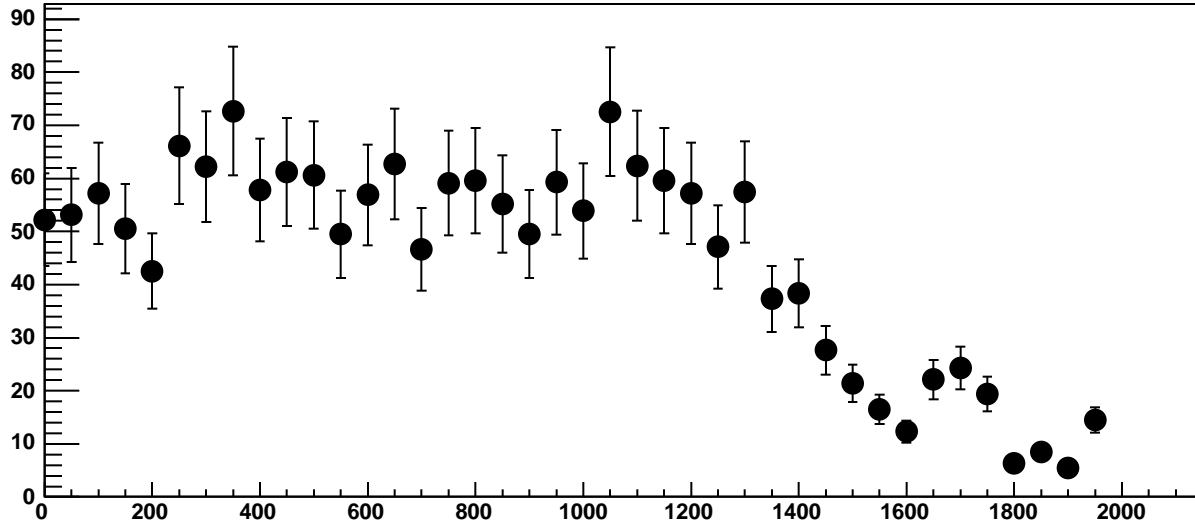


Chip 6, Channel 4, Enable 4!, Hold=30, ADC Mean vs DAC

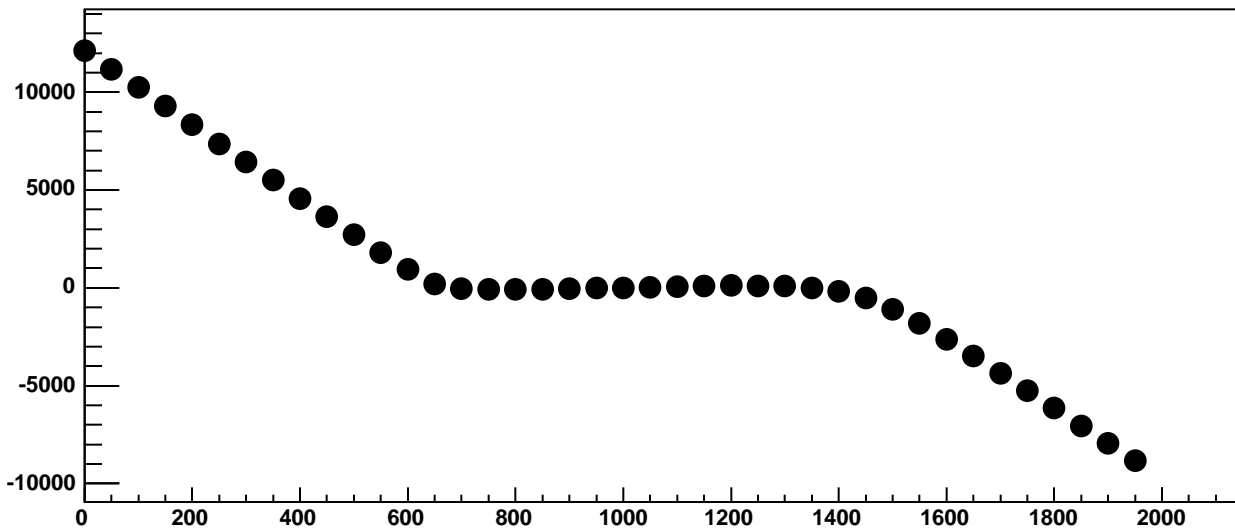


$\chi^2 / \text{ndf}$  777.9 / 11  
p0  $-1134 \pm 19.14$   
p1  $19.25 \pm 0.01645$

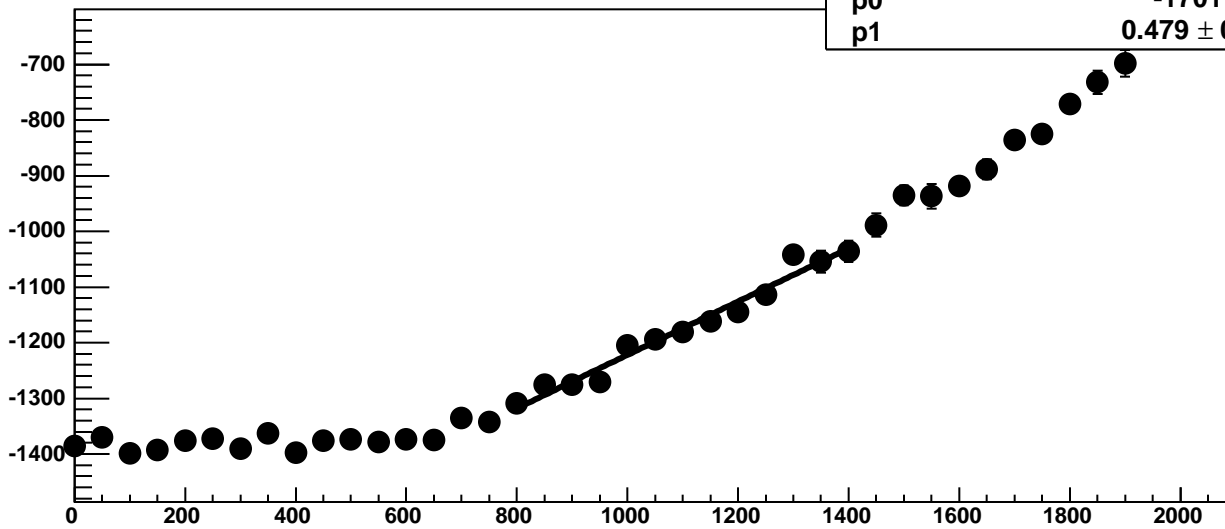
Chip 6, Channel 4, Enable 4!, Hold=30, ADC Noise vs DAC



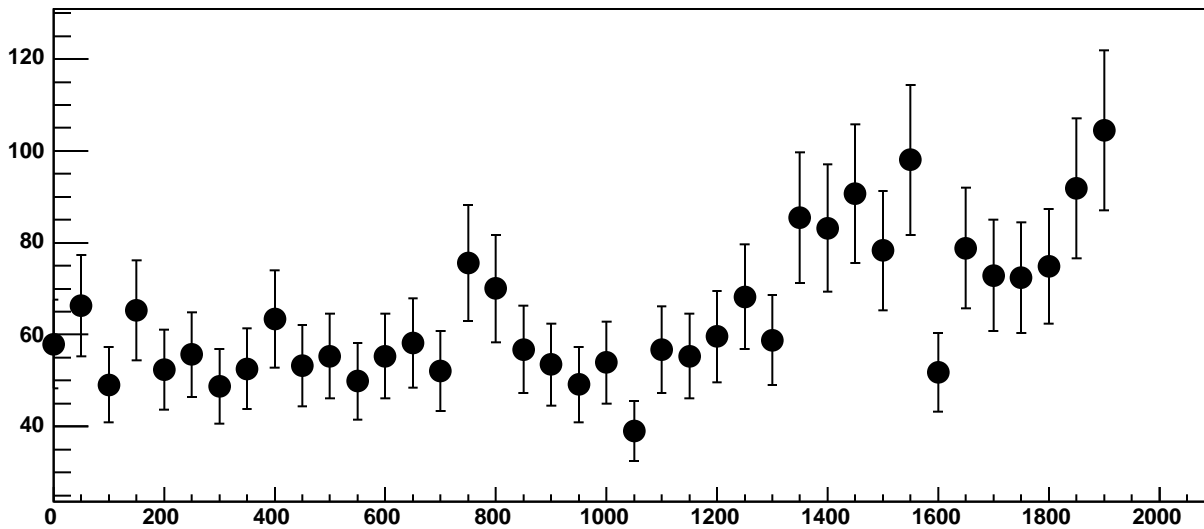
Chip 6, Channel 4, Enable 4!, Hold=30, ADC Residuals vs DAC



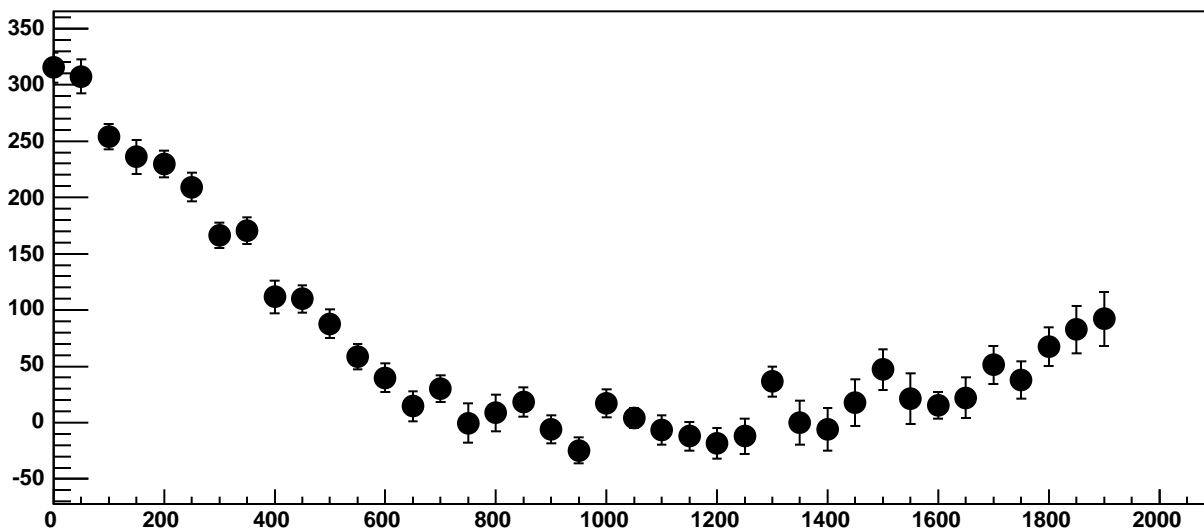
Chip 6, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC



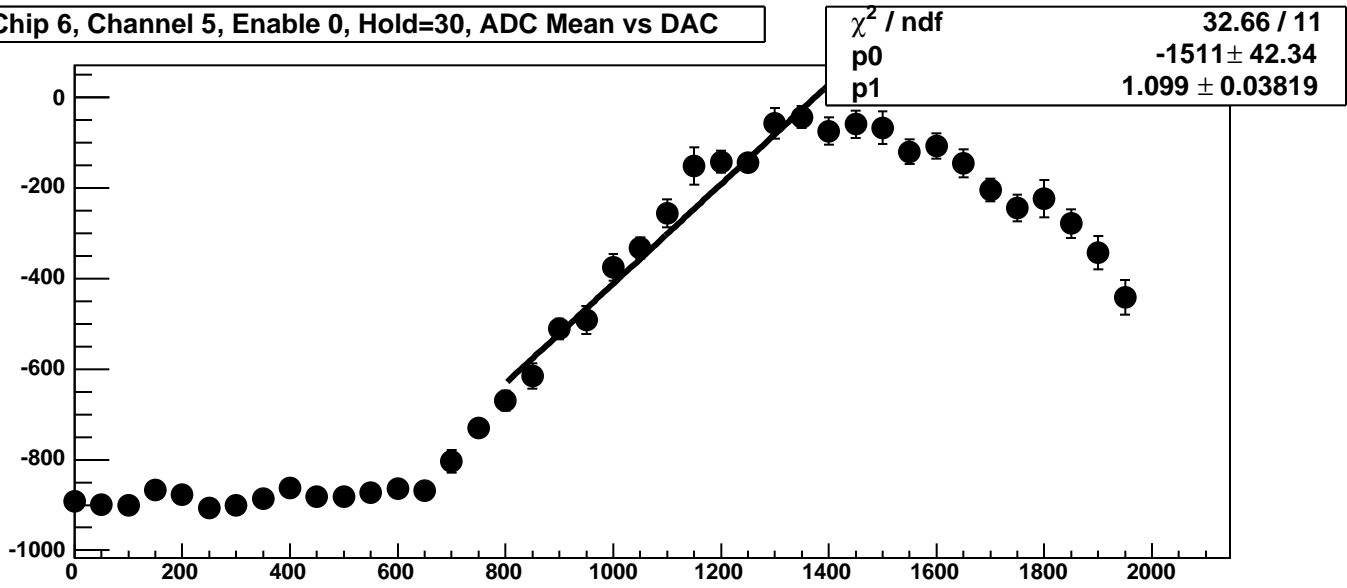
Chip 6, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC



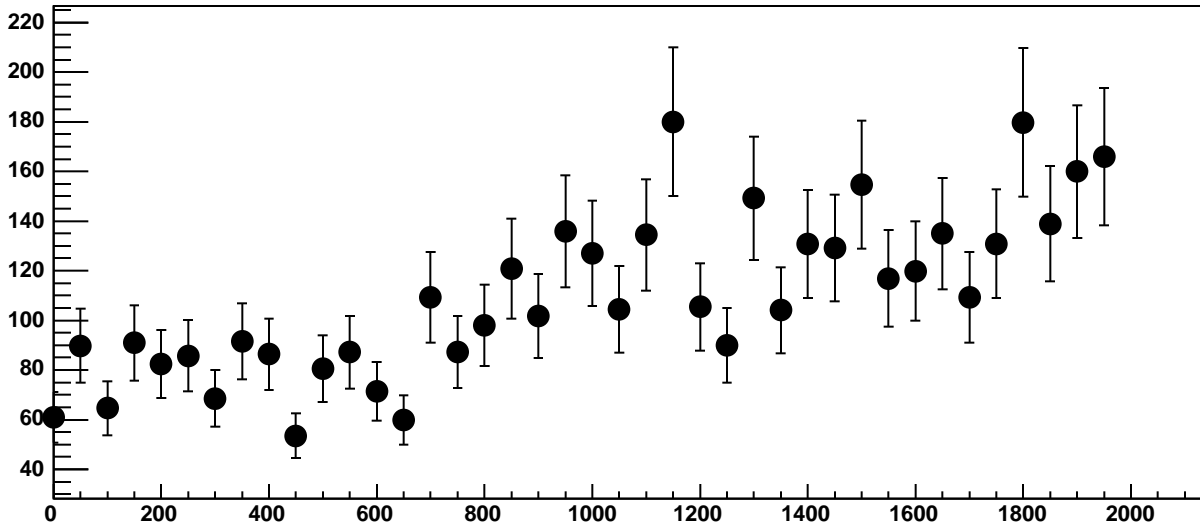
Chip 6, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC



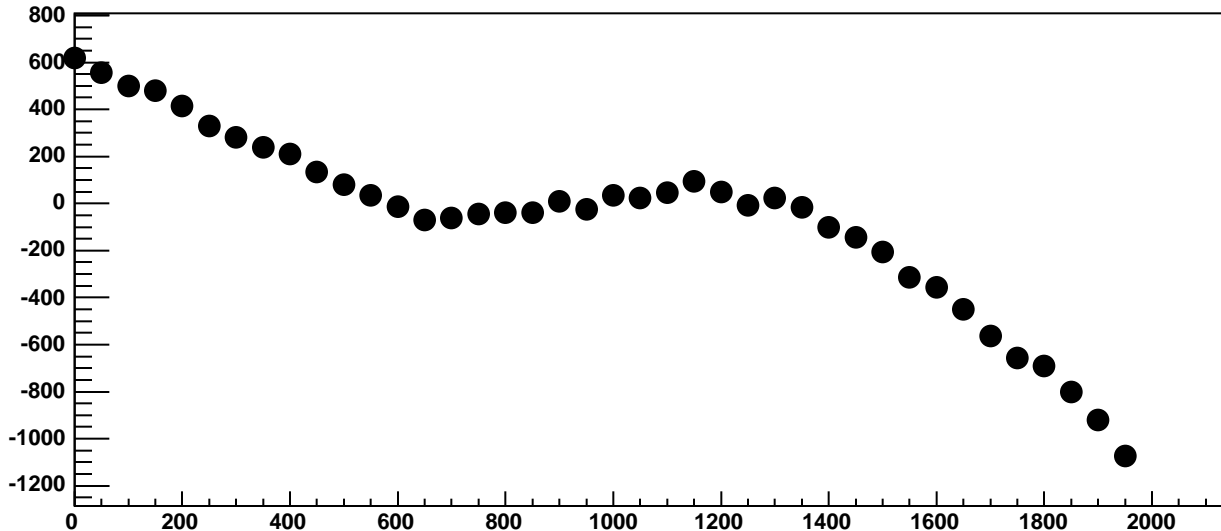
Chip 6, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC



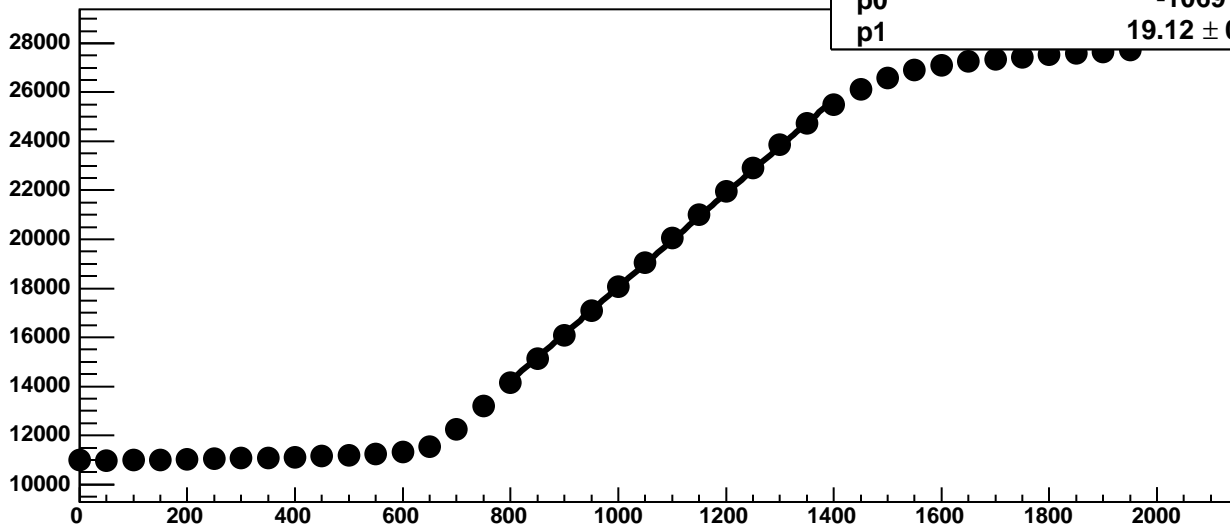
Chip 6, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC



Chip 6, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC

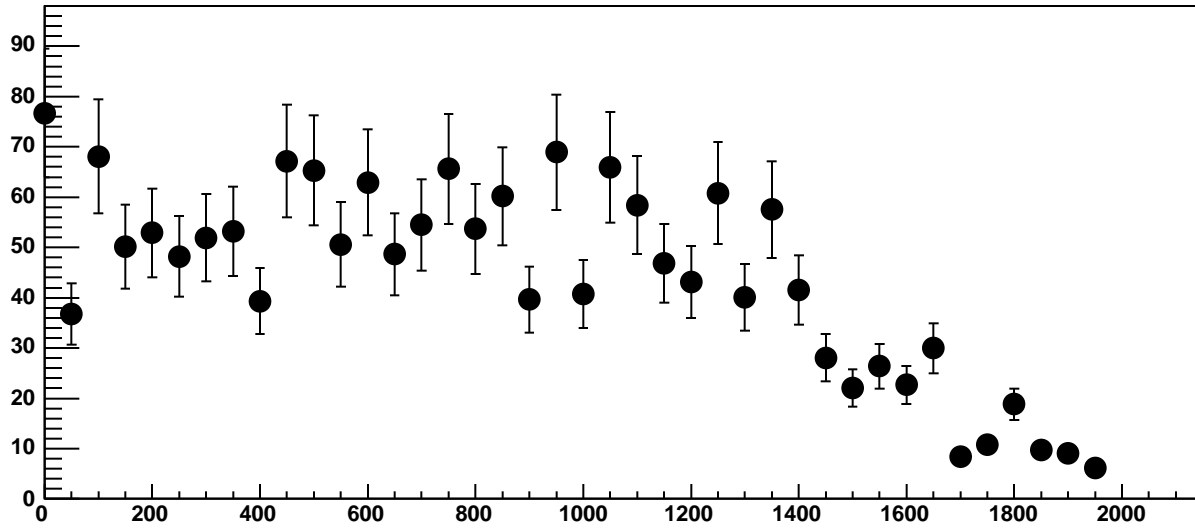


Chip 6, Channel 5, Enable 1!, Hold=30, ADC Mean vs DAC

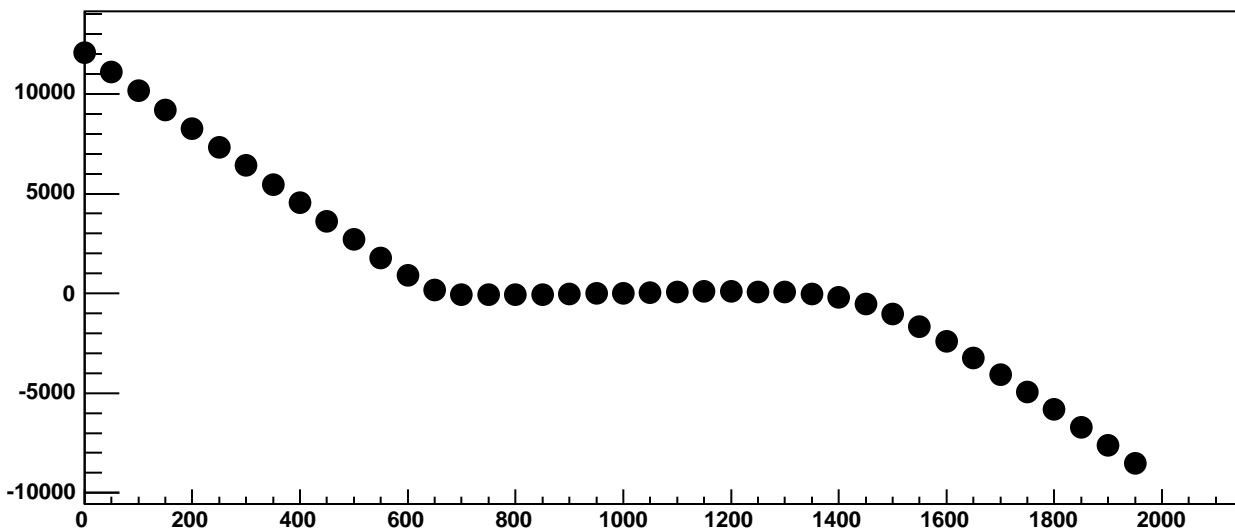


$\chi^2 / \text{ndf}$	790.2 / 11
p0	-1069 ± 18.66
p1	19.12 ± 0.01649

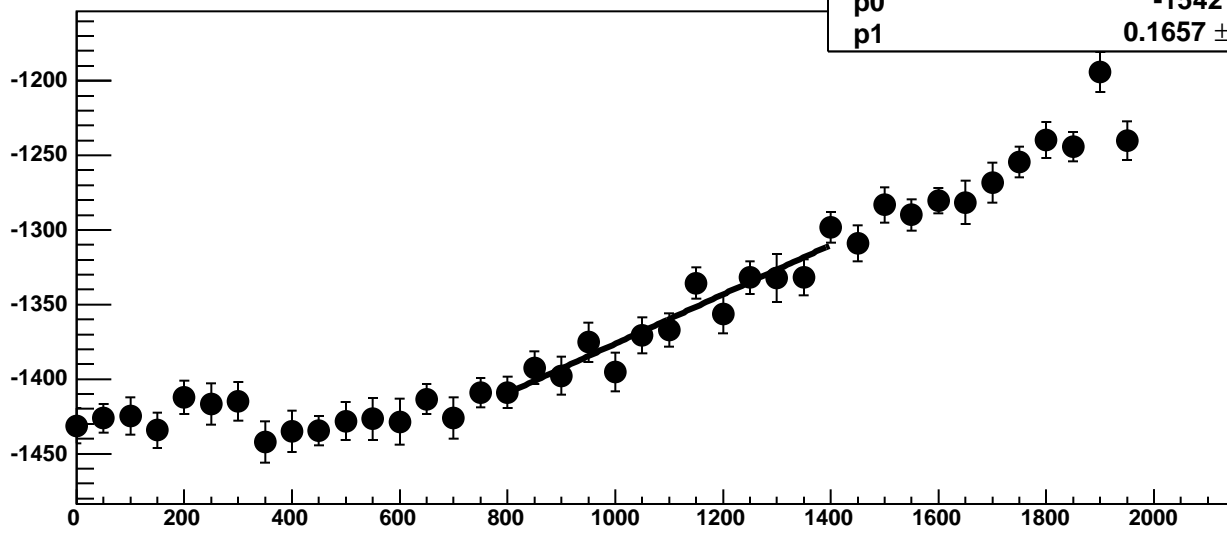
Chip 6, Channel 5, Enable 1!, Hold=30, ADC Noise vs DAC



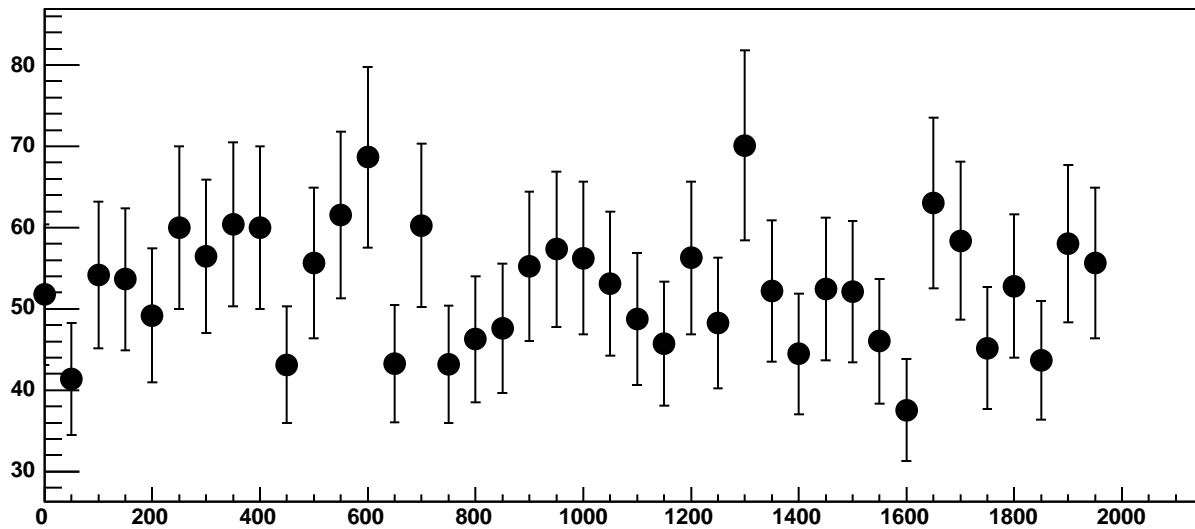
Chip 6, Channel 5, Enable 1!, Hold=30, ADC Residuals vs DAC



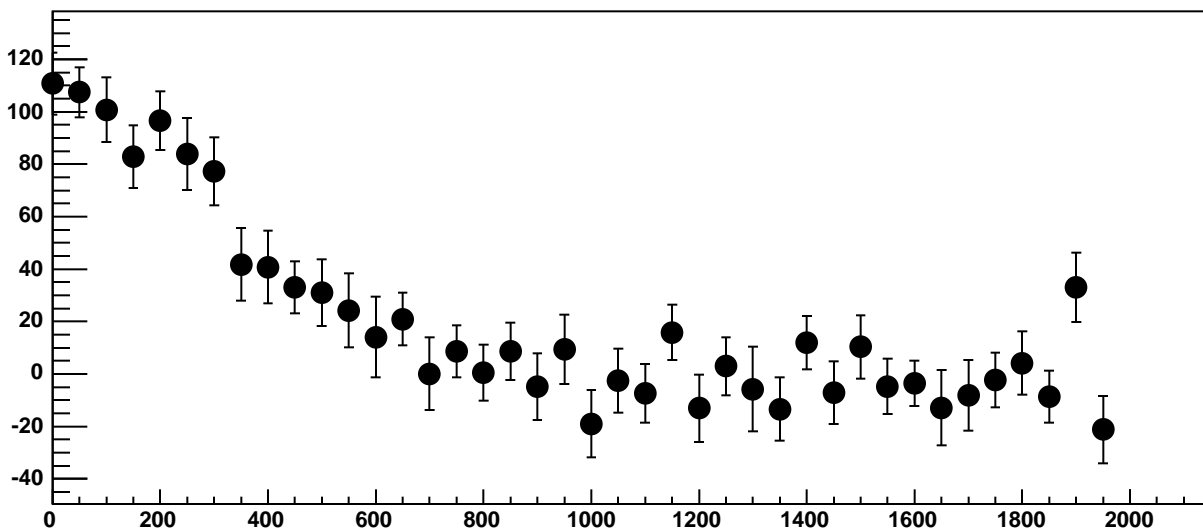
Chip 6, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC



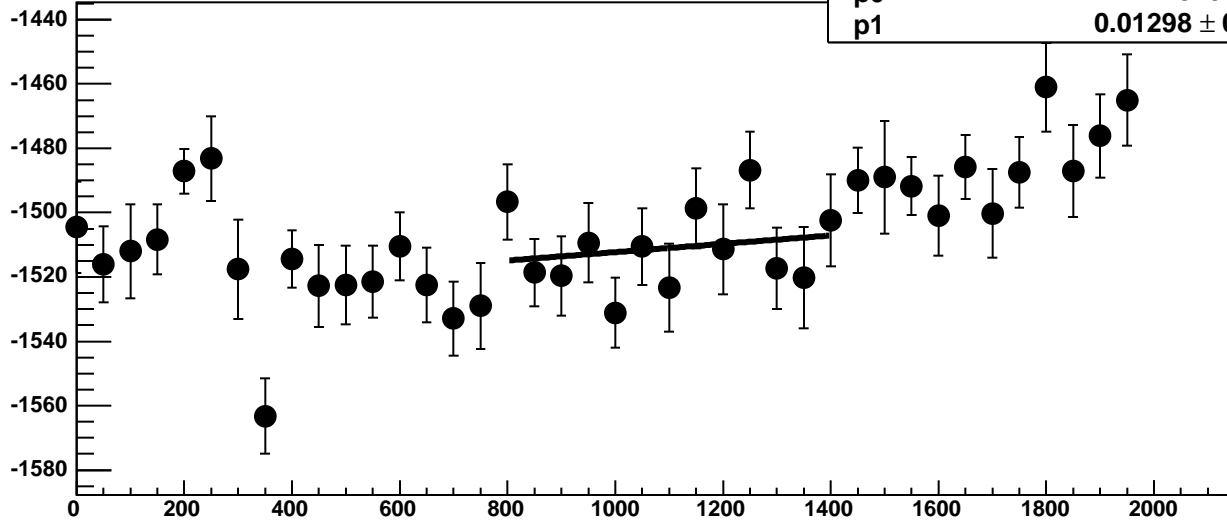
Chip 6, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC



Chip 6, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC

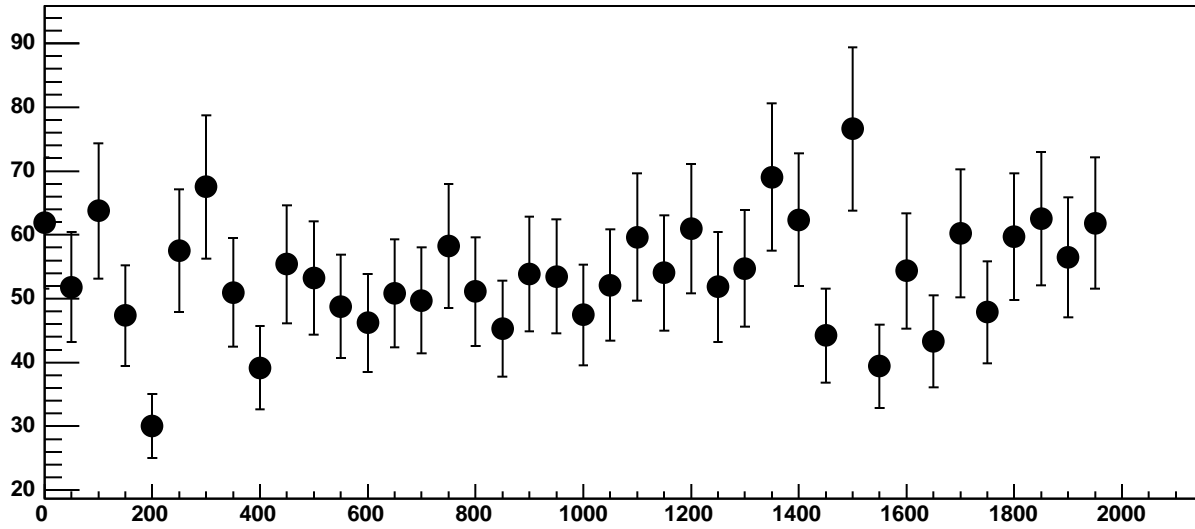


Chip 6, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC

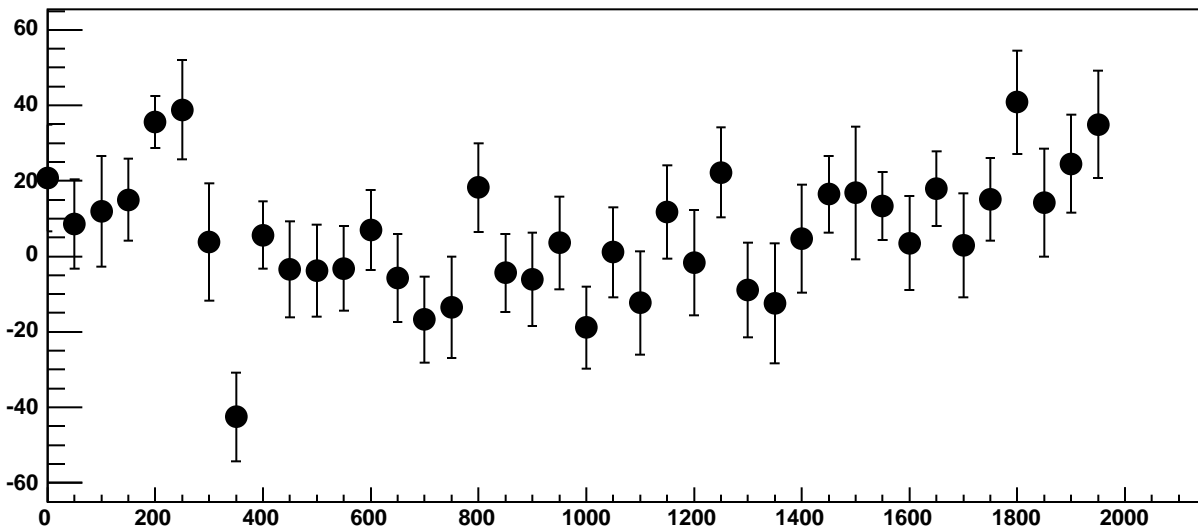


$\chi^2 / \text{ndf}$  12.39 / 11  
p0  $-1525 \pm 20.38$   
p1  $0.01298 \pm 0.01874$

Chip 6, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC

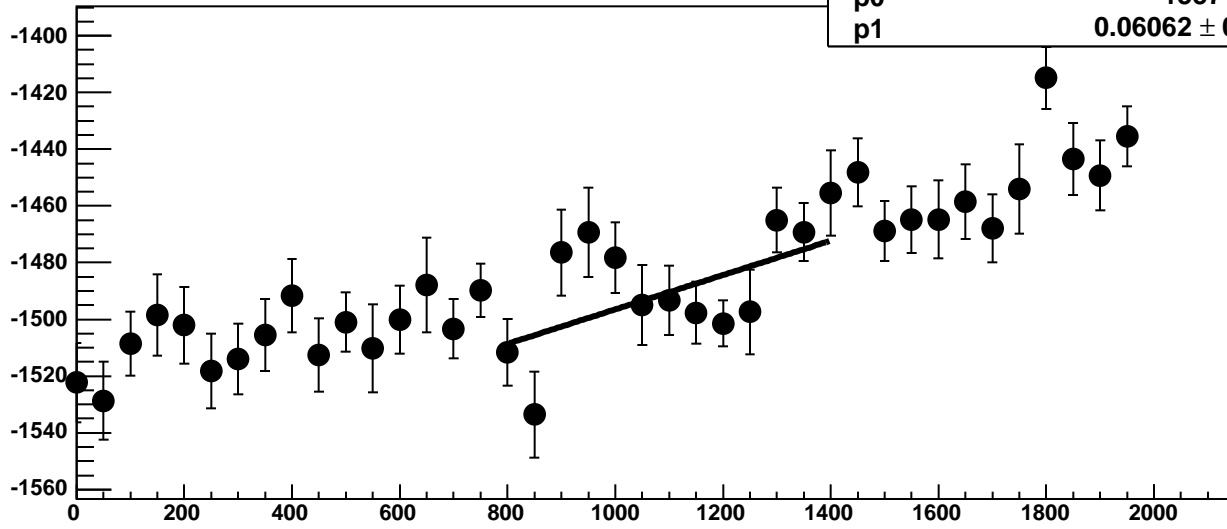


Chip 6, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC



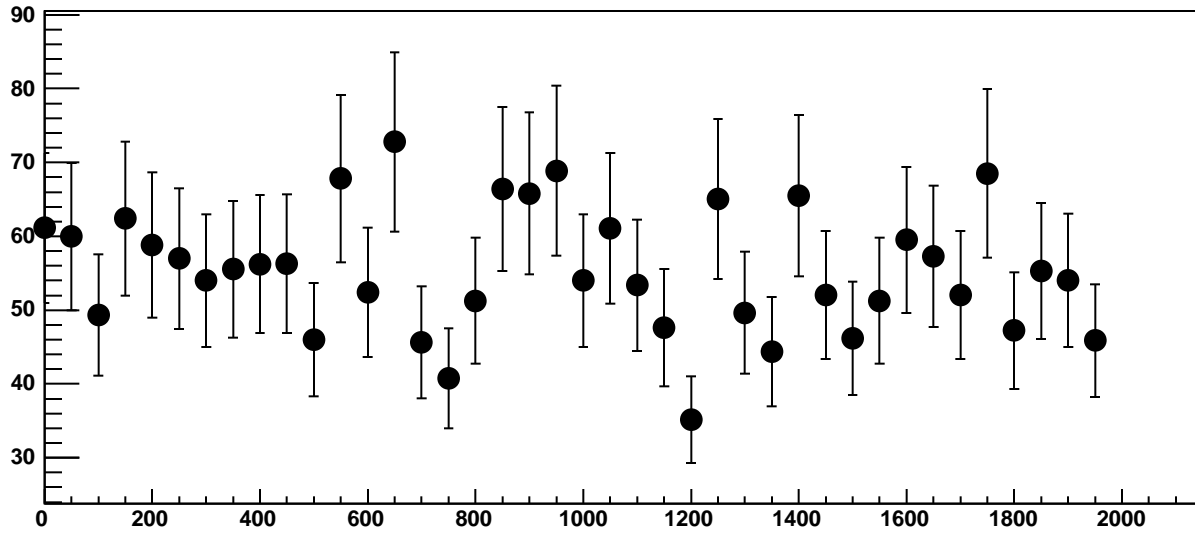


Chip 6, Channel 5, Enable 4, Hold=30, ADC Mean vs DAC

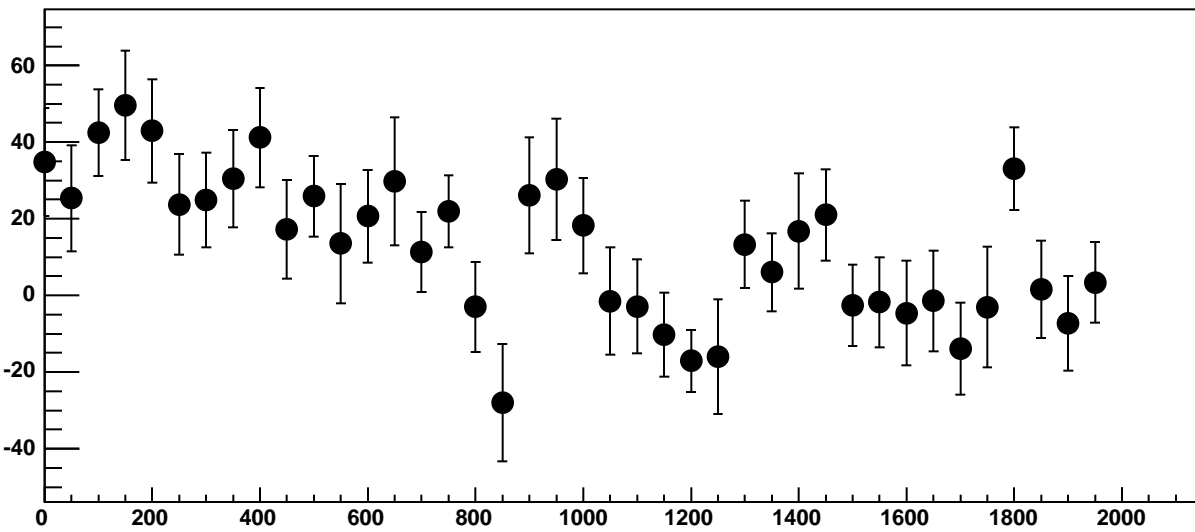


$\chi^2 / \text{ndf}$  21.81 / 11  
p0  $-1557 \pm 21.58$   
p1  $0.06062 \pm 0.01893$

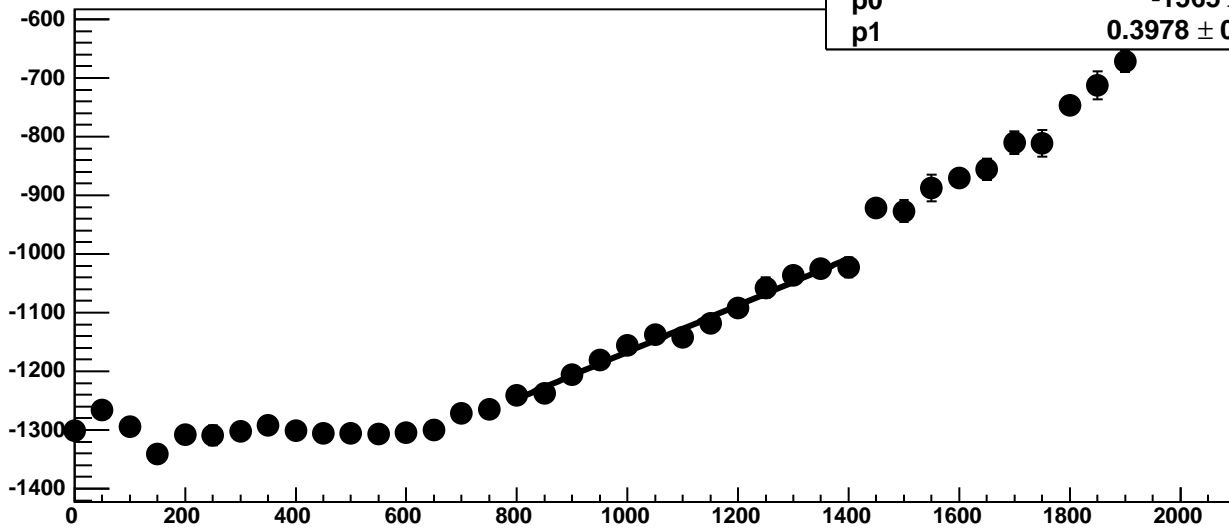
Chip 6, Channel 5, Enable 4, Hold=30, ADC Noise vs DAC



Chip 6, Channel 5, Enable 4, Hold=30, ADC Residuals vs DAC

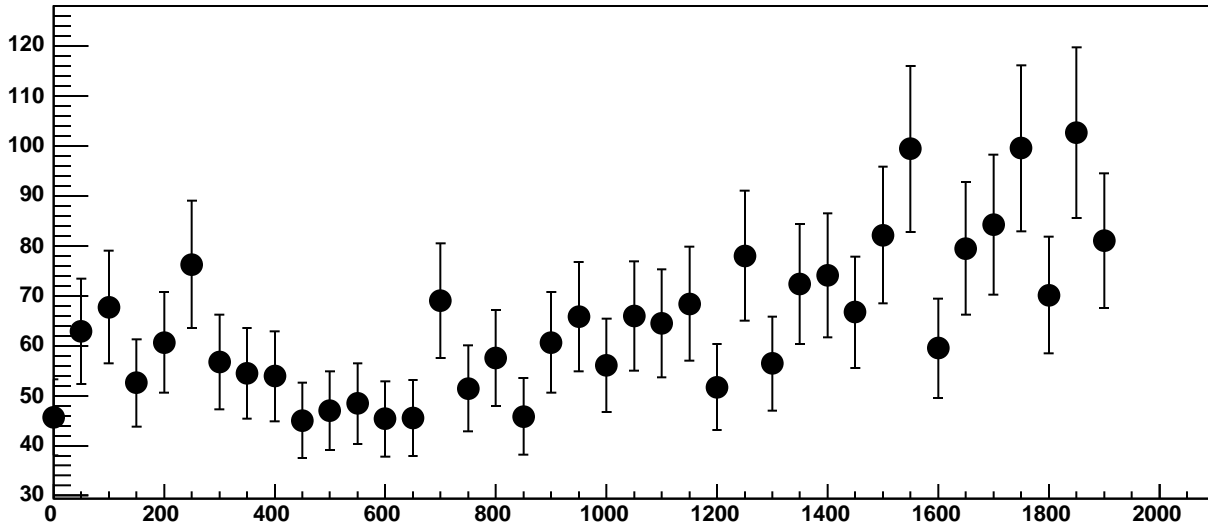


Chip 6, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC

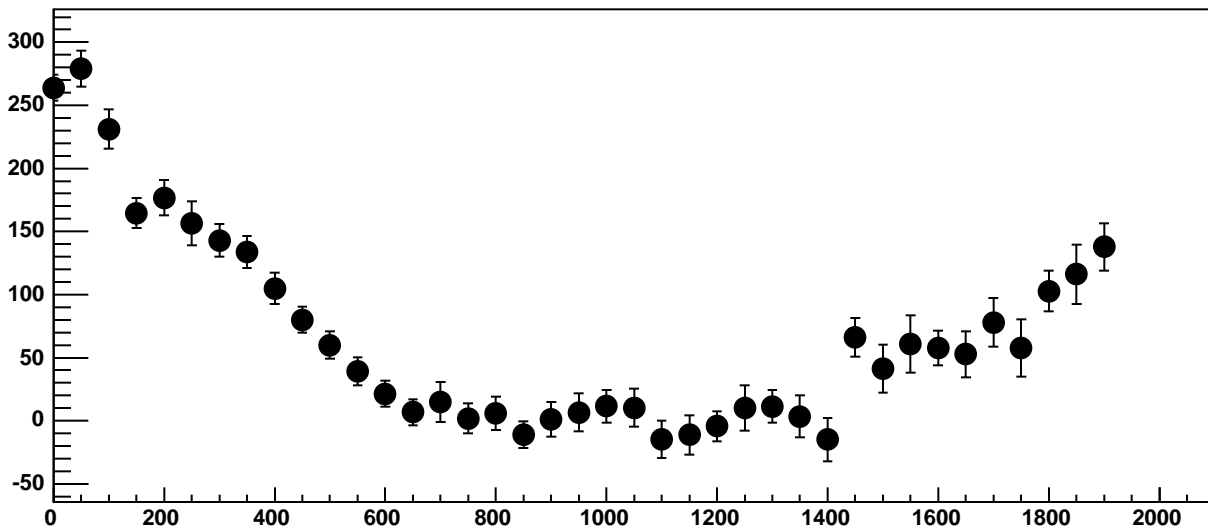


$\chi^2 / \text{ndf}$  6.267 / 11  
p0  $-1565 \pm 22.52$   
p1  $0.3978 \pm 0.02073$

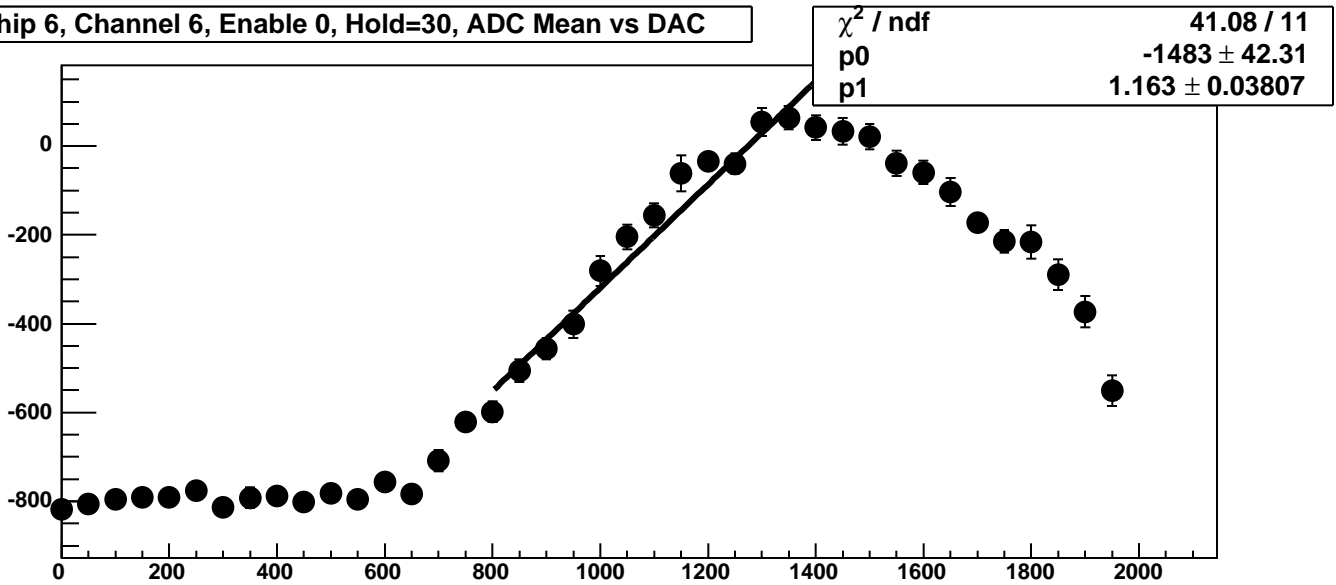
Chip 6, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC



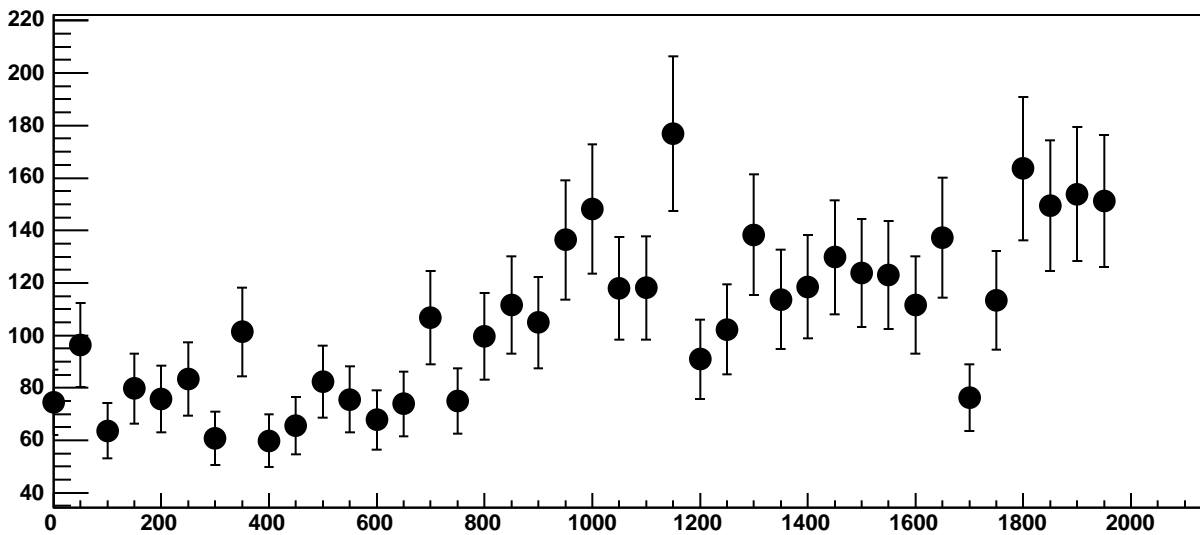
Chip 6, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC



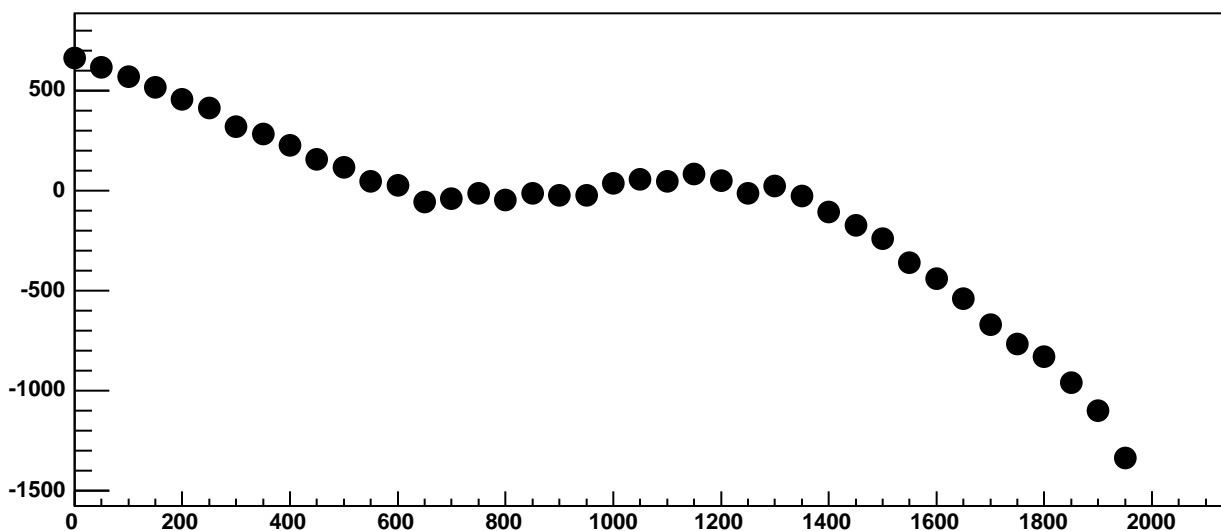
Chip 6, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC



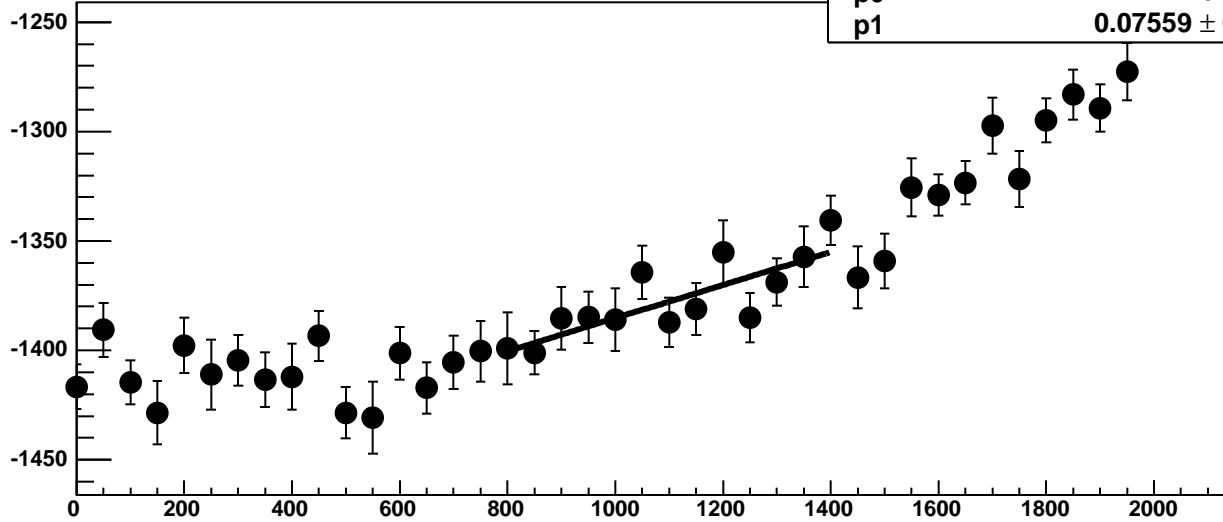
Chip 6, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC



Chip 6, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC

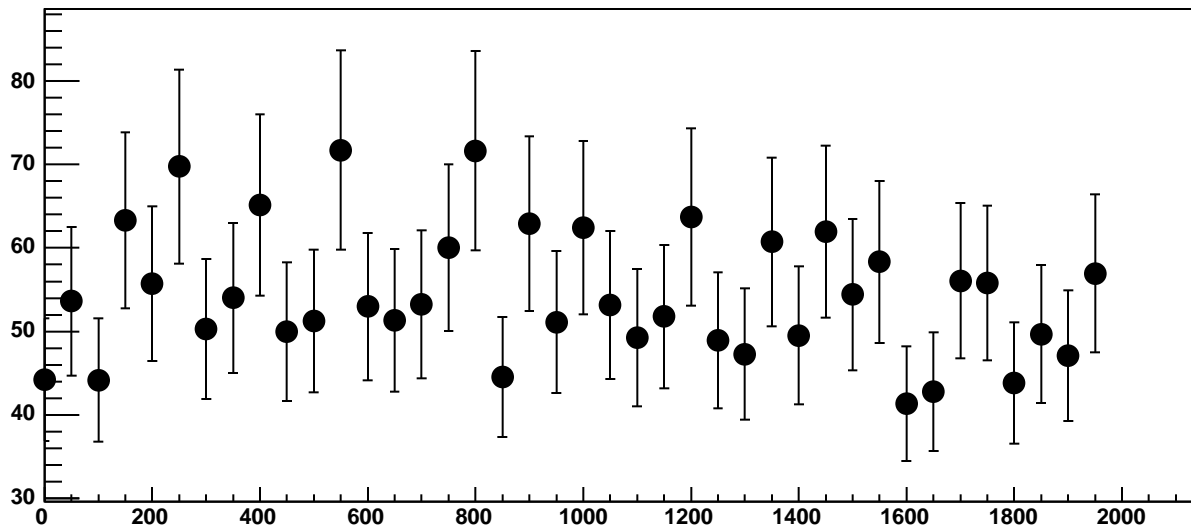


Chip 6, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC

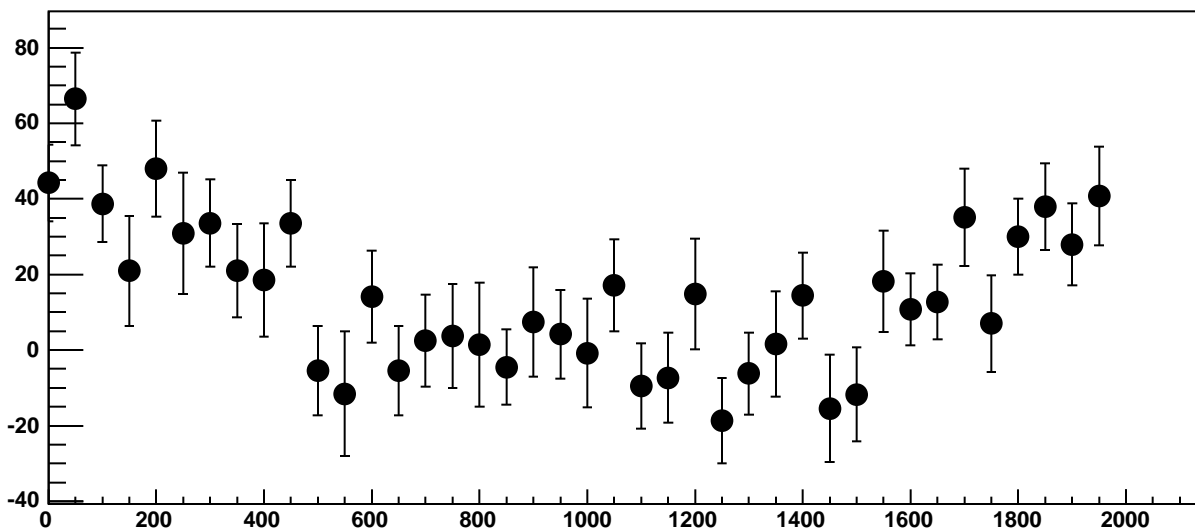


$\chi^2 / \text{ndf}$  9.406 / 11  
p0  $-1461 \pm 20.61$   
p1  $0.07559 \pm 0.01832$

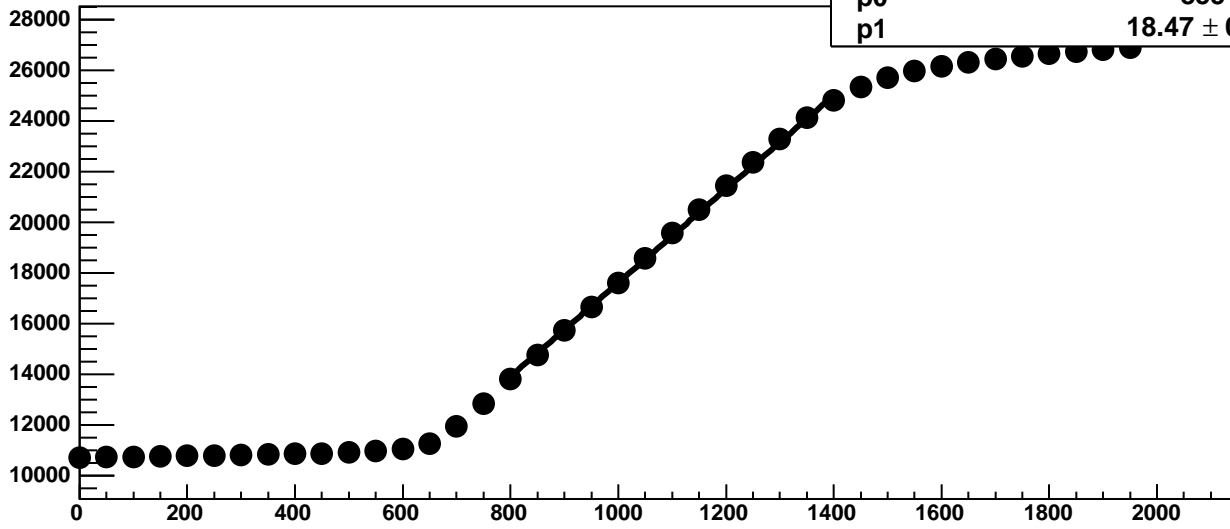
Chip 6, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC



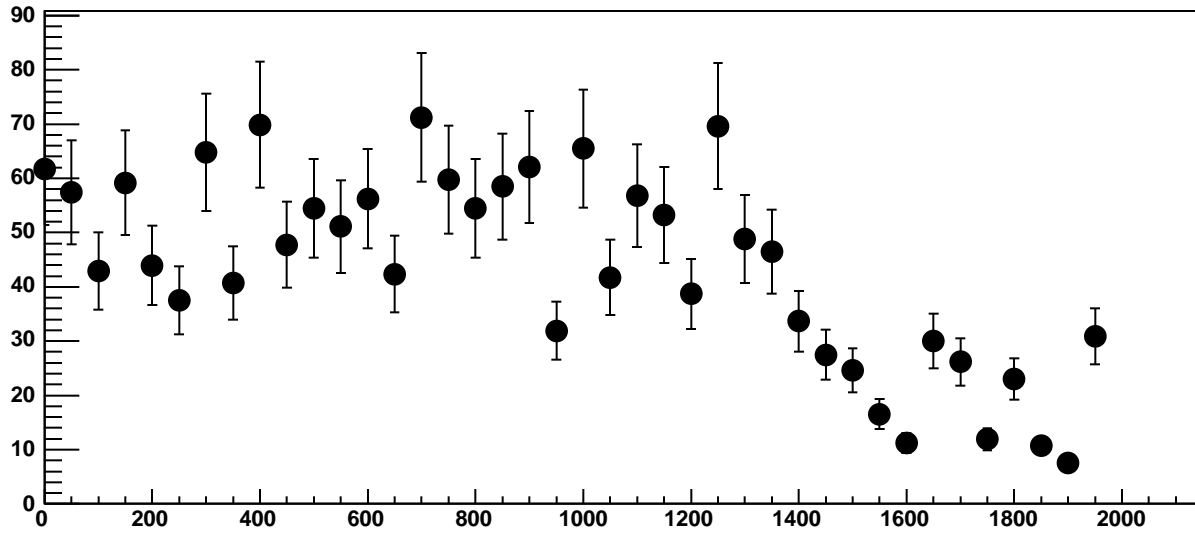
Chip 6, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC



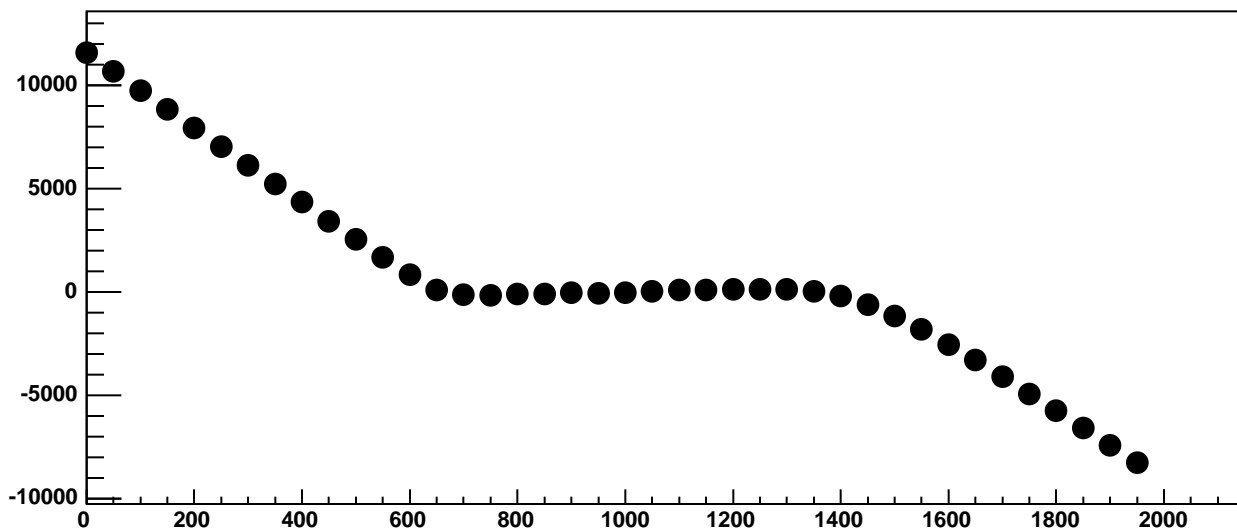
Chip 6, Channel 6, Enable 2!, Hold=30, ADC Mean vs DAC



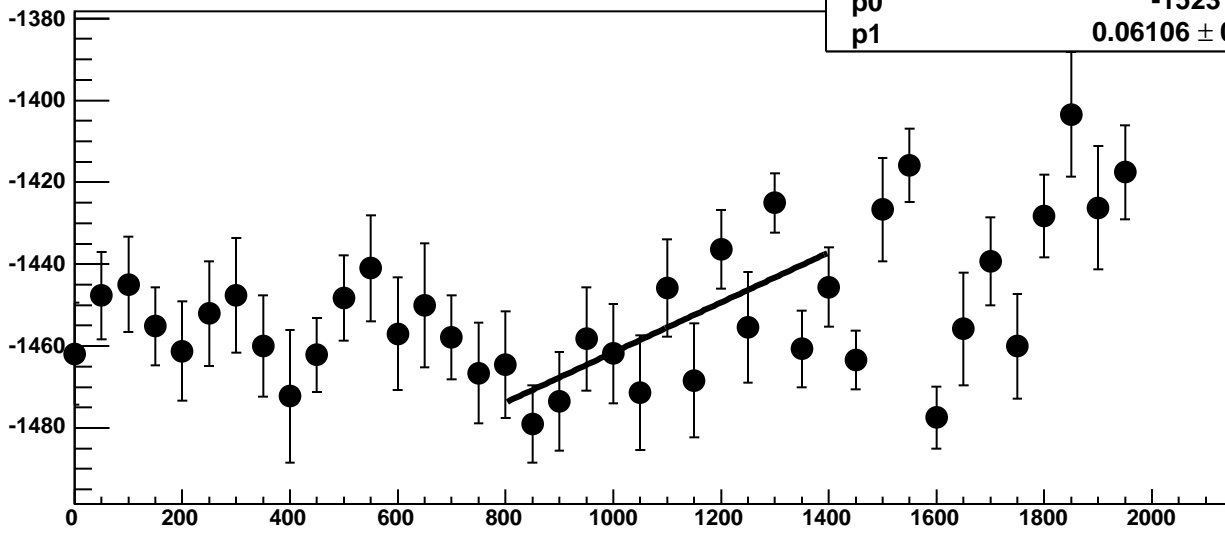
Chip 6, Channel 6, Enable 2!, Hold=30, ADC Noise vs DAC



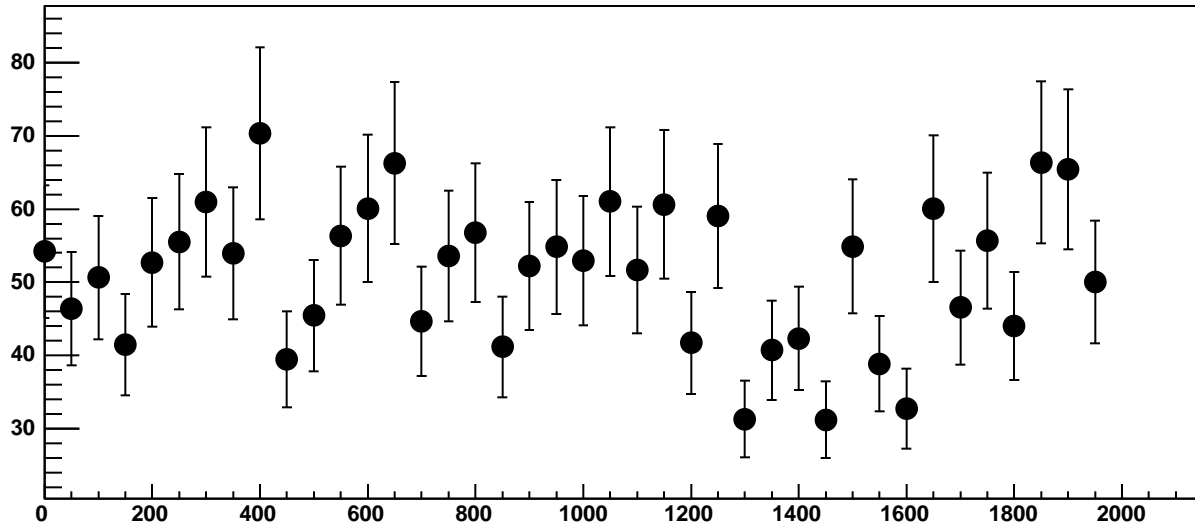
Chip 6, Channel 6, Enable 2!, Hold=30, ADC Residuals vs DAC



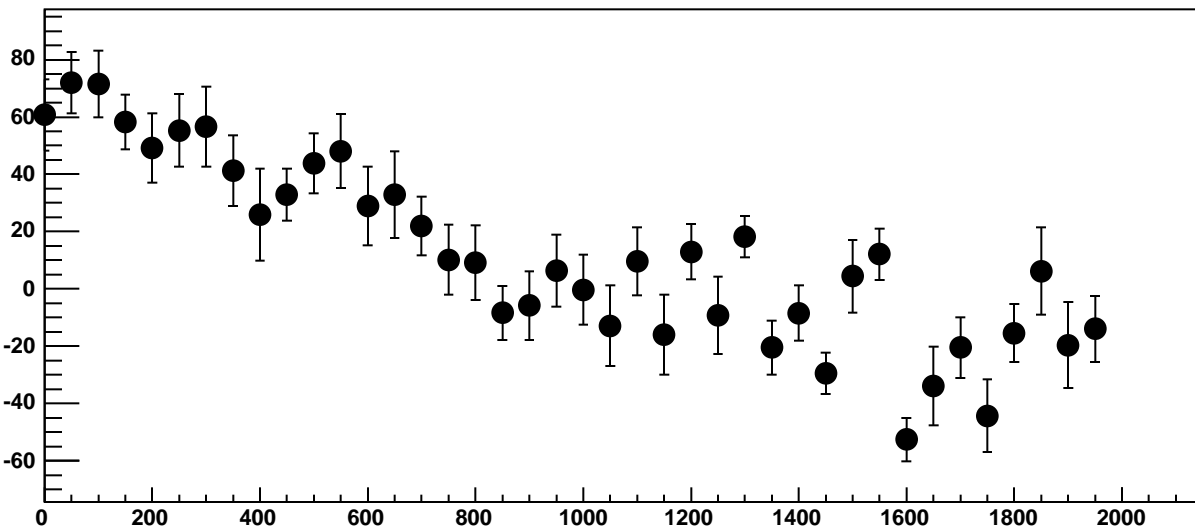
Chip 6, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC



Chip 6, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC



Chip 6, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC



Chip 6, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

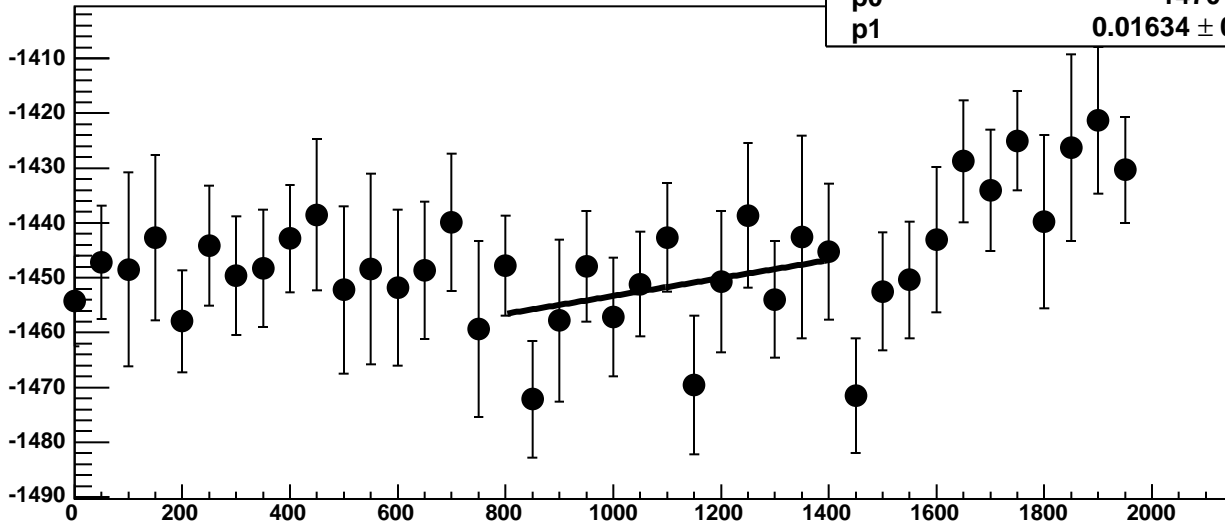
7.888 / 11

p0

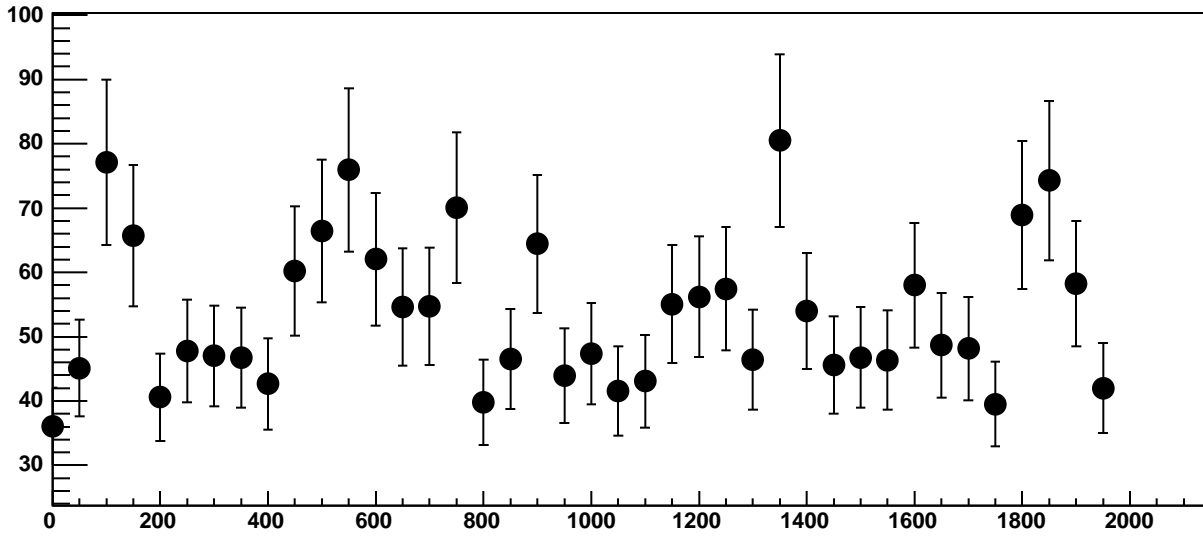
$-1470 \pm 18.67$

p1

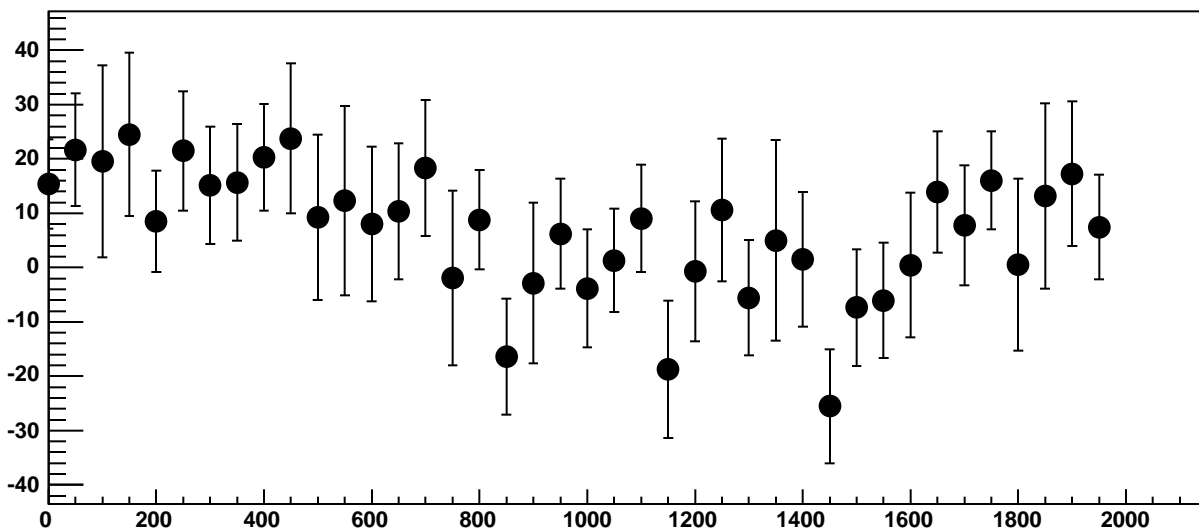
$0.01634 \pm 0.01725$



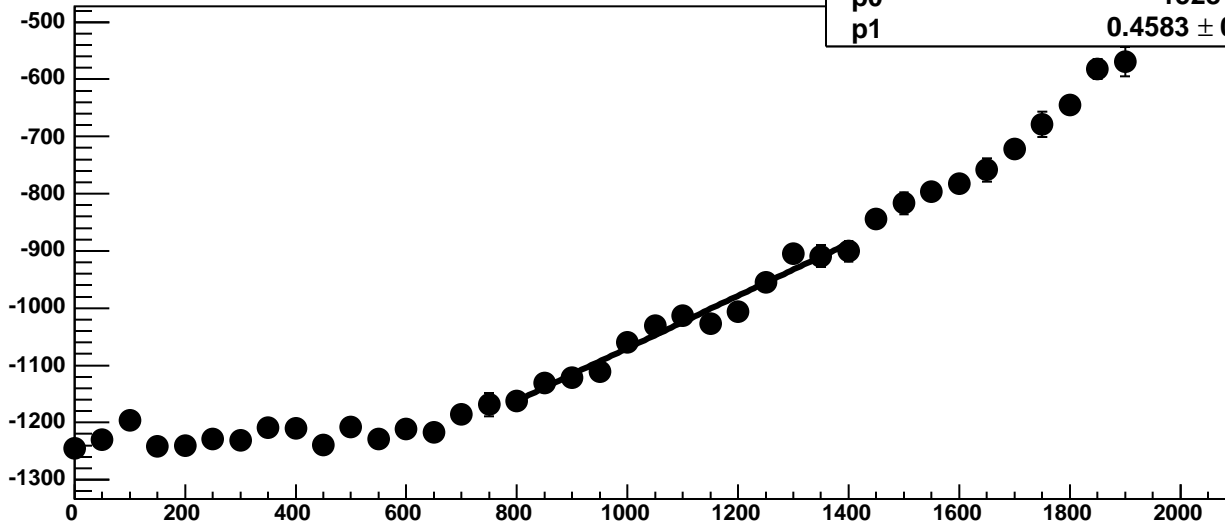
Chip 6, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC



Chip 6, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC

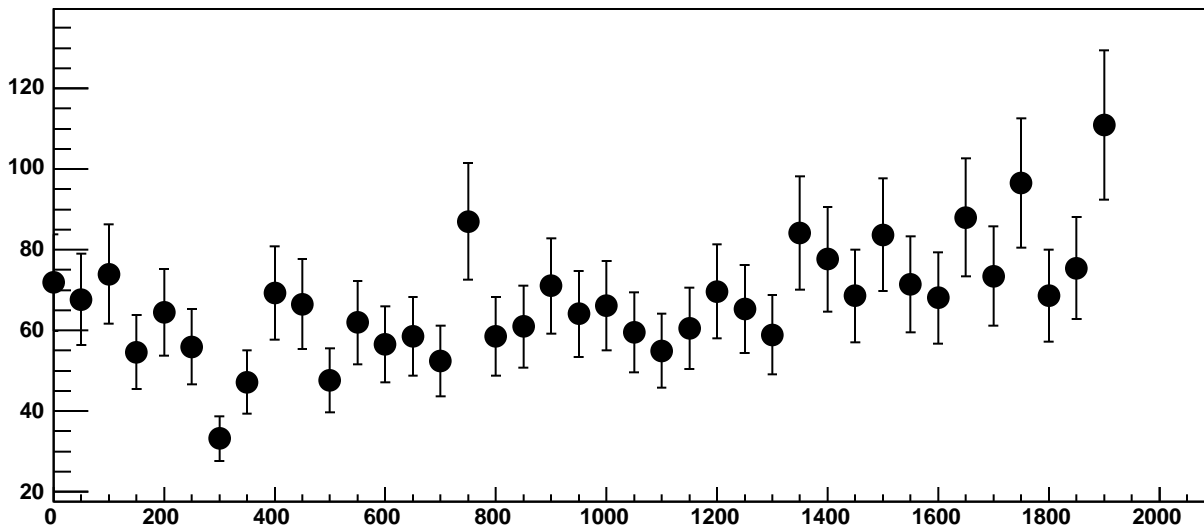


Chip 6, Channel 6, Enable 5, Hold=30, ADC Mean vs DAC

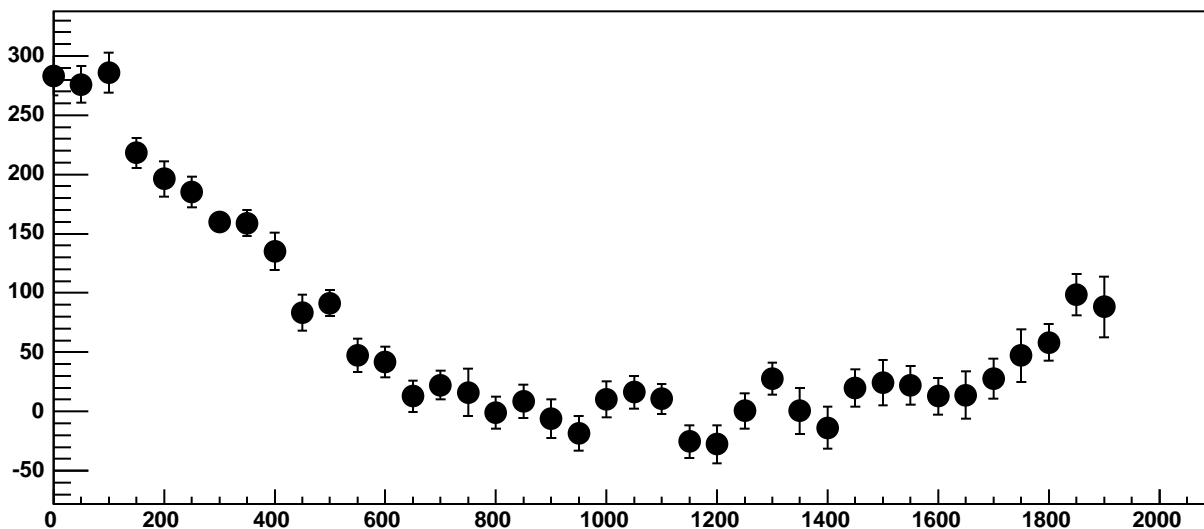


$\chi^2 / \text{ndf}$  15.77 / 11  
p0  $-1528 \pm 24.96$   
p1  $0.4583 \pm 0.02275$

Chip 6, Channel 6, Enable 5, Hold=30, ADC Noise vs DAC

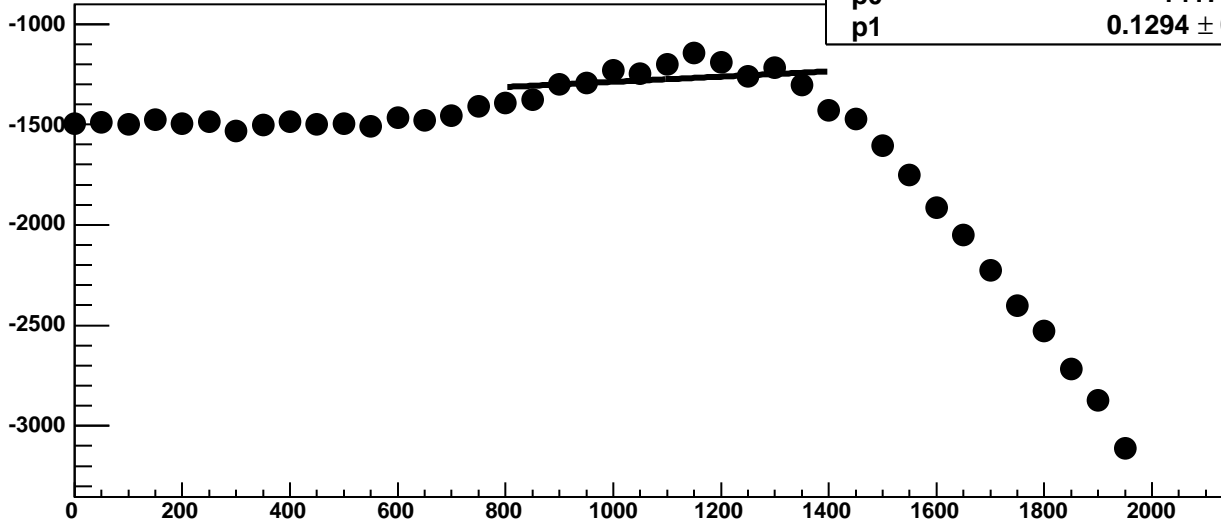


Chip 6, Channel 6, Enable 5, Hold=30, ADC Residuals vs DAC





Chip 6, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

93.56 / 11

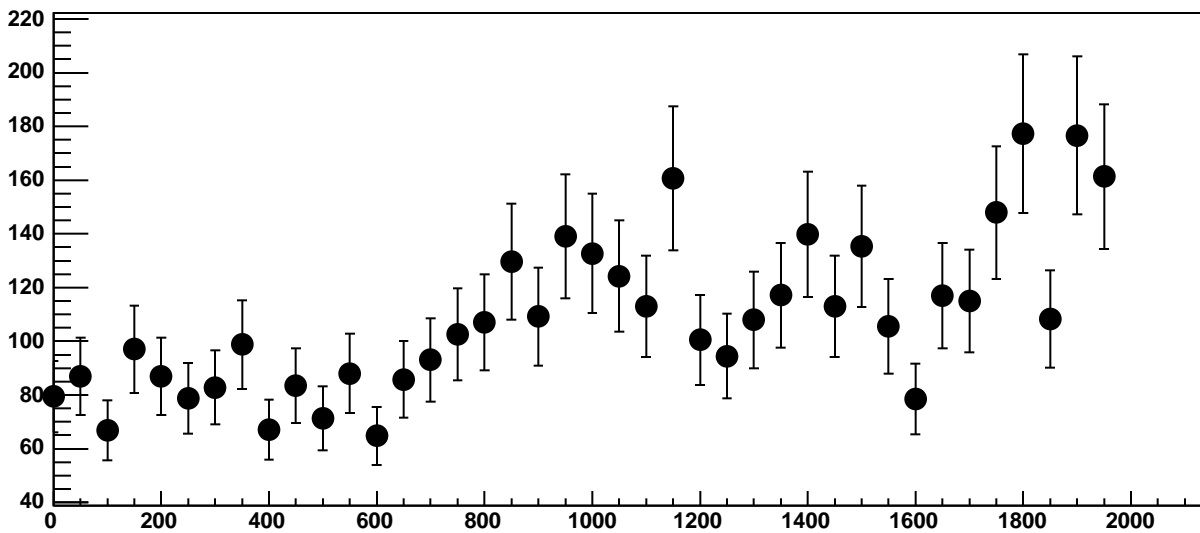
p0

$-1417 \pm 44.77$

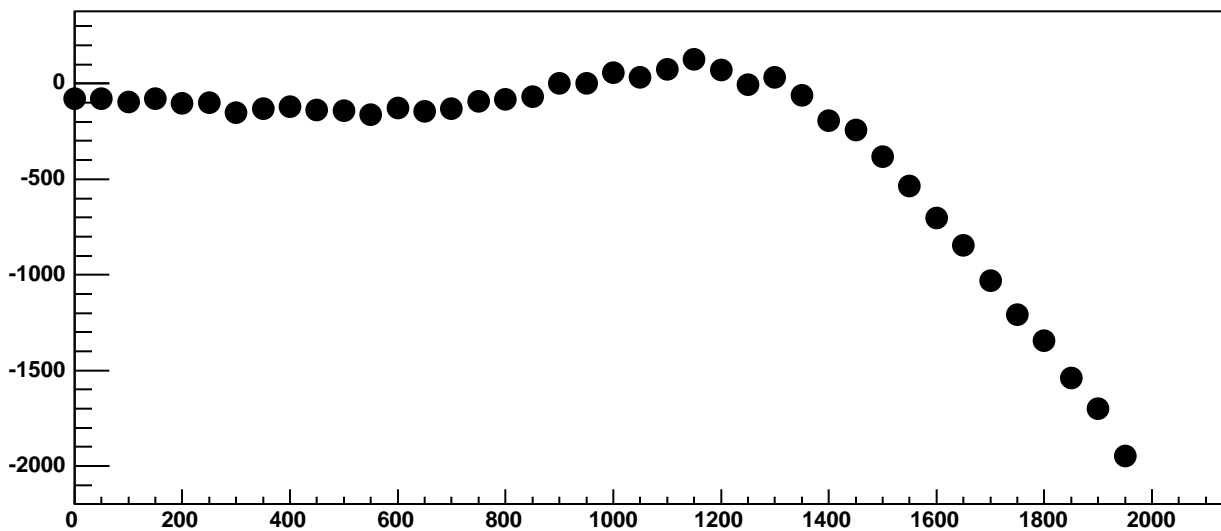
p1

$0.1294 \pm 0.03994$

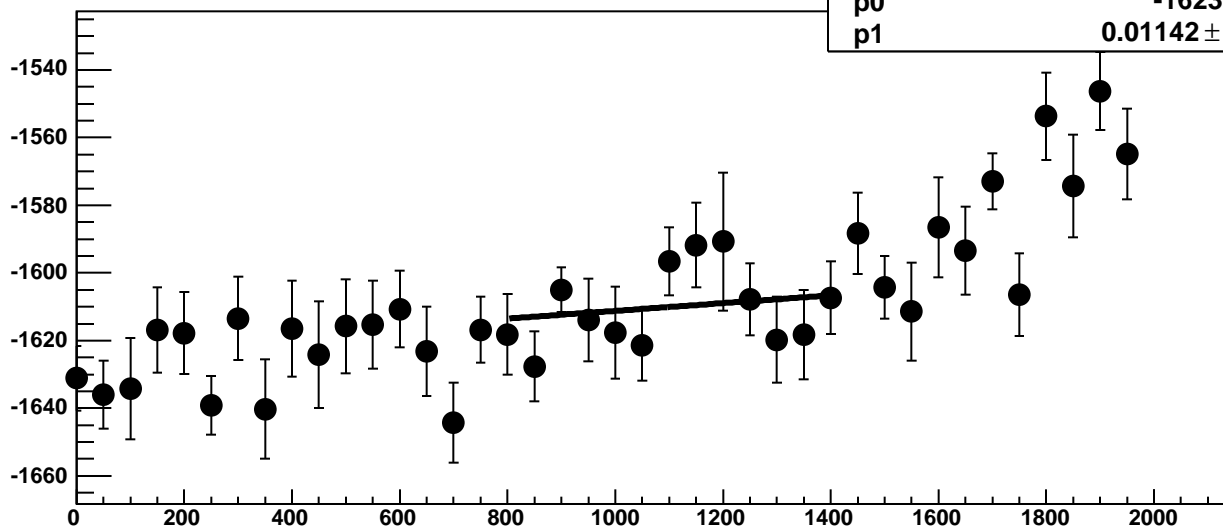
Chip 6, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC



Chip 6, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC

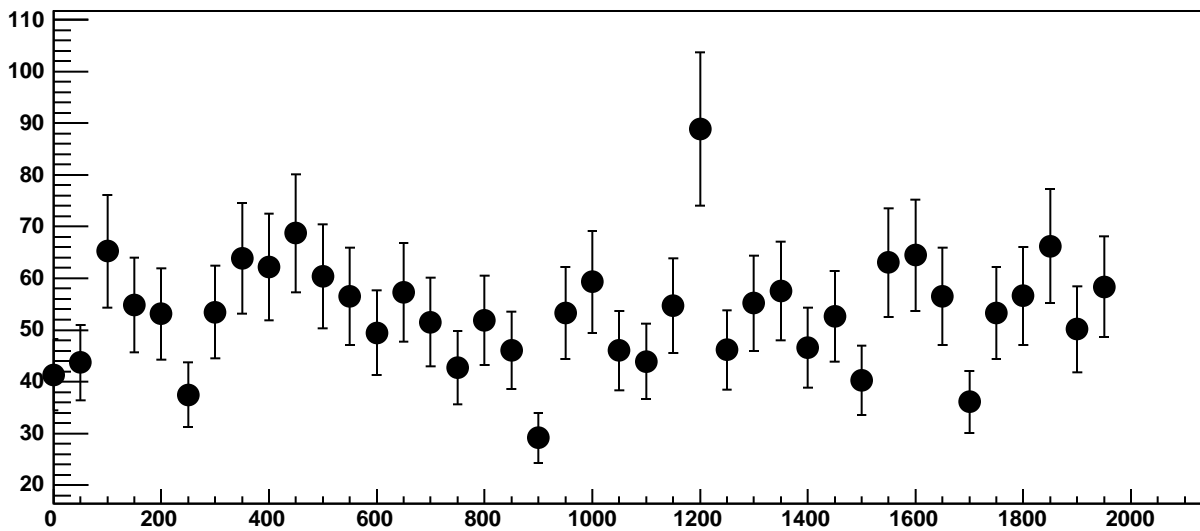


Chip 6, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC

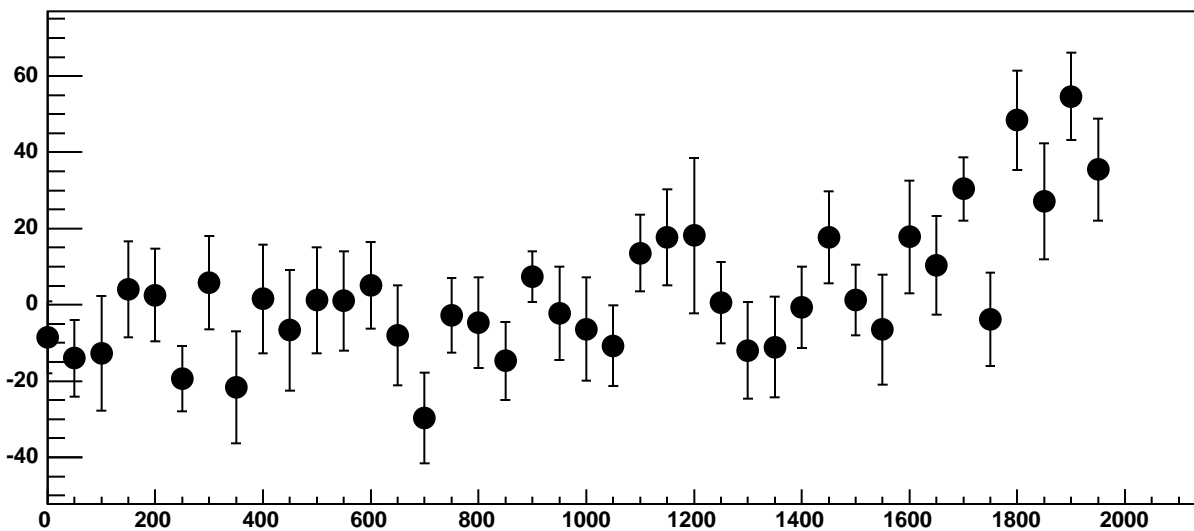


$\chi^2 / \text{ndf}$  10.9 / 11  
p0  $-1623 \pm 17.43$   
p1  $0.01142 \pm 0.01611$

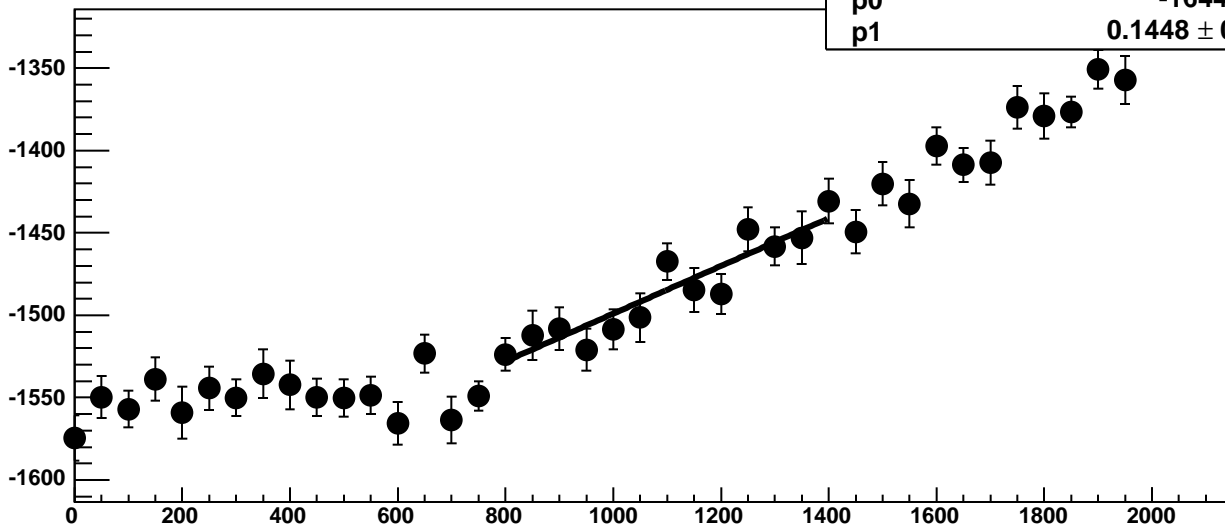
Chip 6, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC



Chip 6, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC

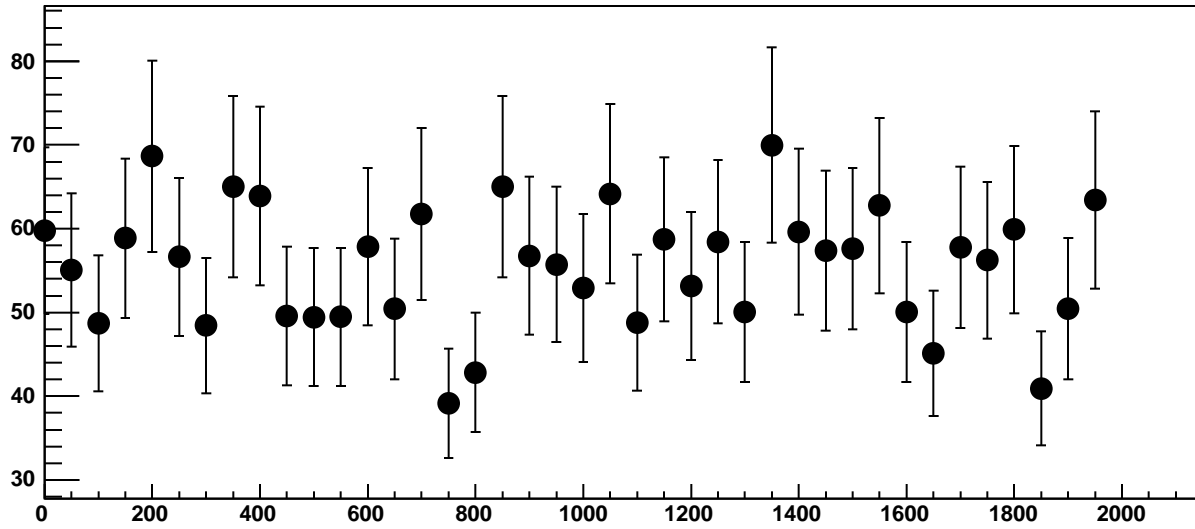


Chip 6, Channel 7, Enable 2, Hold=30, ADC Mean vs DAC

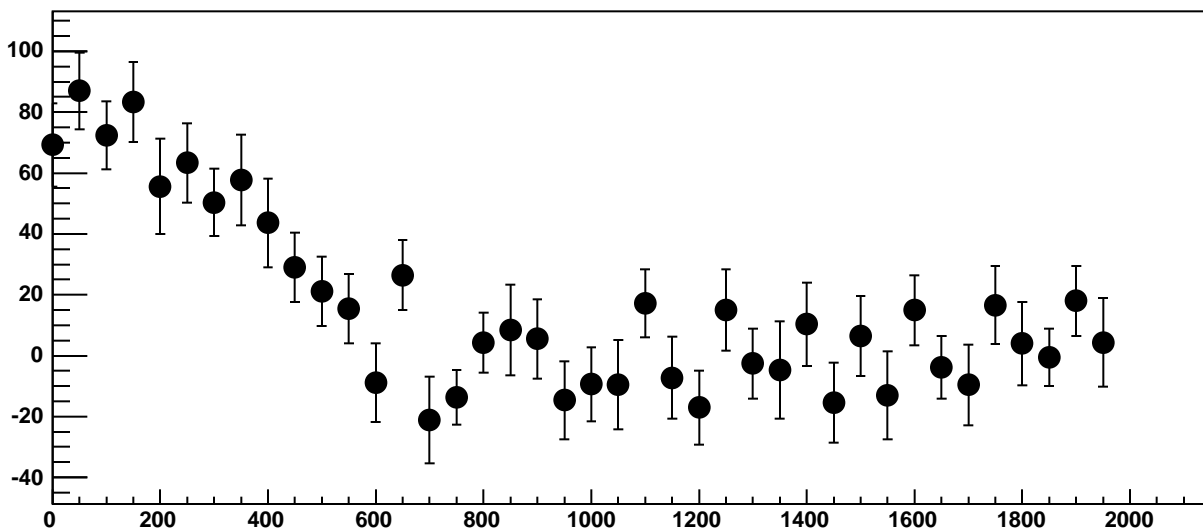


$\chi^2 / \text{ndf}$  9.563 / 11  
p0  $-1644 \pm 20.5$   
p1  $0.1448 \pm 0.01865$

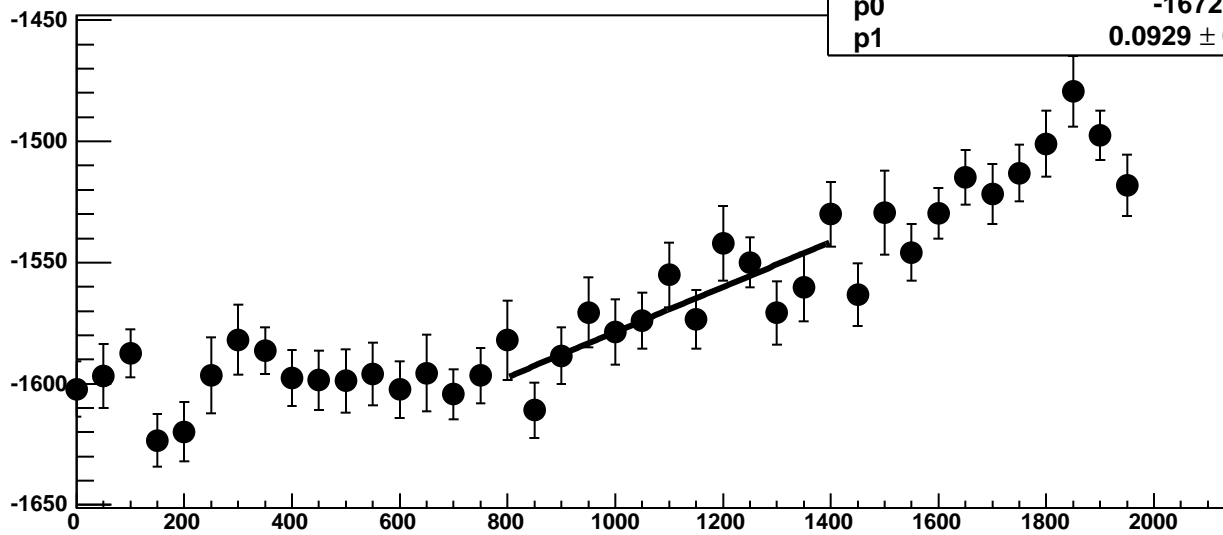
Chip 6, Channel 7, Enable 2, Hold=30, ADC Noise vs DAC



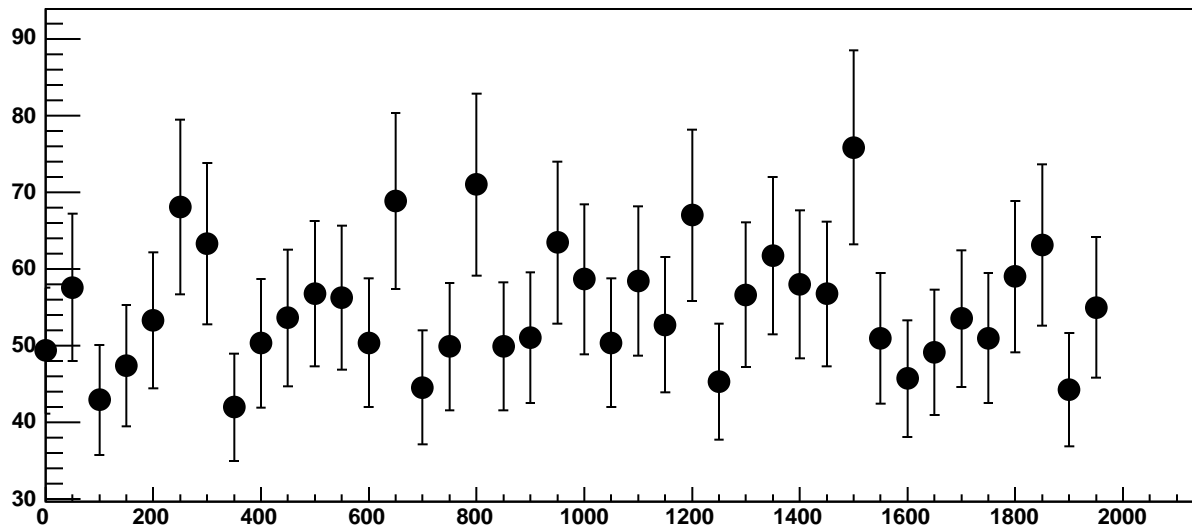
Chip 6, Channel 7, Enable 2, Hold=30, ADC Residuals vs DAC



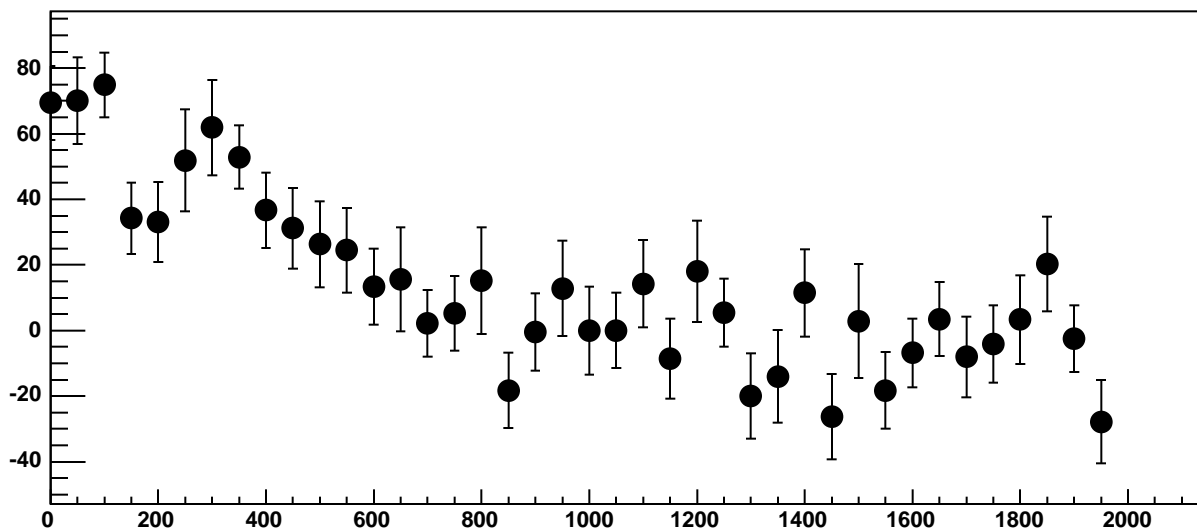
Chip 6, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC



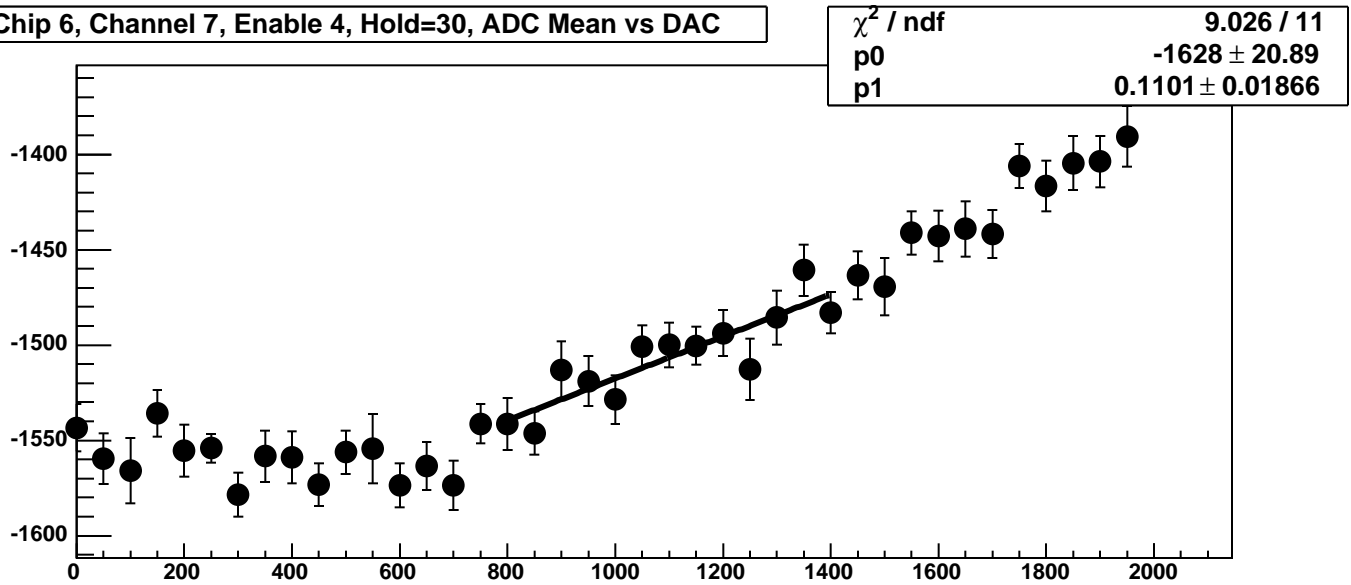
Chip 6, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC



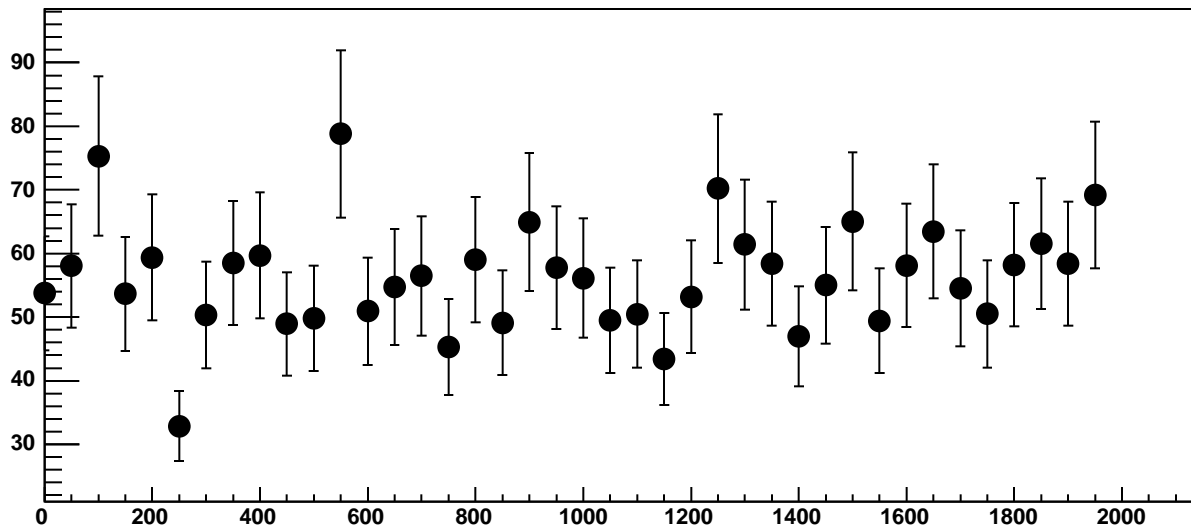
Chip 6, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC



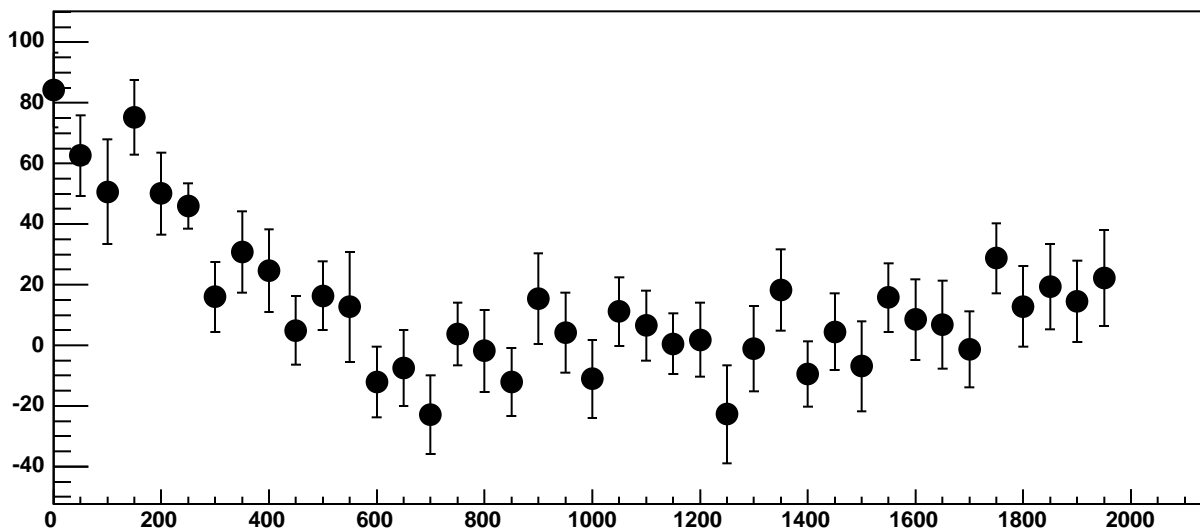
Chip 6, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC



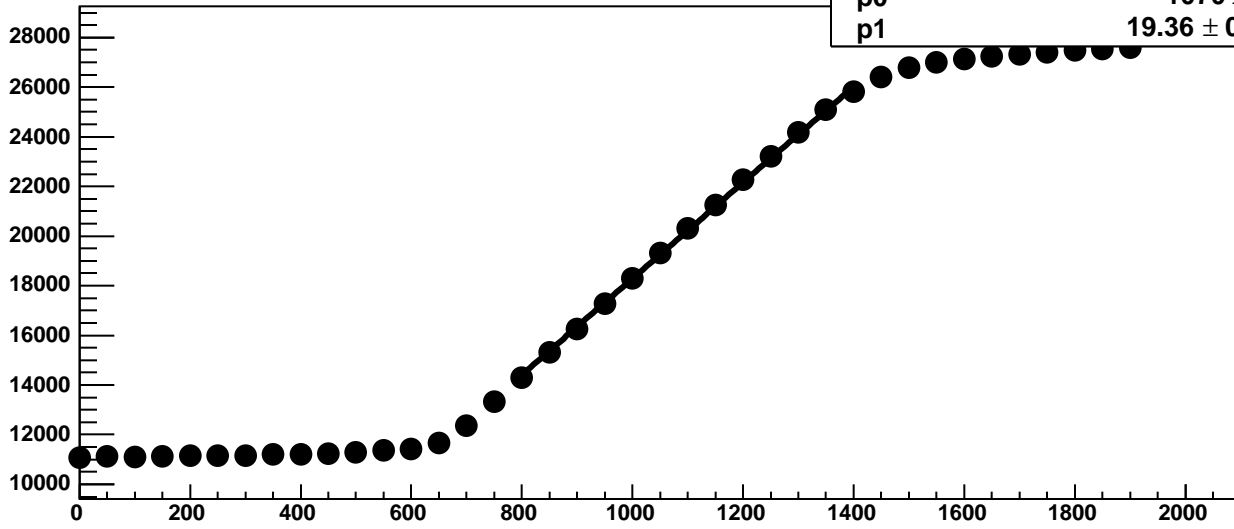
Chip 6, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC



Chip 6, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC

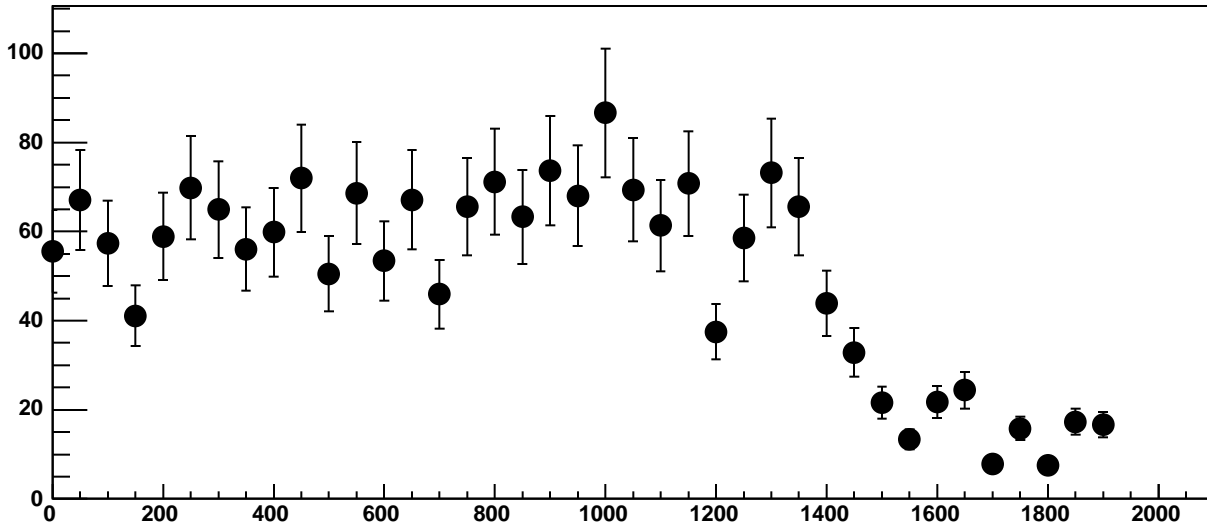


Chip 6, Channel 7, Enable 5!, Hold=30, ADC Mean vs DAC

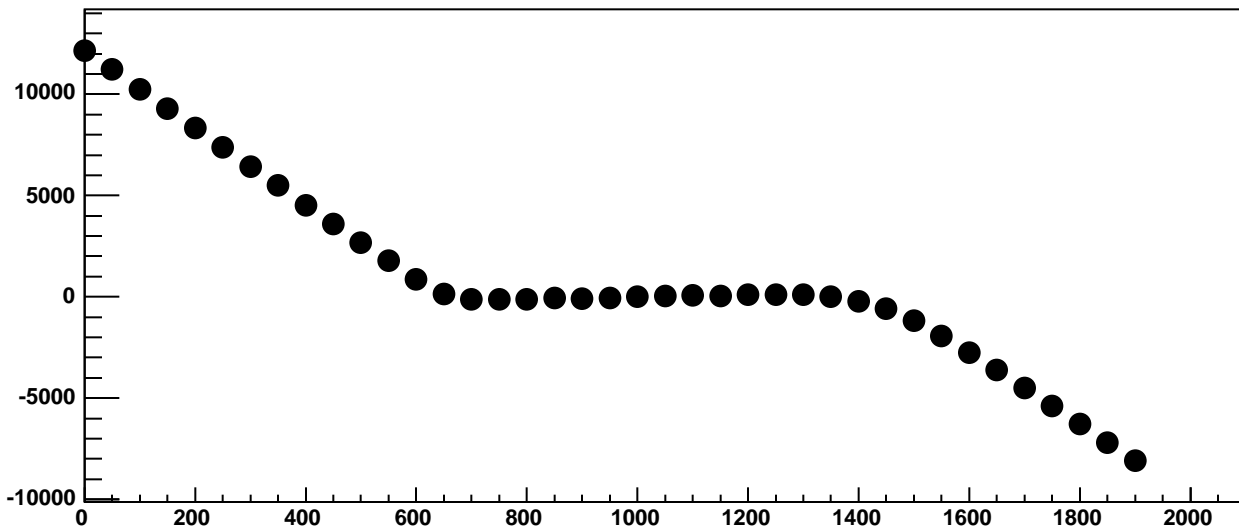


$\chi^2 / \text{ndf}$  833.8 / 11  
p0  $-1070 \pm 23.99$   
p1  $19.36 \pm 0.02069$

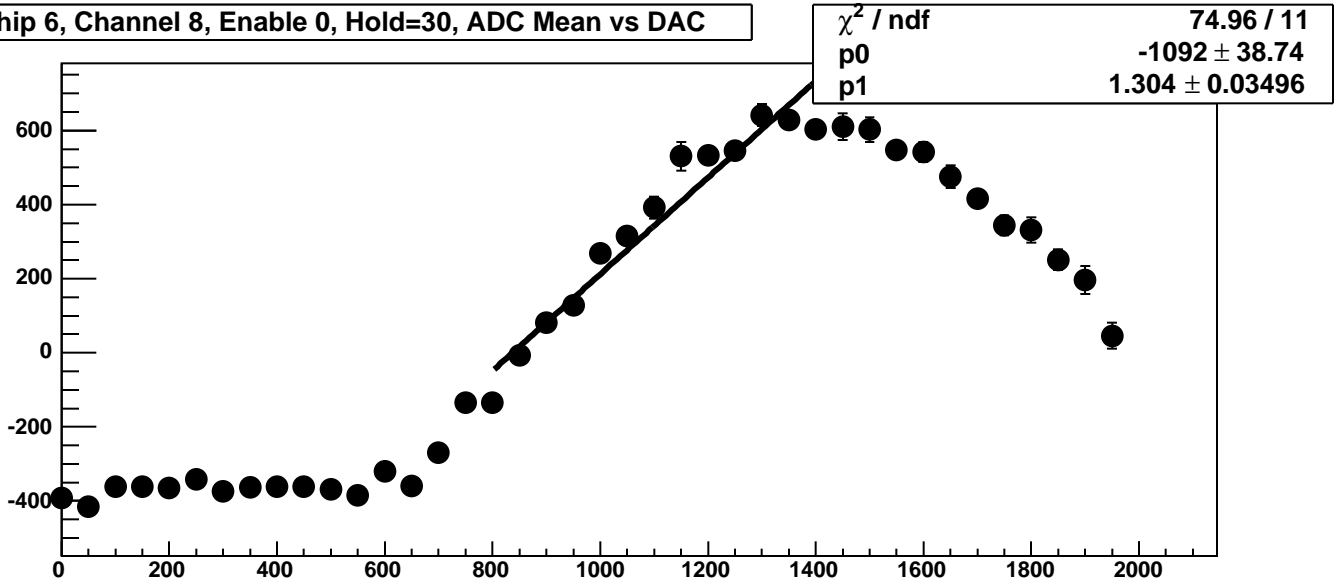
Chip 6, Channel 7, Enable 5!, Hold=30, ADC Noise vs DAC



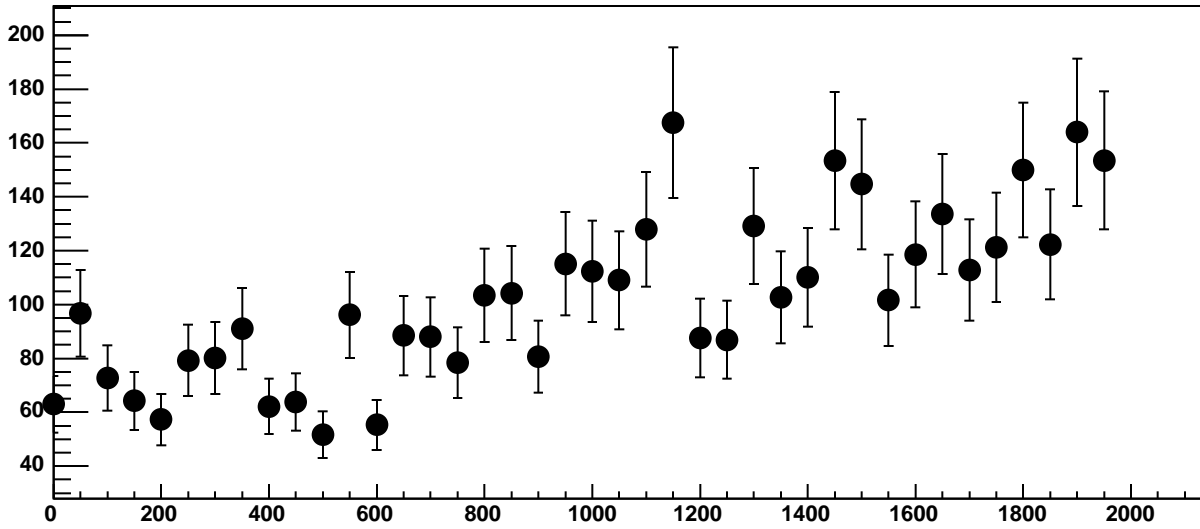
Chip 6, Channel 7, Enable 5!, Hold=30, ADC Residuals vs DAC



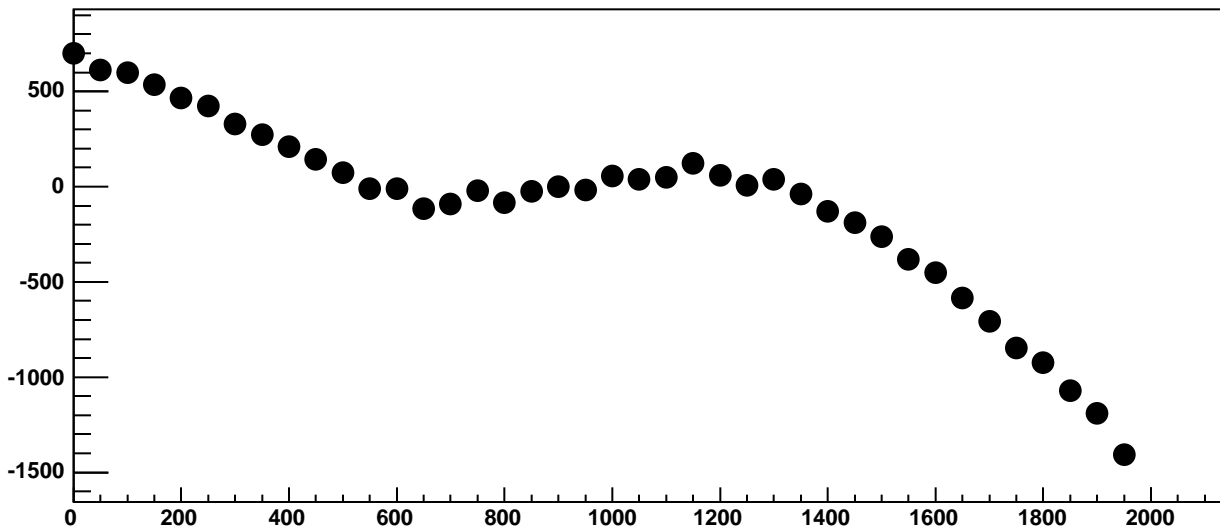
Chip 6, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC



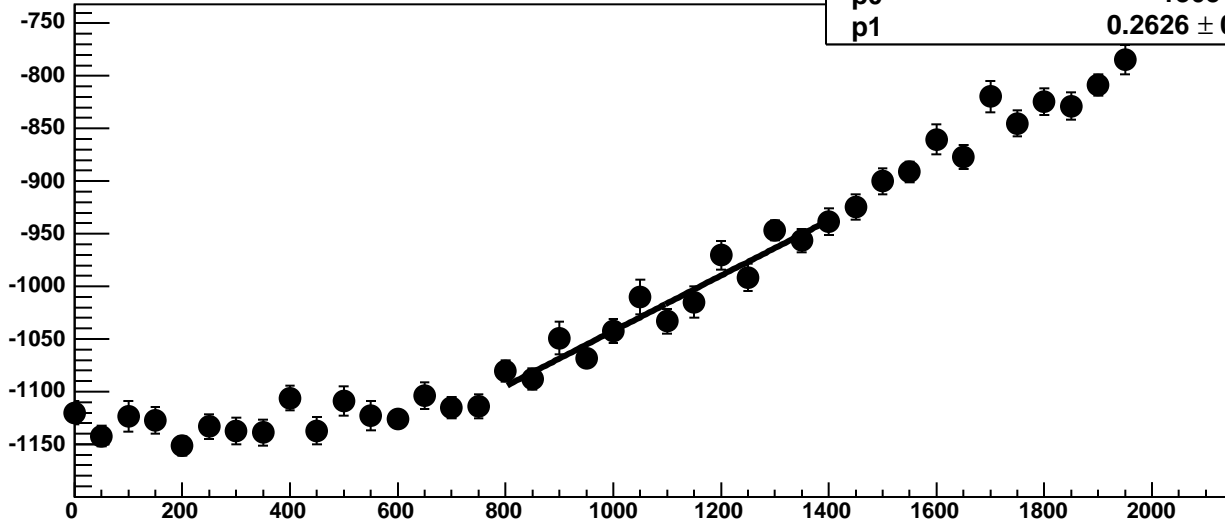
Chip 6, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC



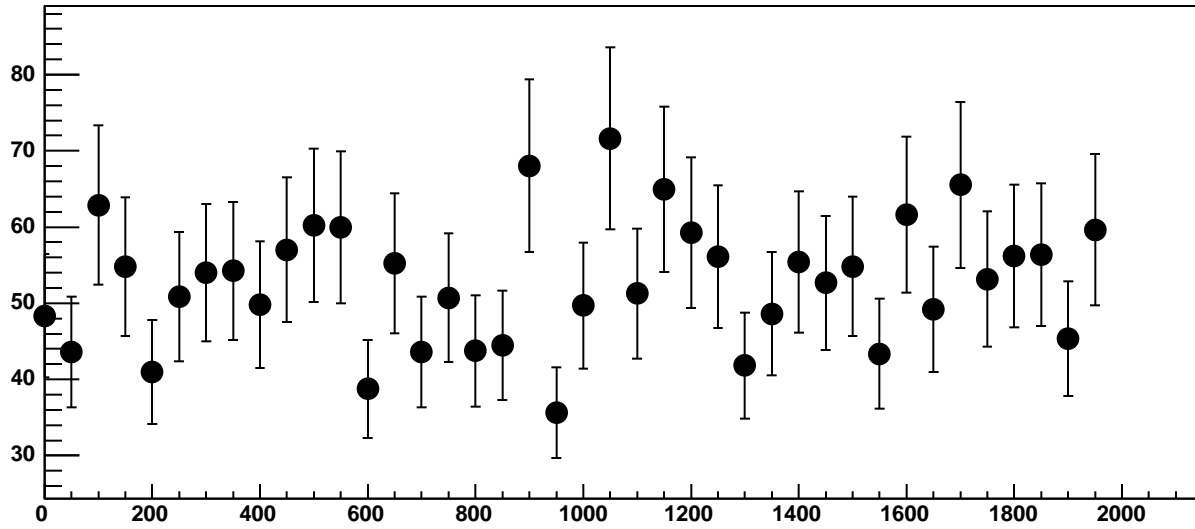
Chip 6, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC



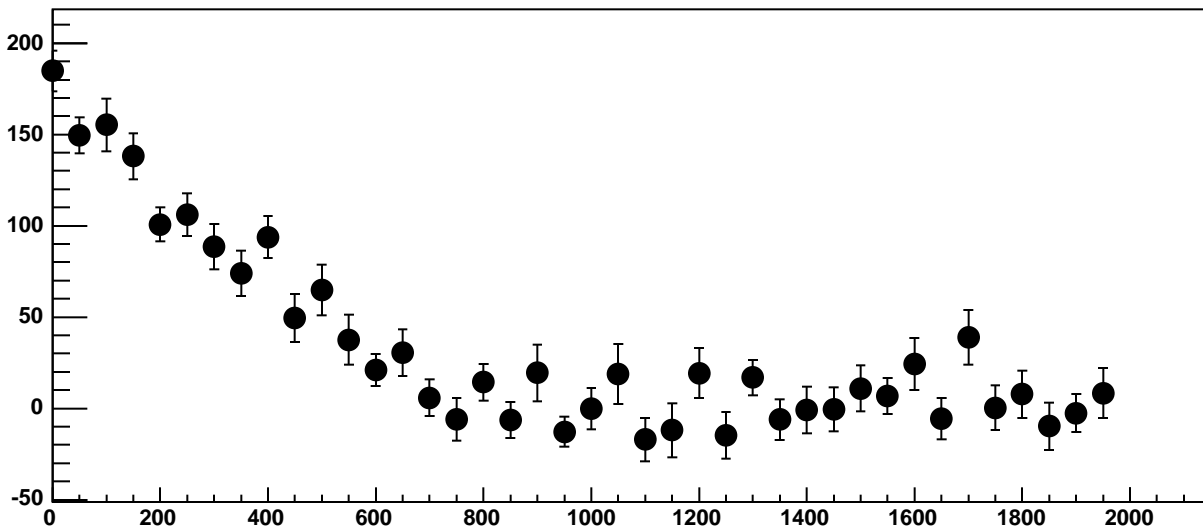
Chip 6, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC



Chip 6, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC

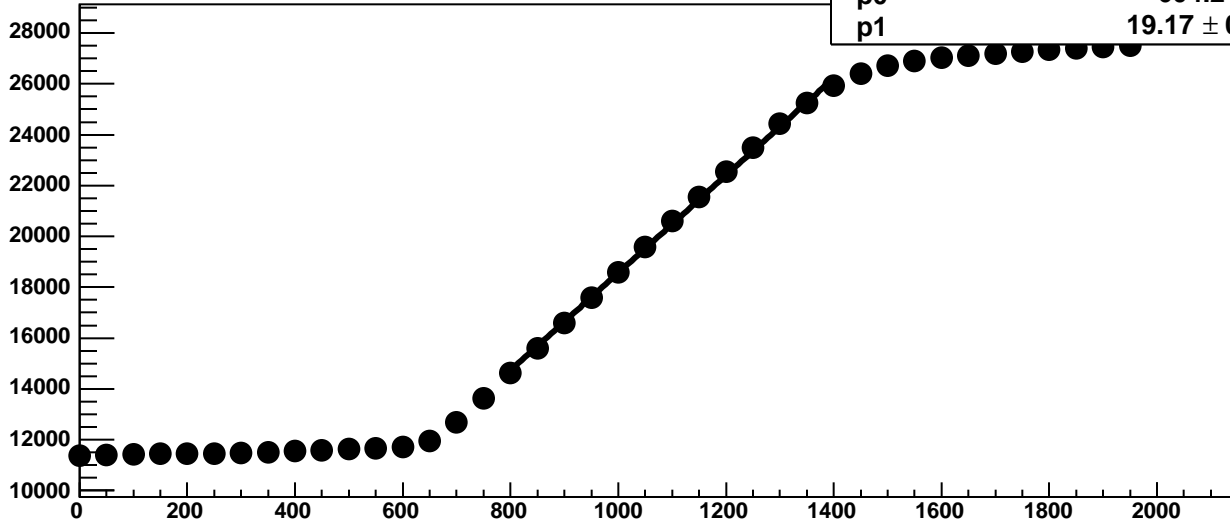


Chip 6, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC



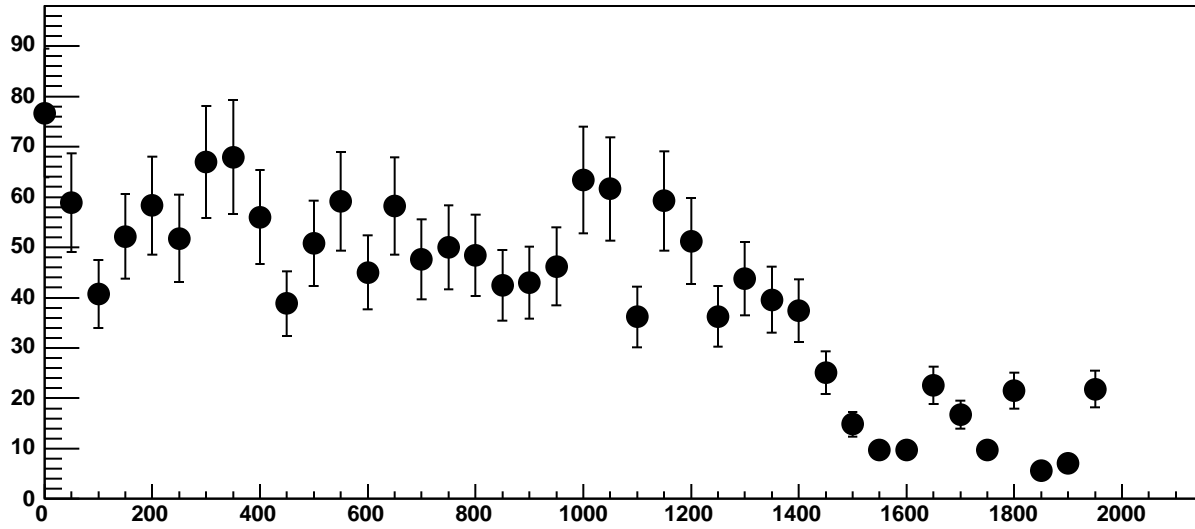


Chip 6, Channel 8, Enable 2!, Hold=30, ADC Mean vs DAC

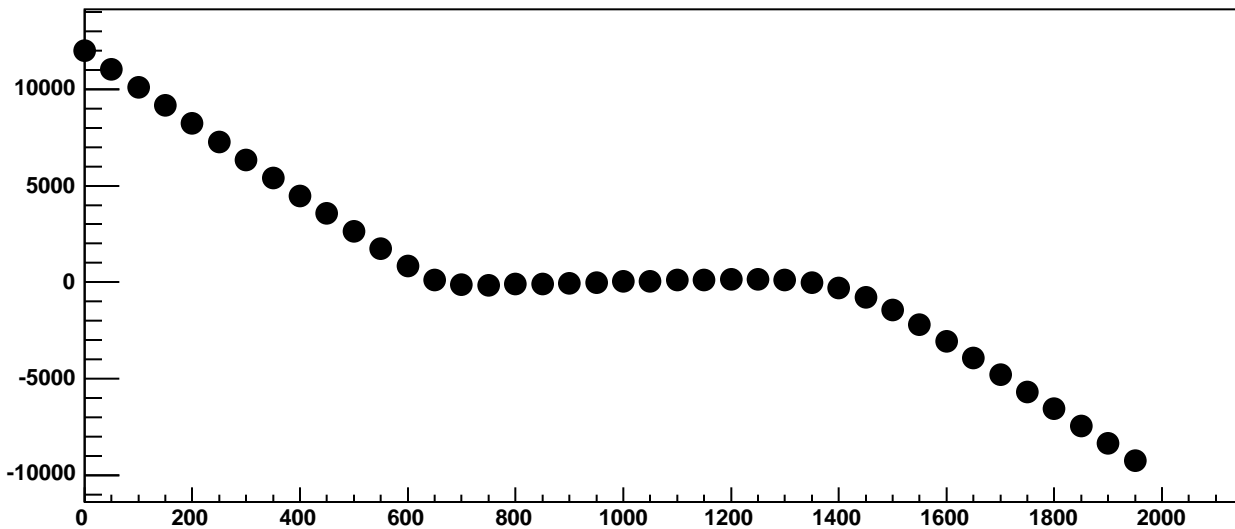


$\chi^2 / \text{ndf}$  2204 / 11  
p0  $-604.2 \pm 16.52$   
p1  $19.17 \pm 0.01448$

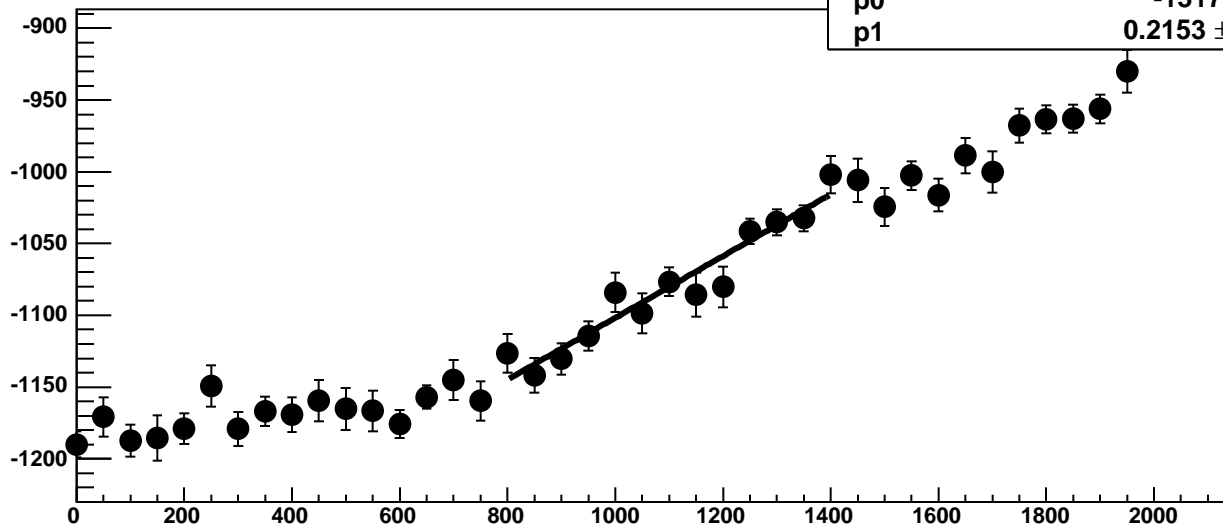
Chip 6, Channel 8, Enable 2!, Hold=30, ADC Noise vs DAC



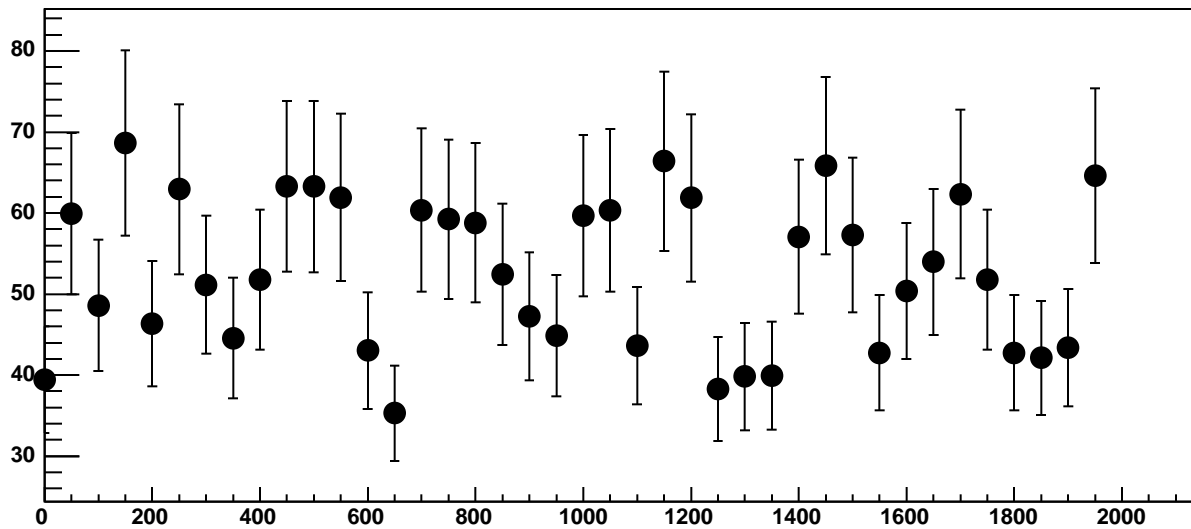
Chip 6, Channel 8, Enable 2!, Hold=30, ADC Residuals vs DAC



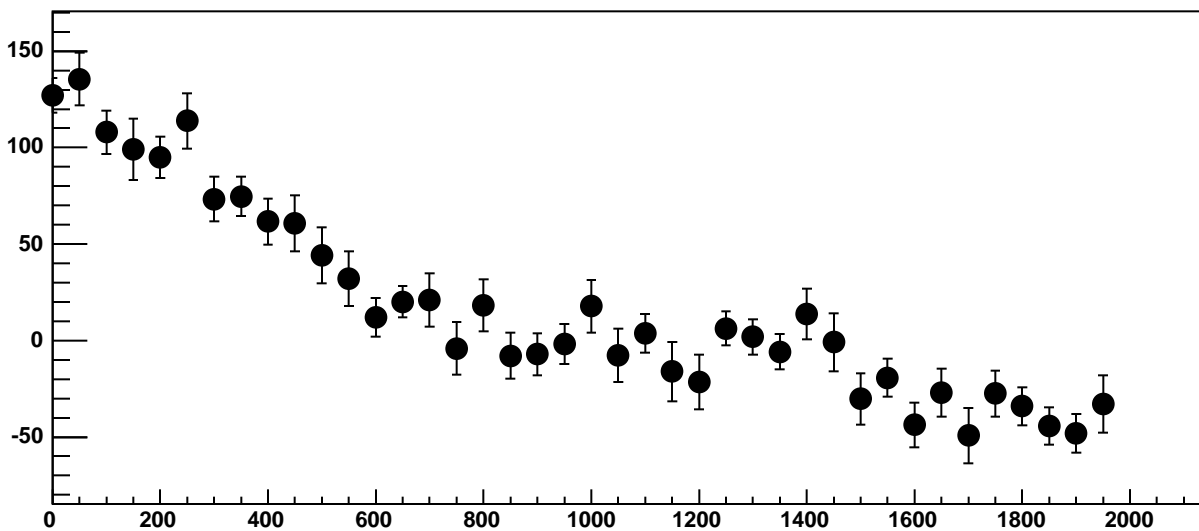
Chip 6, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC



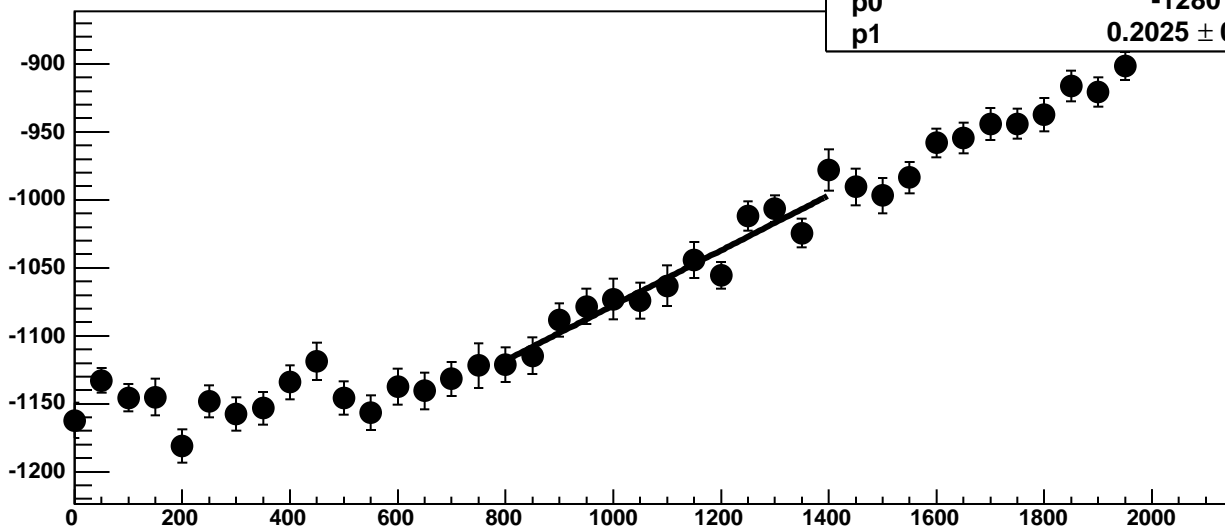
Chip 6, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC



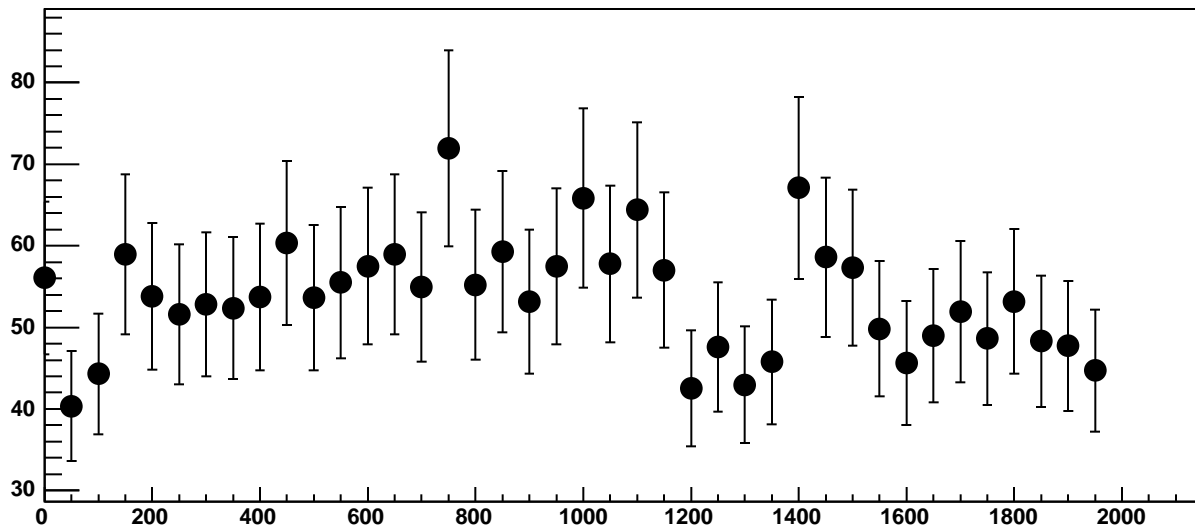
Chip 6, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC



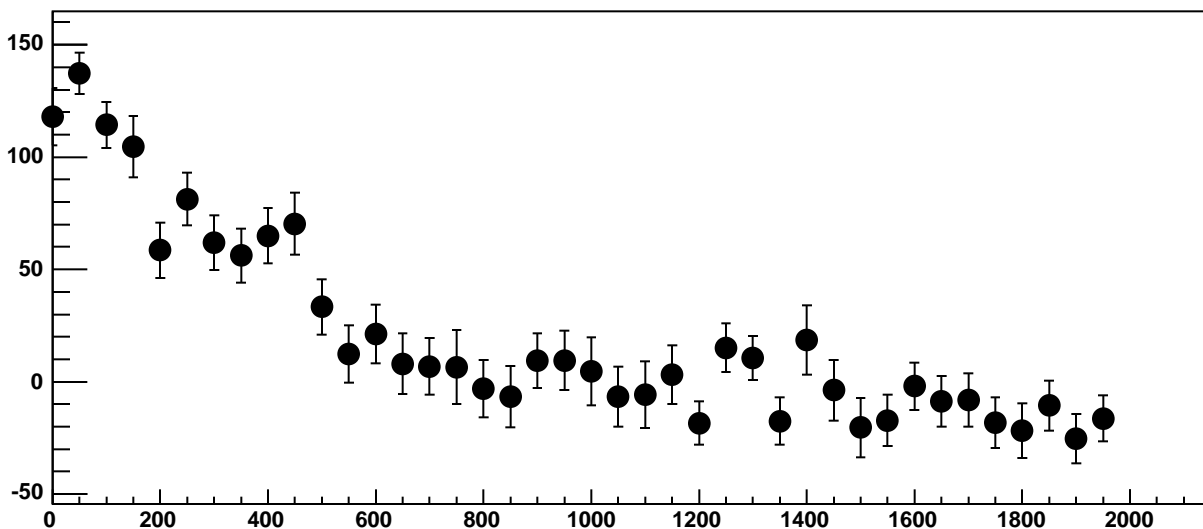
Chip 6, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC



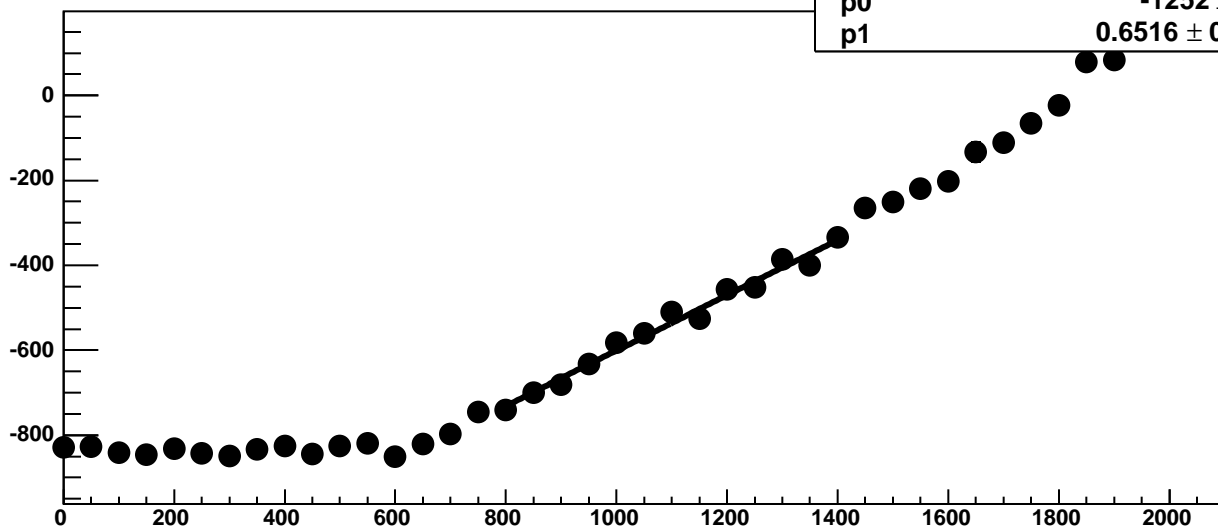
Chip 6, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC



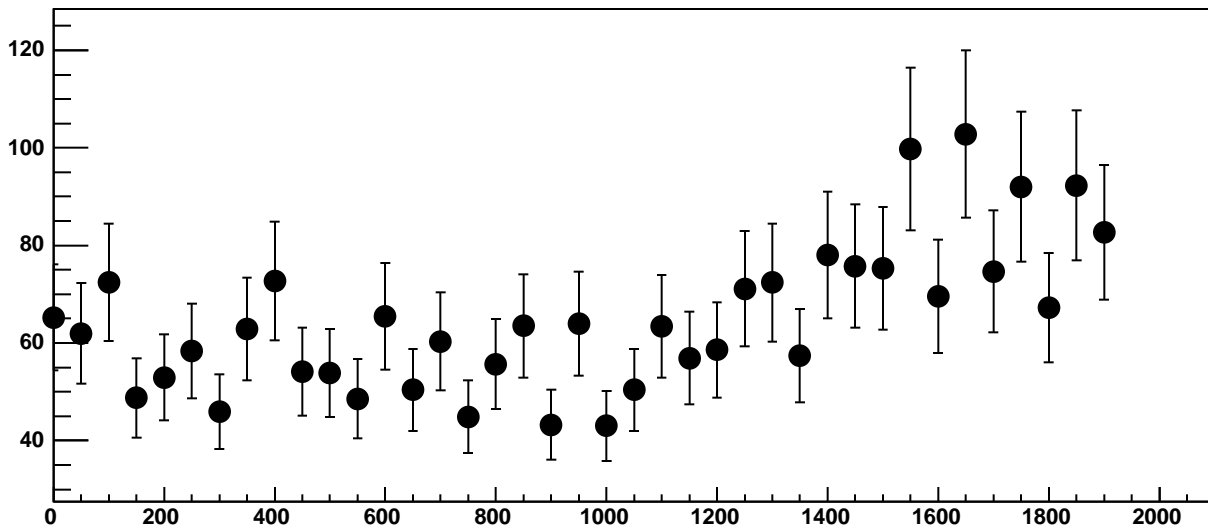
Chip 6, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC



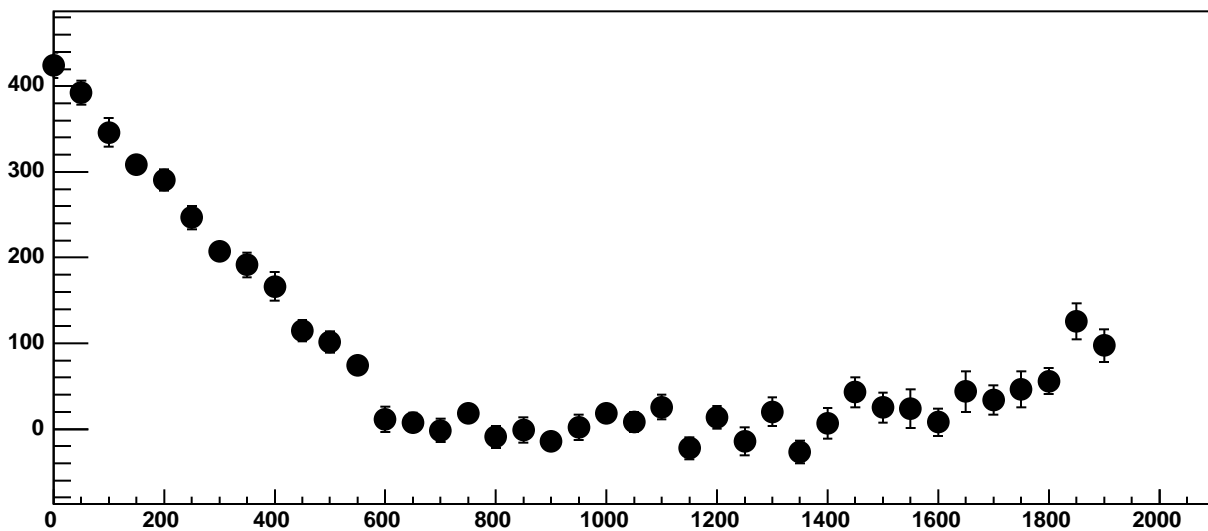
Chip 6, Channel 8, Enable 5, Hold=30, ADC Mean vs DAC



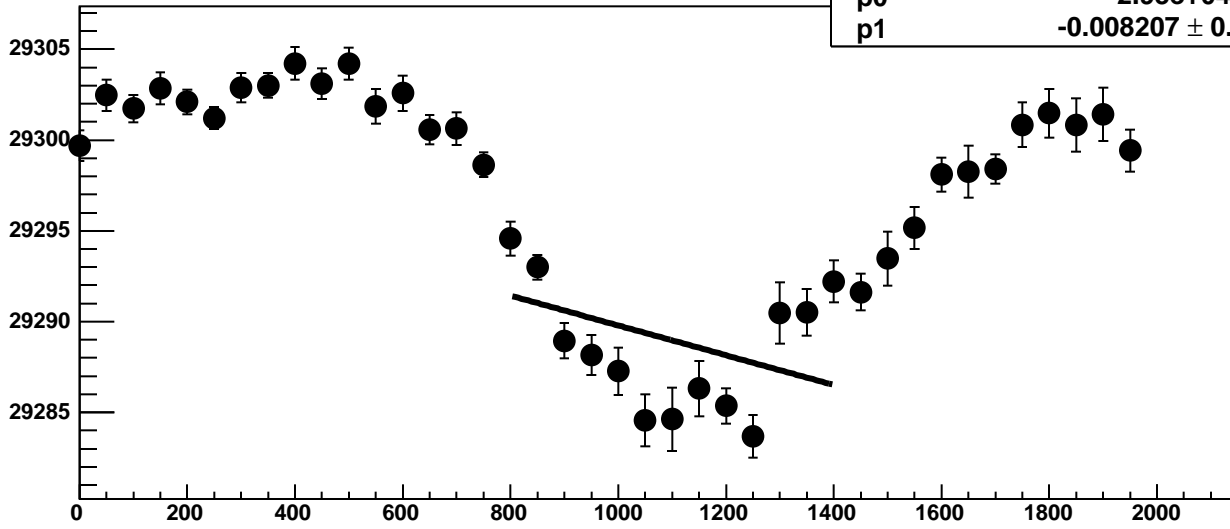
Chip 6, Channel 8, Enable 5, Hold=30, ADC Noise vs DAC



Chip 6, Channel 8, Enable 5, Hold=30, ADC Residuals vs DAC

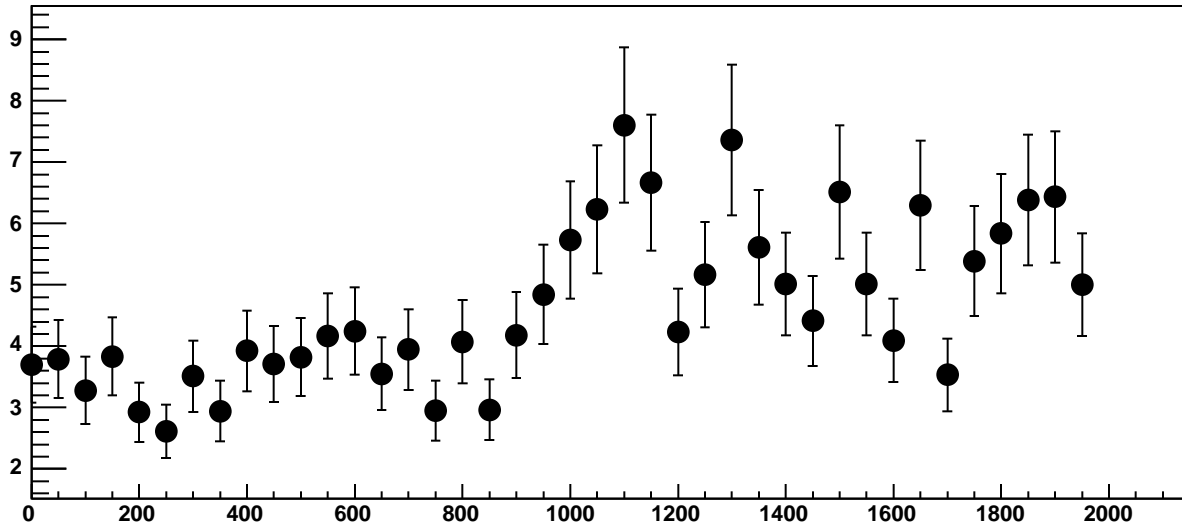


Chip 6, Channel 9, Enable 0!, Hold=30, ADC Mean vs DAC

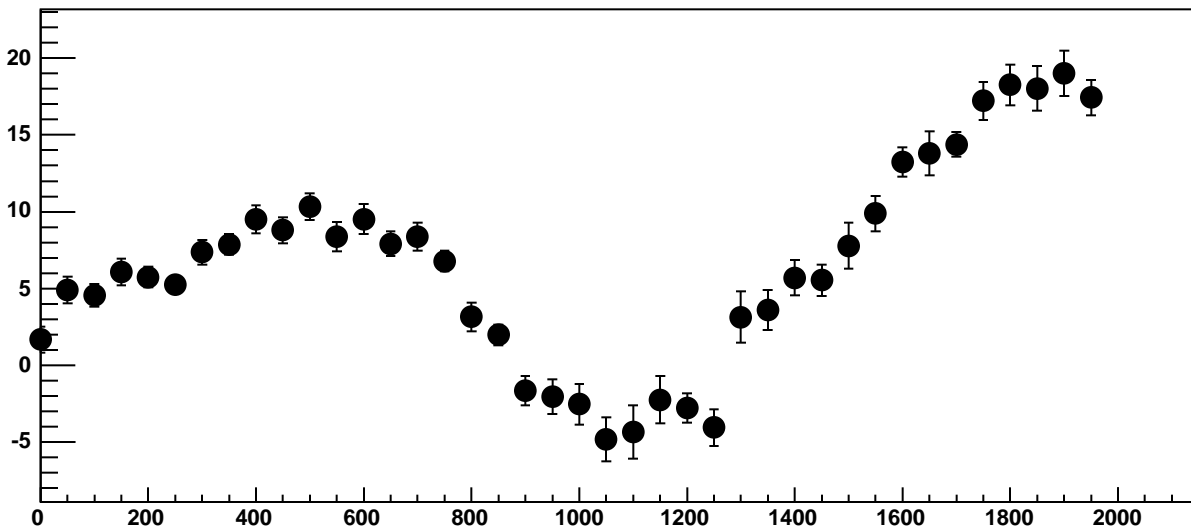


$\chi^2 / \text{ndf}$  105.3 / 11  
p0  $2.93\text{e}+04 \pm 1.64$   
p1  $-0.008207 \pm 0.001548$

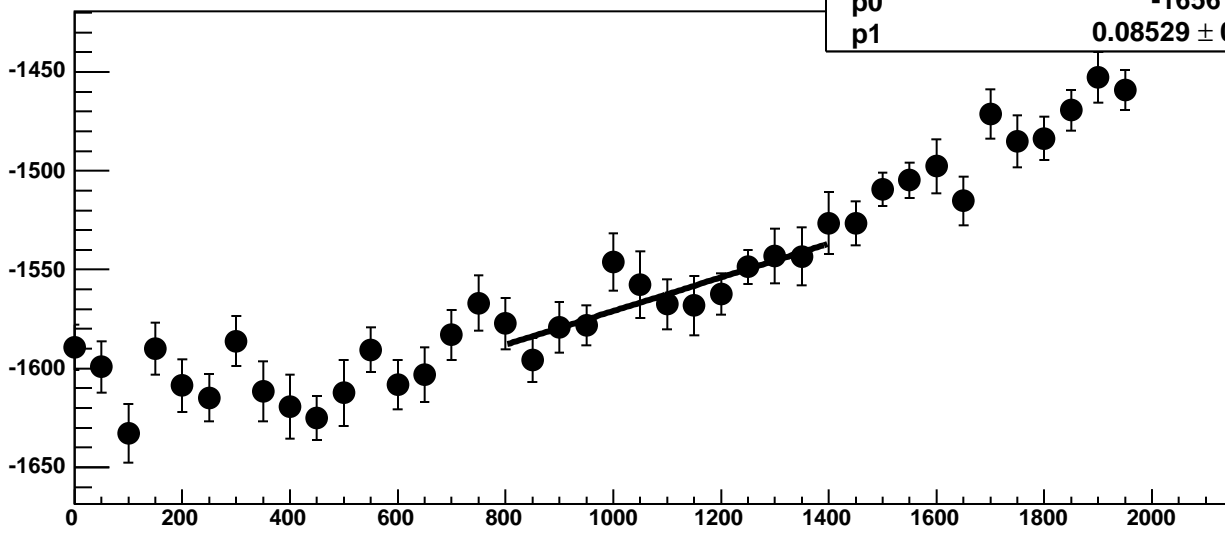
Chip 6, Channel 9, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 6, Channel 9, Enable 0!, Hold=30, ADC Residuals vs DAC

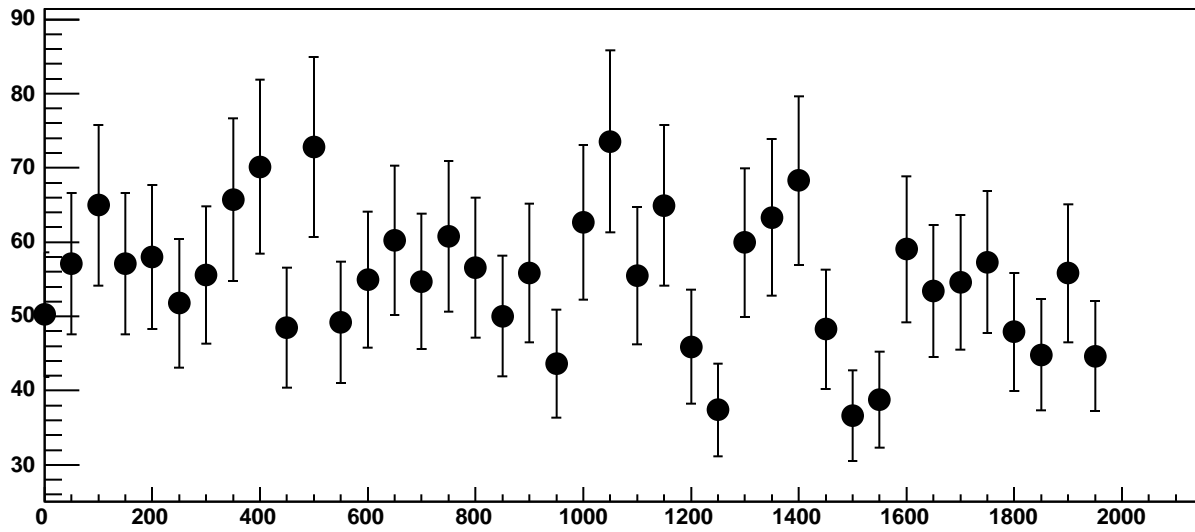


Chip 6, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC

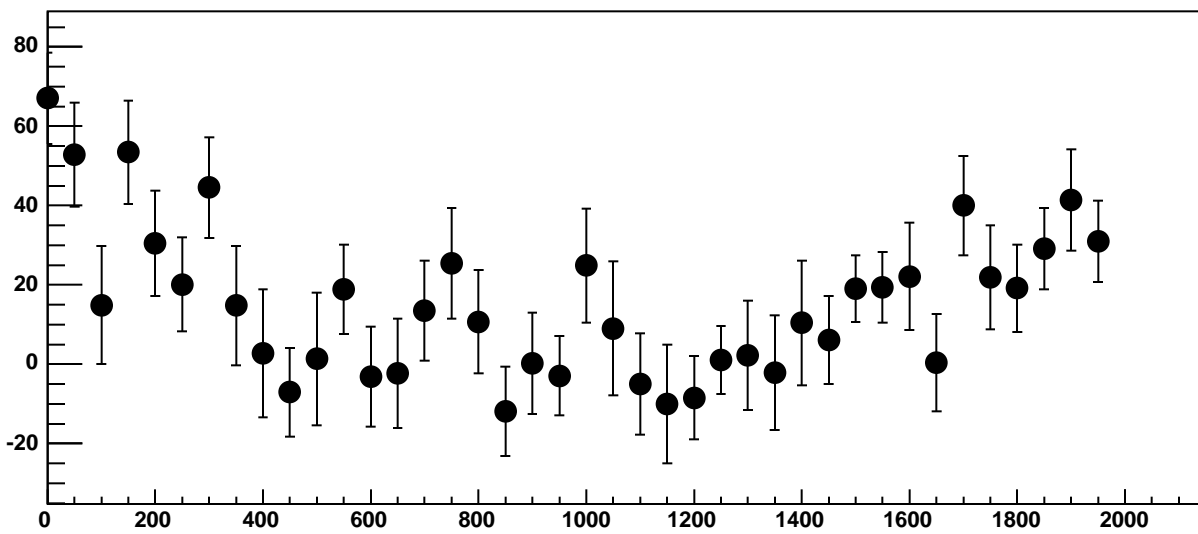


$\chi^2 / \text{ndf}$  6.932 / 11  
p0  $-1656 \pm 20.82$   
p1  $0.08529 \pm 0.01877$

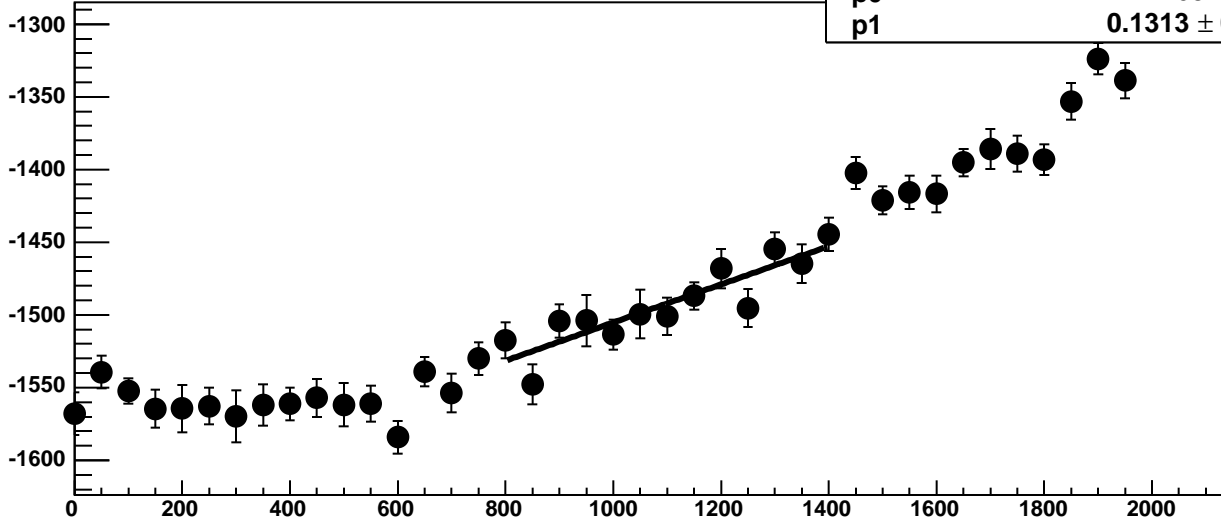
Chip 6, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC



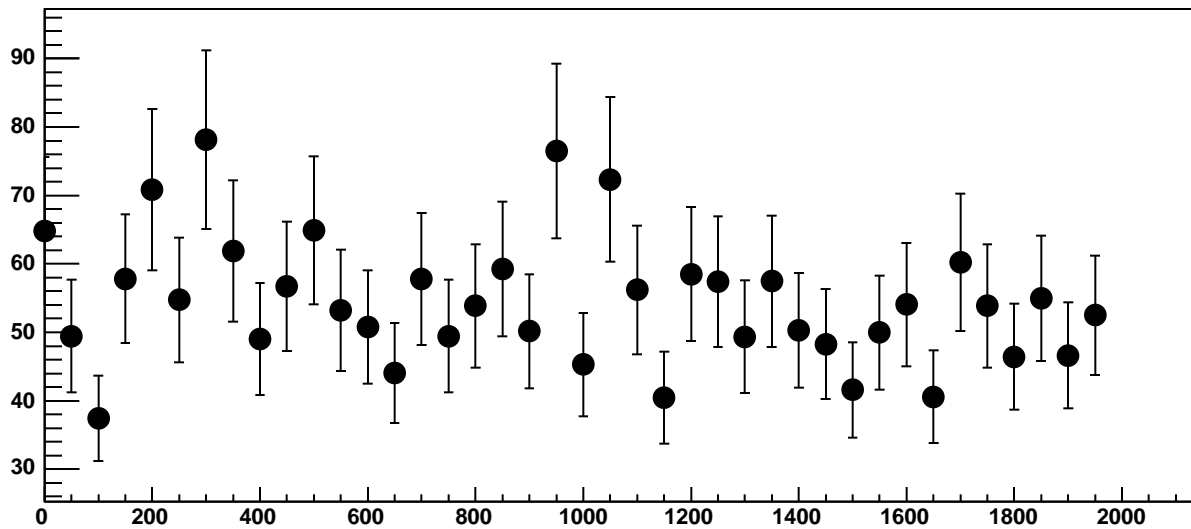
Chip 6, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC



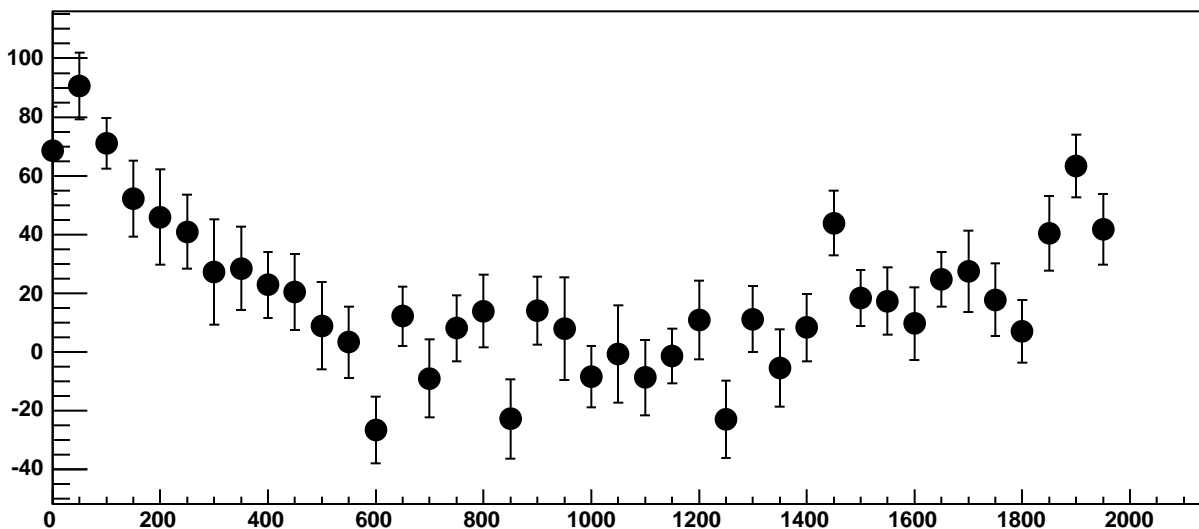
Chip 6, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC



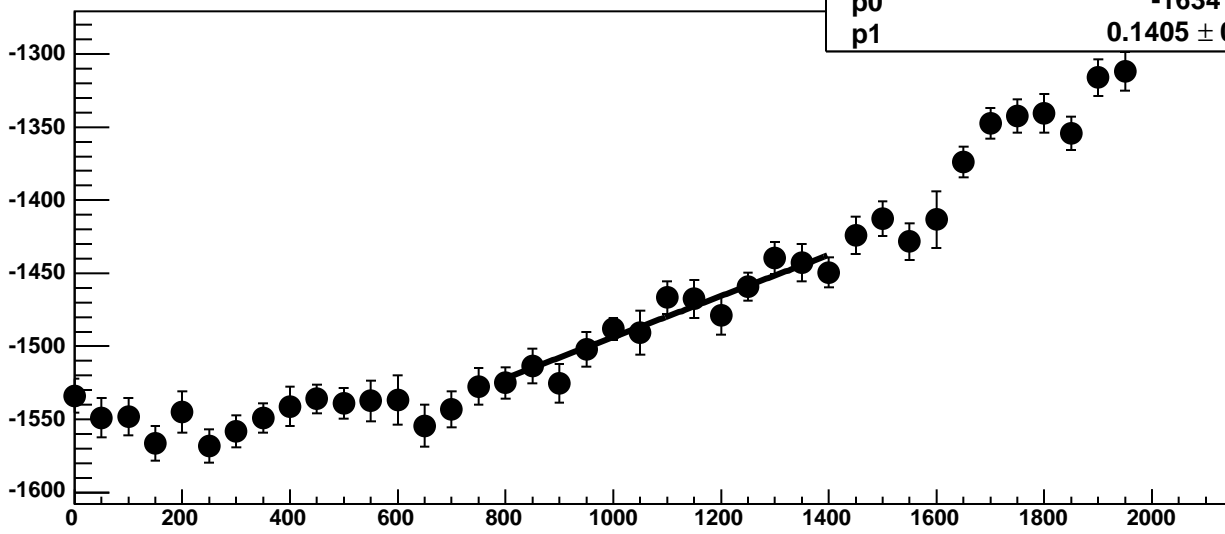
Chip 6, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC



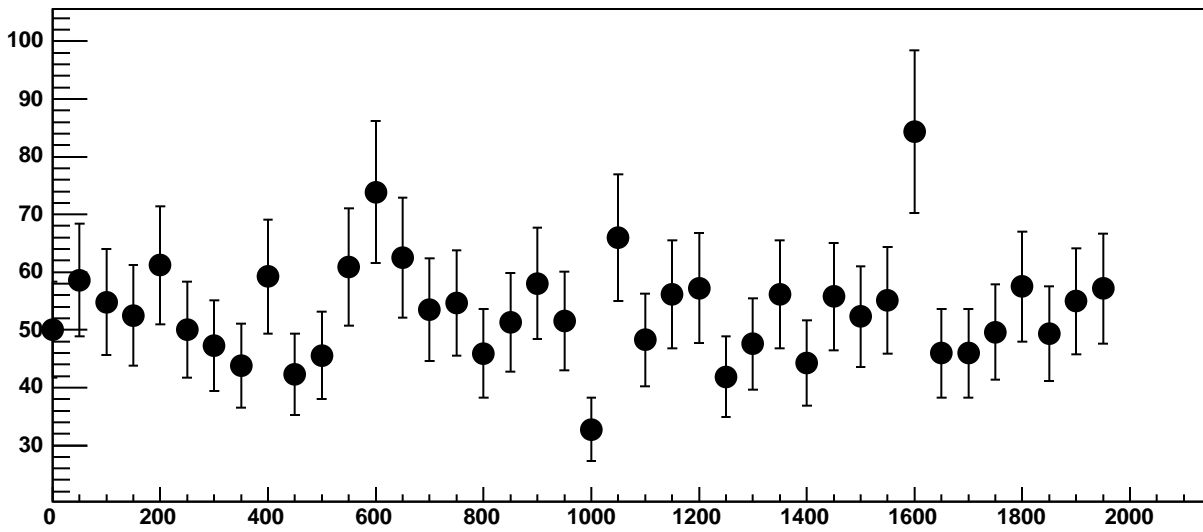
Chip 6, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC



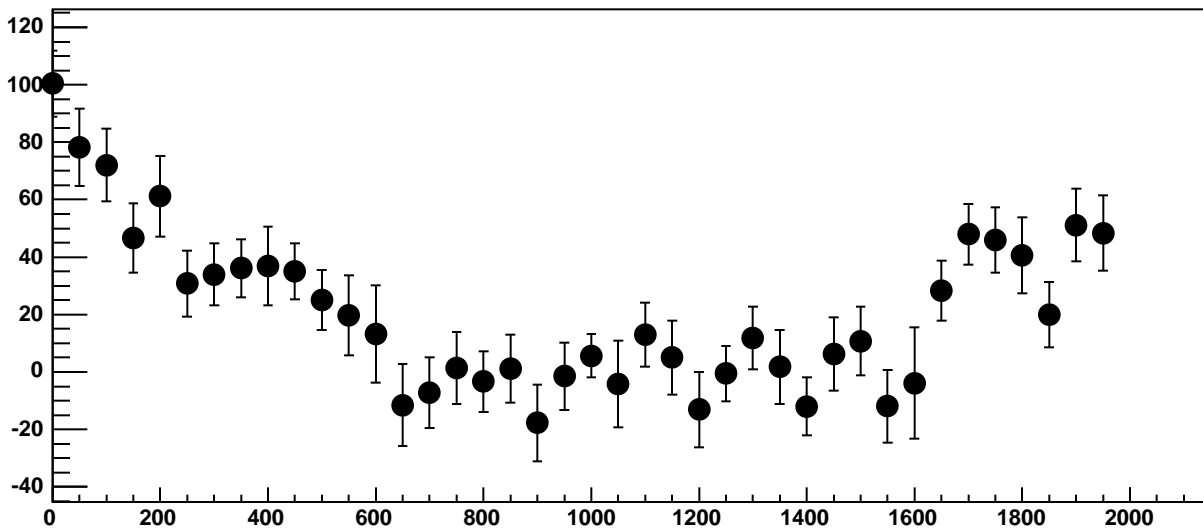
Chip 6, Channel 9, Enable 3, Hold=30, ADC Mean vs DAC



Chip 6, Channel 9, Enable 3, Hold=30, ADC Noise vs DAC

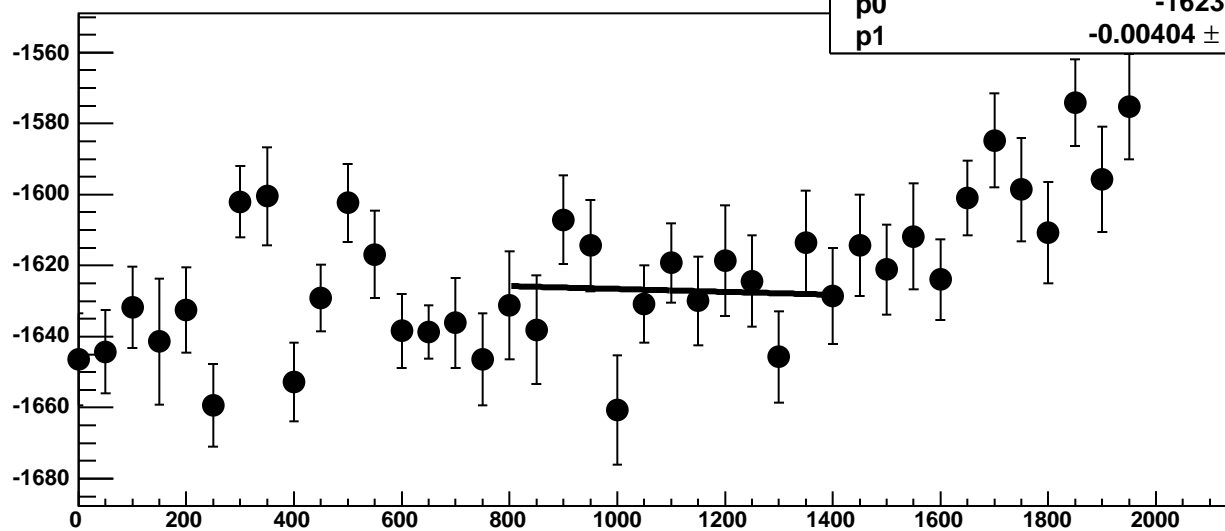


Chip 6, Channel 9, Enable 3, Hold=30, ADC Residuals vs DAC



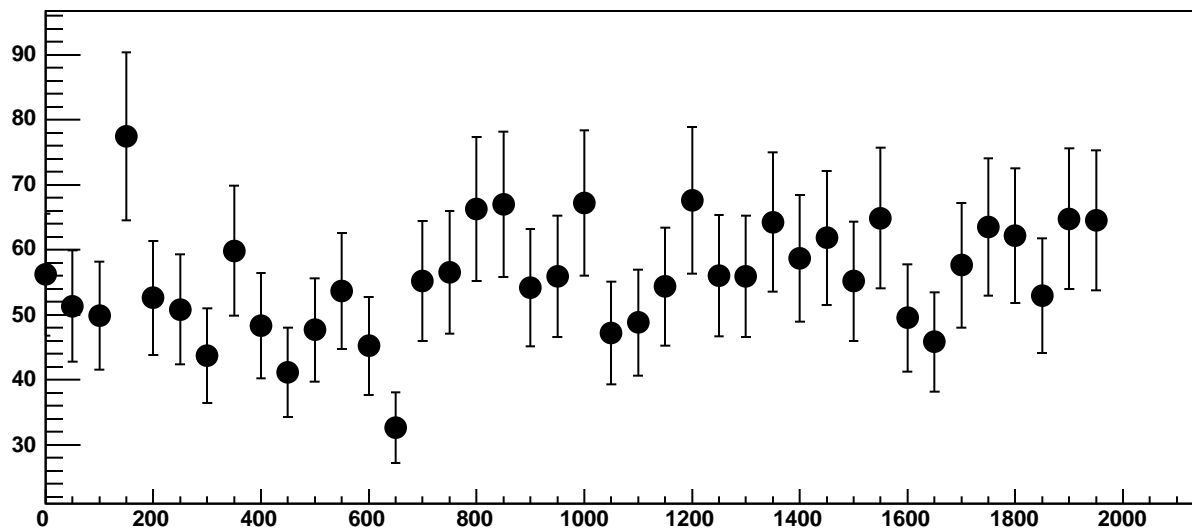


Chip 6, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC

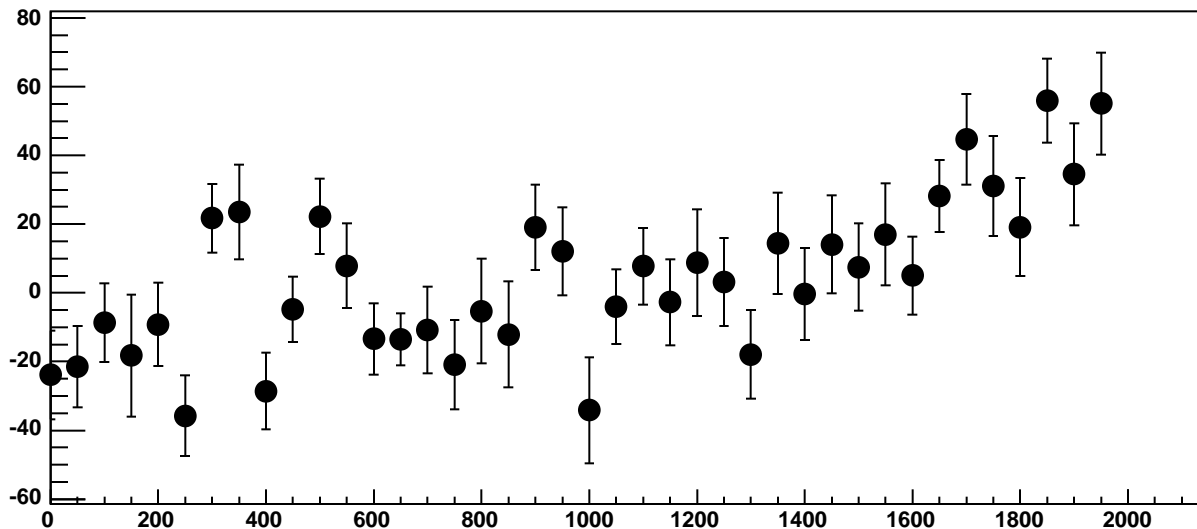


$\chi^2 / \text{ndf}$  12.8 / 11  
p0  $-1623 \pm 23.14$   
p1  $-0.00404 \pm 0.02071$

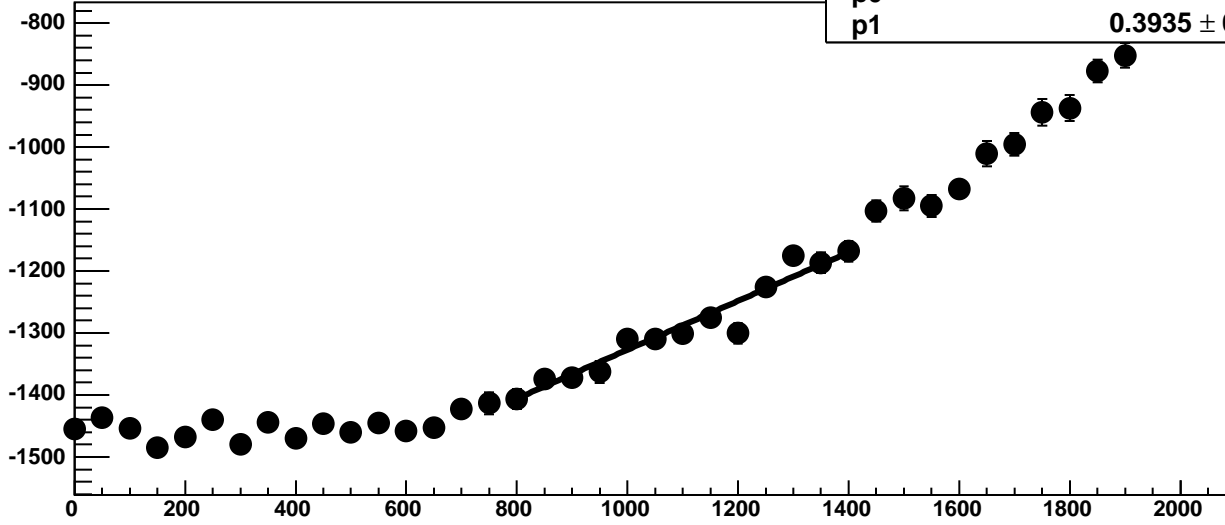
Chip 6, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC



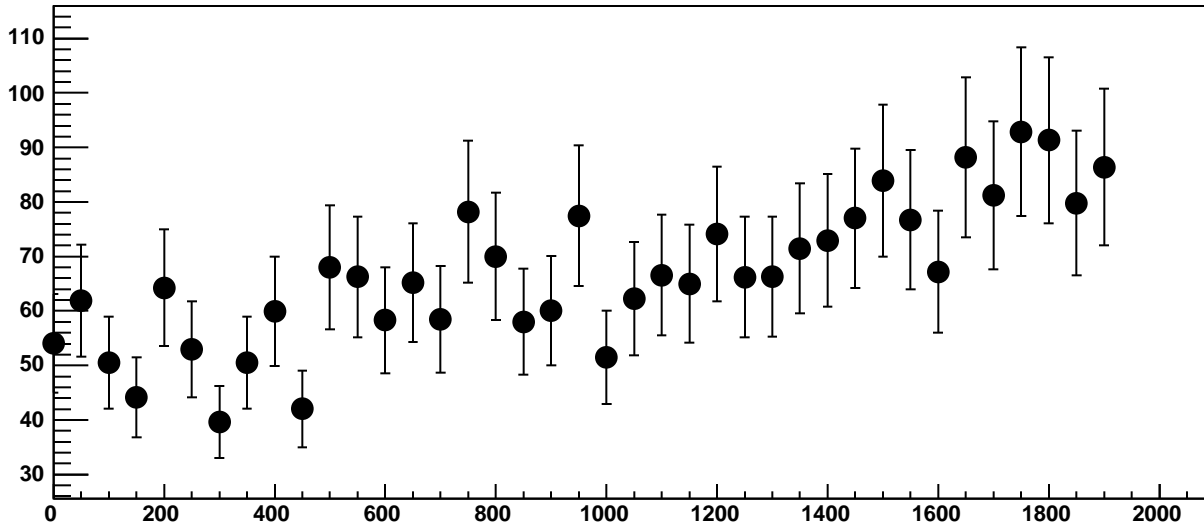
Chip 6, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC



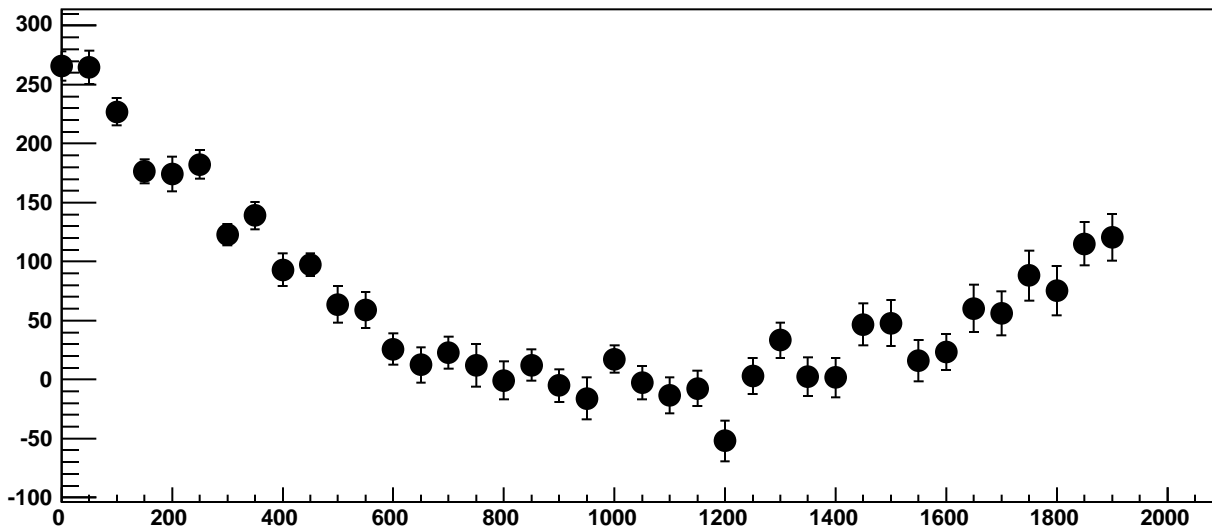
Chip 6, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC



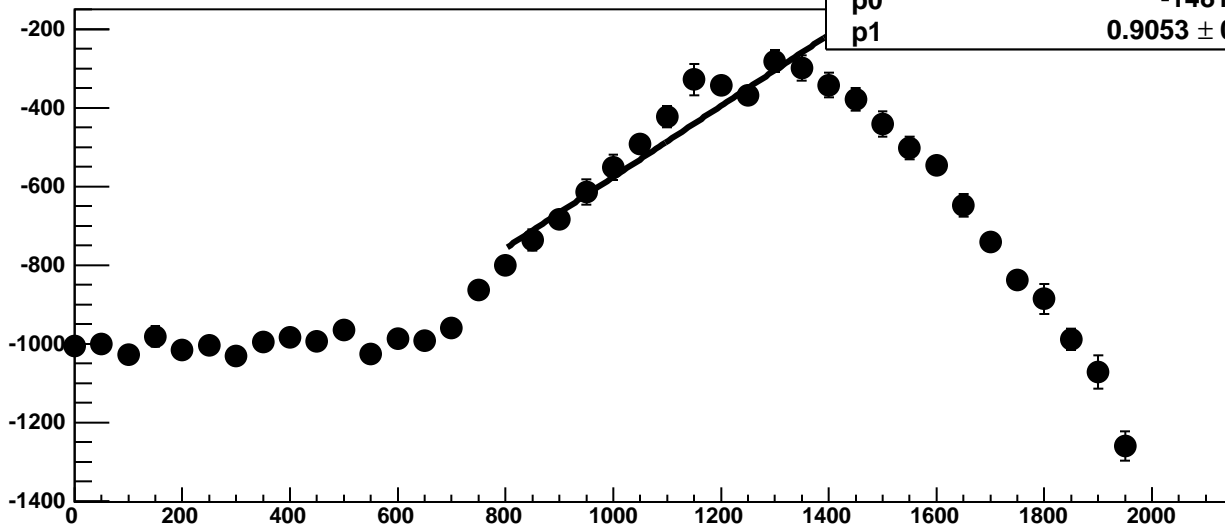
Chip 6, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC



Chip 6, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC

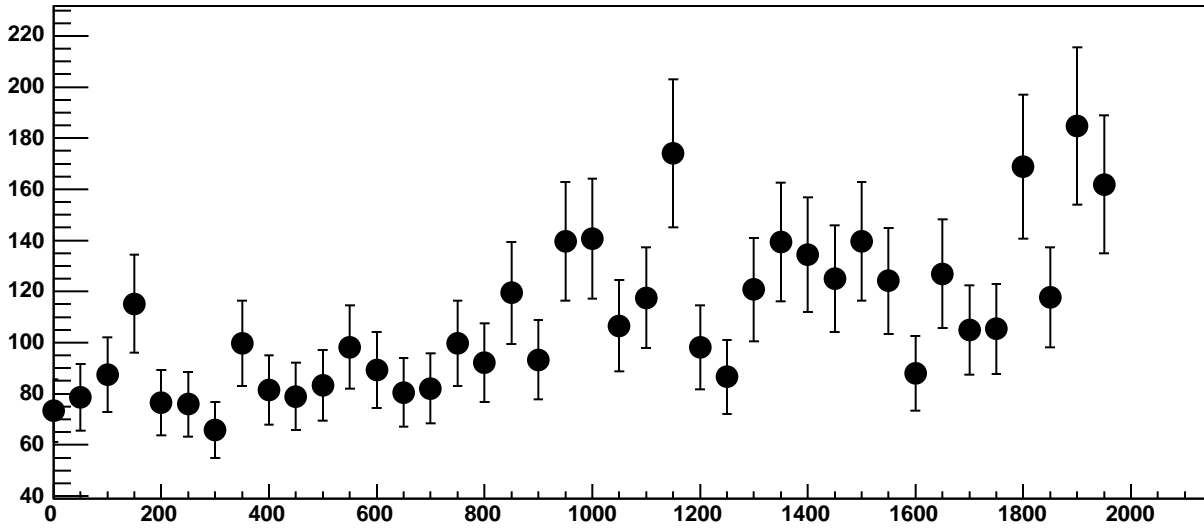


Chip 6, Channel 10, Enable 0, Hold=30, ADC Mean vs DAC

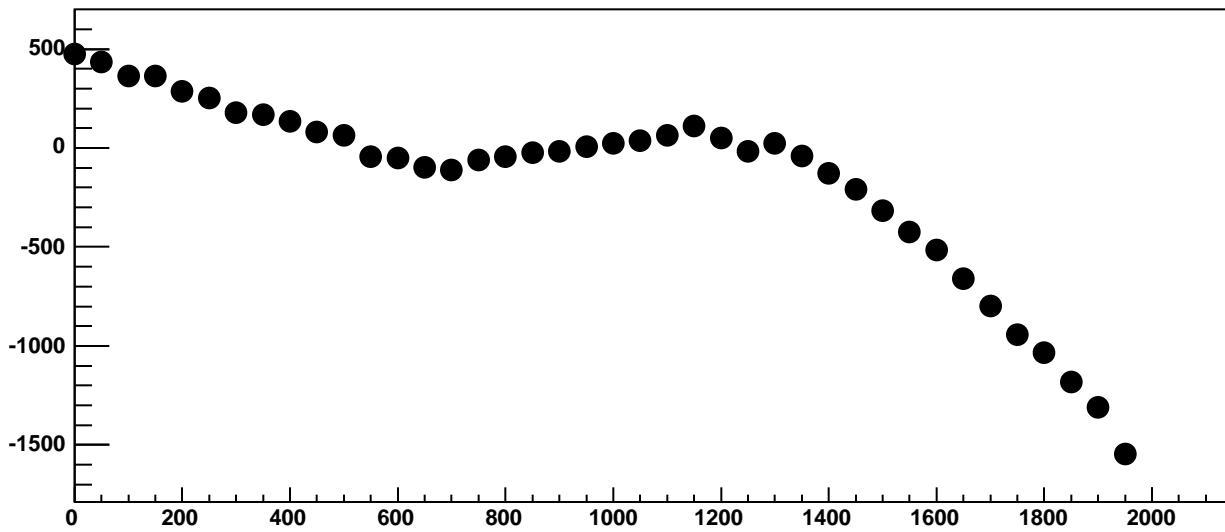


$\chi^2 / \text{ndf}$  47.56 / 11  
p0  $-1481 \pm 41.9$   
p1  $0.9053 \pm 0.03813$

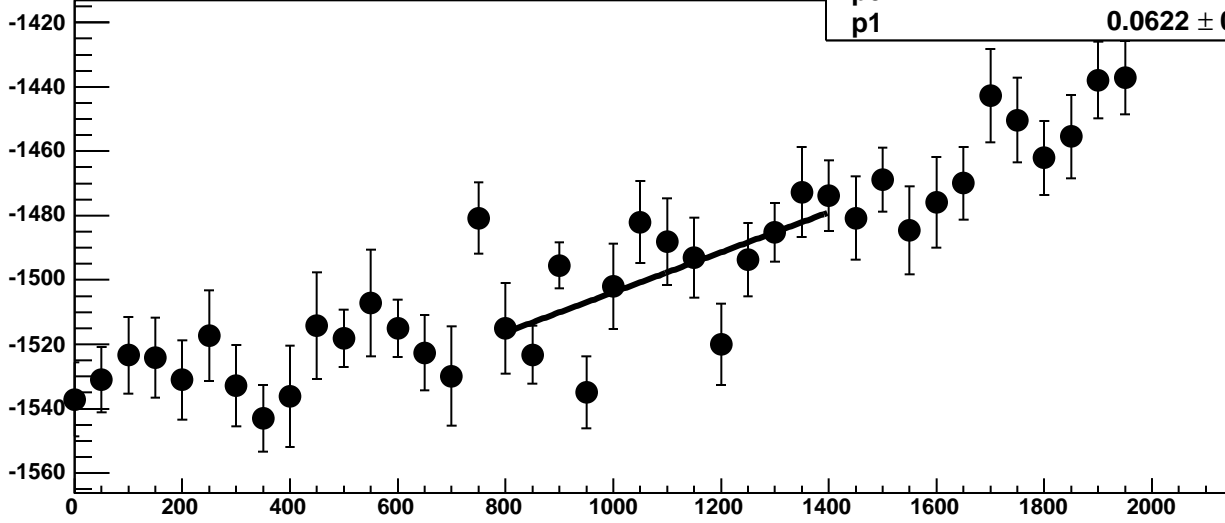
Chip 6, Channel 10, Enable 0, Hold=30, ADC Noise vs DAC



Chip 6, Channel 10, Enable 0, Hold=30, ADC Residuals vs DAC

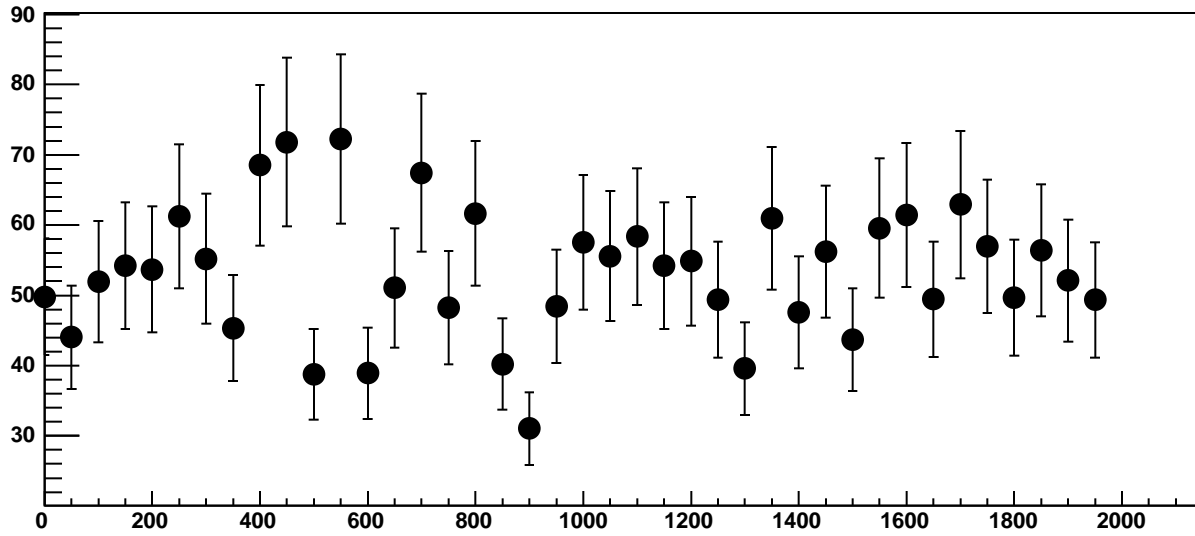


Chip 6, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC

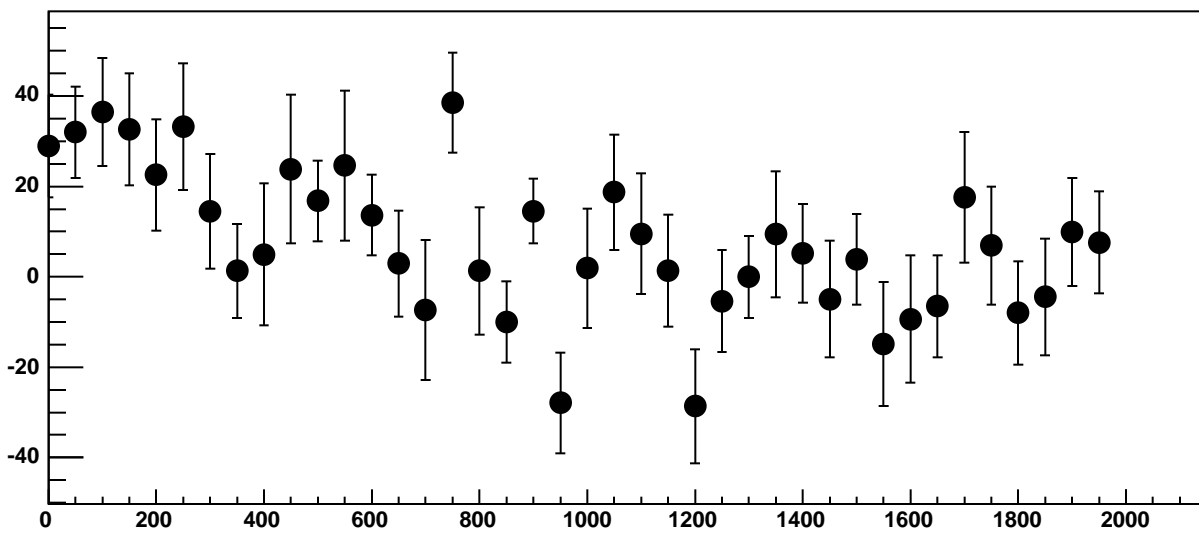


$\chi^2 / \text{ndf}$  20.54 / 11  
p0  $-1566 \pm 17.26$   
p1  $0.0622 \pm 0.01575$

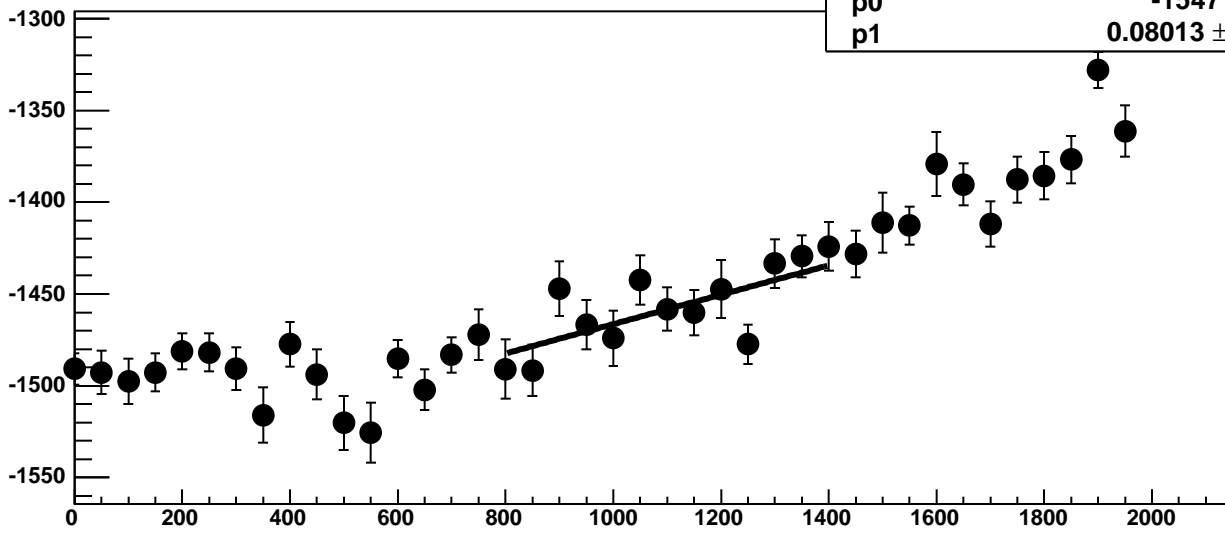
Chip 6, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC



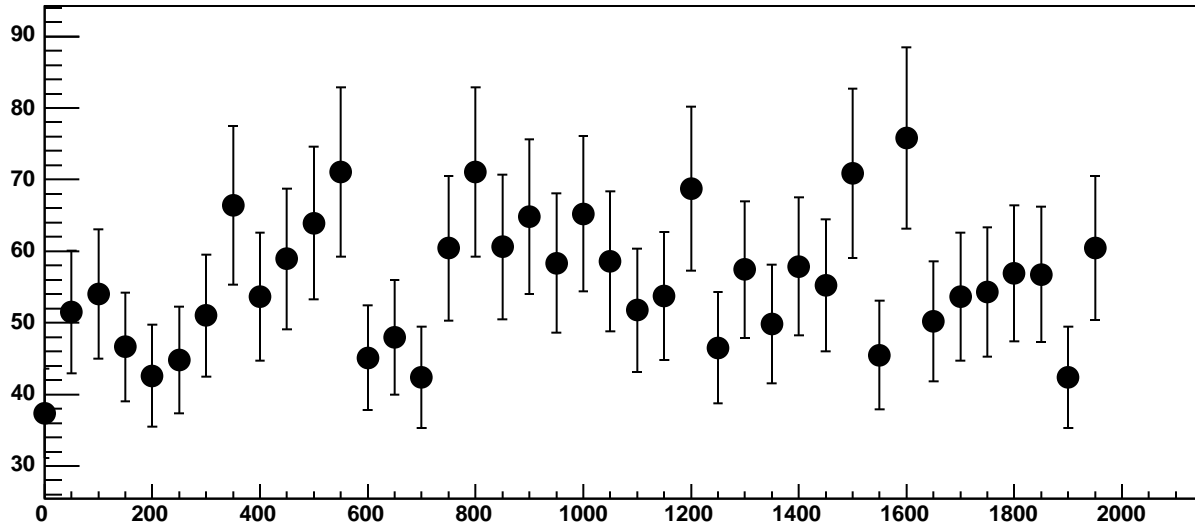
Chip 6, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC



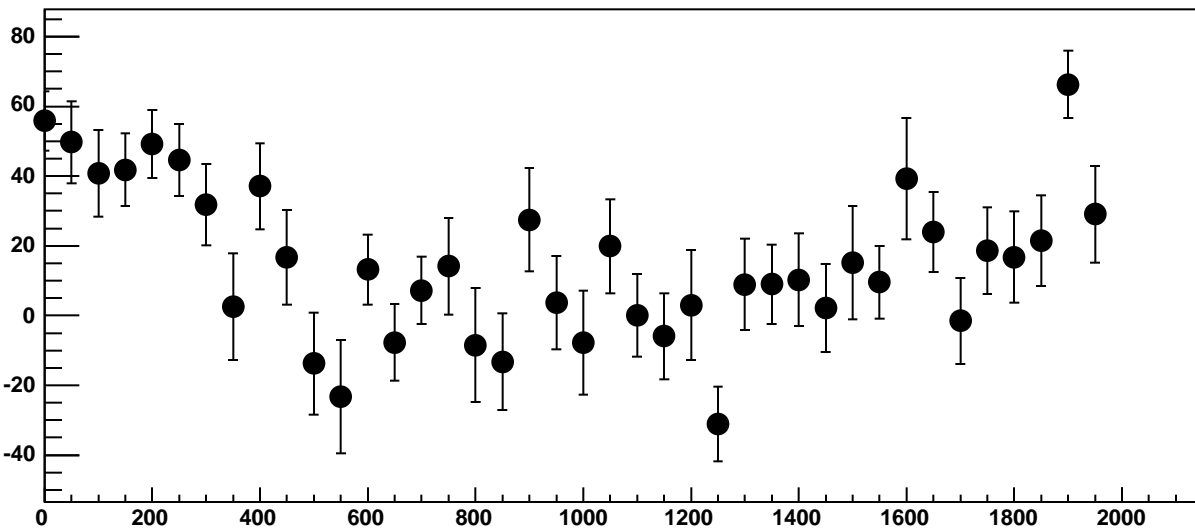
Chip 6, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC



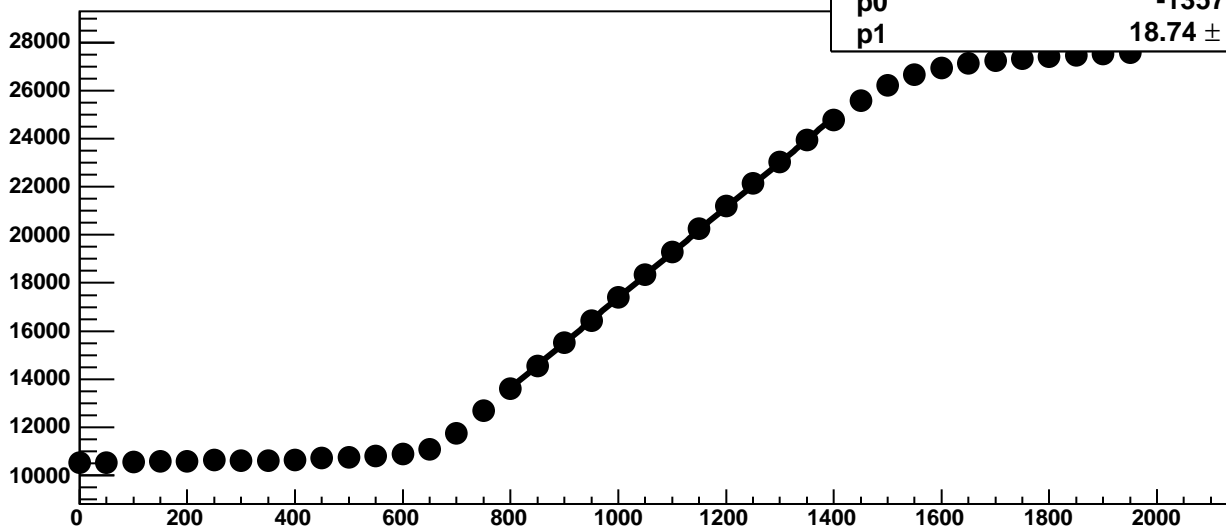
Chip 6, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC



Chip 6, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC

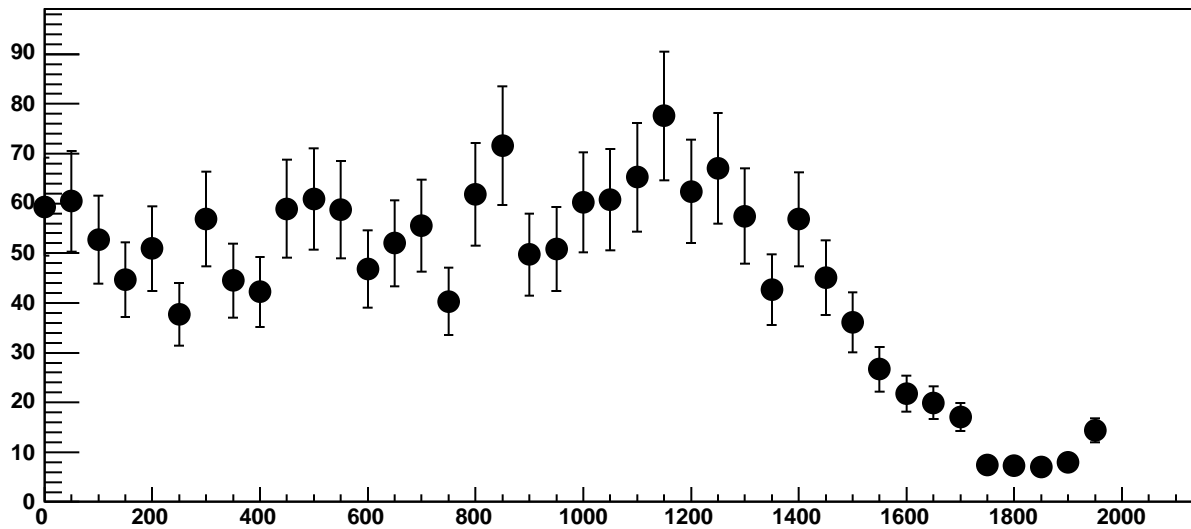


Chip 6, Channel 10, Enable 3!, Hold=30, ADC Mean vs DAC

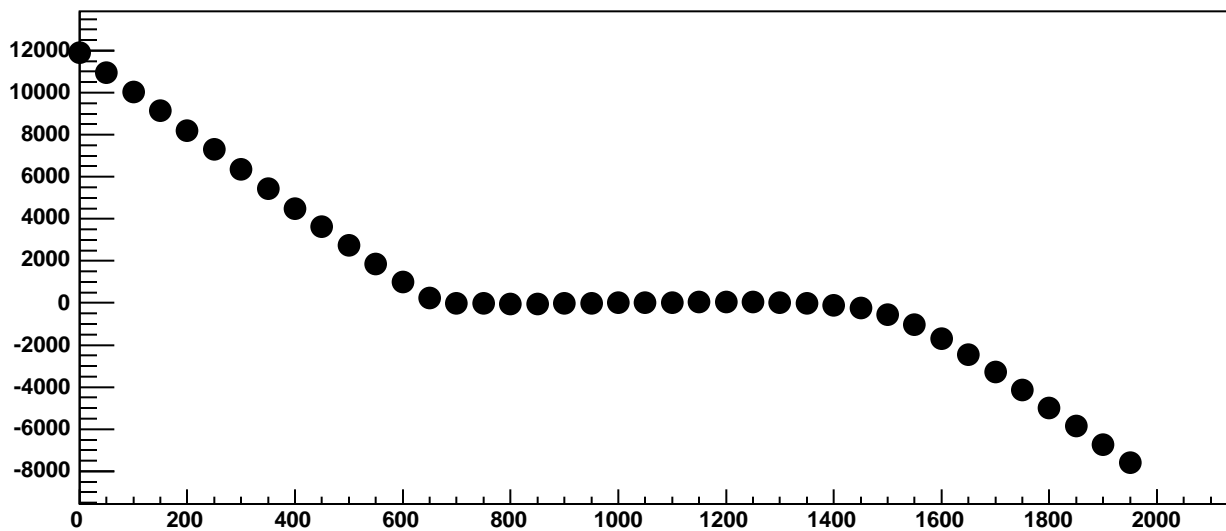


$\chi^2 / \text{ndf}$  113.7 / 11  
p0  $-1357 \pm 21.48$   
p1  $18.74 \pm 0.01901$

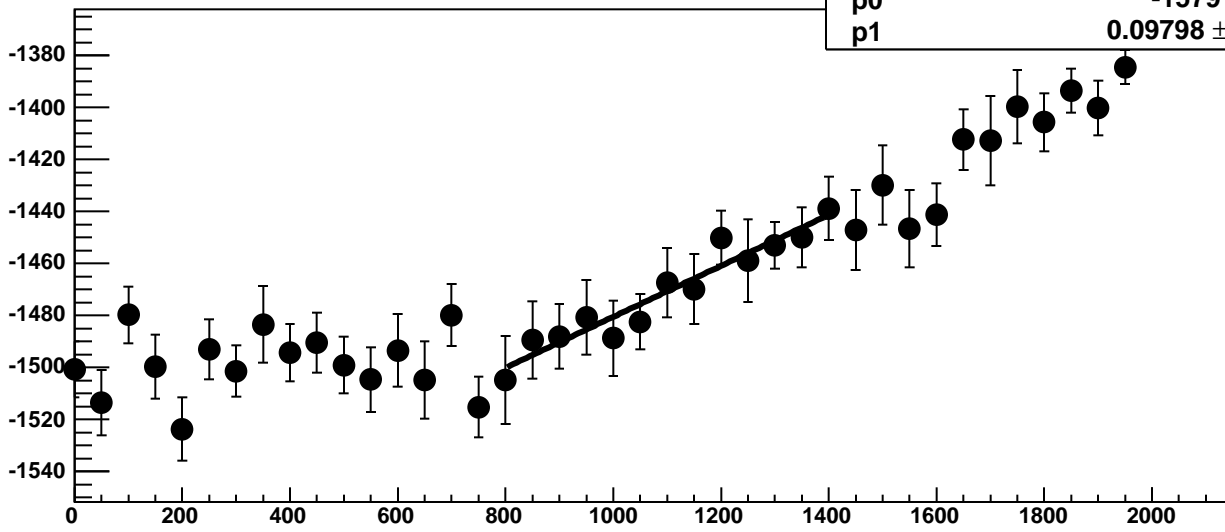
Chip 6, Channel 10, Enable 3!, Hold=30, ADC Noise vs DAC



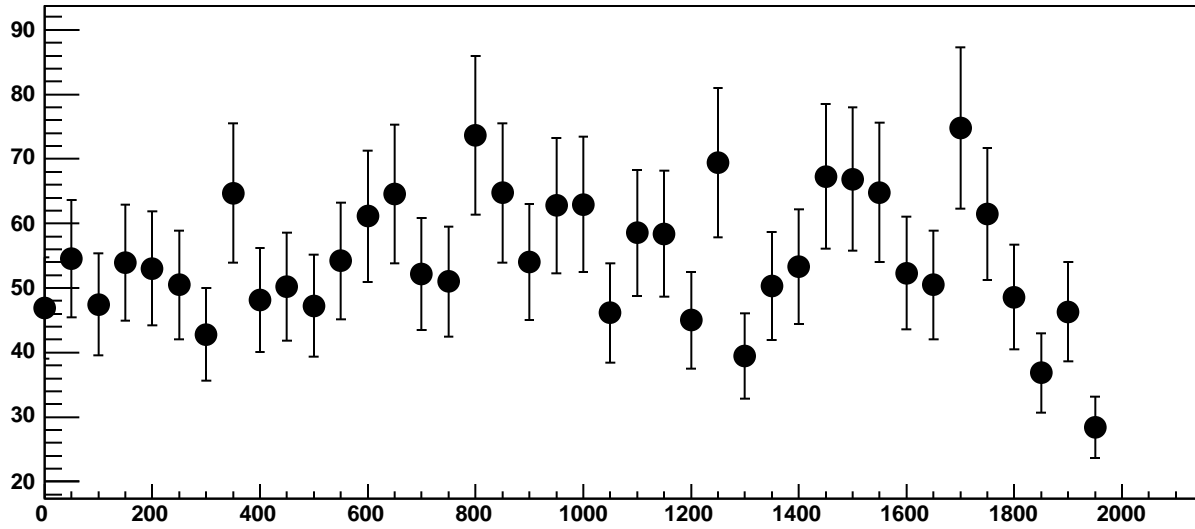
Chip 6, Channel 10, Enable 3!, Hold=30, ADC Residuals vs DAC



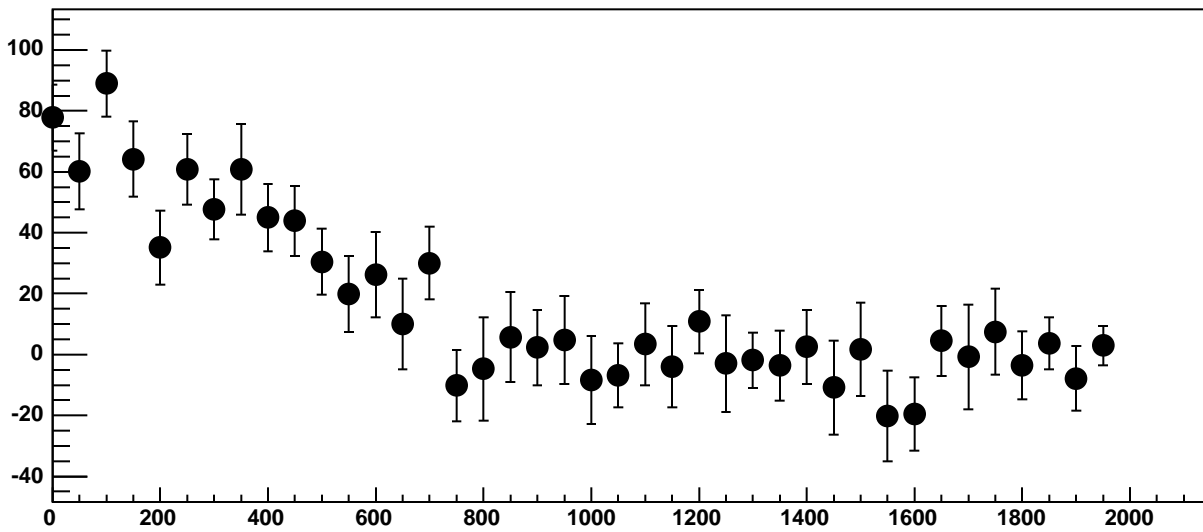
Chip 6, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC



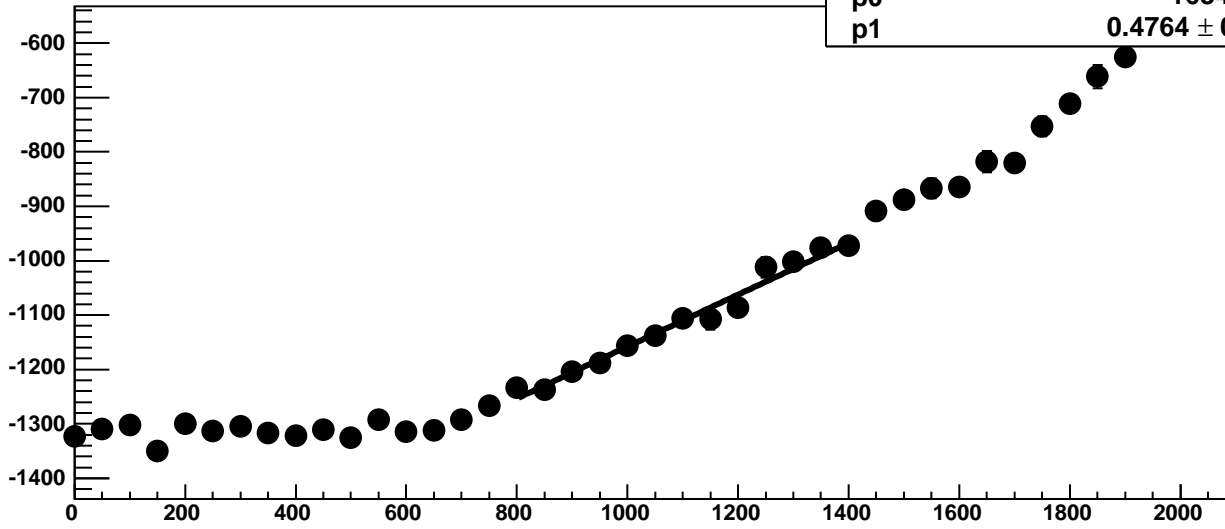
Chip 6, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC



Chip 6, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC

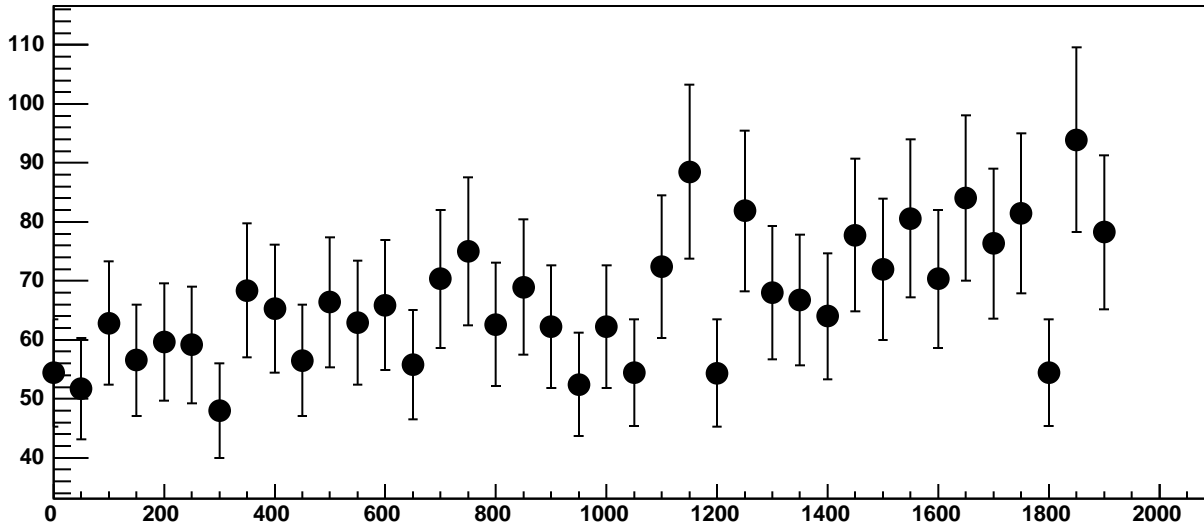


Chip 6, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC

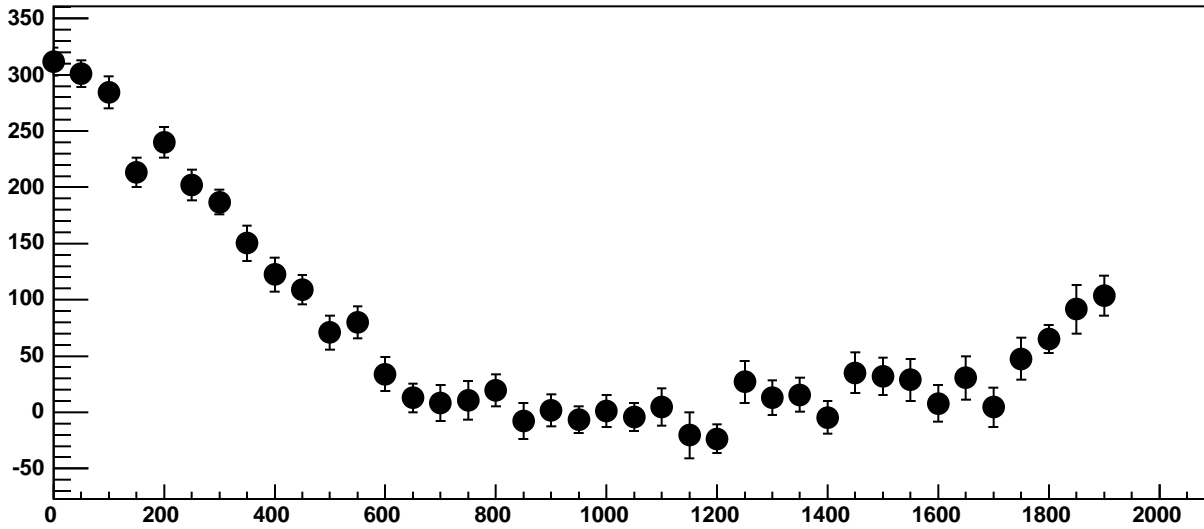


$\chi^2 / \text{ndf}$  11.01 / 11  
p0  $-1634 \pm 24.2$   
p1  $0.4764 \pm 0.02195$

Chip 6, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC

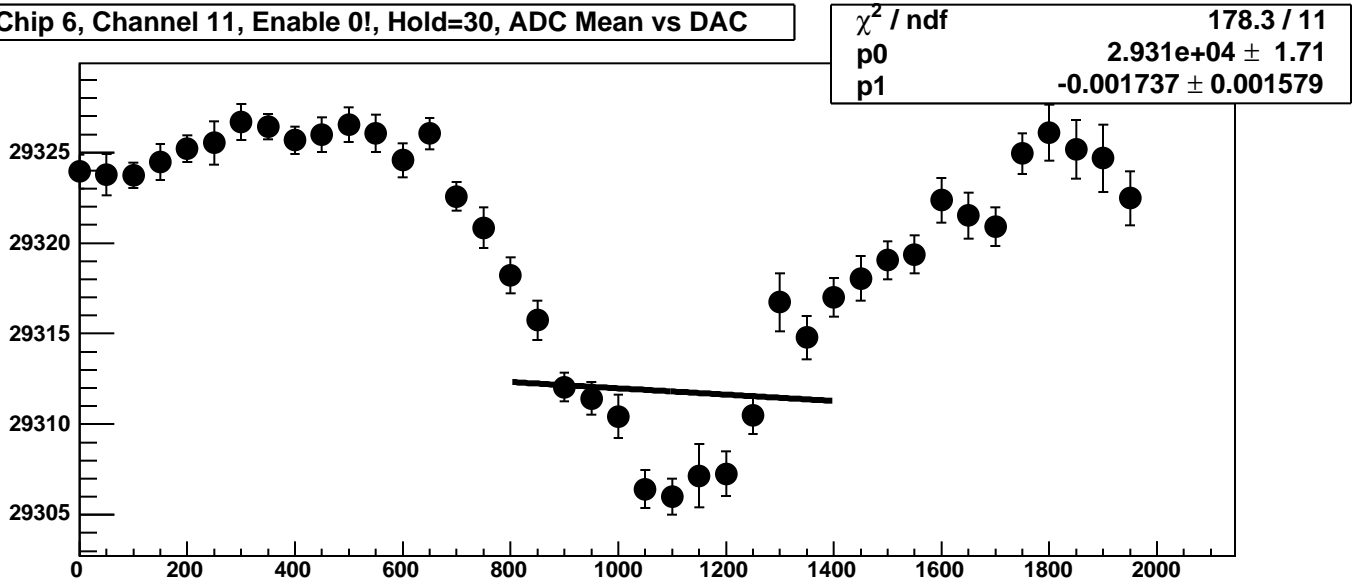


Chip 6, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC

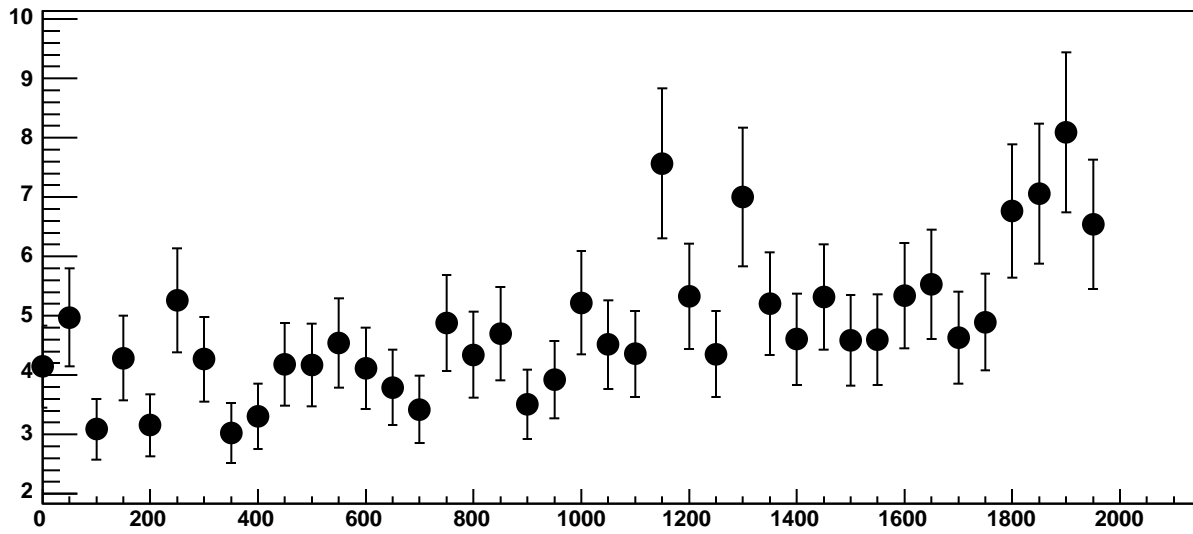




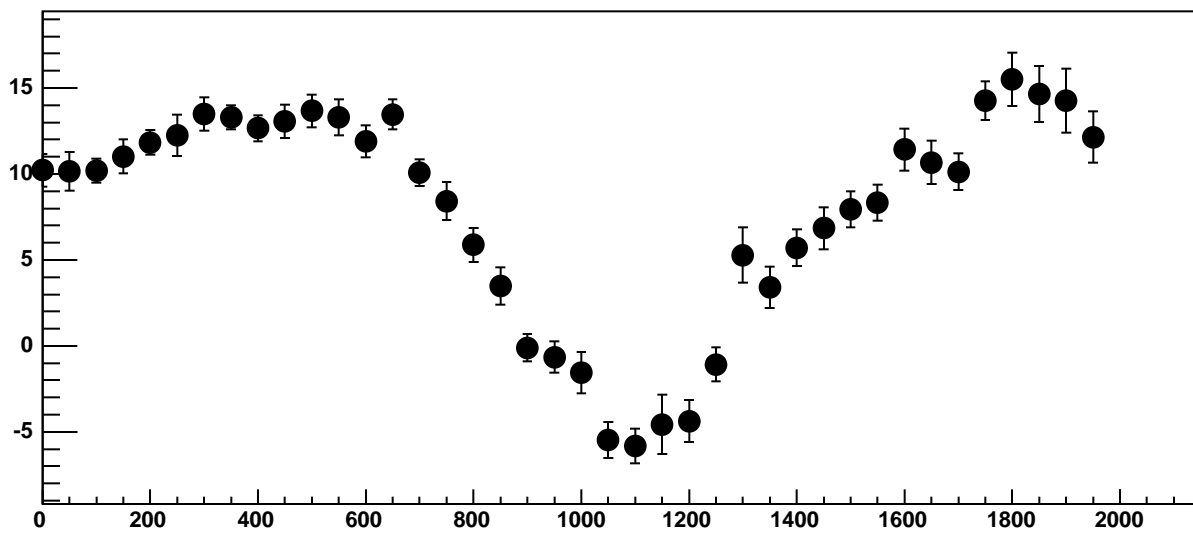
Chip 6, Channel 11, Enable 0!, Hold=30, ADC Mean vs DAC



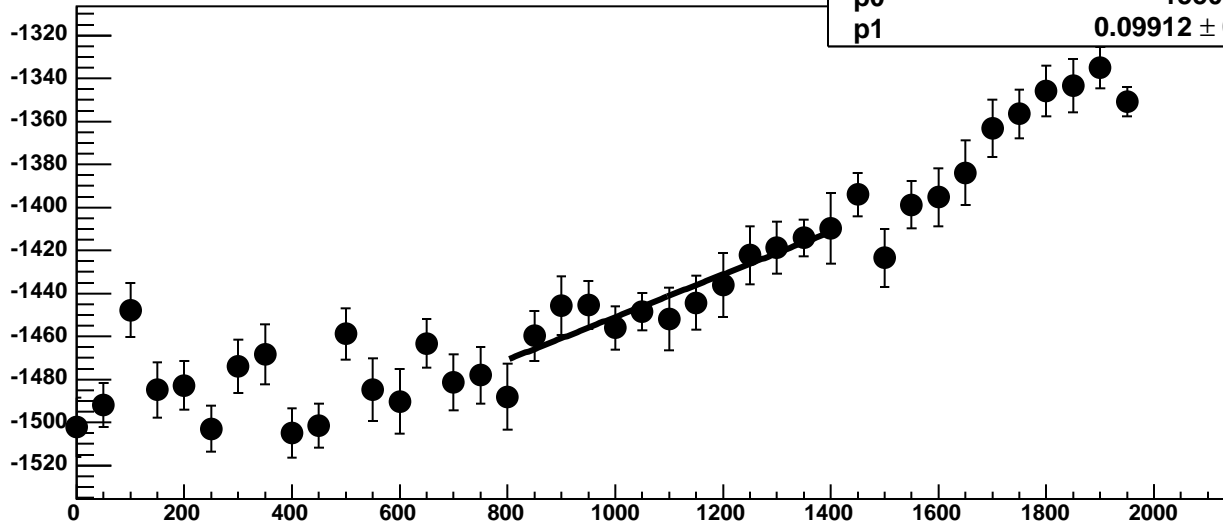
Chip 6, Channel 11, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 6, Channel 11, Enable 0!, Hold=30, ADC Residuals vs DAC

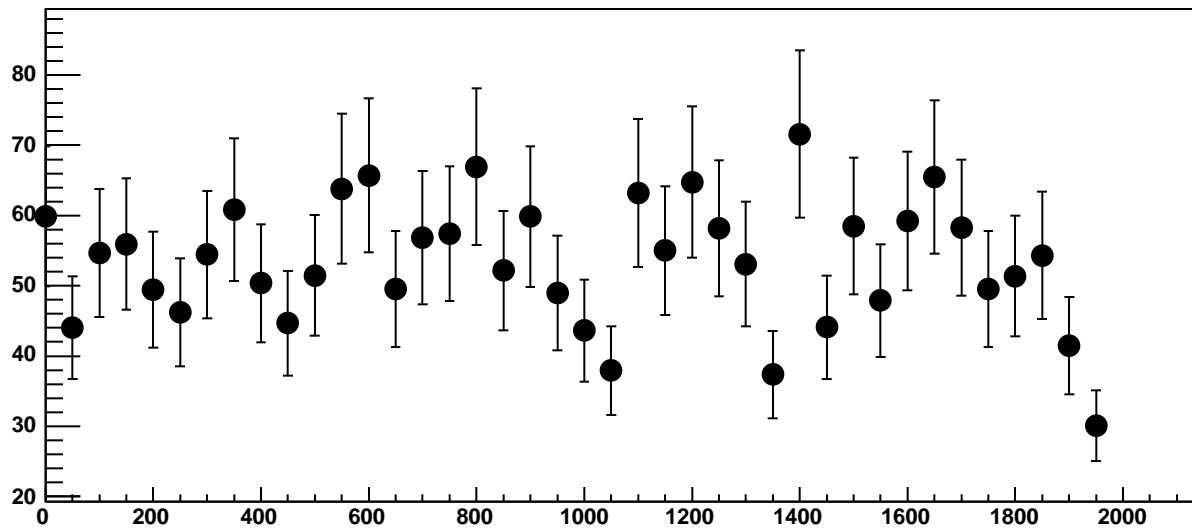


Chip 6, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC

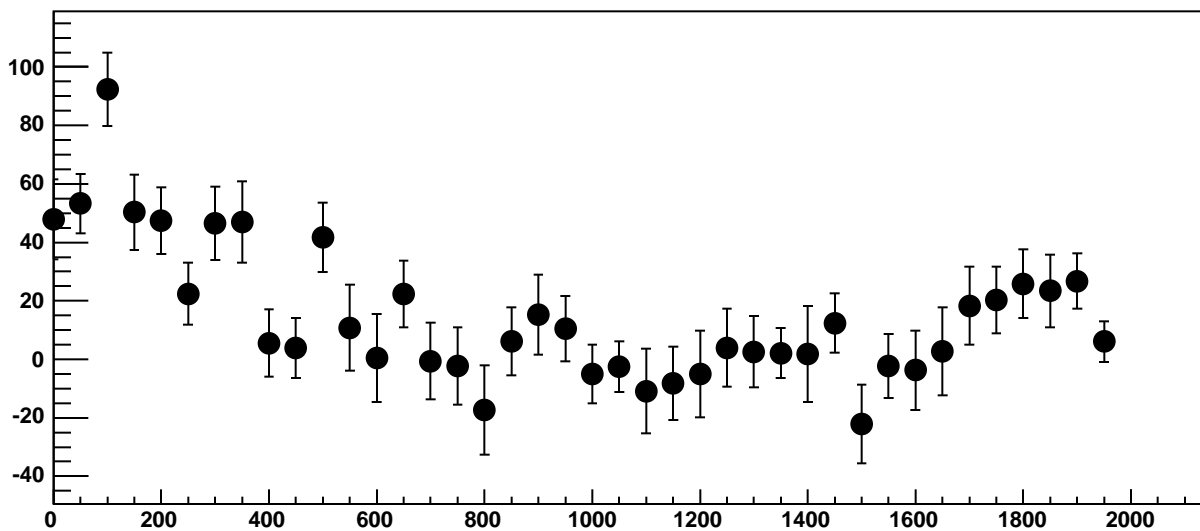


$\chi^2 / \text{ndf}$  5.299 / 11  
p0  $-1550 \pm 20.42$   
p1  $0.09912 \pm 0.01825$

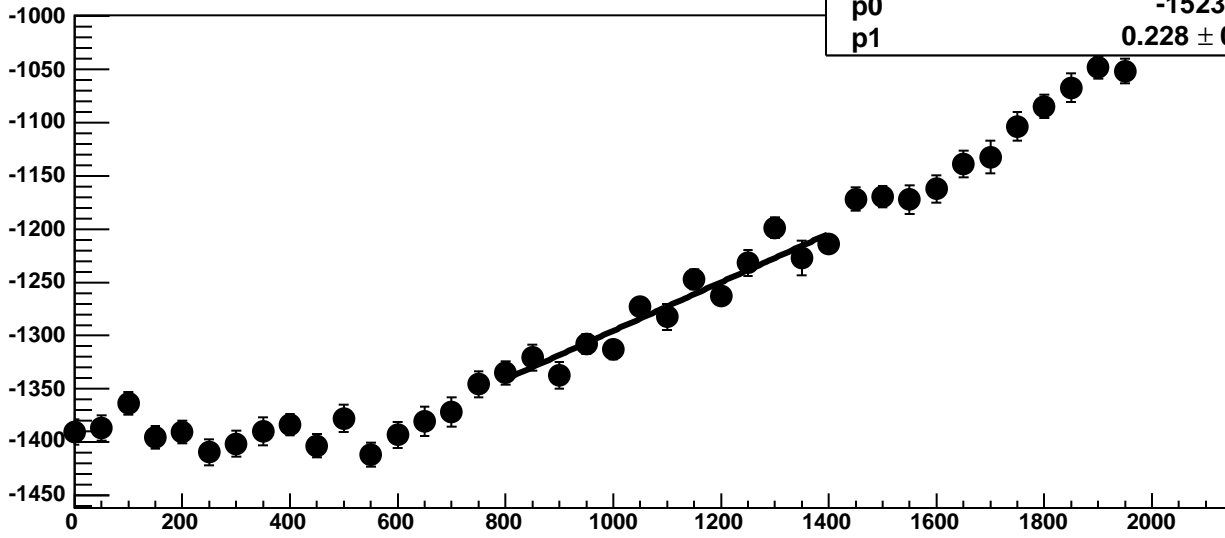
Chip 6, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC



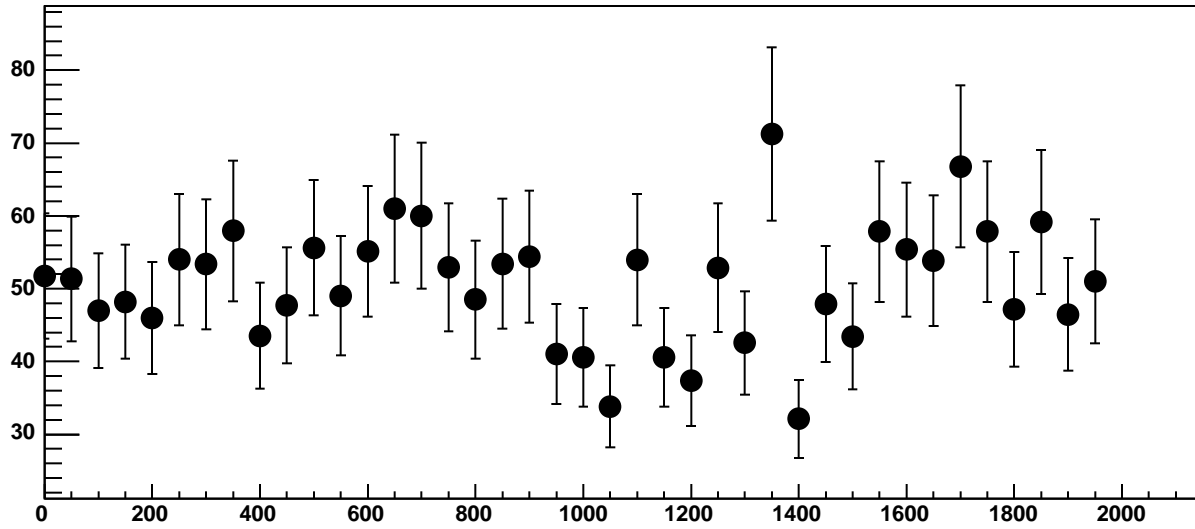
Chip 6, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC



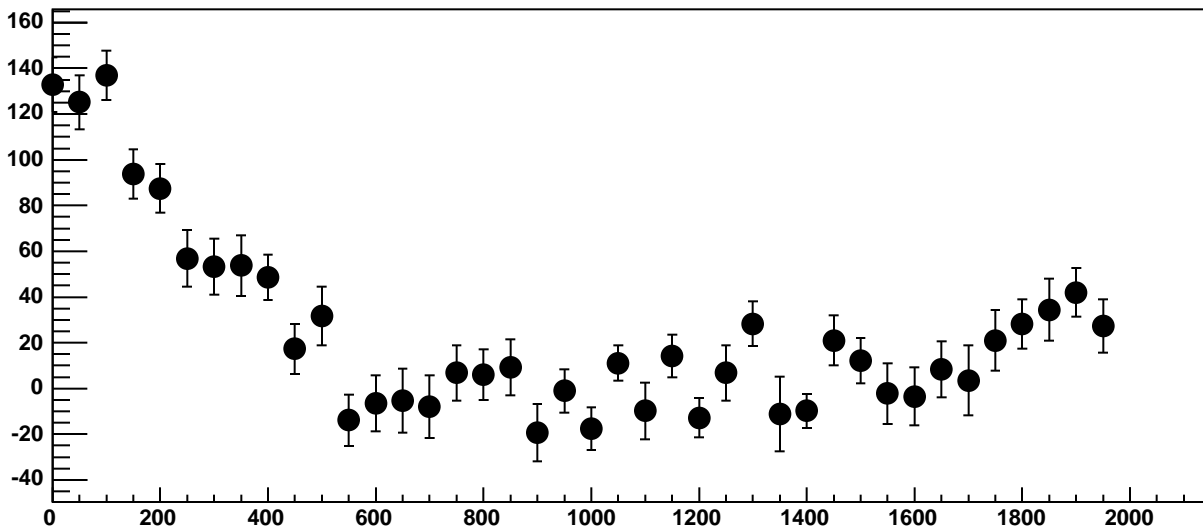
Chip 6, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC



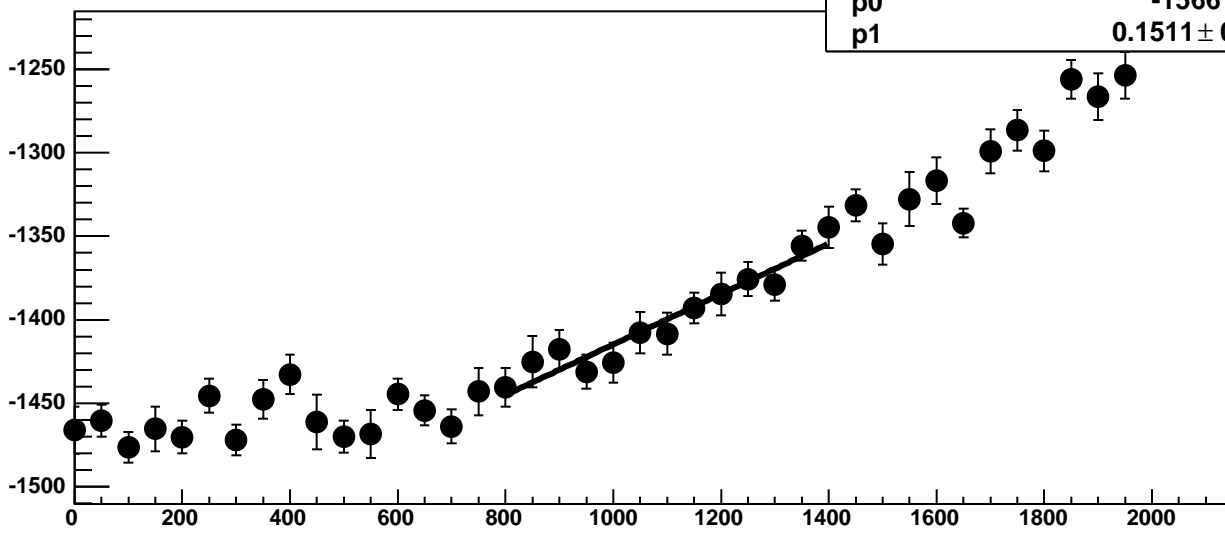
Chip 6, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC



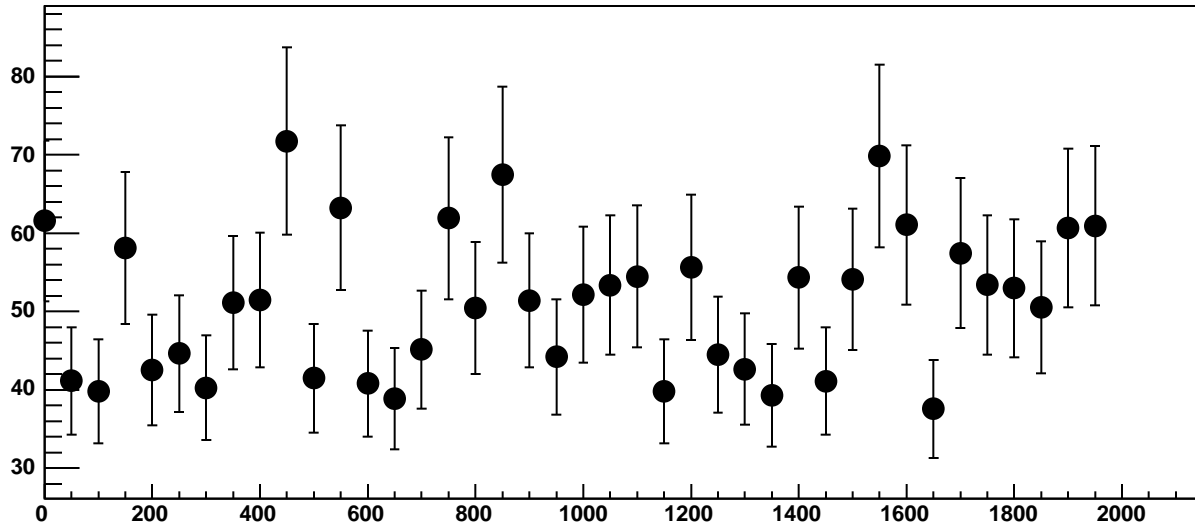
Chip 6, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC



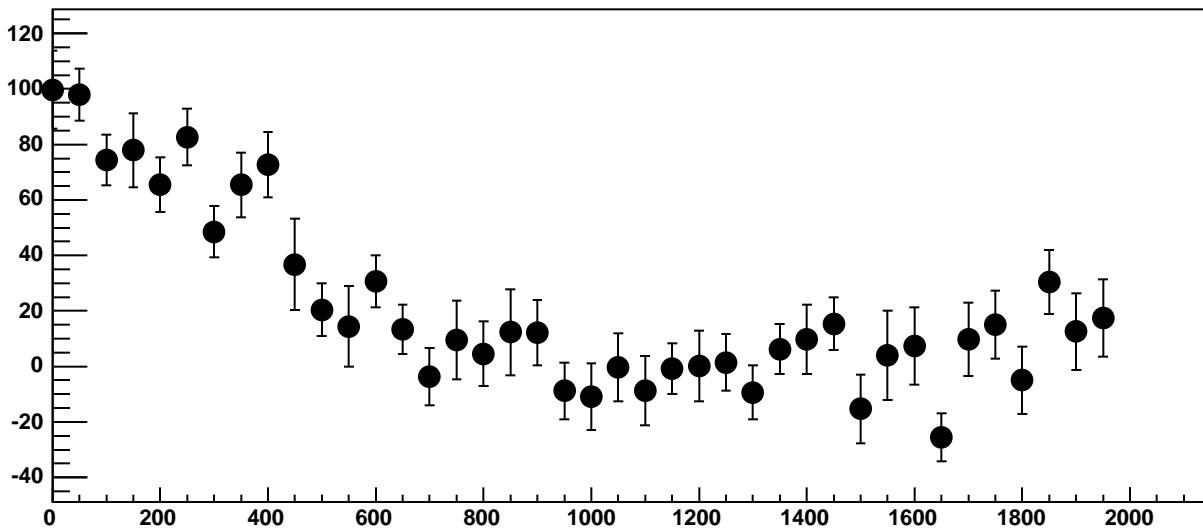
Chip 6, Channel 11, Enable 3, Hold=30, ADC Mean vs DAC



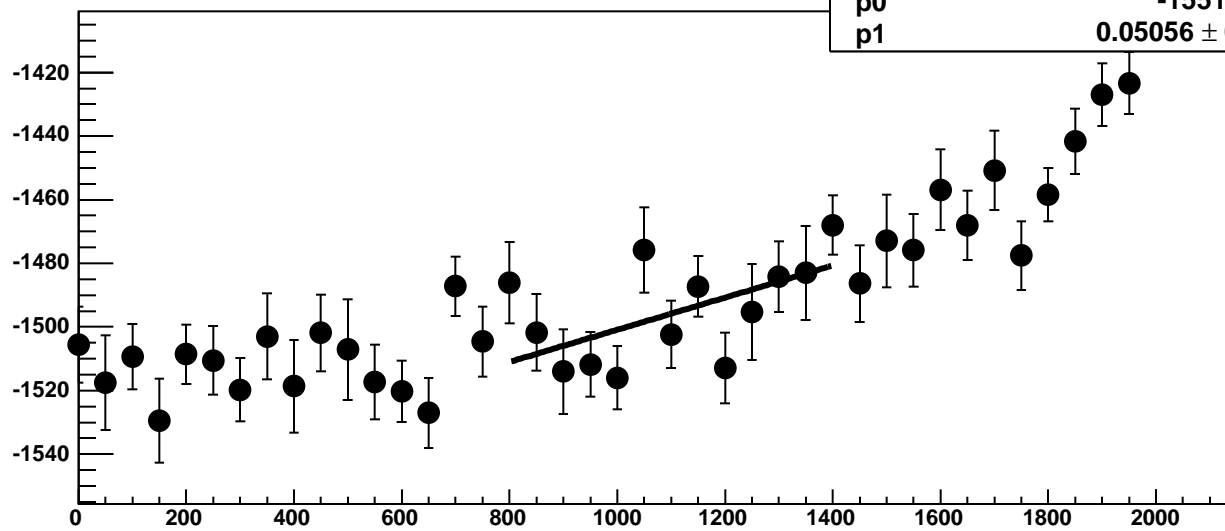
Chip 6, Channel 11, Enable 3, Hold=30, ADC Noise vs DAC



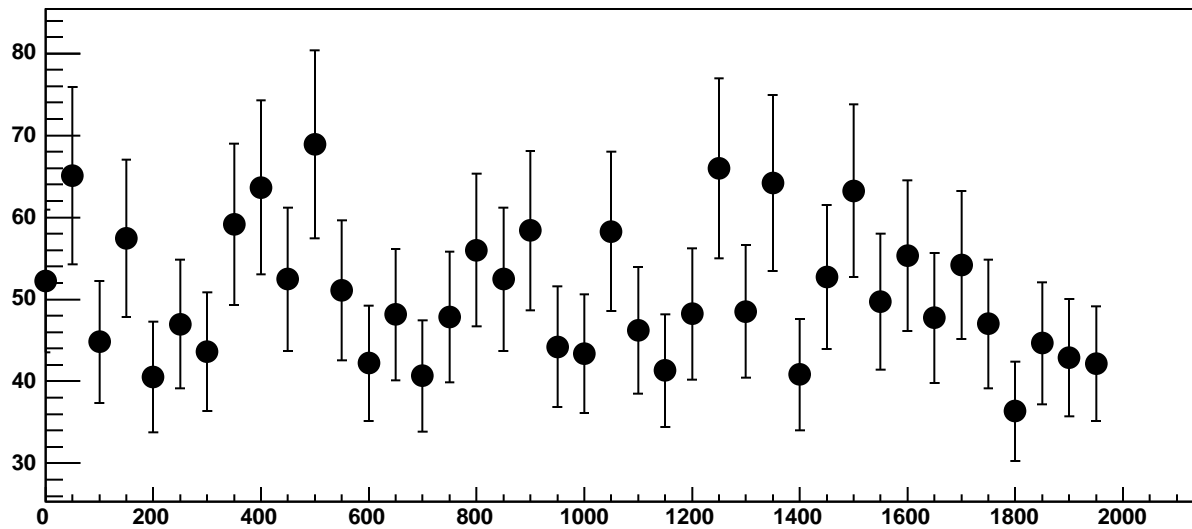
Chip 6, Channel 11, Enable 3, Hold=30, ADC Residuals vs DAC



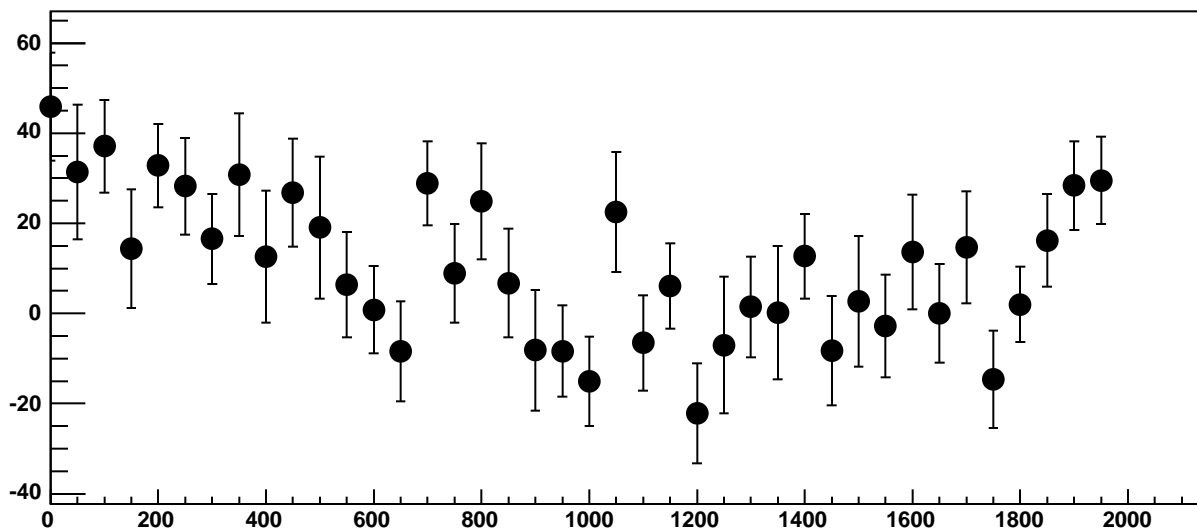
Chip 6, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC



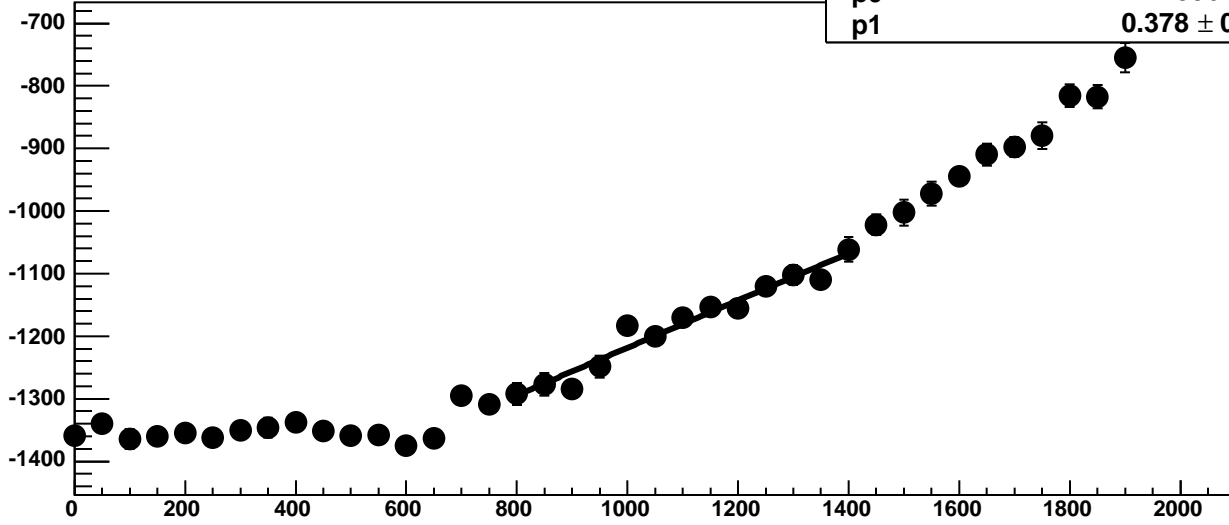
Chip 6, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC



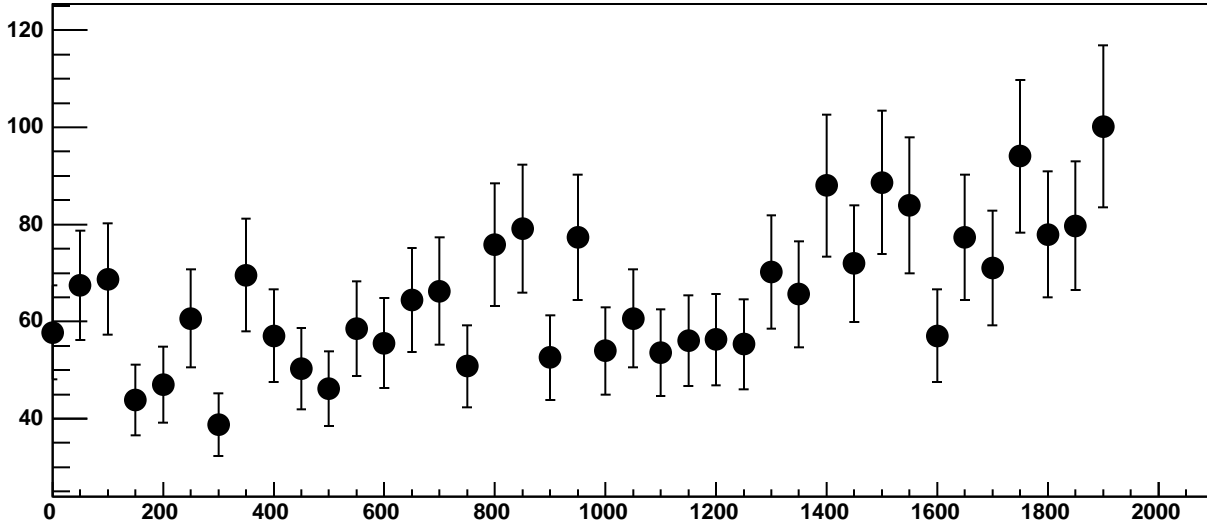
Chip 6, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC



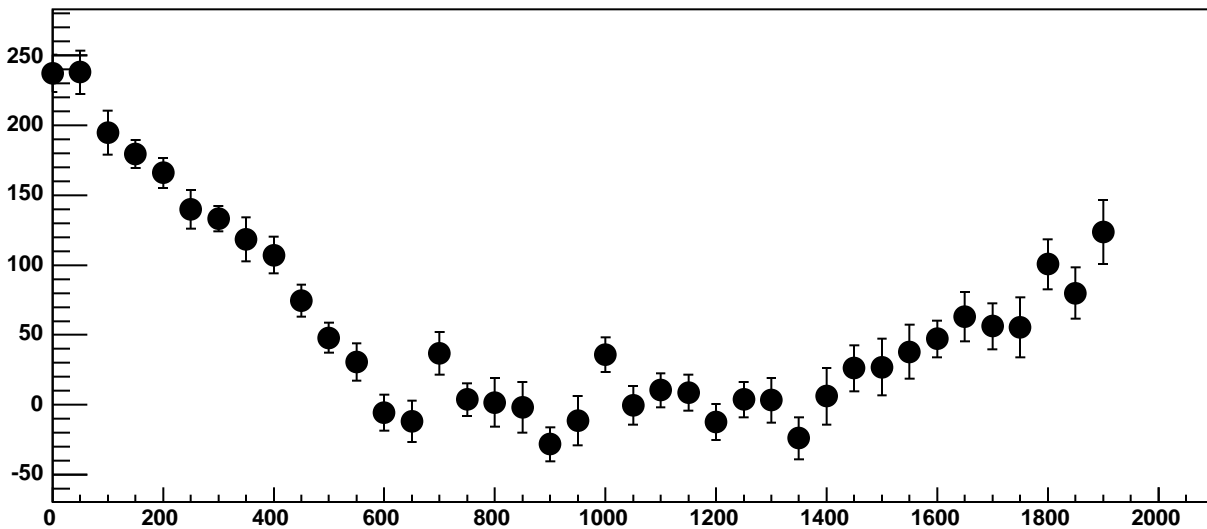
Chip 6, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC



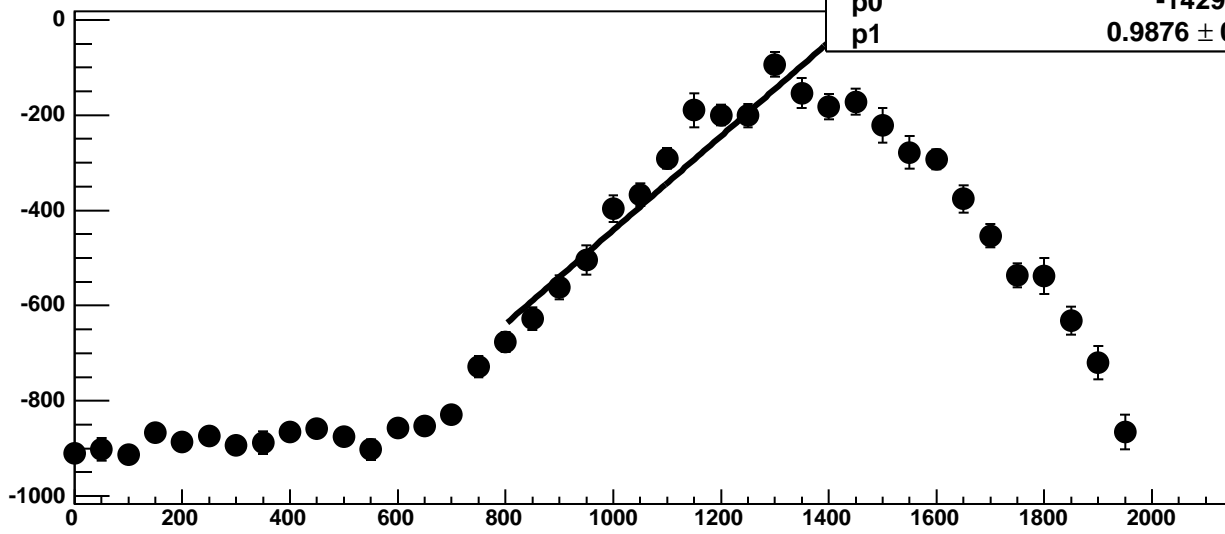
Chip 6, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC



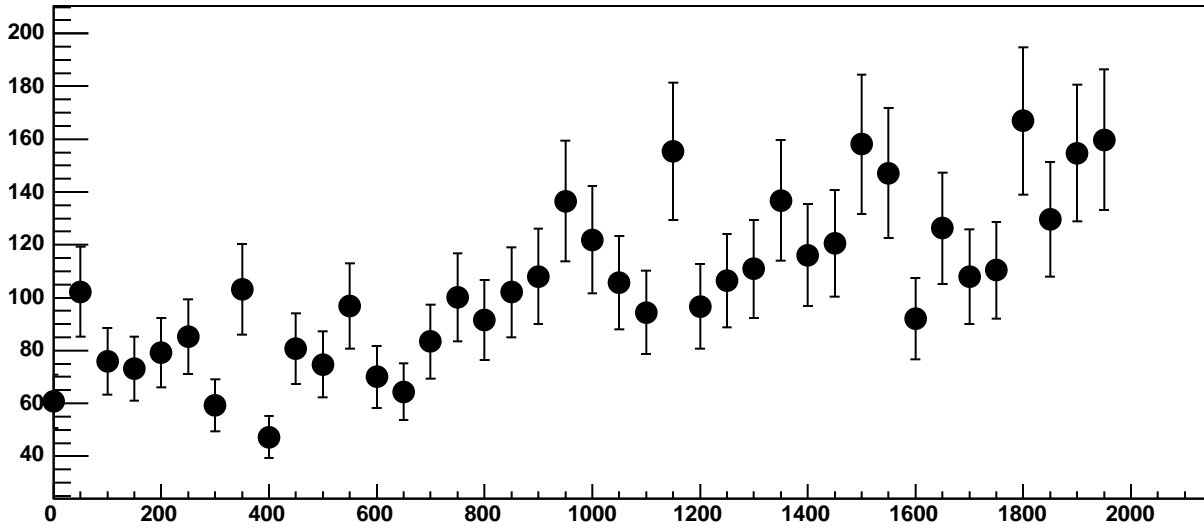
Chip 6, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC



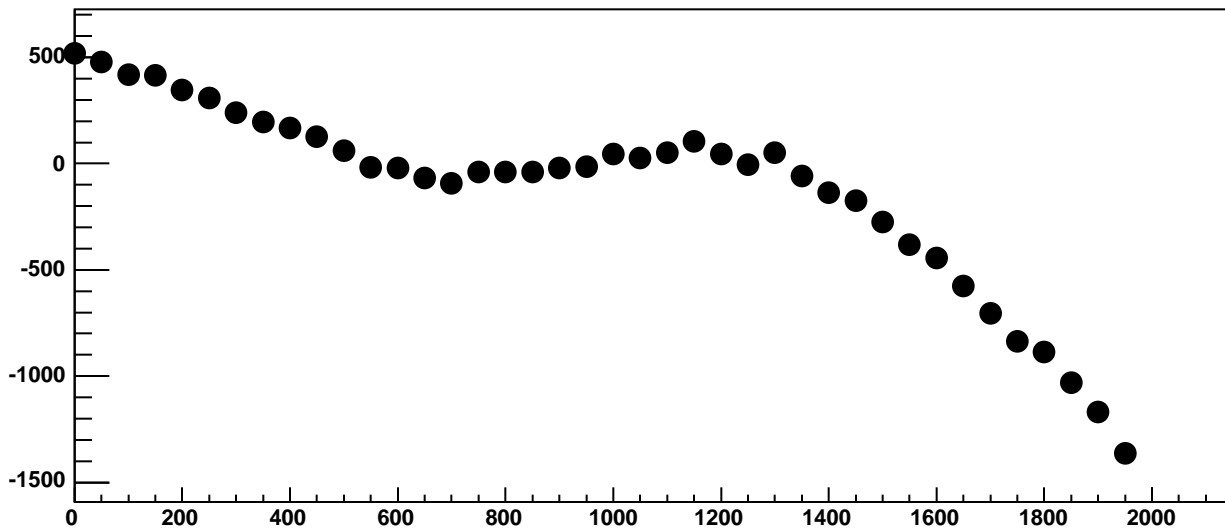
Chip 6, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC



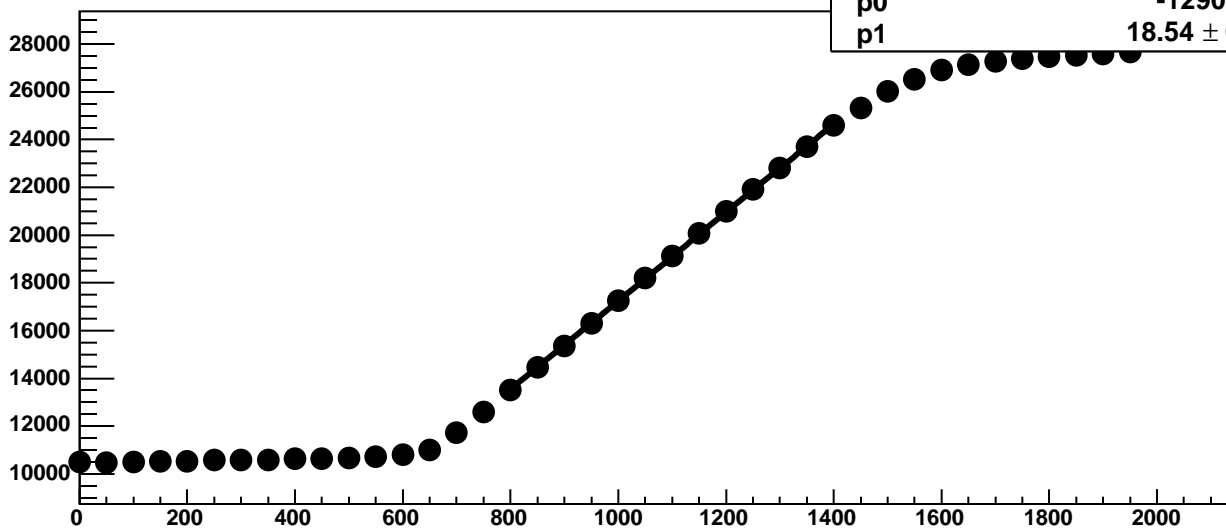
Chip 6, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC



Chip 6, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC

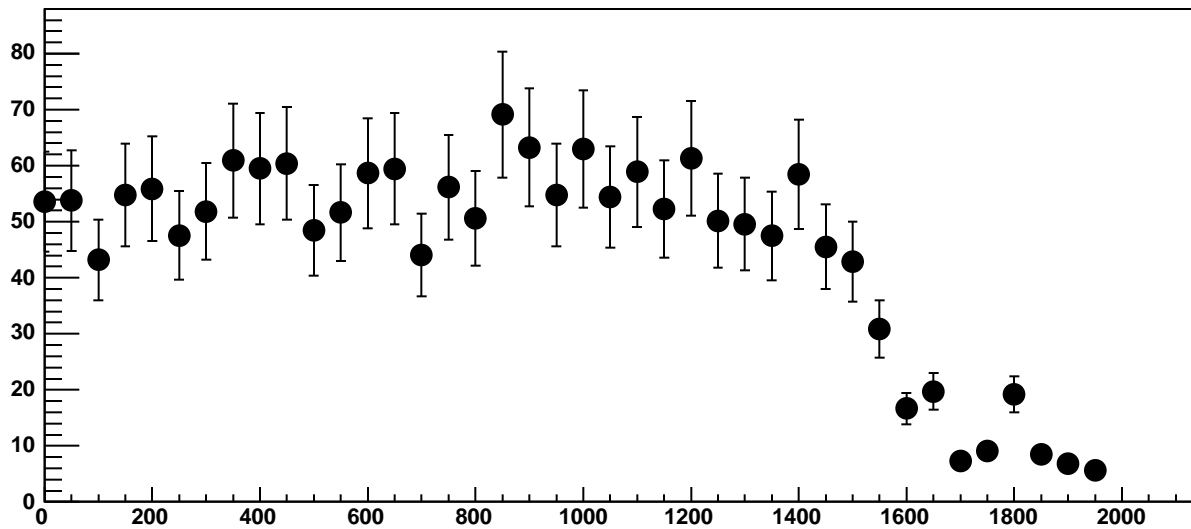


Chip 6, Channel 12, Enable 1!, Hold=30, ADC Mean vs DAC

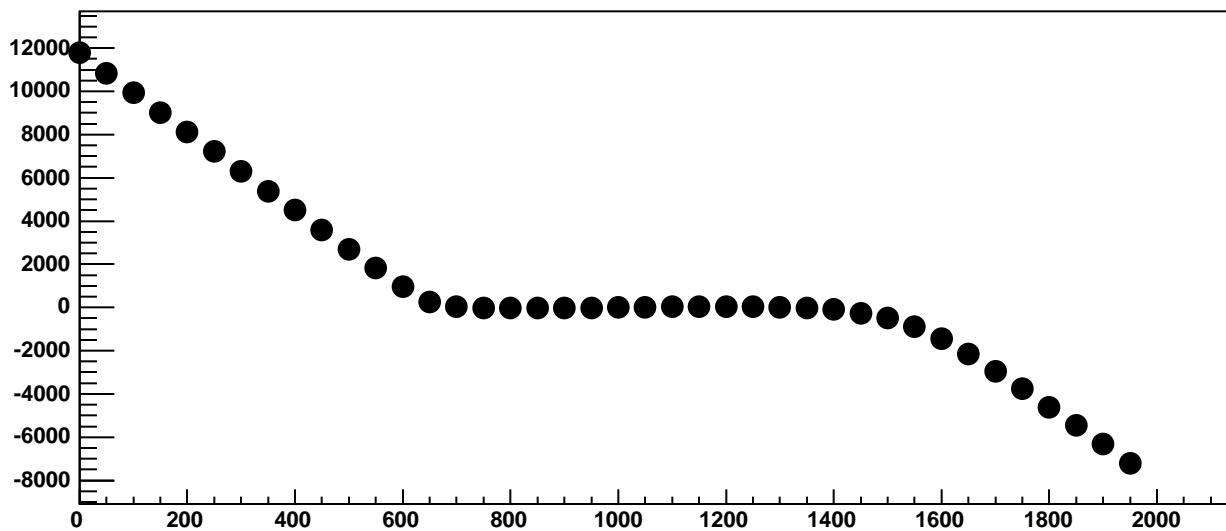


$\chi^2 / \text{ndf}$  90.72 / 11  
p0  $-1290 \pm 21.12$   
p1  $18.54 \pm 0.01865$

Chip 6, Channel 12, Enable 1!, Hold=30, ADC Noise vs DAC

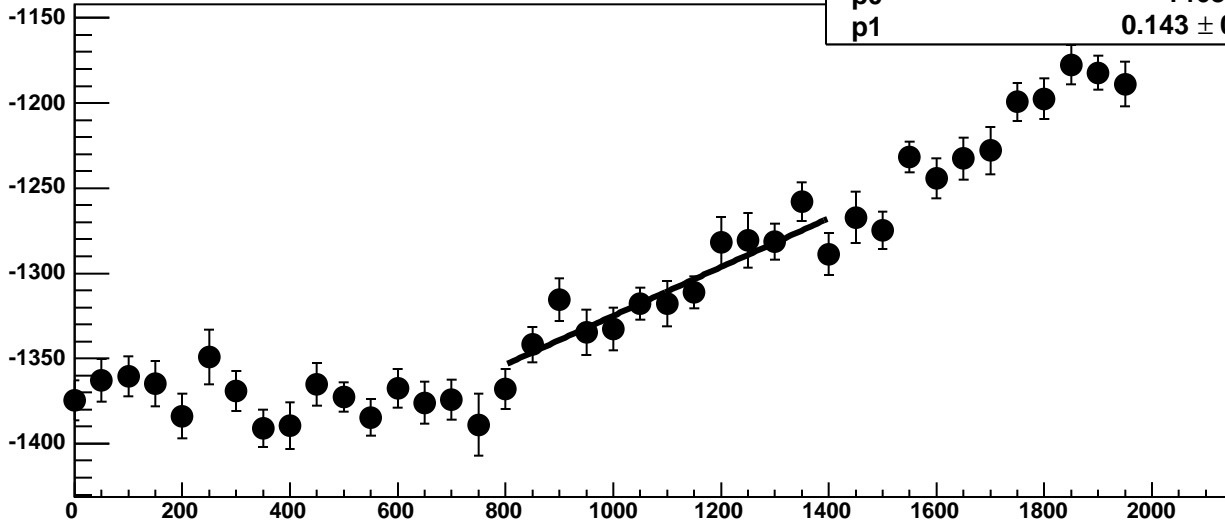


Chip 6, Channel 12, Enable 1!, Hold=30, ADC Residuals vs DAC

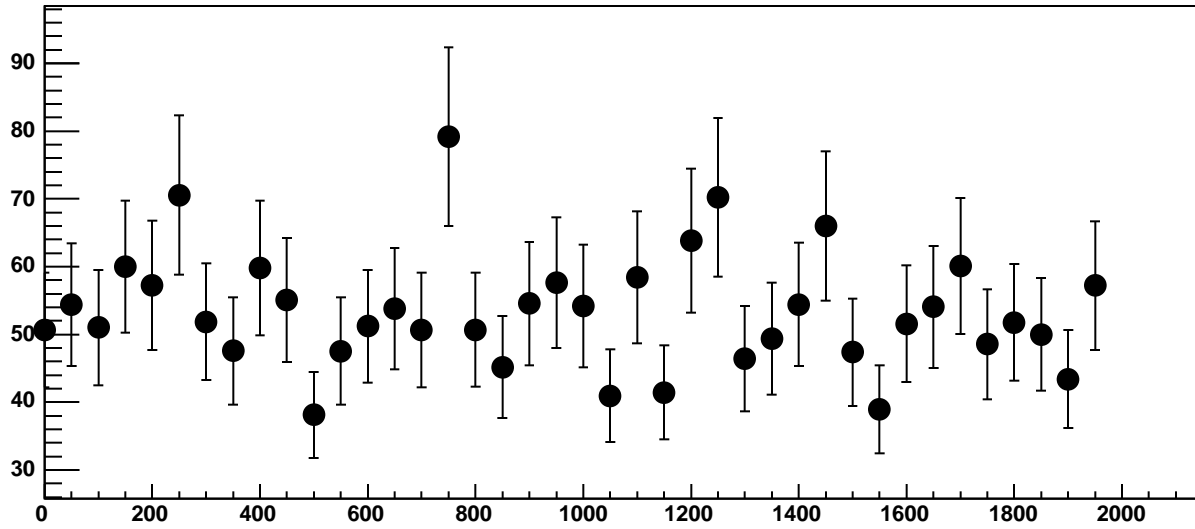




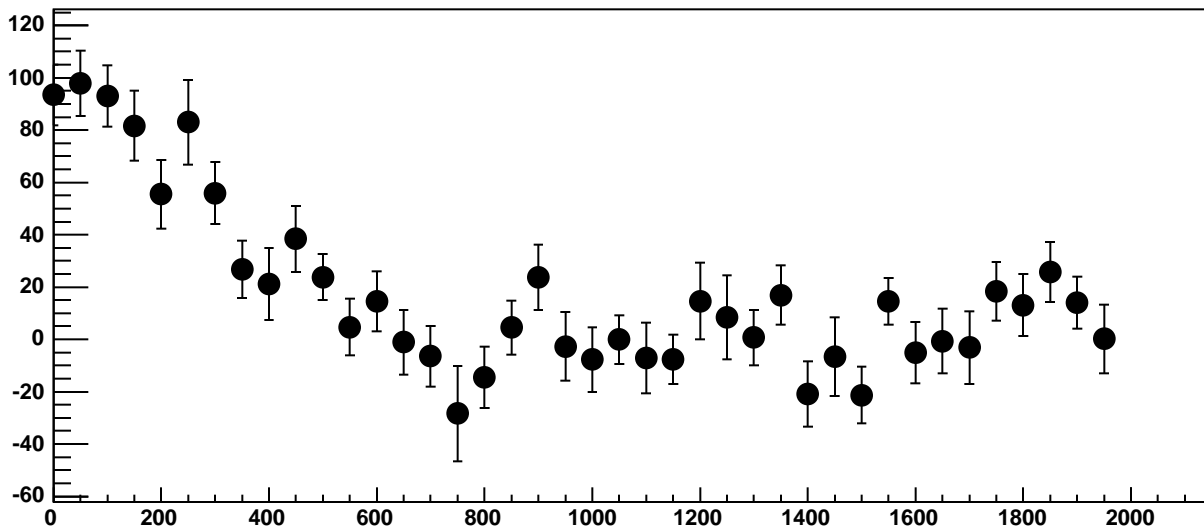
Chip 6, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC



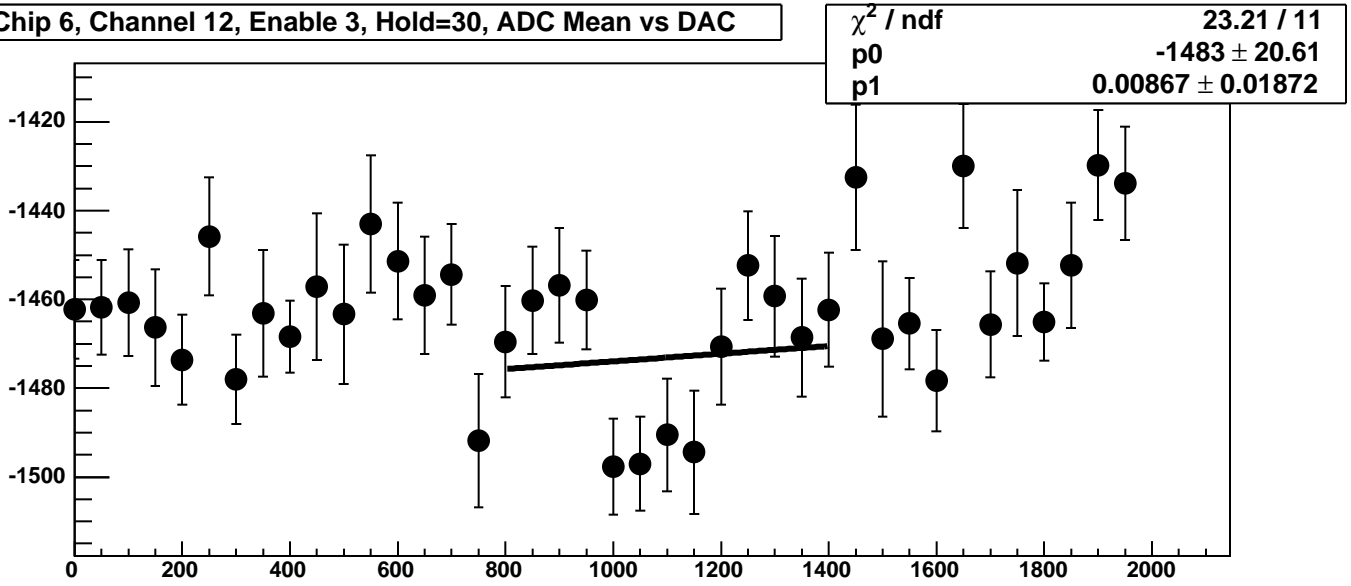
Chip 6, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC



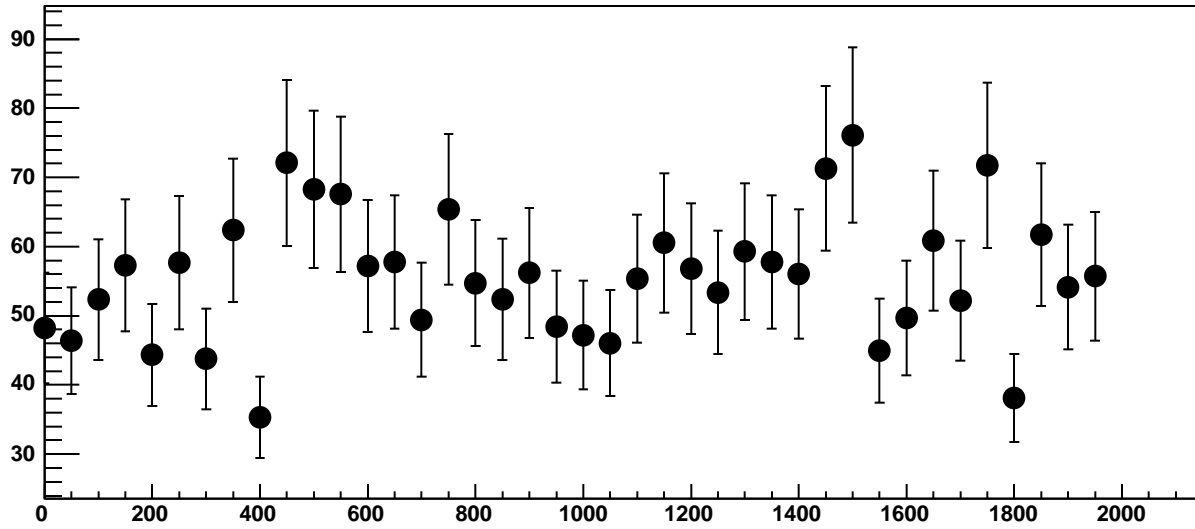
Chip 6, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC



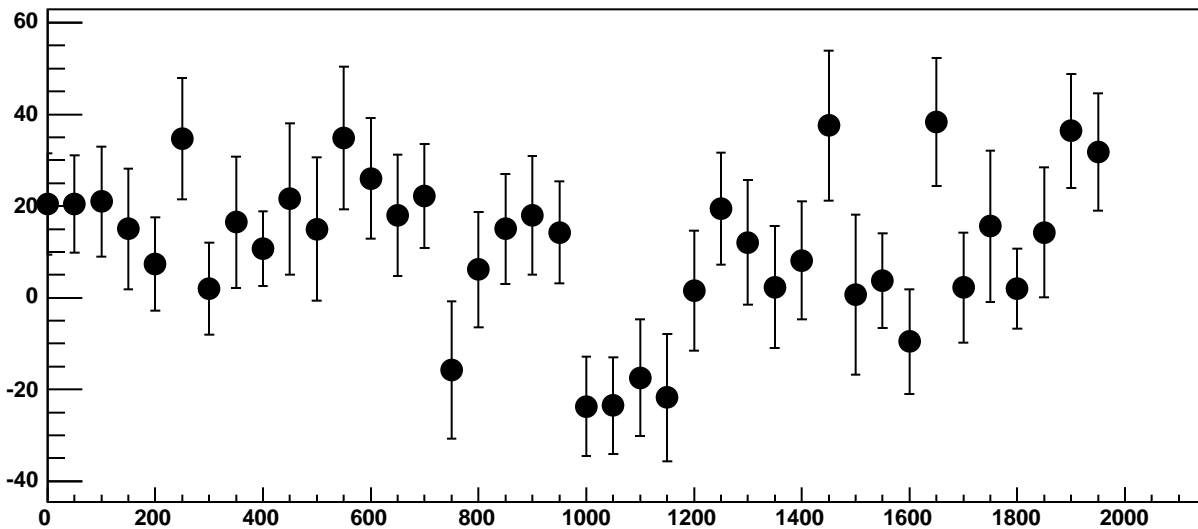
Chip 6, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC



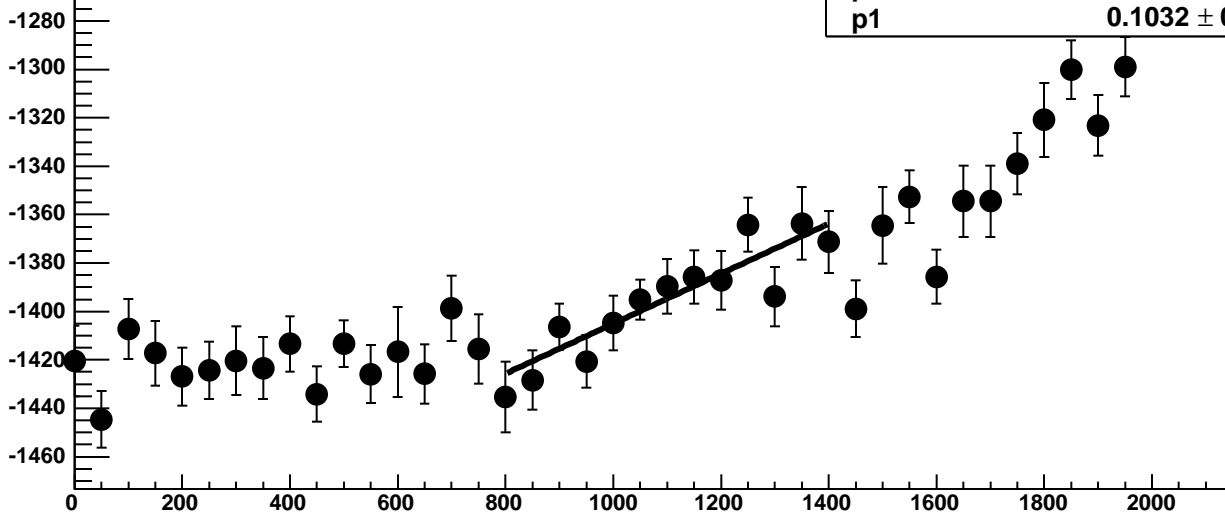
Chip 6, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC



Chip 6, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC

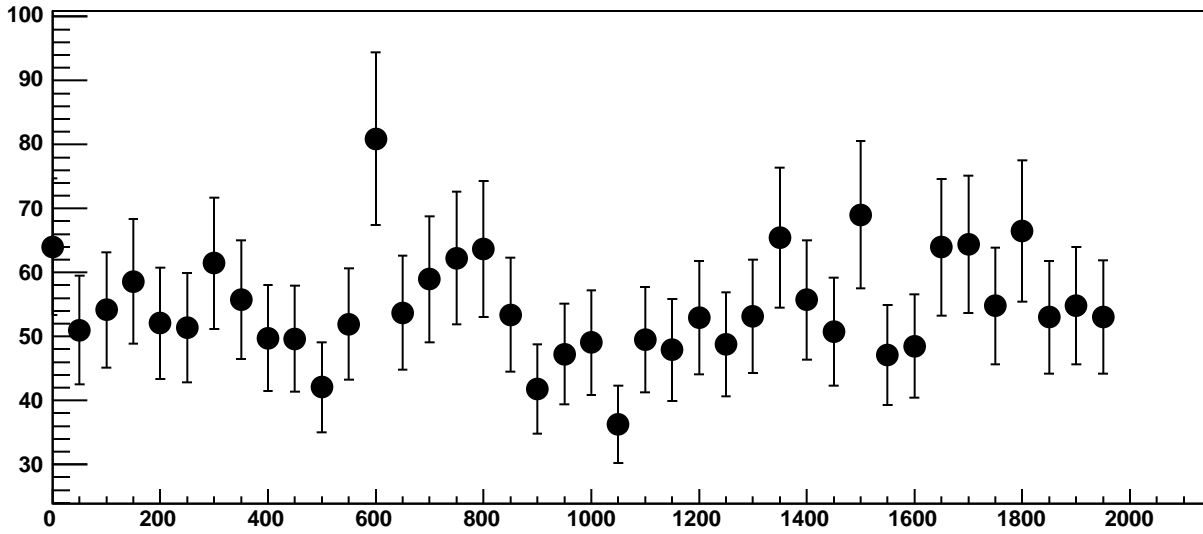


Chip 6, Channel 12, Enable 4, Hold=30, ADC Mean vs DAC

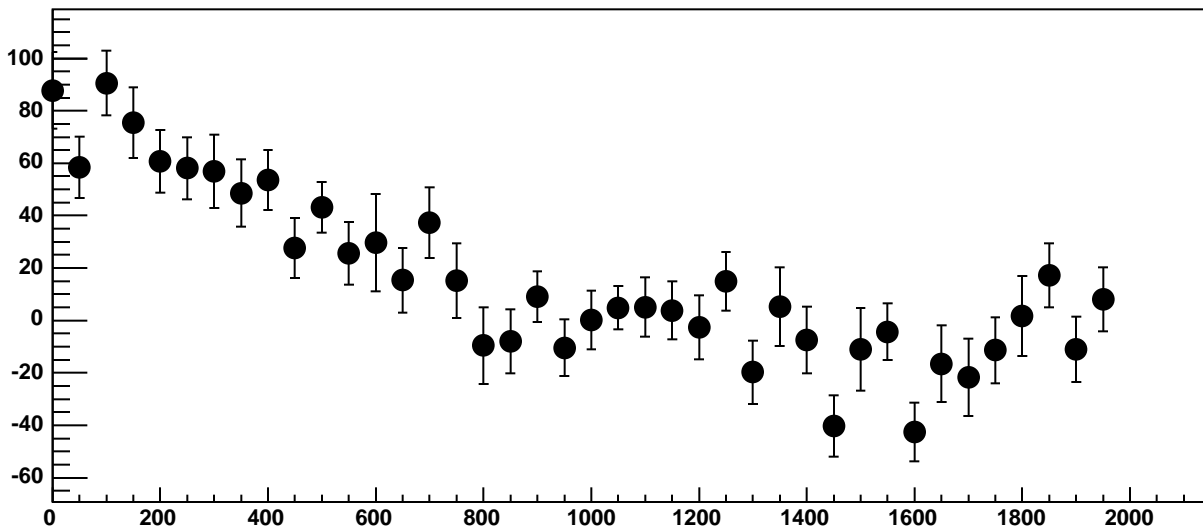


$\chi^2 / \text{ndf}$  8.263 / 11  
p0  $-1508 \pm 20.32$   
p1  $0.1032 \pm 0.01849$

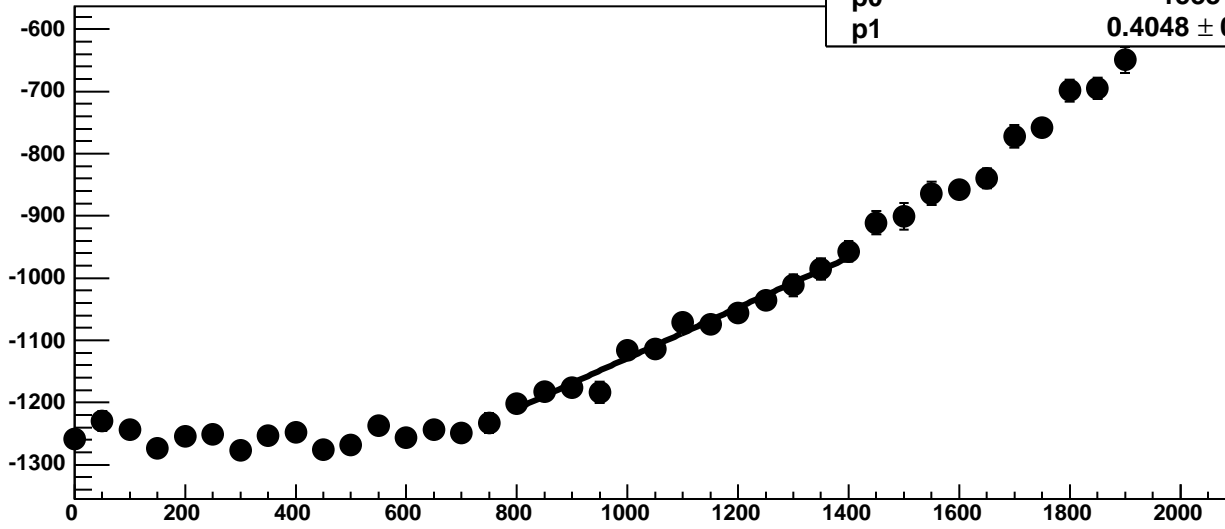
Chip 6, Channel 12, Enable 4, Hold=30, ADC Noise vs DAC



Chip 6, Channel 12, Enable 4, Hold=30, ADC Residuals vs DAC

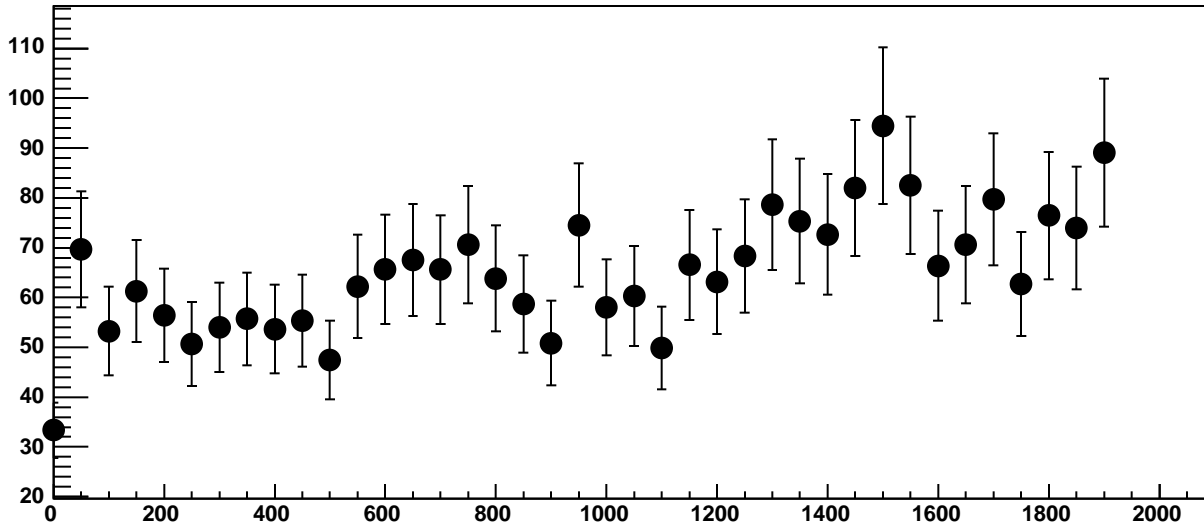


Chip 6, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC

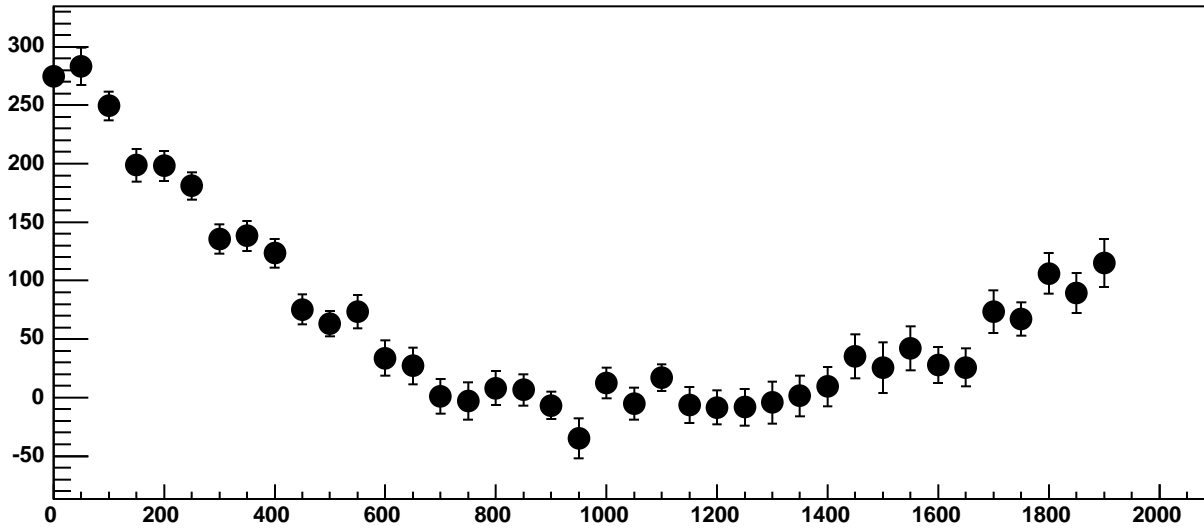


$\chi^2 / \text{ndf}$  9.363 / 11  
p0  $-1533 \pm 24.53$   
p1  $0.4048 \pm 0.02258$

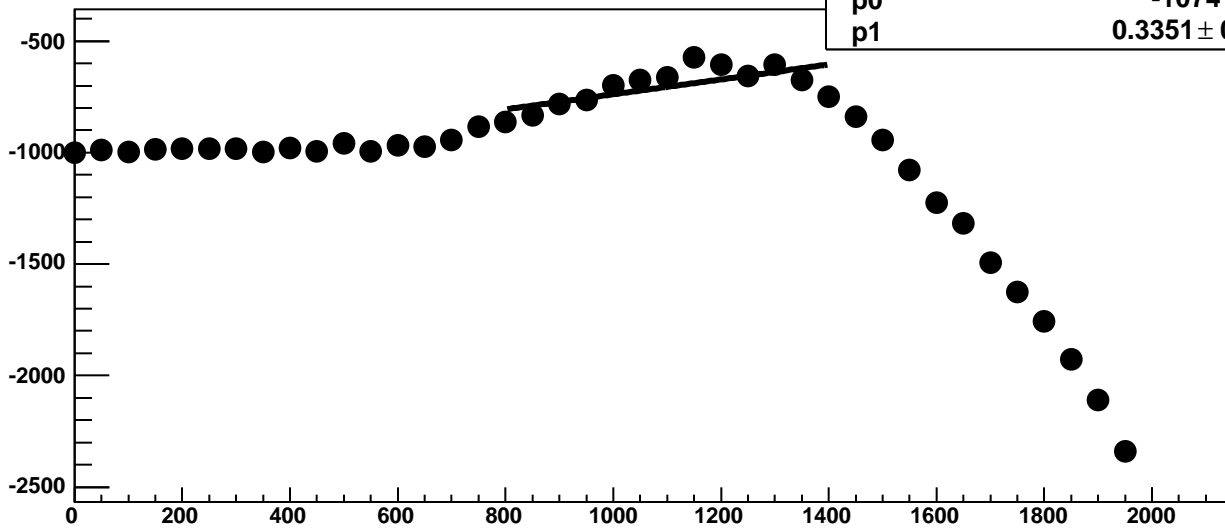
Chip 6, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC



Chip 6, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC

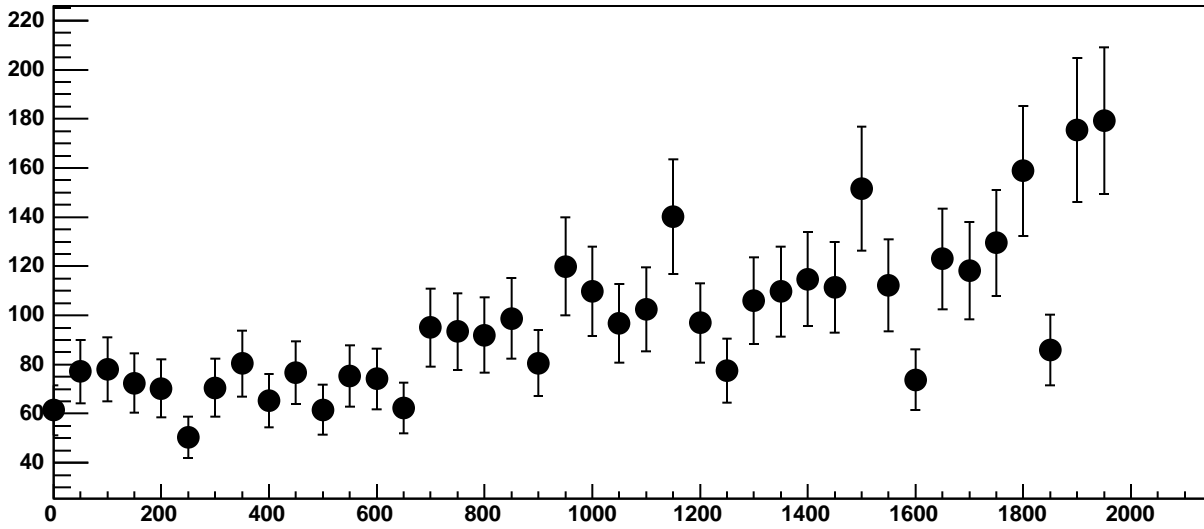


Chip 6, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC

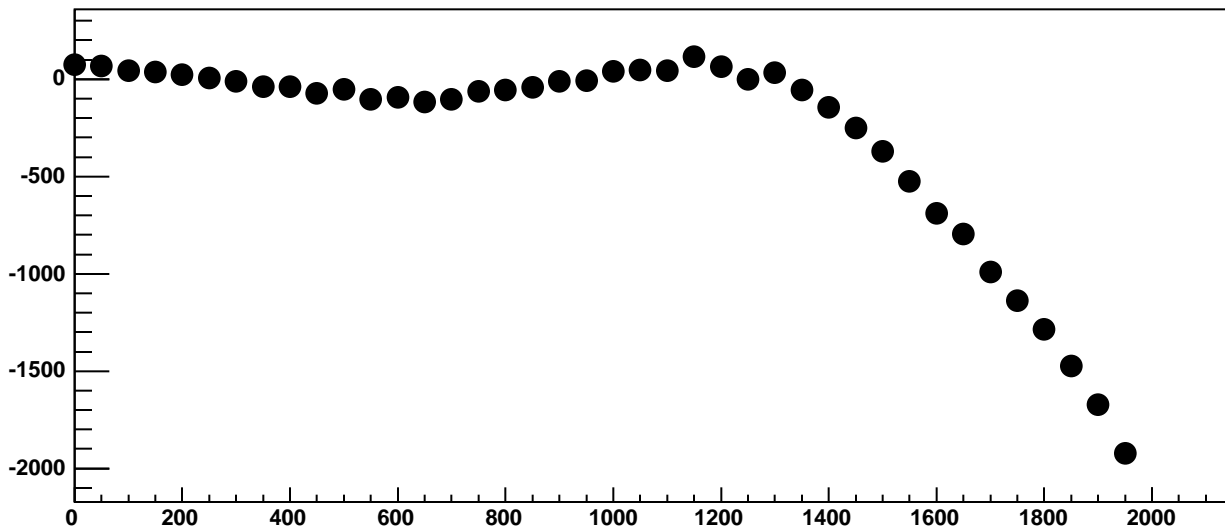


$\chi^2 / \text{ndf}$  79.86 / 11  
p0  $-1074 \pm 37.25$   
p1  $0.3351 \pm 0.03376$

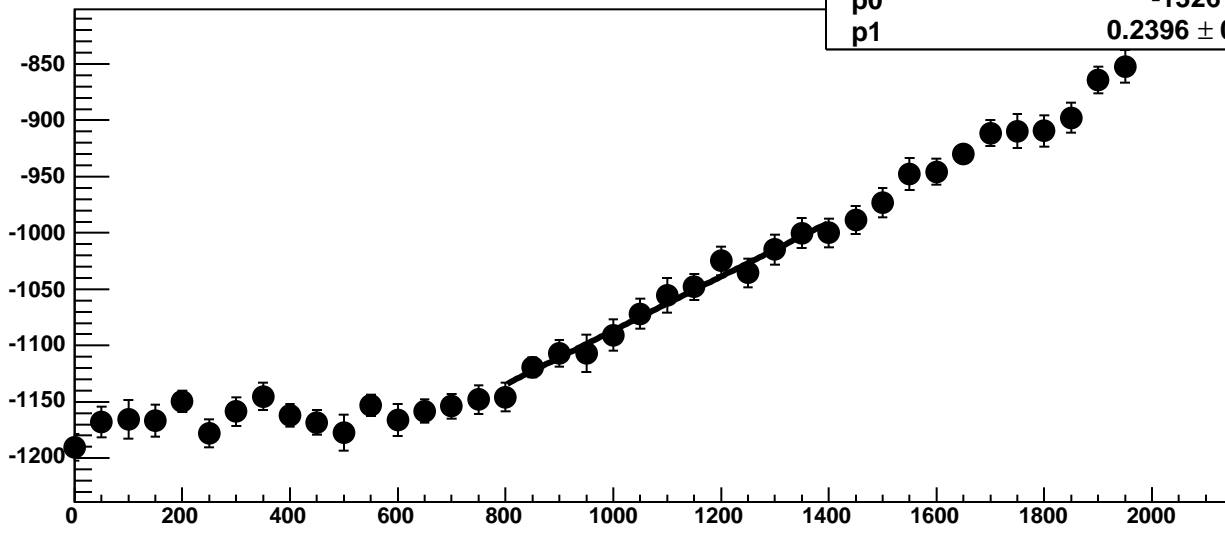
Chip 6, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC



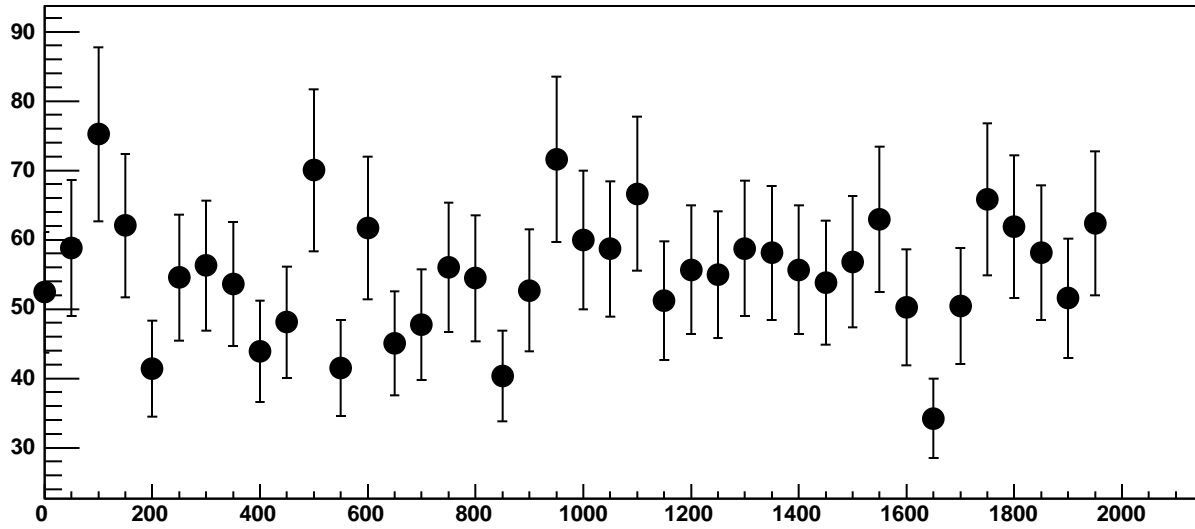
Chip 6, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC



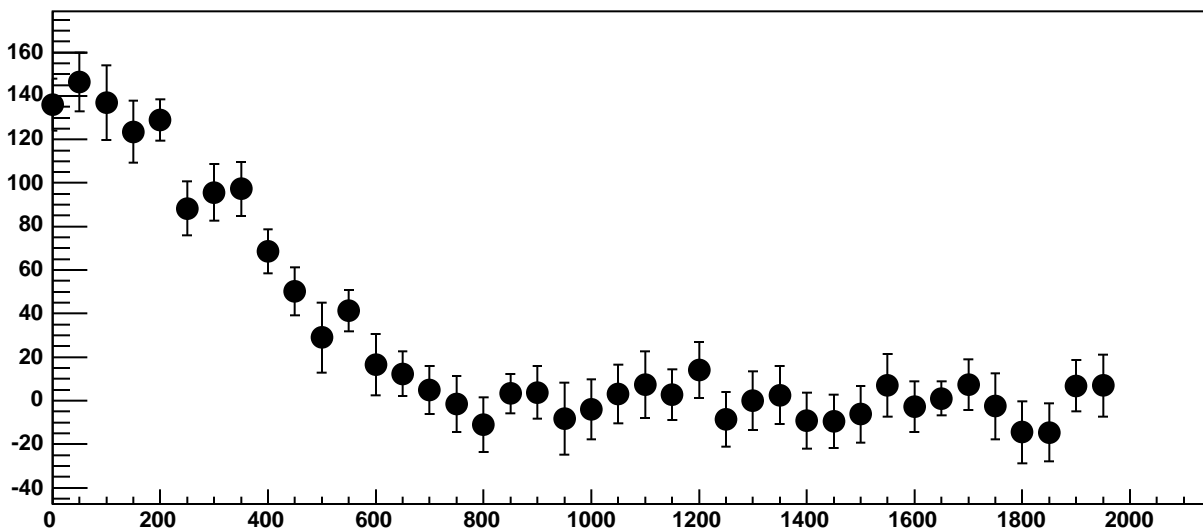
Chip 6, Channel 13, Enable 1, Hold=30, ADC Mean vs DAC



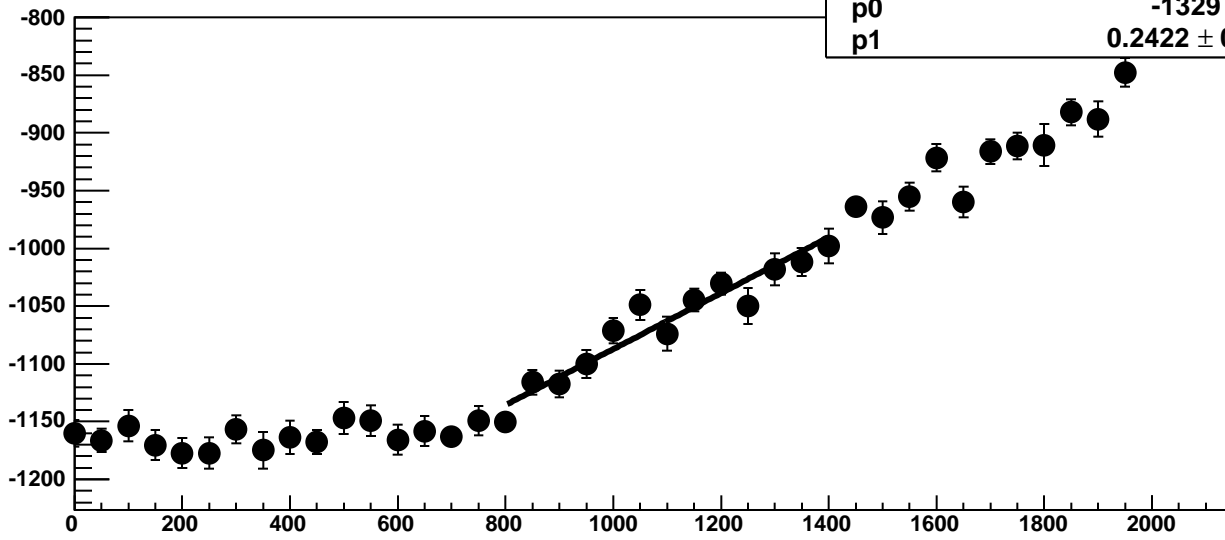
Chip 6, Channel 13, Enable 1, Hold=30, ADC Noise vs DAC



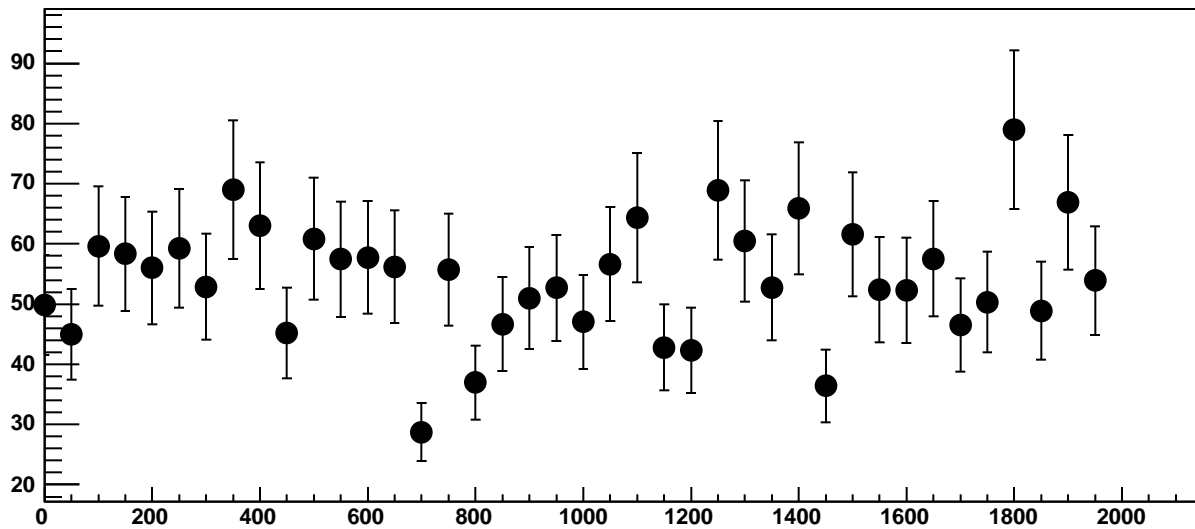
Chip 6, Channel 13, Enable 1, Hold=30, ADC Residuals vs DAC



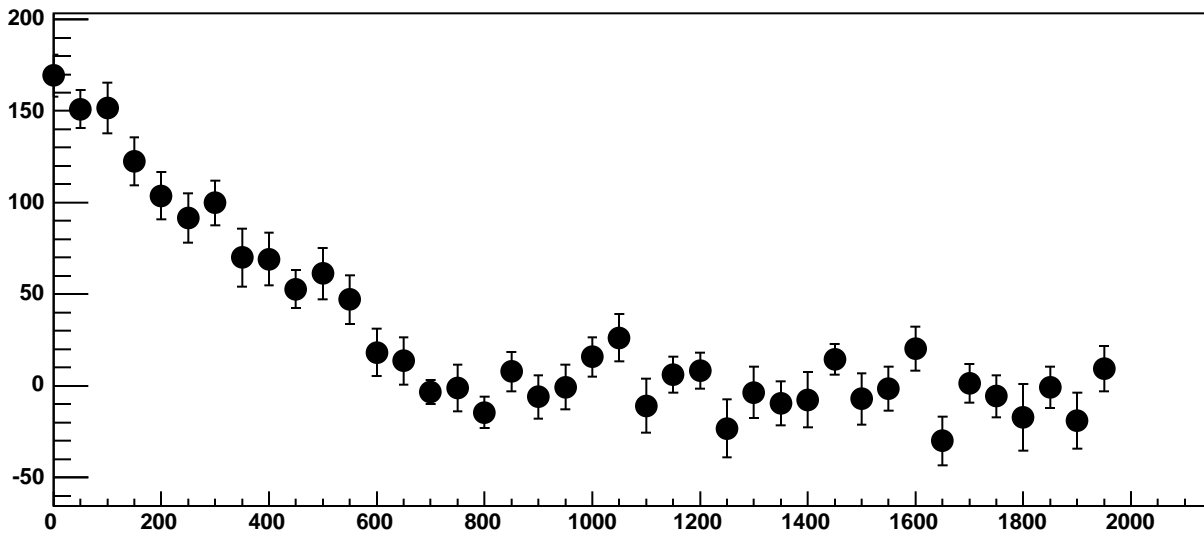
Chip 6, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC



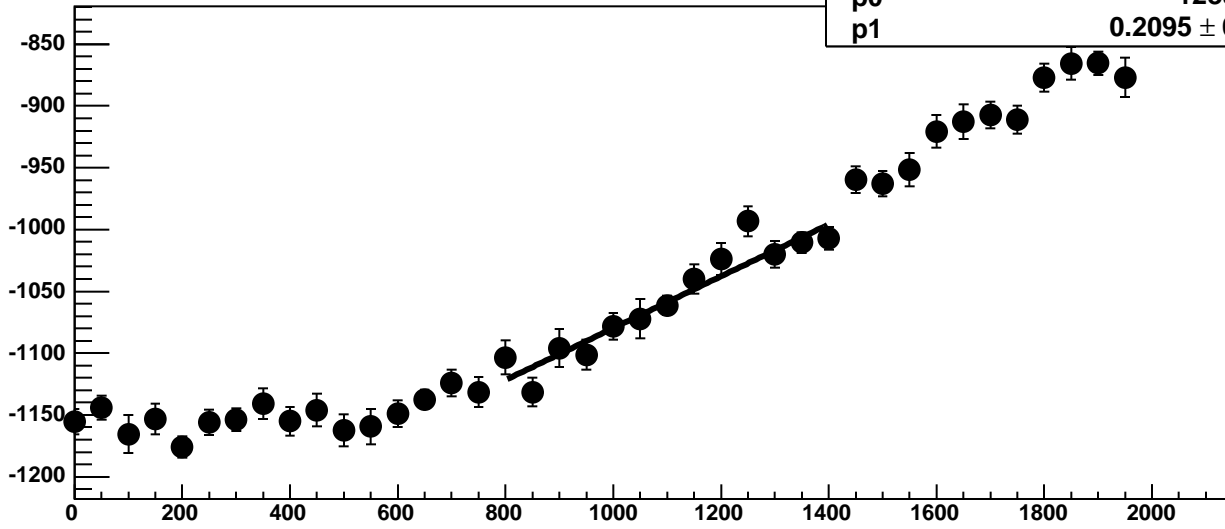
Chip 6, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC



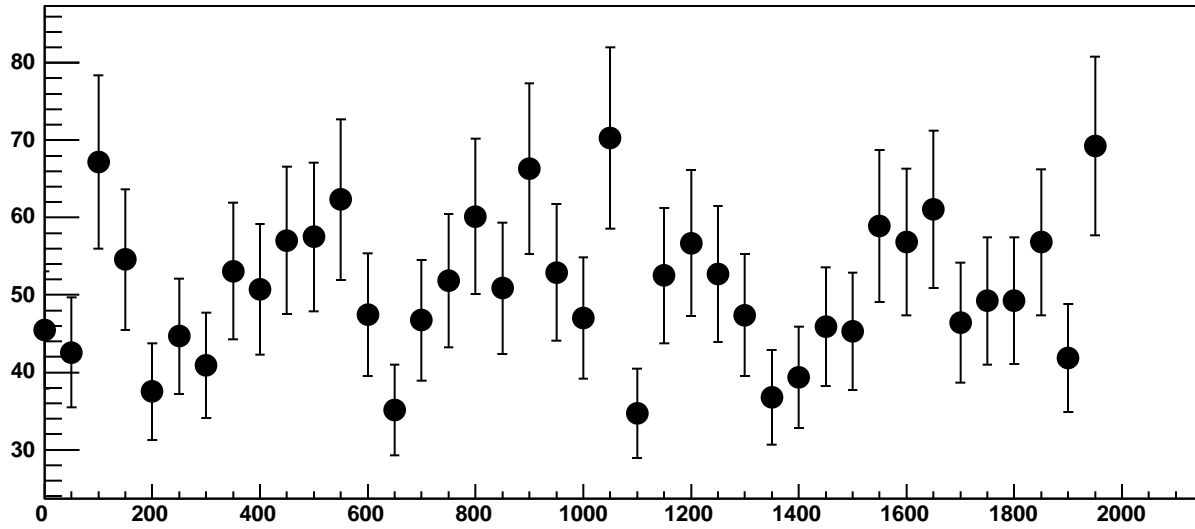
Chip 6, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC



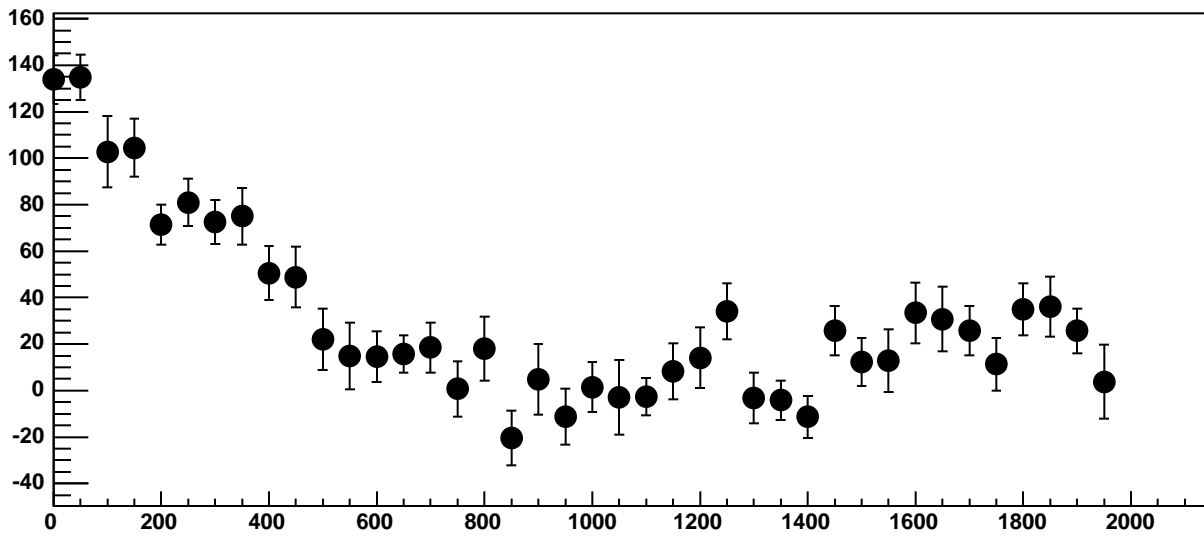
Chip 6, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC



Chip 6, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC

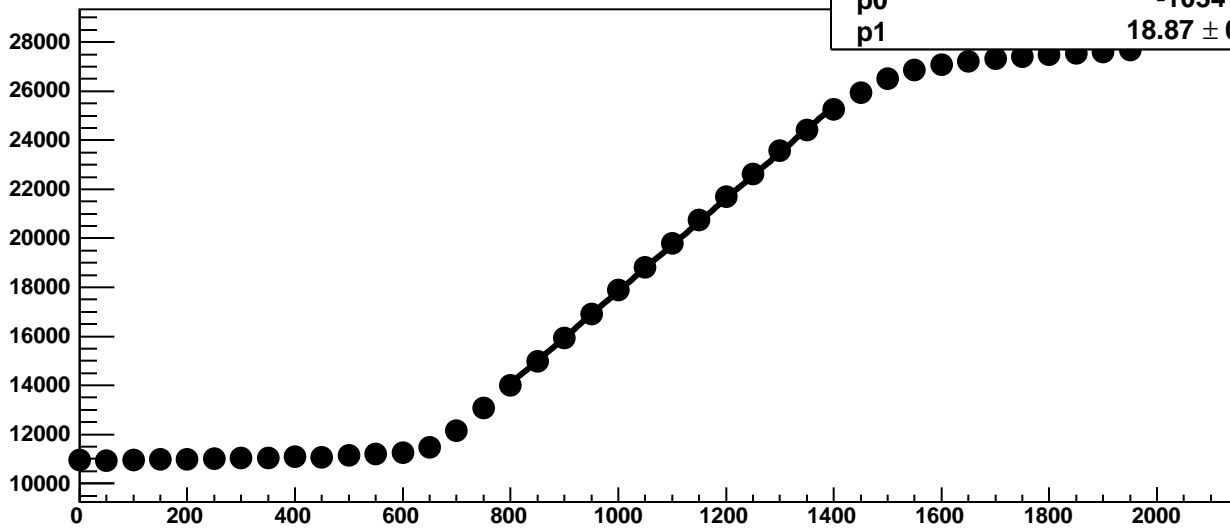


Chip 6, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC



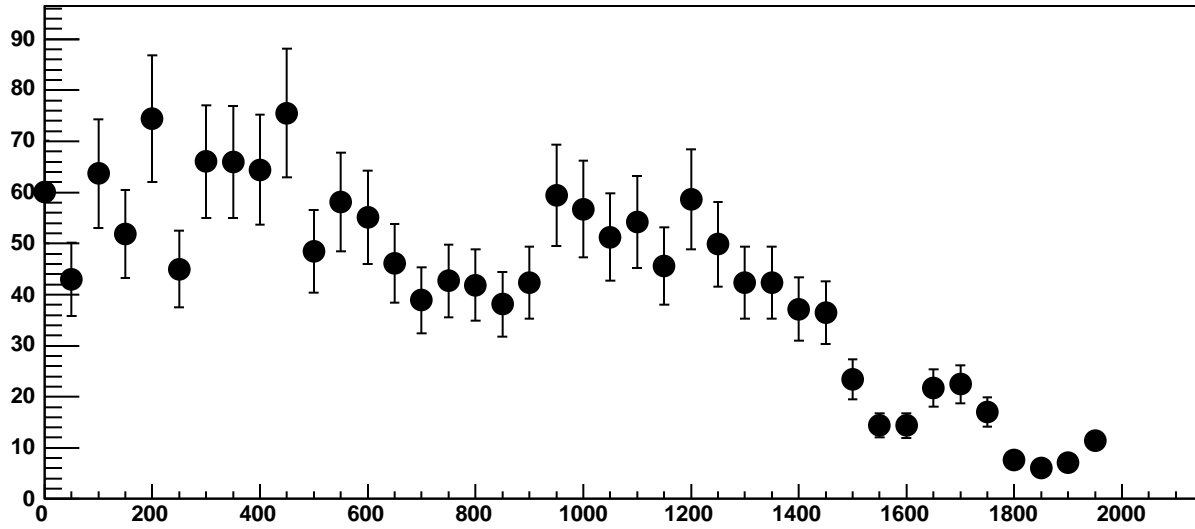


Chip 6, Channel 13, Enable 4!, Hold=30, ADC Mean vs DAC

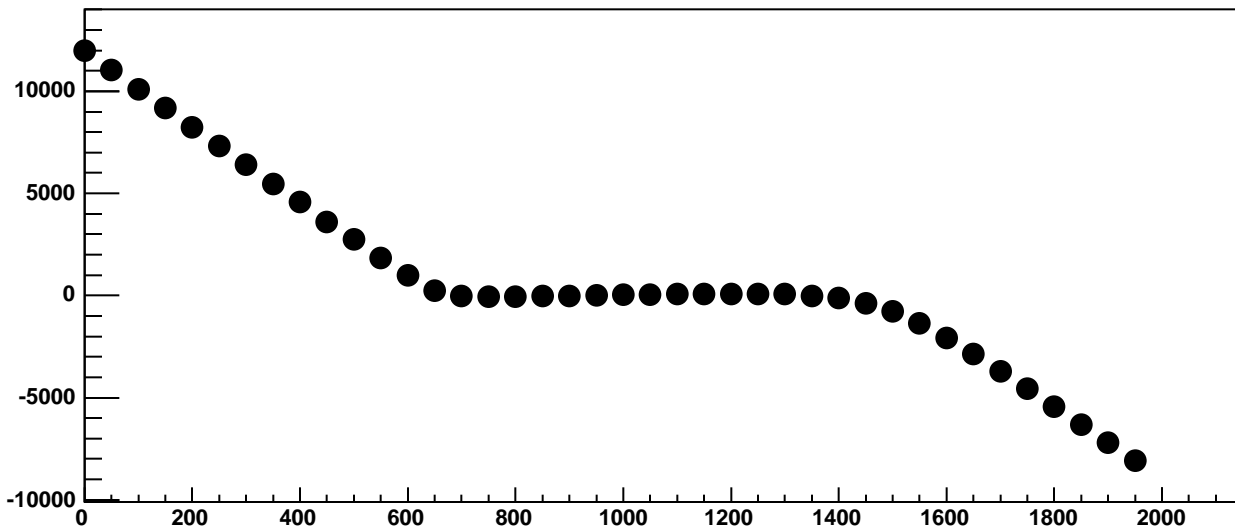


$\chi^2 / \text{ndf}$  517.4 / 11  
p0  $-1034 \pm 15.93$   
p1  $18.87 \pm 0.01416$

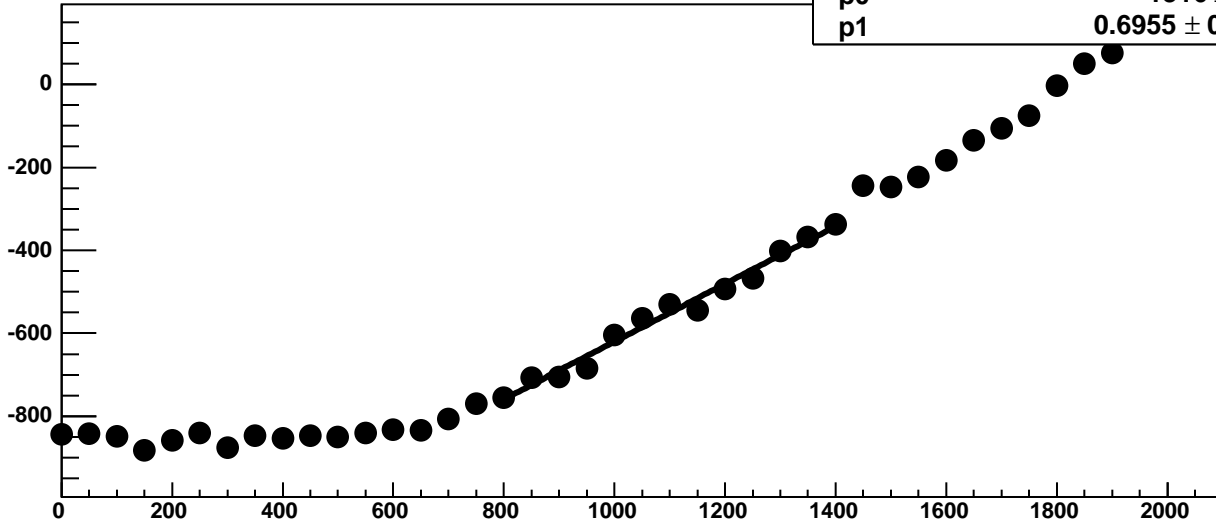
Chip 6, Channel 13, Enable 4!, Hold=30, ADC Noise vs DAC



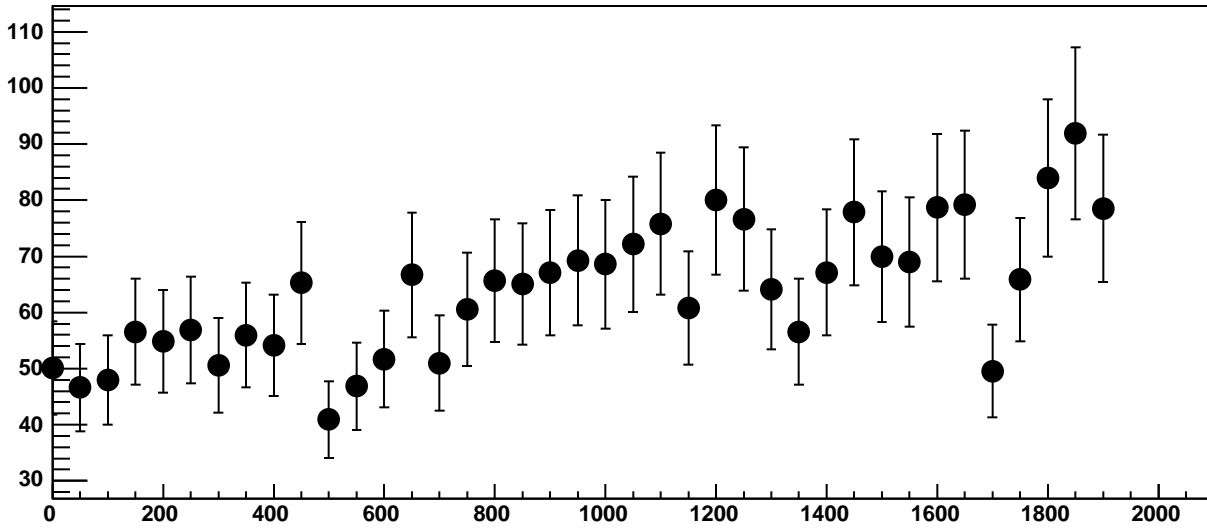
Chip 6, Channel 13, Enable 4!, Hold=30, ADC Residuals vs DAC



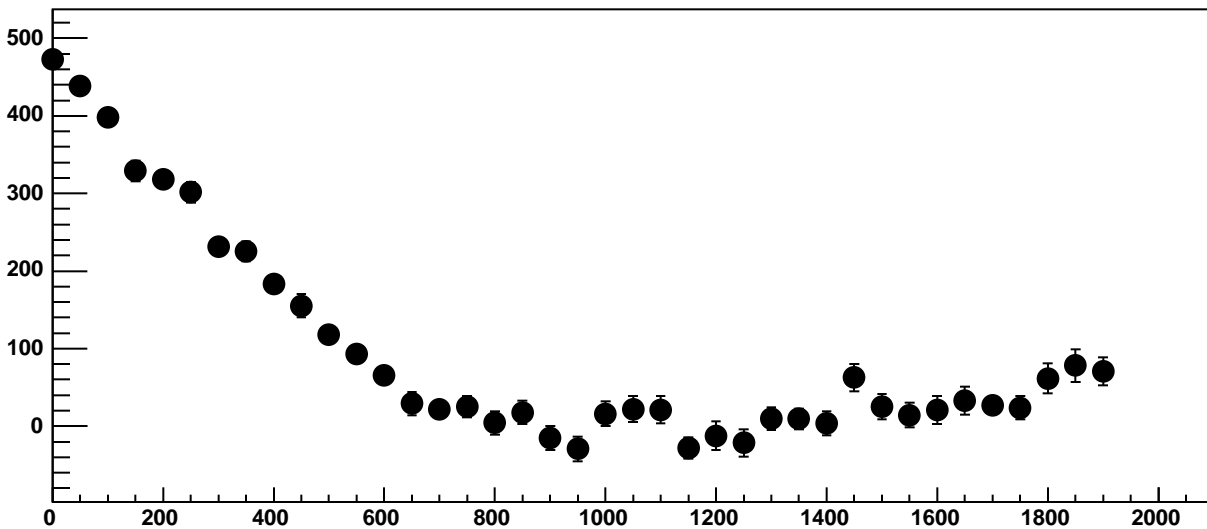
Chip 6, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC



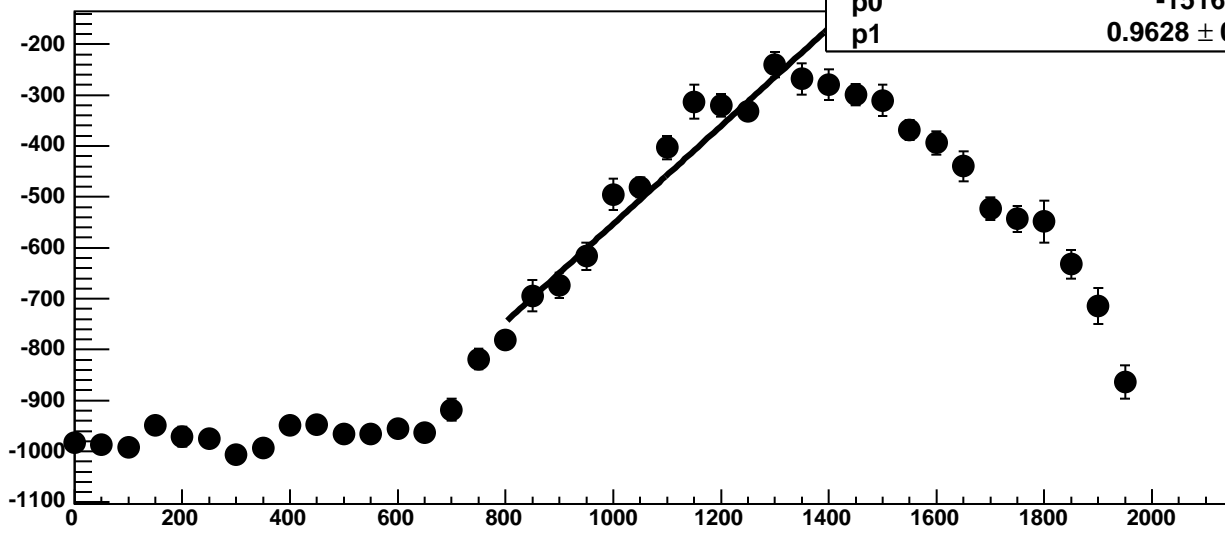
Chip 6, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC



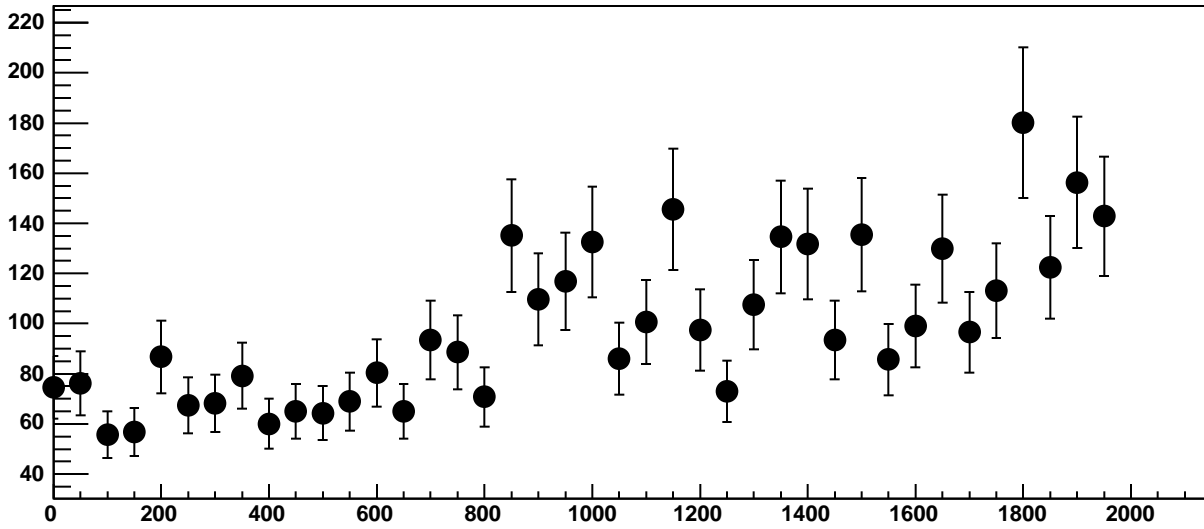
Chip 6, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC



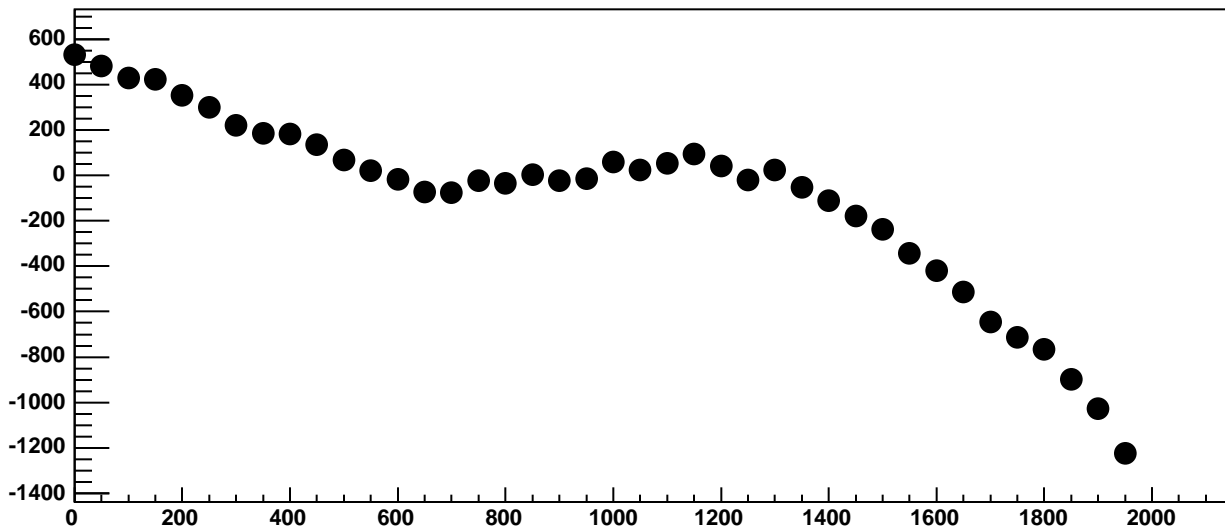
Chip 6, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC



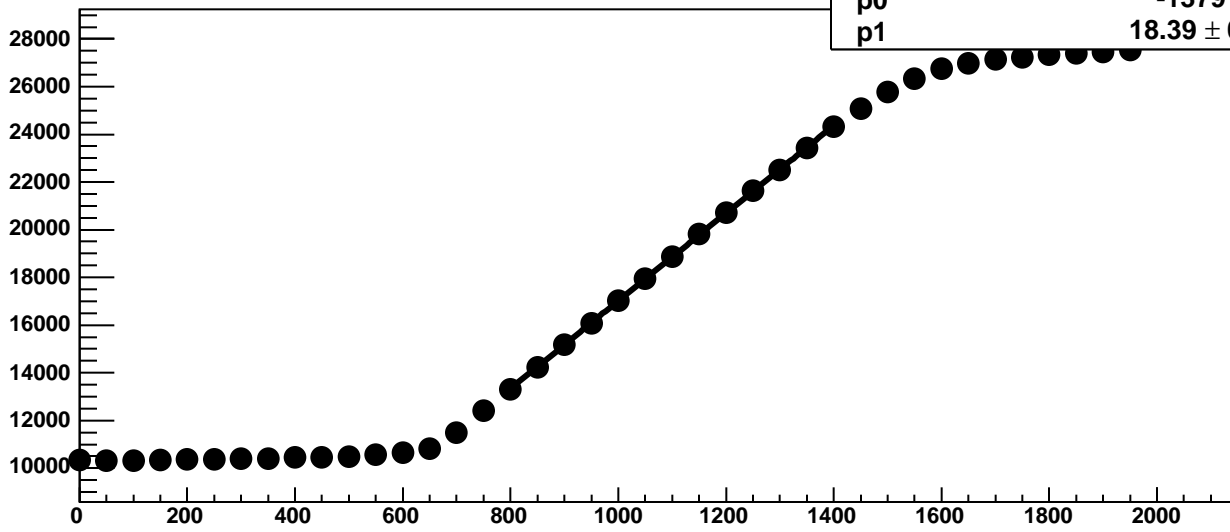
Chip 6, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC



Chip 6, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC

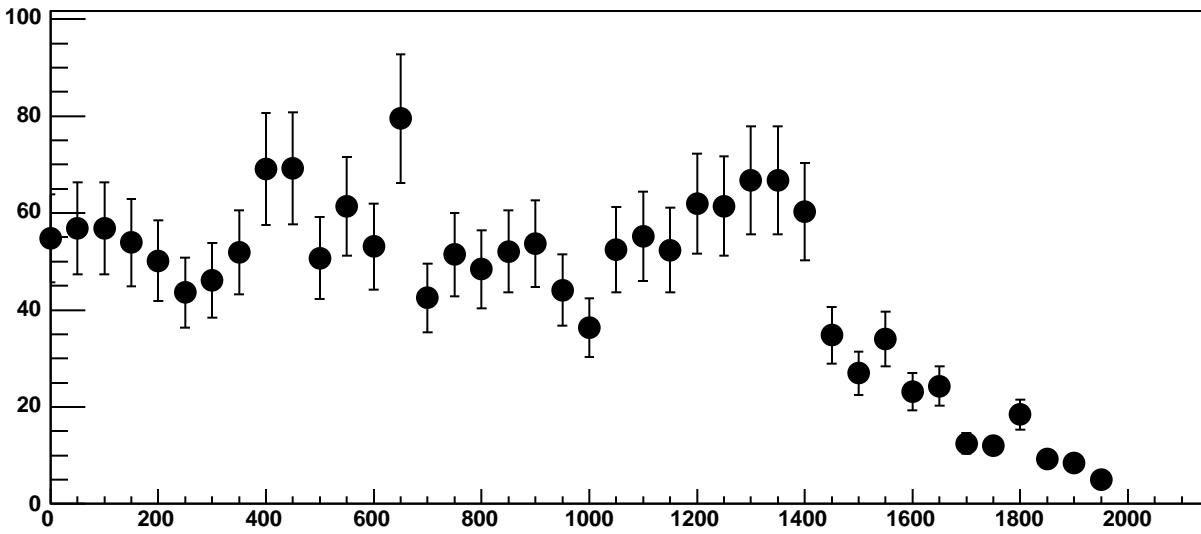


Chip 6, Channel 14, Enable 1!, Hold=30, ADC Mean vs DAC

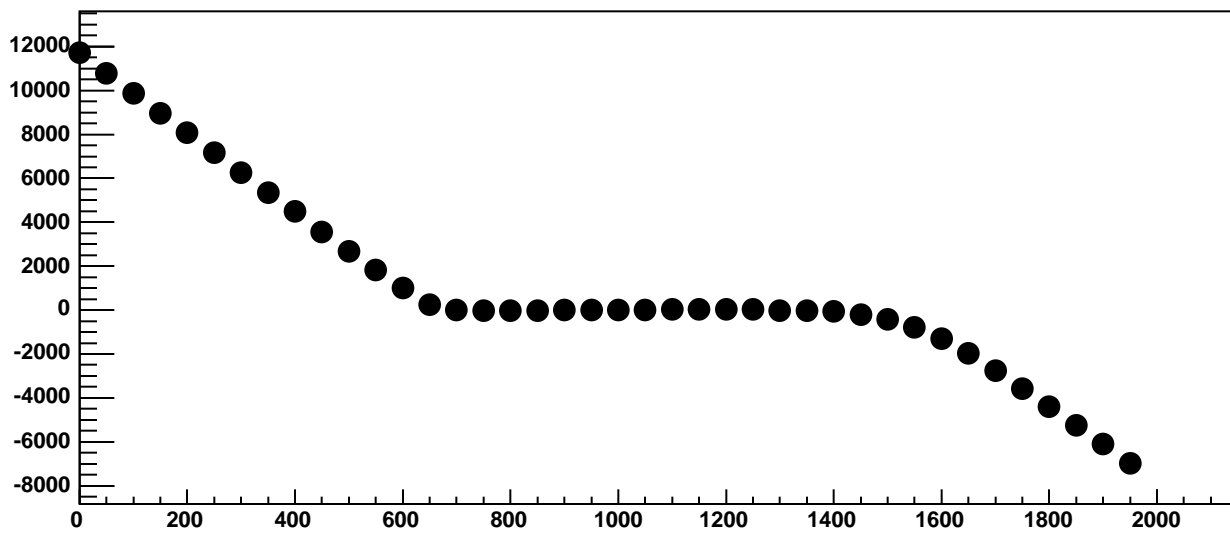


$\chi^2 / \text{ndf}$  47.93 / 11  
p0  $-1379 \pm 20.39$   
p1  $18.39 \pm 0.01901$

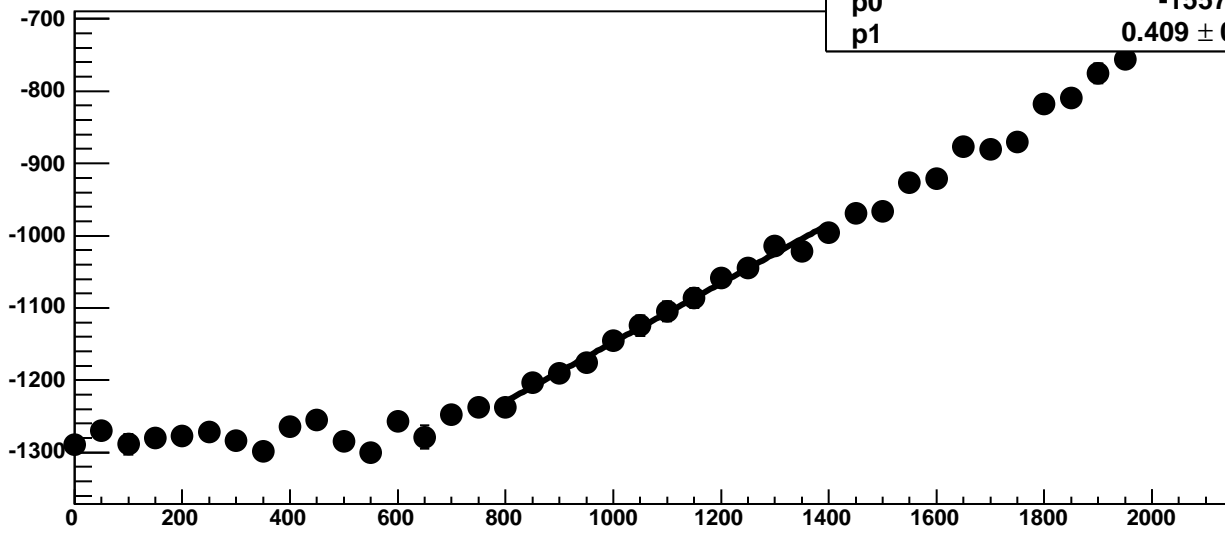
Chip 6, Channel 14, Enable 1!, Hold=30, ADC Noise vs DAC



Chip 6, Channel 14, Enable 1!, Hold=30, ADC Residuals vs DAC

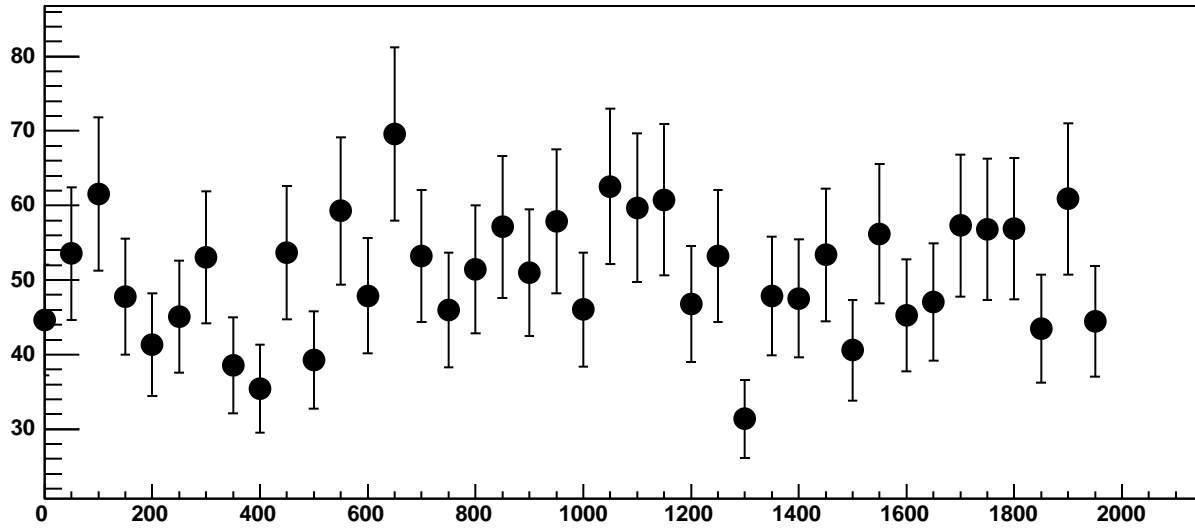


Chip 6, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC

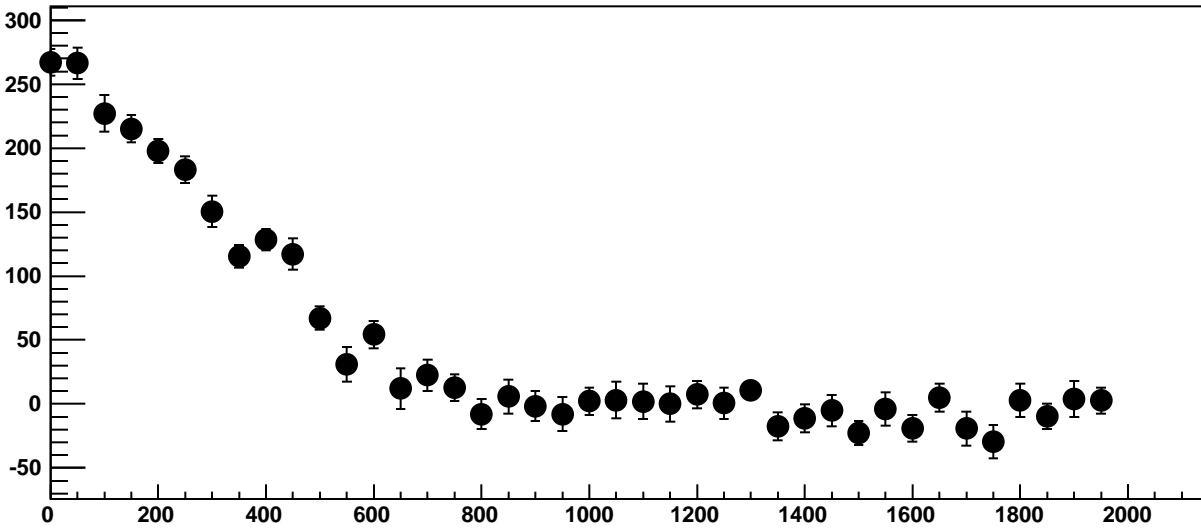


$\chi^2 / \text{ndf}$  7.488 / 11  
p0  $-1557 \pm 18.9$   
p1  $0.409 \pm 0.01642$

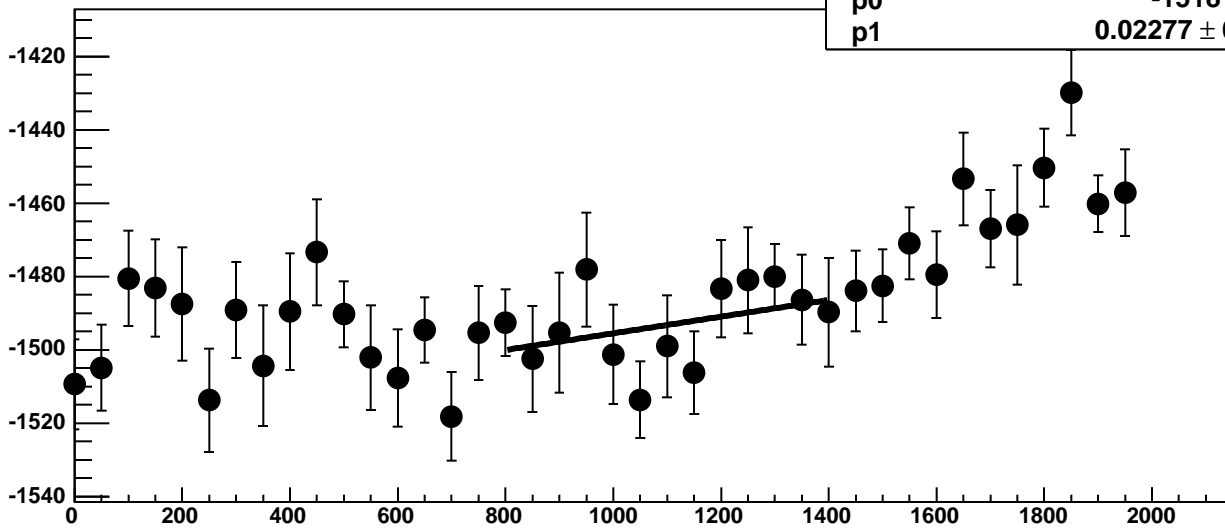
Chip 6, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC



Chip 6, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC

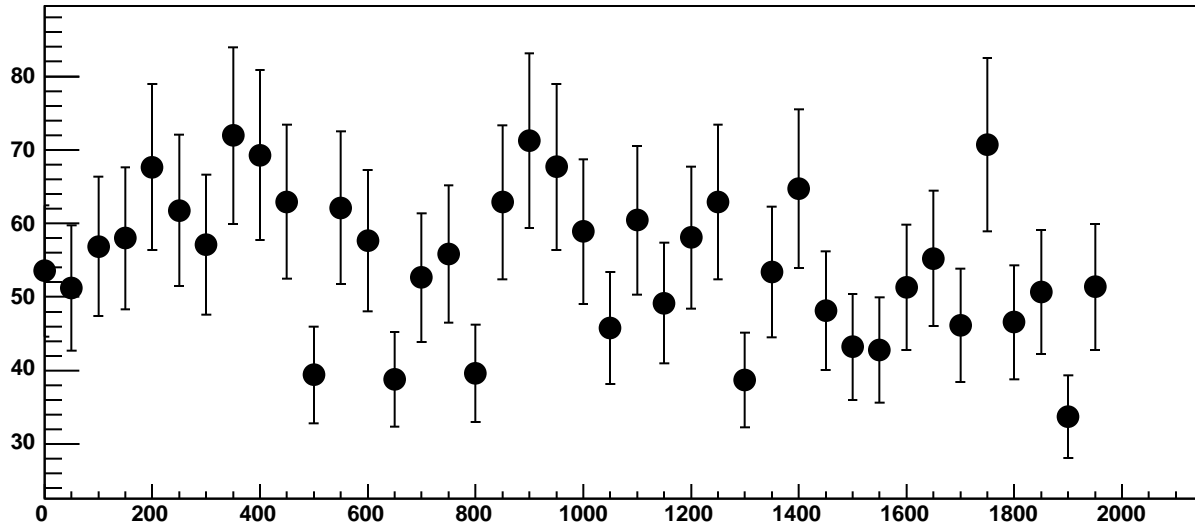


Chip 6, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC

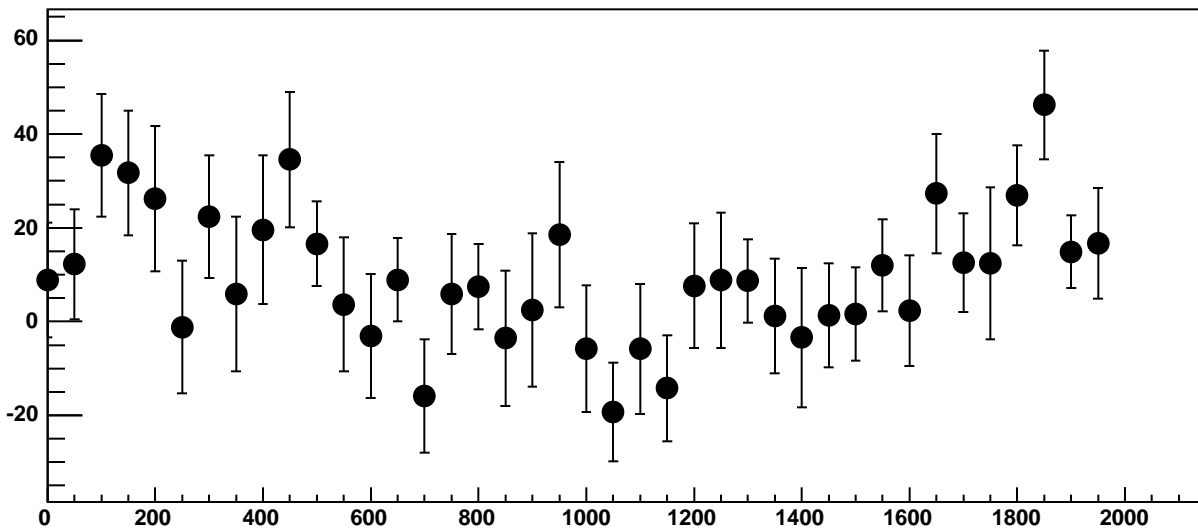


$\chi^2 / \text{ndf}$  9.217 / 11  
p0  $-1518 \pm 19.67$   
p1  $0.02277 \pm 0.01761$

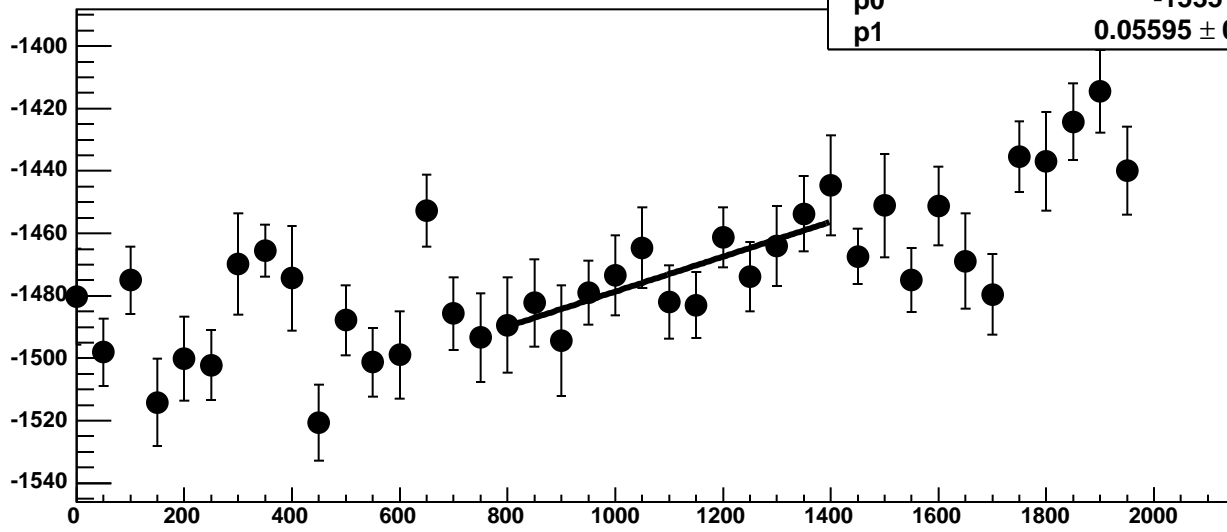
Chip 6, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC



Chip 6, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC

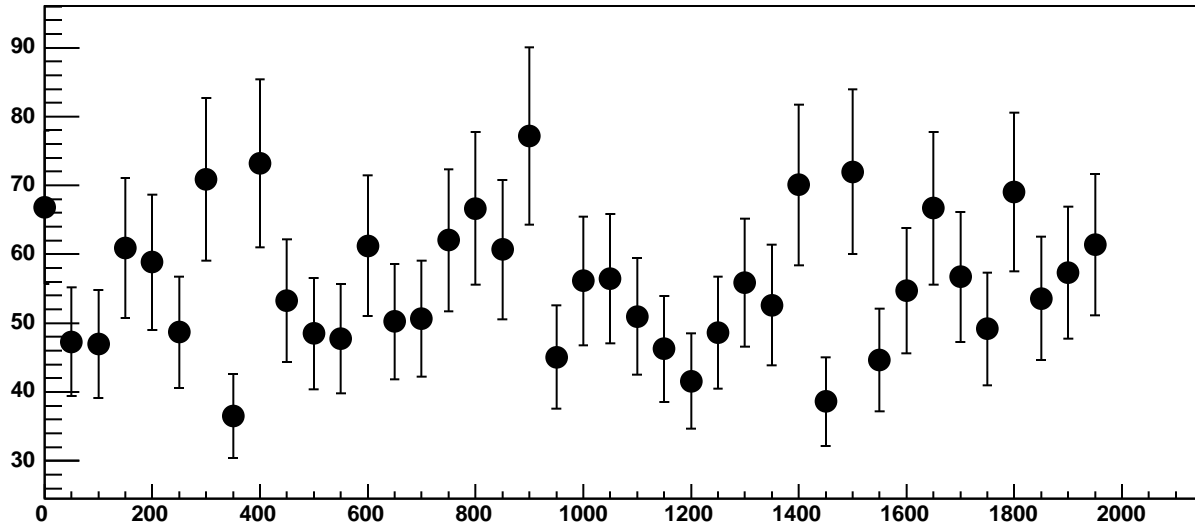


Chip 6, Channel 14, Enable 4, Hold=30, ADC Mean vs DAC

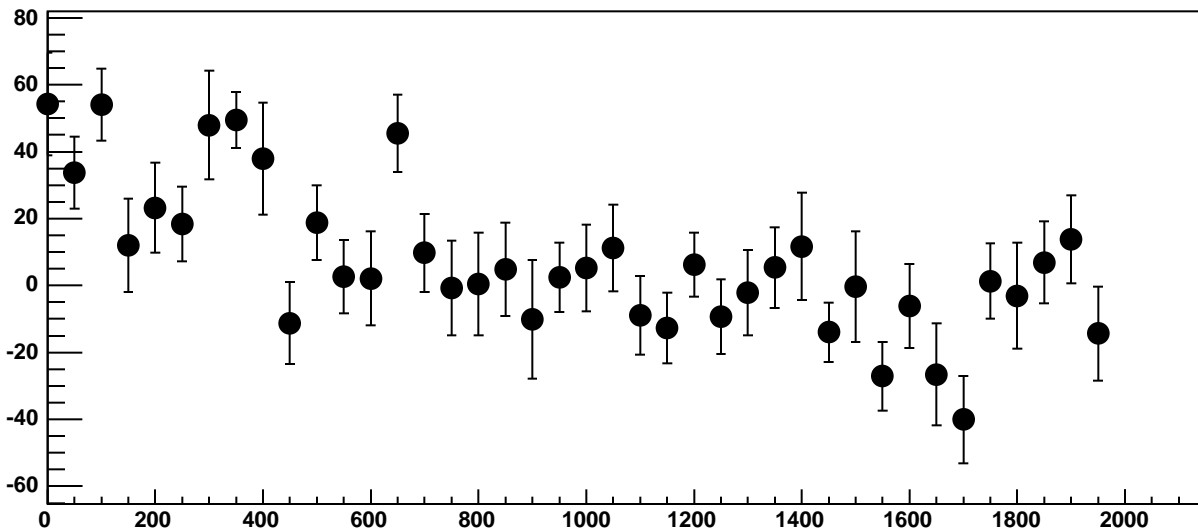


$\chi^2 / \text{ndf}$  5.287 / 11  
p0  $-1535 \pm 22.87$   
p1  $0.05595 \pm 0.02027$

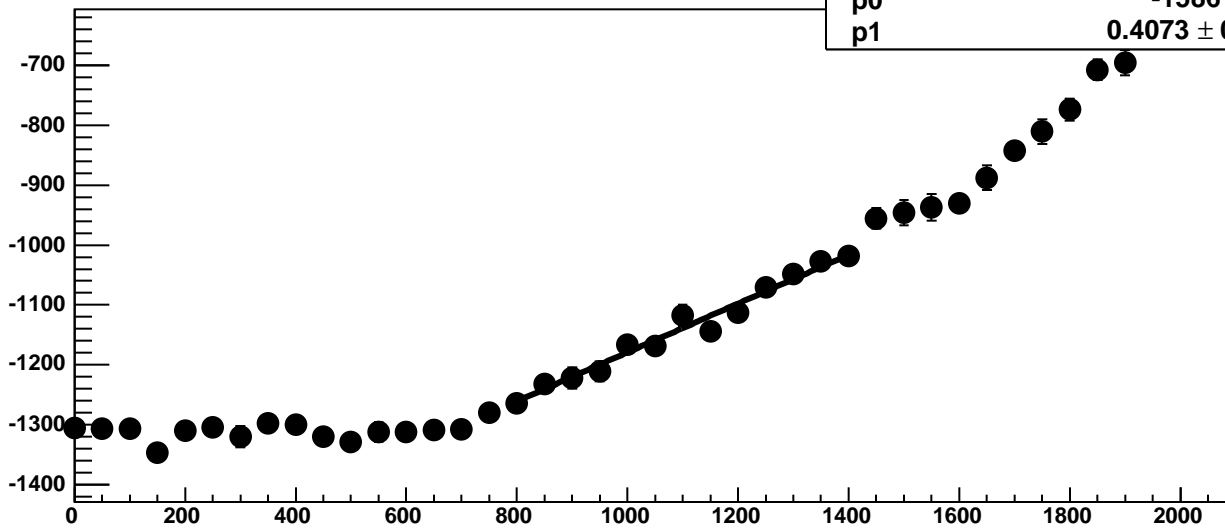
Chip 6, Channel 14, Enable 4, Hold=30, ADC Noise vs DAC



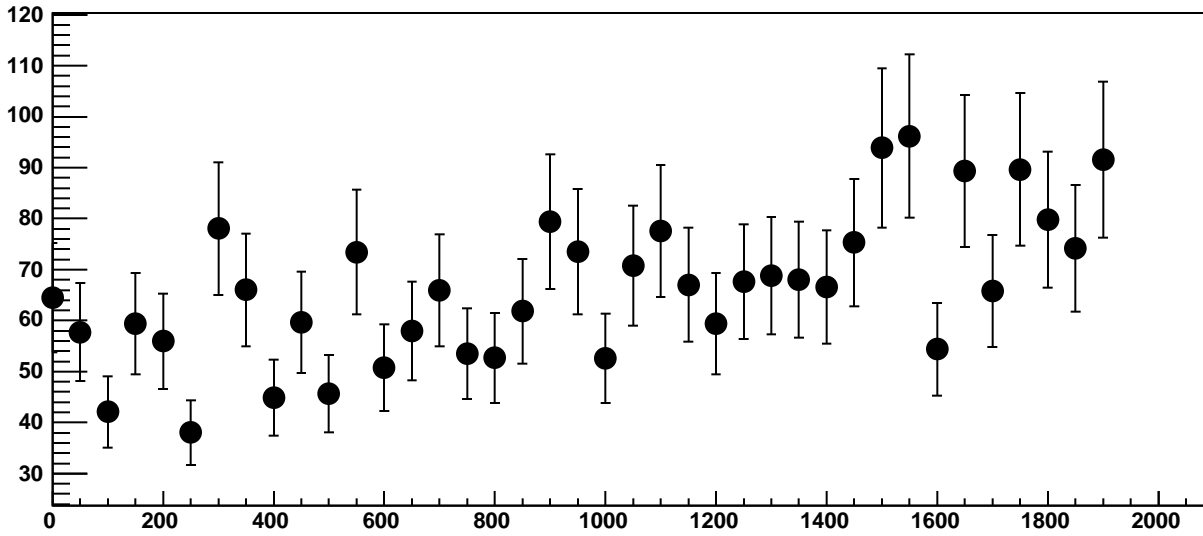
Chip 6, Channel 14, Enable 4, Hold=30, ADC Residuals vs DAC



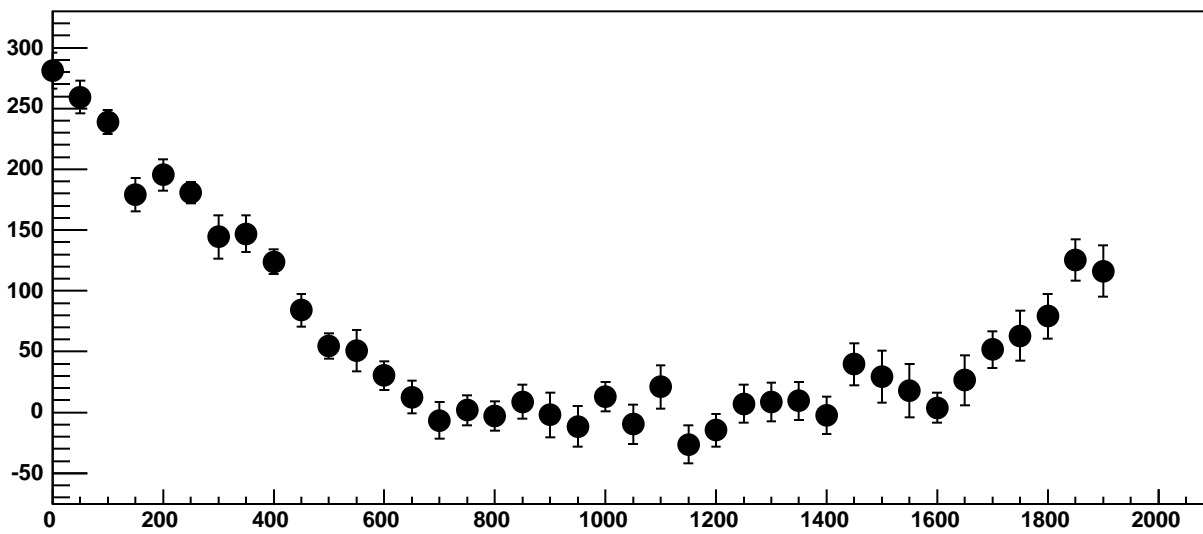
Chip 6, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC



Chip 6, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC

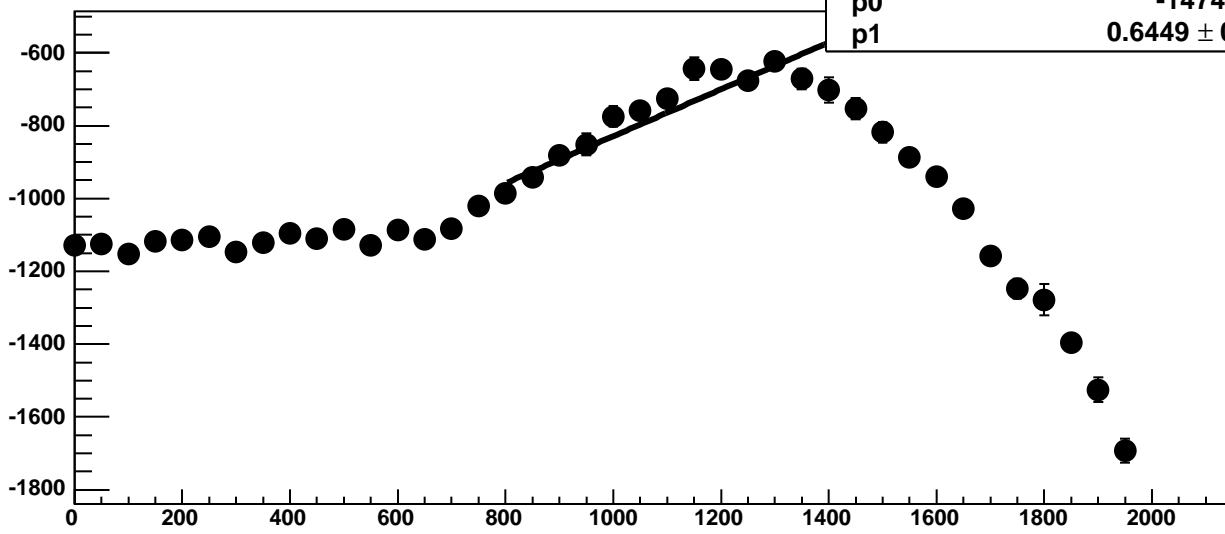


Chip 6, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC



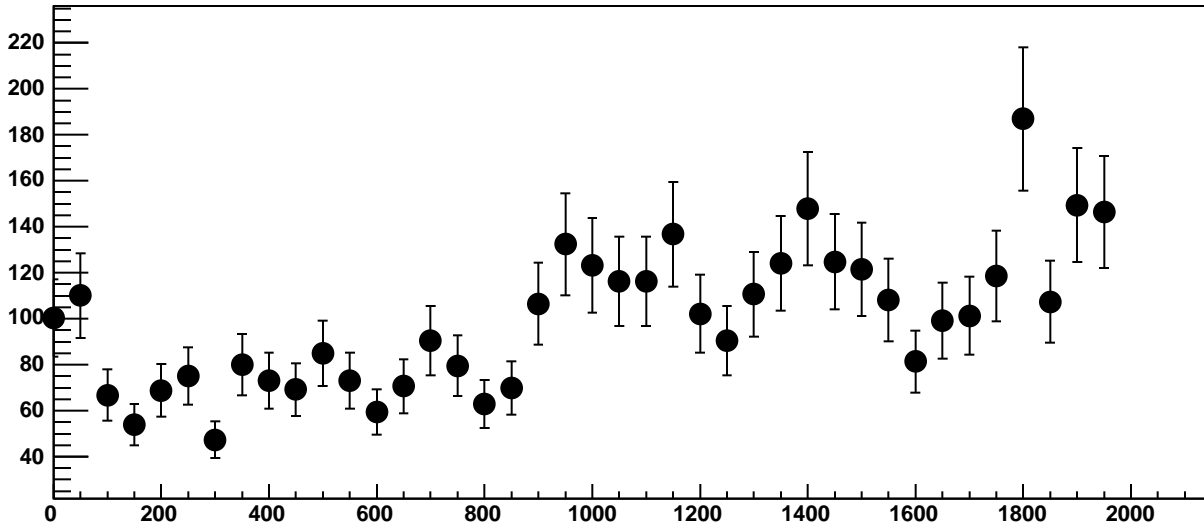


Chip 6, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC

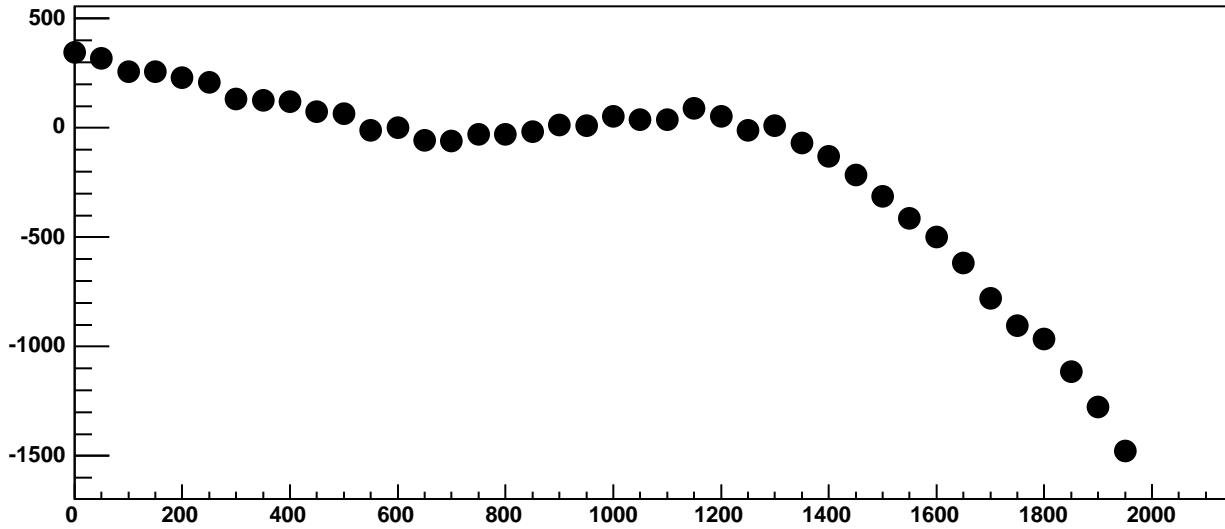


$\chi^2 / \text{ndf}$  47.43 / 11  
p0 -1474 ± 33.91  
p1 0.6449 ± 0.03225

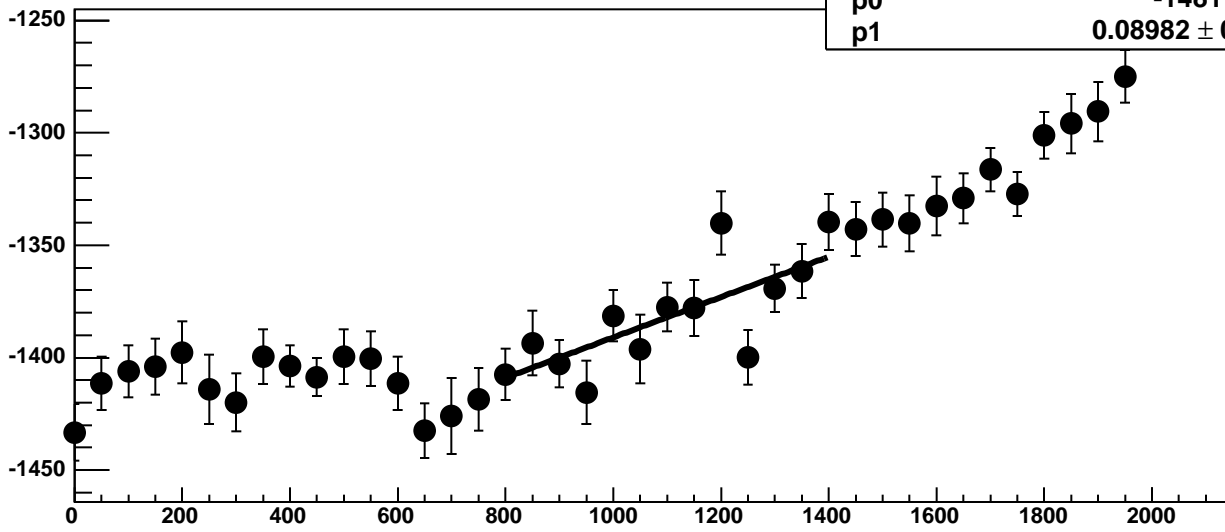
Chip 6, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC



Chip 6, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC

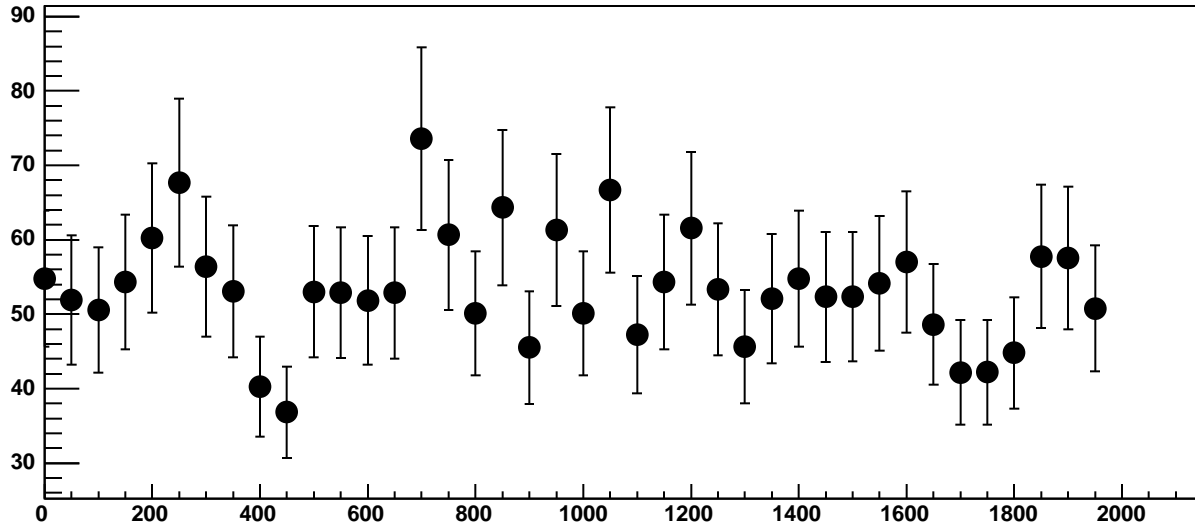


Chip 6, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC

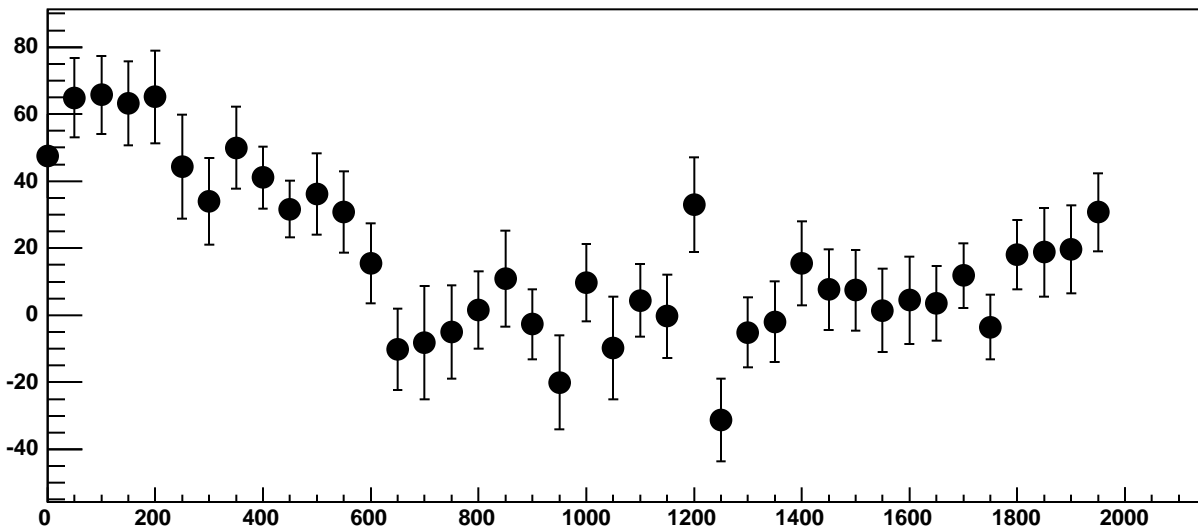


$\chi^2 / \text{ndf}$  17.69 / 11  
p0  $-1481 \pm 19.98$   
p1  $0.08982 \pm 0.01785$

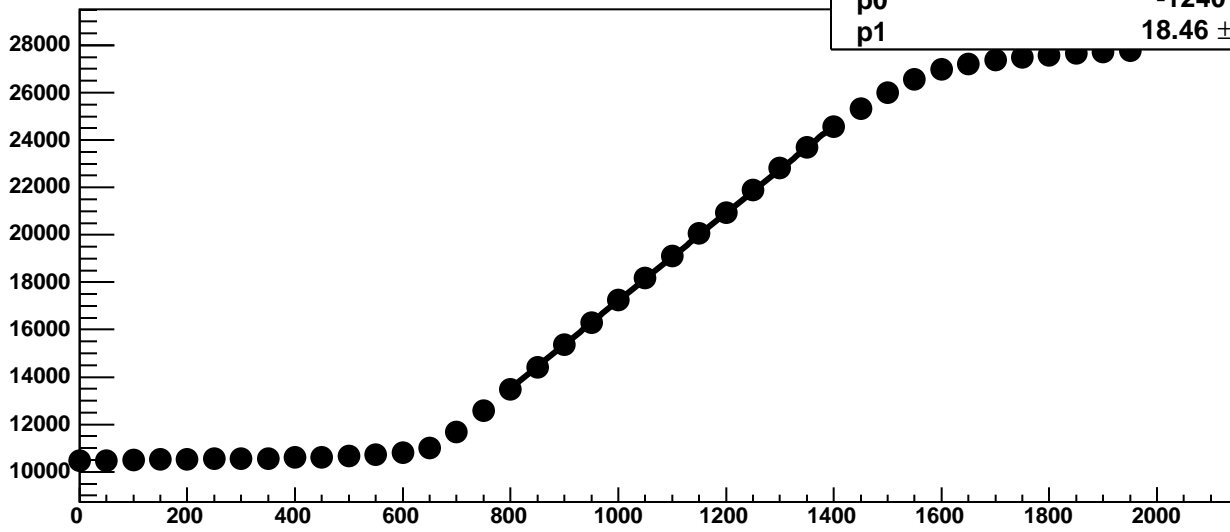
Chip 6, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC



Chip 6, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC

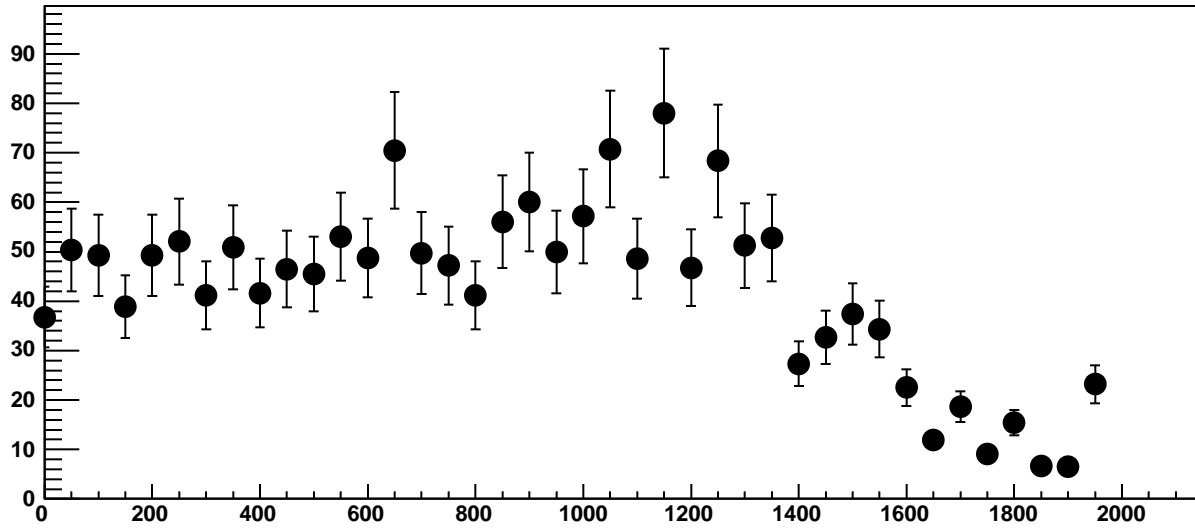


Chip 6, Channel 15, Enable 2!, Hold=30, ADC Mean vs DAC

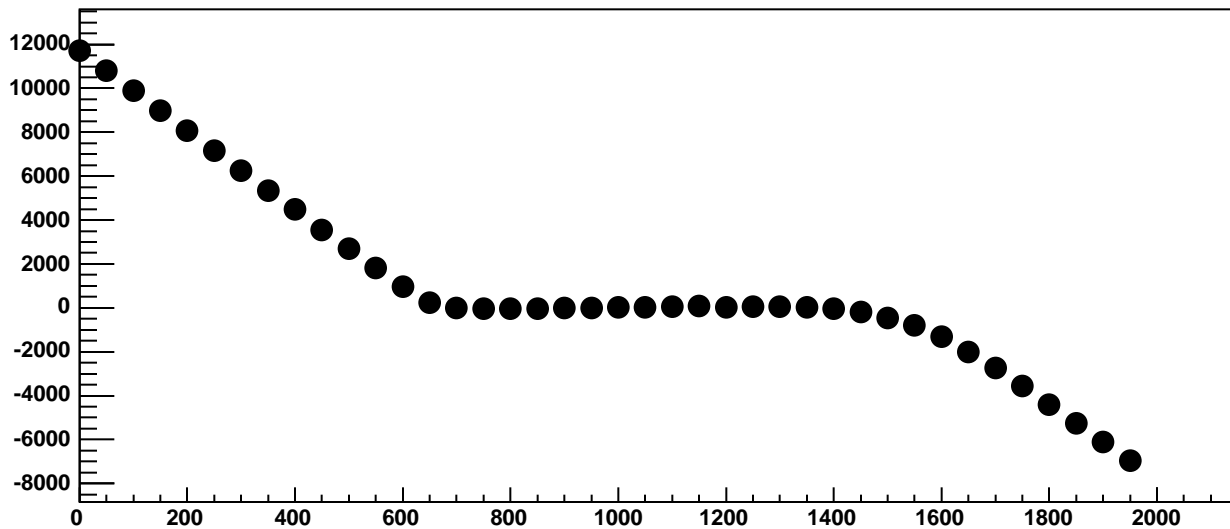


$\chi^2 / \text{ndf}$  174.7 / 11  
p0  $-1240 \pm 16.77$   
p1  $18.46 \pm 0.0144$

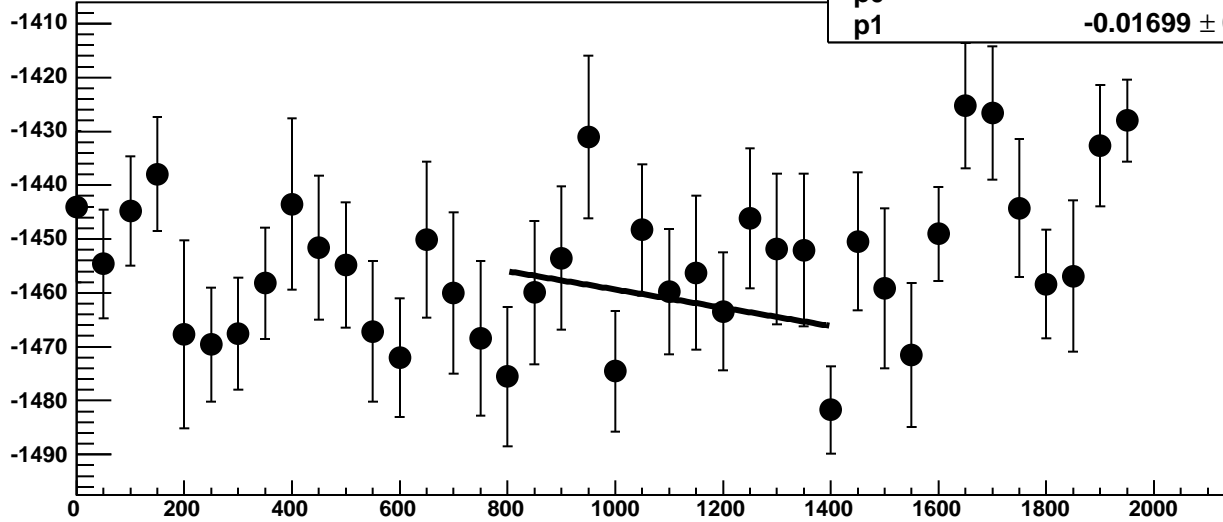
Chip 6, Channel 15, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 6, Channel 15, Enable 2!, Hold=30, ADC Residuals vs DAC

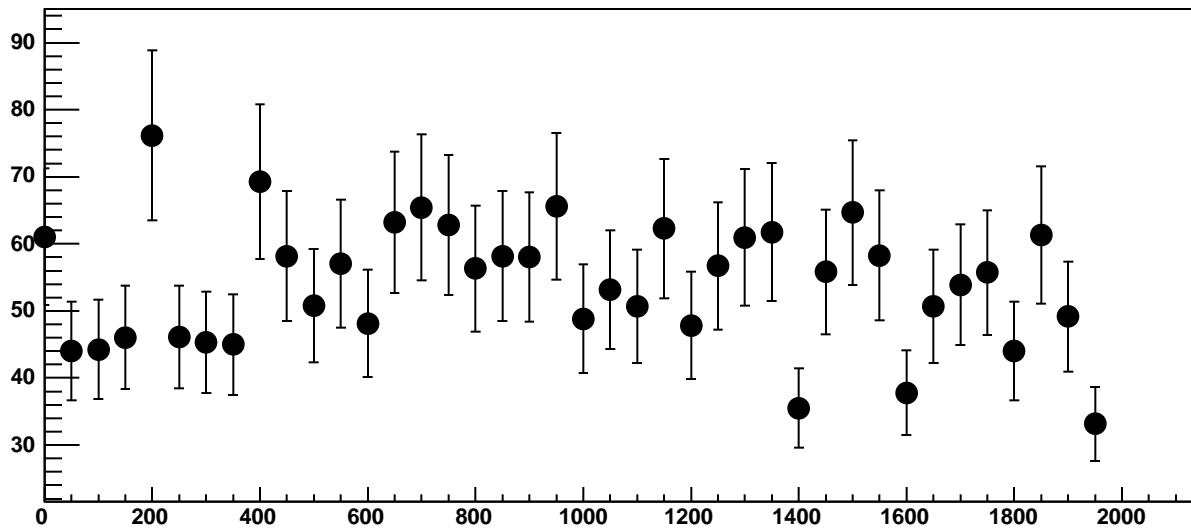


Chip 6, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC

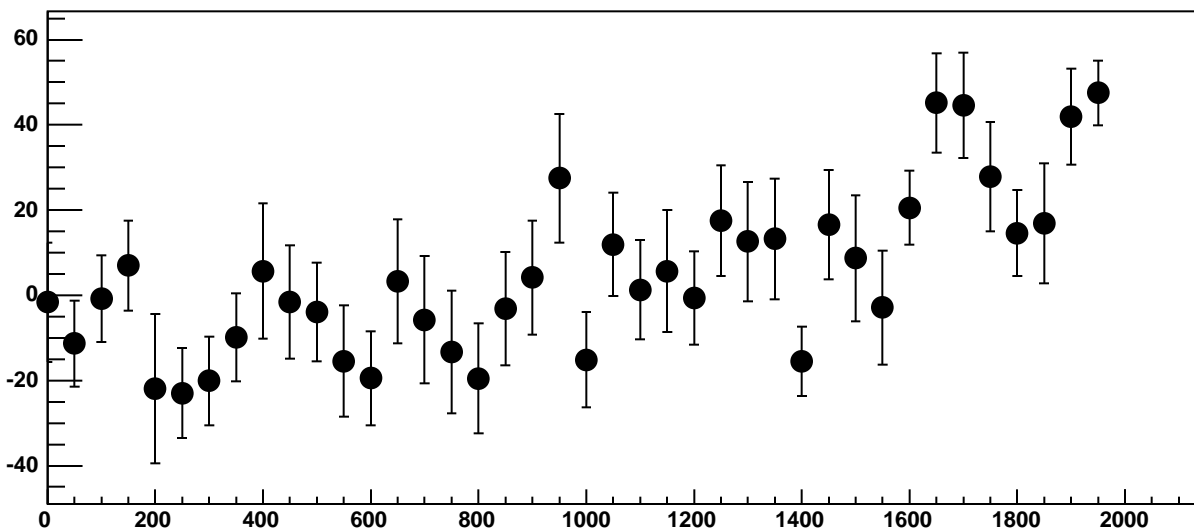


$\chi^2 / \text{ndf}$  15.9 / 11  
p0  $-1442 \pm 19.85$   
p1  $-0.01699 \pm 0.01732$

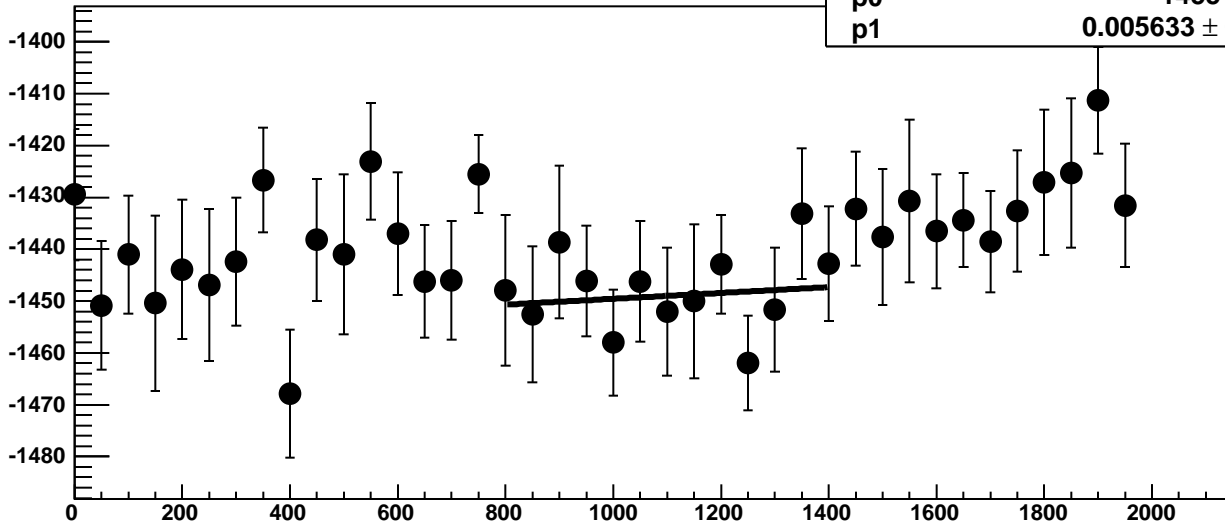
Chip 6, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC



Chip 6, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC

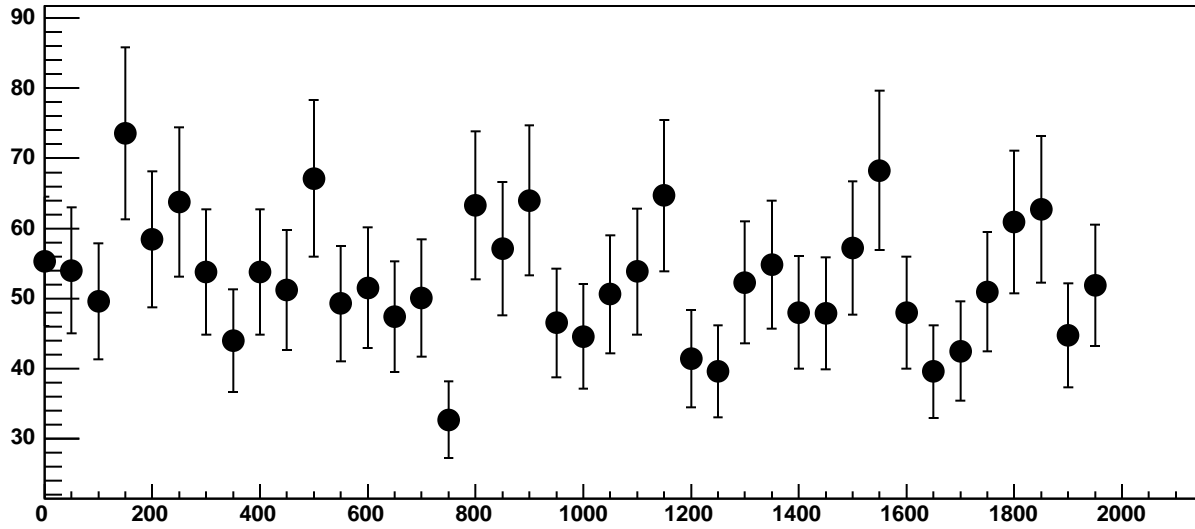


Chip 6, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC

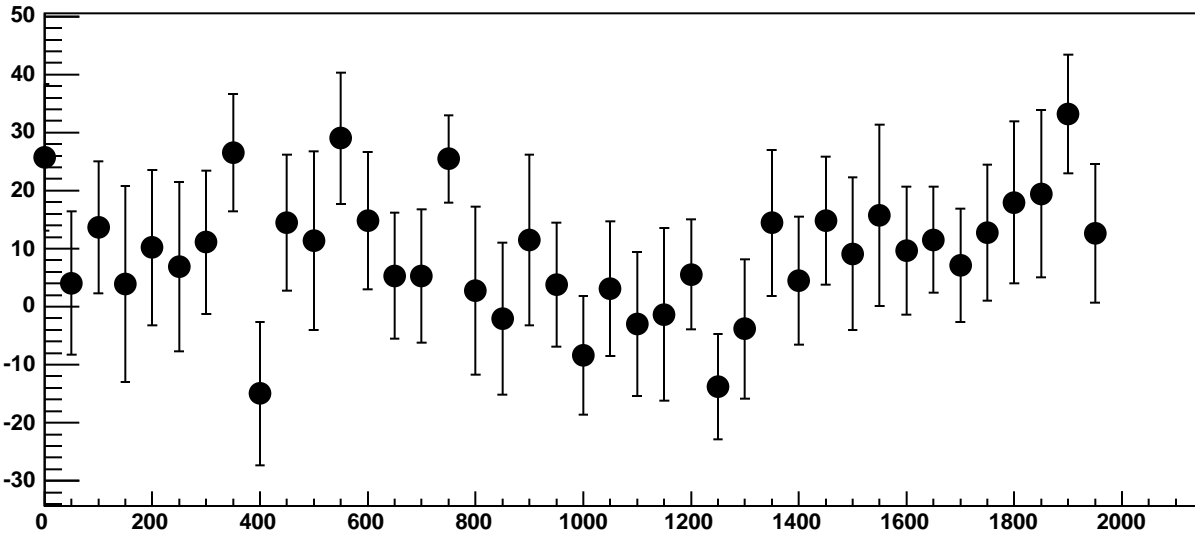


$\chi^2 / \text{ndf}$  5.843 / 11  
p0  $-1455 \pm 20.57$   
p1  $0.005633 \pm 0.01811$

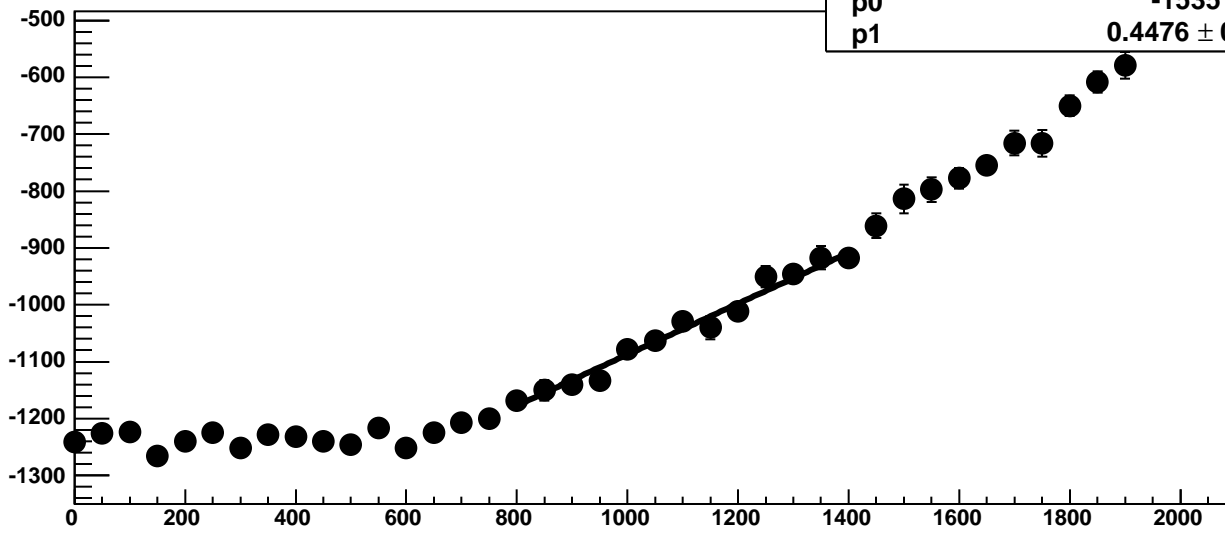
Chip 6, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC



Chip 6, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC

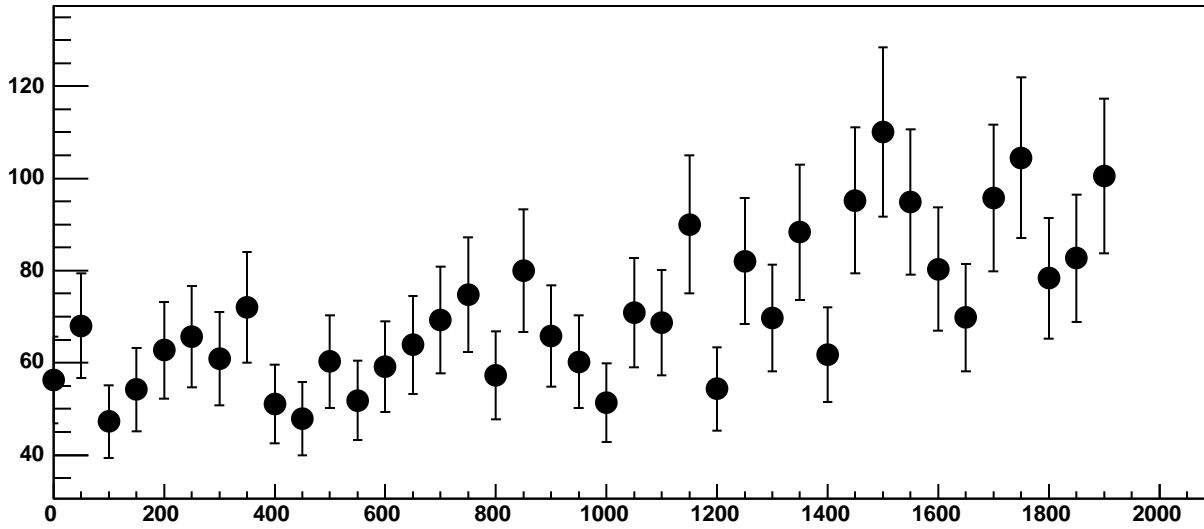


Chip 6, Channel 15, Enable 5, Hold=30, ADC Mean vs DAC

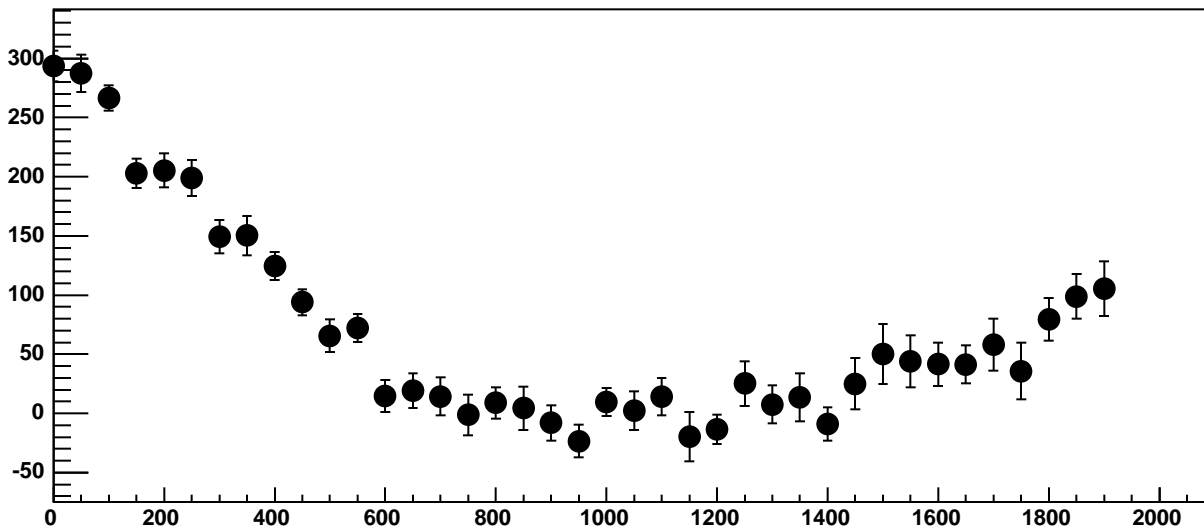


$\chi^2 / \text{ndf}$  10.15 / 11  
p0  $-1535 \pm 24.85$   
p1  $0.4476 \pm 0.02262$

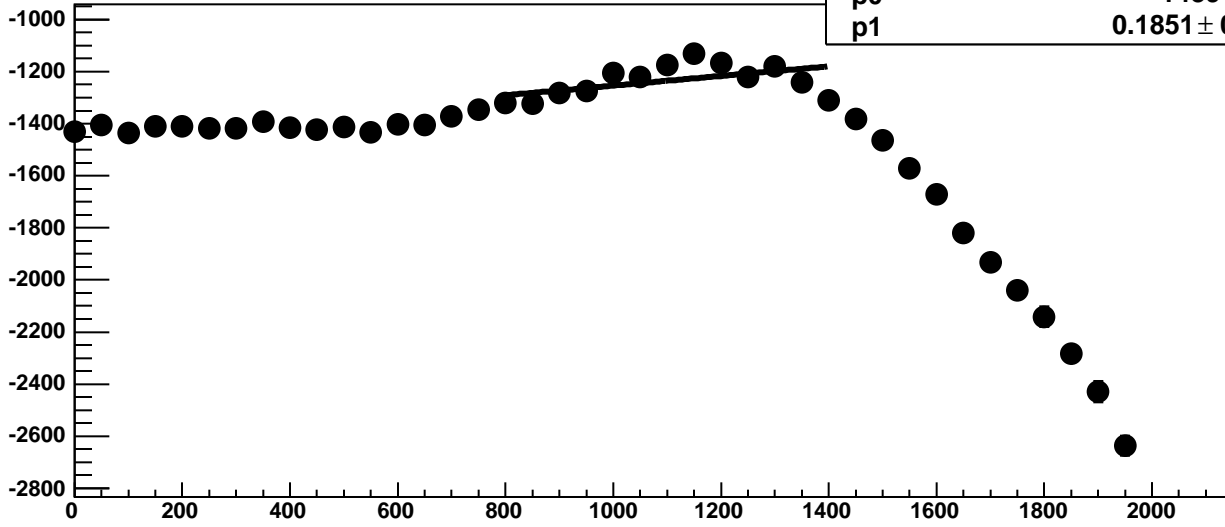
Chip 6, Channel 15, Enable 5, Hold=30, ADC Noise vs DAC



Chip 6, Channel 15, Enable 5, Hold=30, ADC Residuals vs DAC

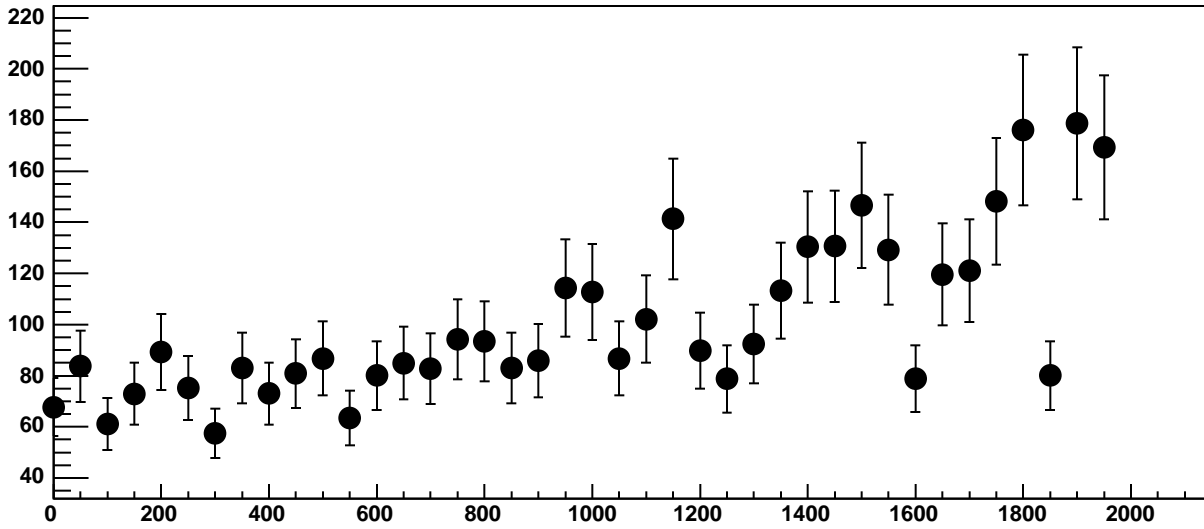


Chip 6, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC

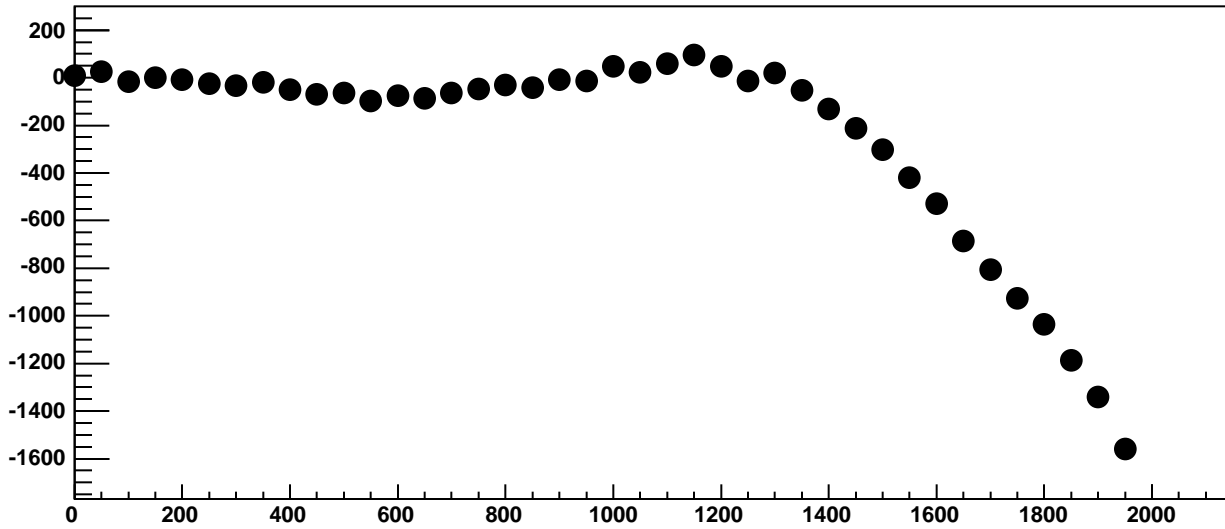


$\chi^2 / \text{ndf}$  57.43 / 11  
p0  $-1439 \pm 36.74$   
p1  $0.1851 \pm 0.03345$

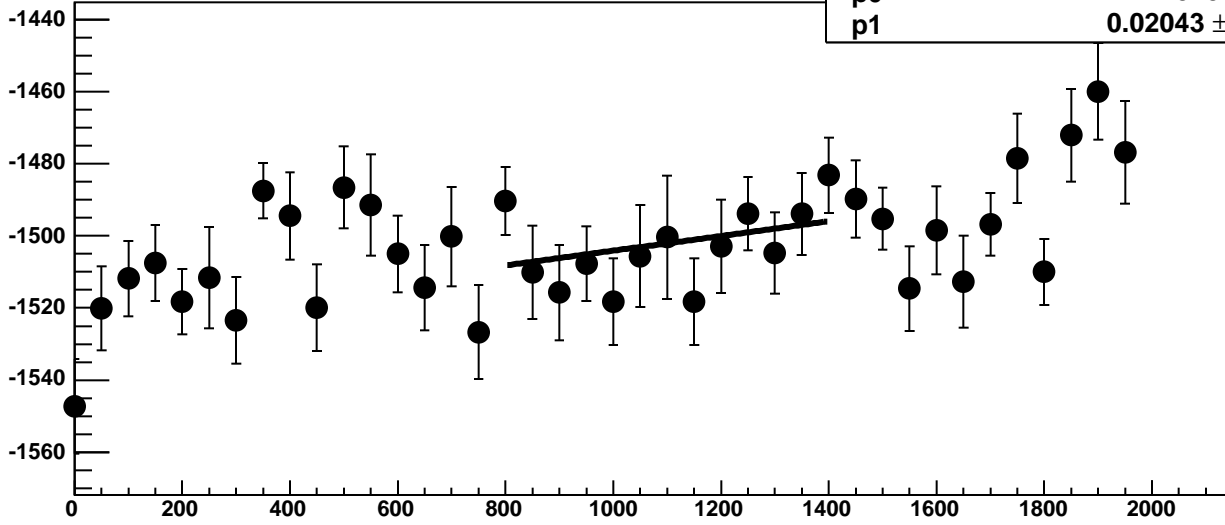
Chip 6, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC



Chip 6, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC

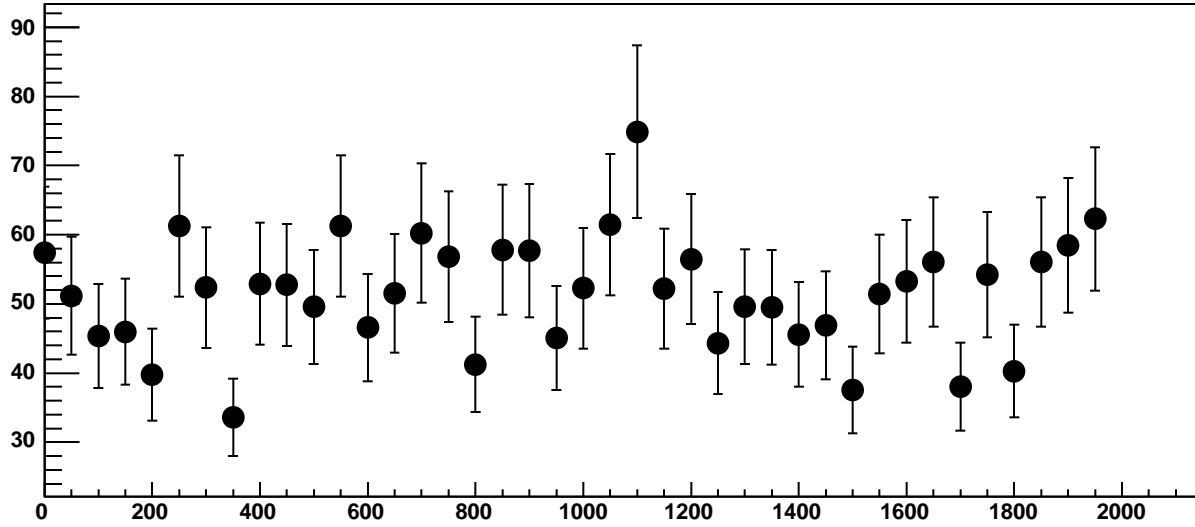


Chip 6, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC

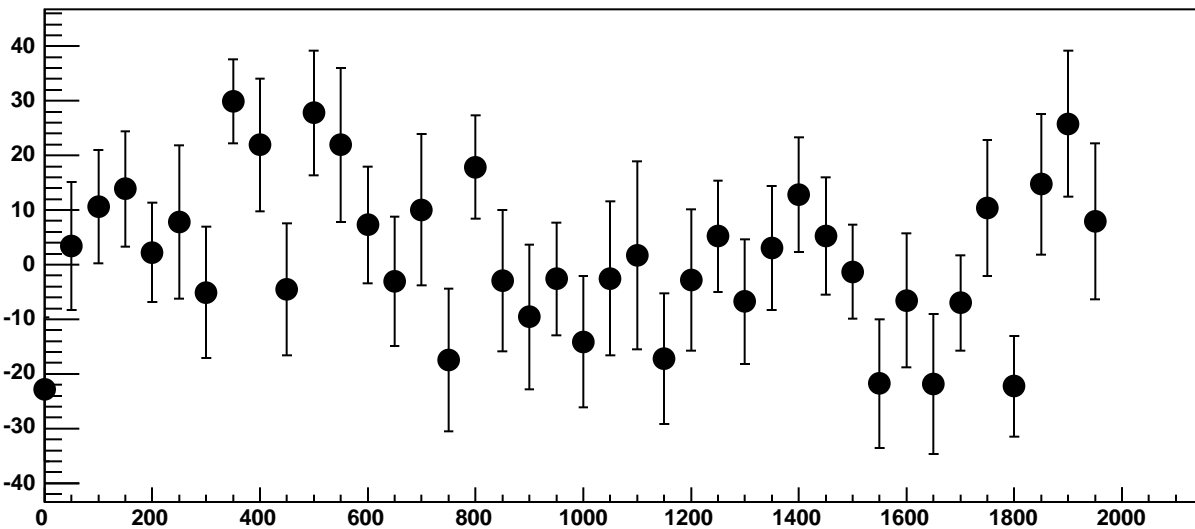


$\chi^2 / \text{ndf}$  9.936 / 11  
p0  $-1525 \pm 18.16$   
p1  $0.02043 \pm 0.0162$

Chip 6, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC

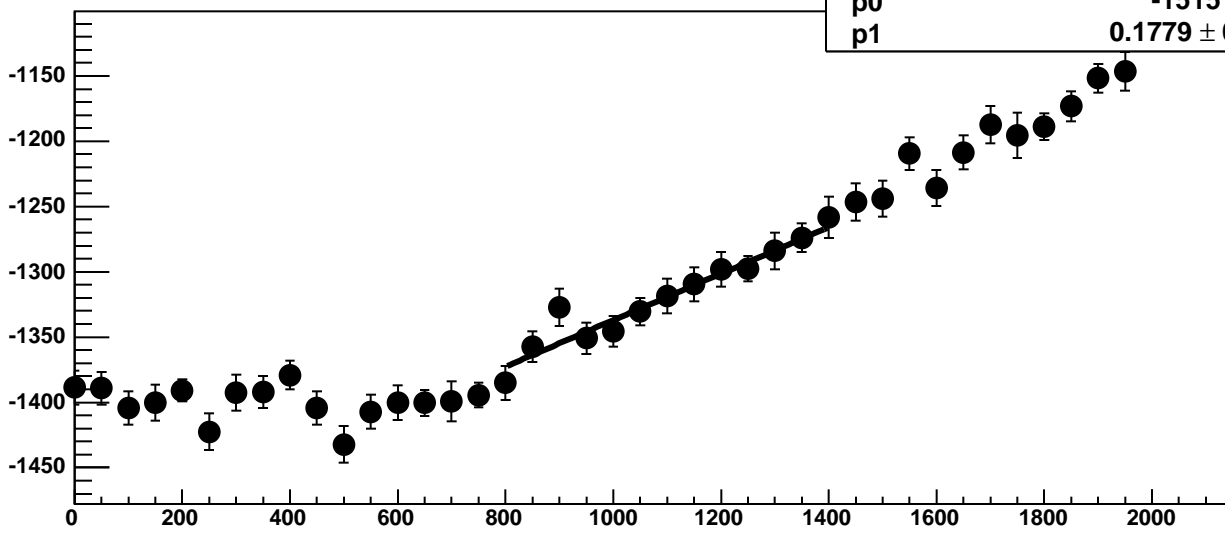


Chip 6, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC

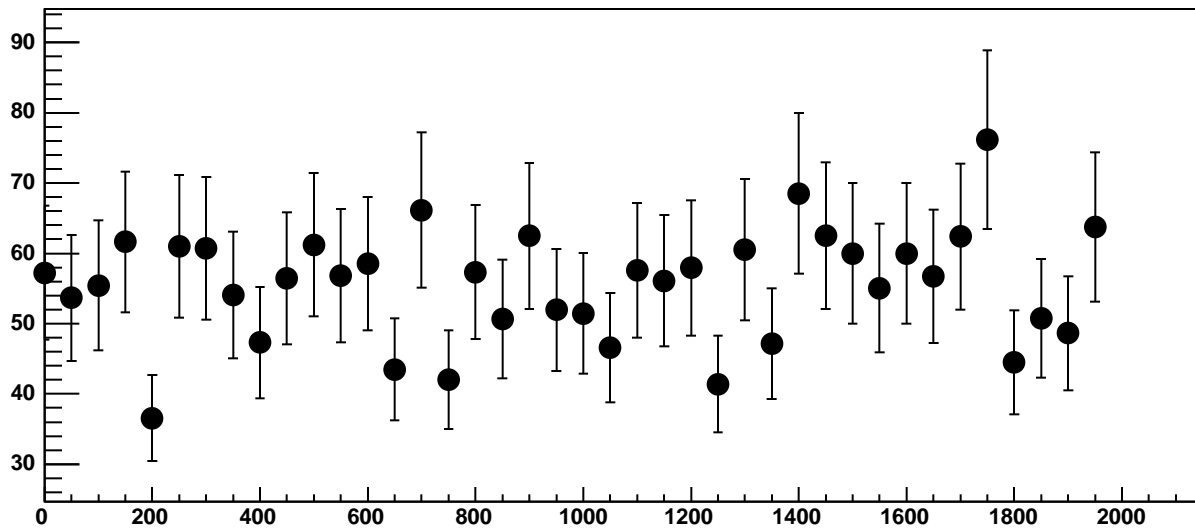




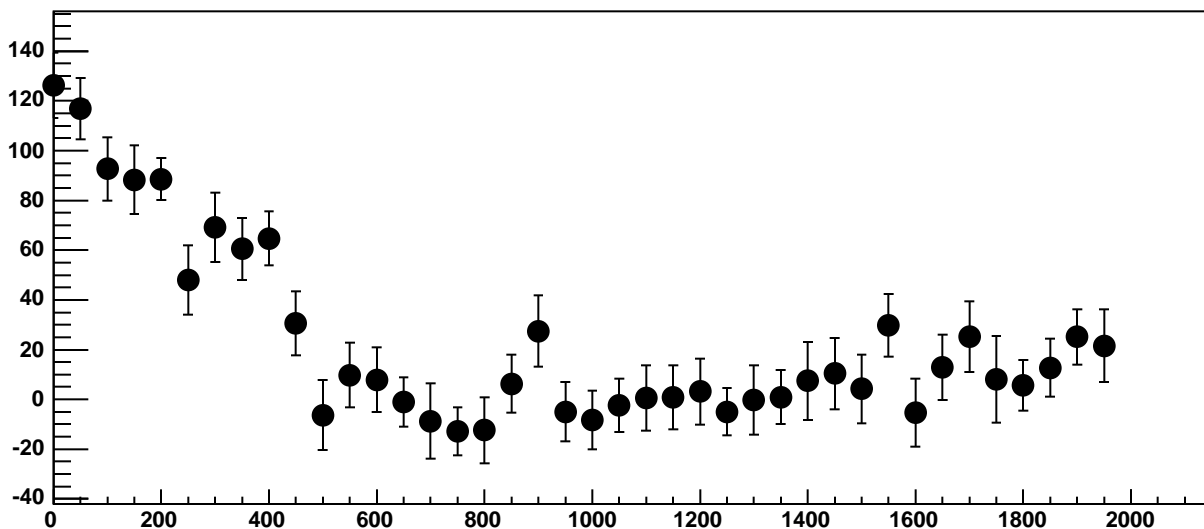
Chip 6, Channel 16, Enable 2, Hold=30, ADC Mean vs DAC



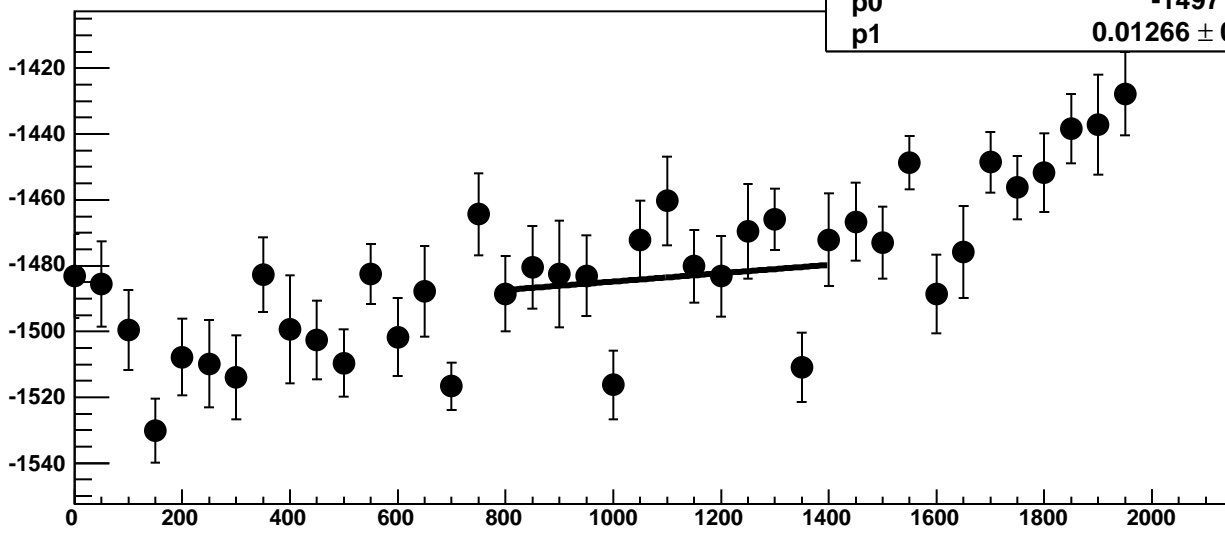
Chip 6, Channel 16, Enable 2, Hold=30, ADC Noise vs DAC



Chip 6, Channel 16, Enable 2, Hold=30, ADC Residuals vs DAC

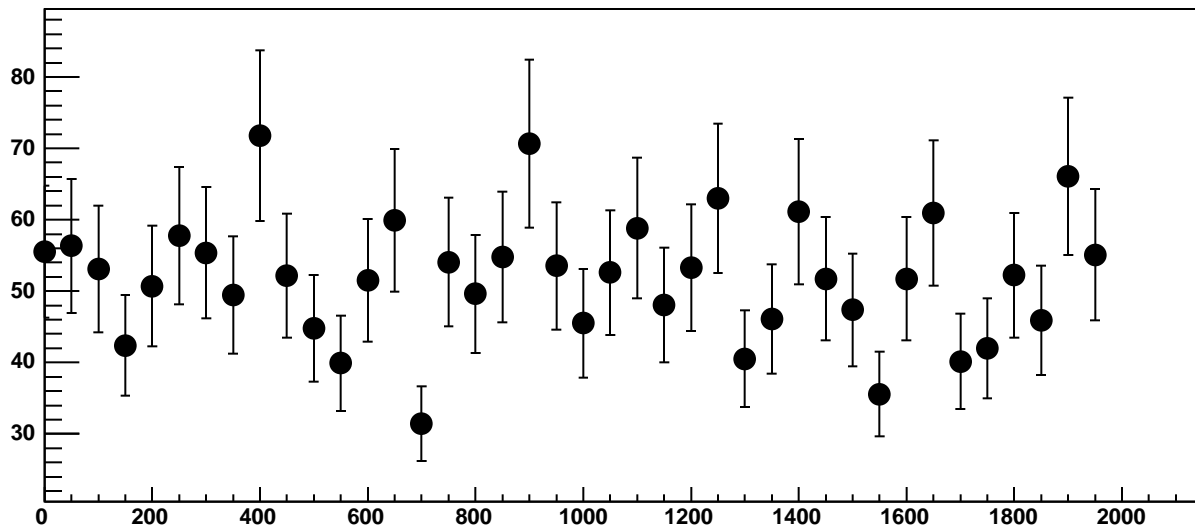


Chip 6, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC

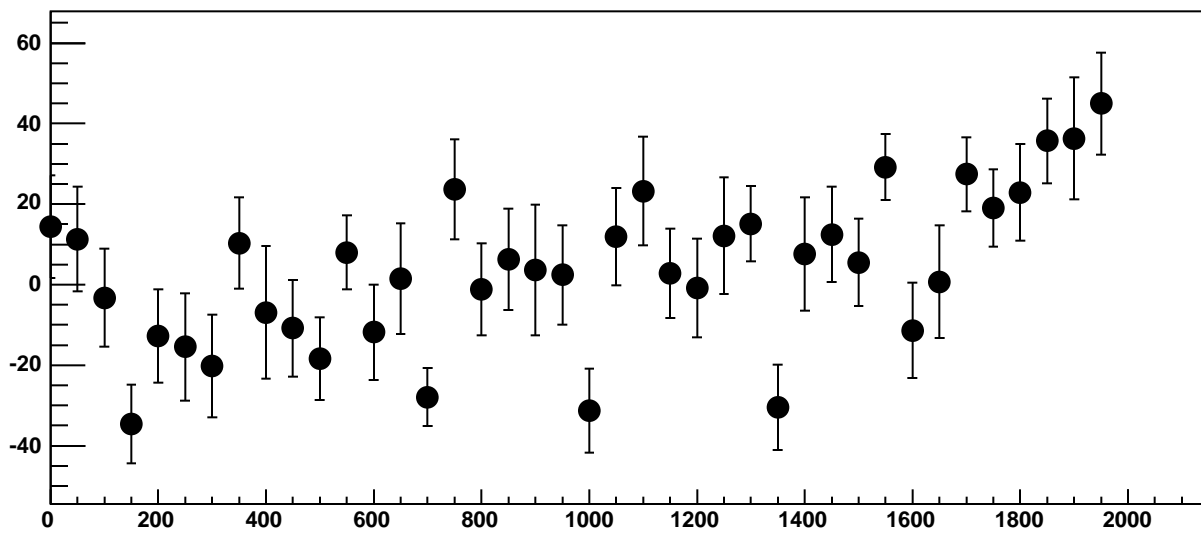


$\chi^2 / \text{ndf}$  25.34 / 11  
p0  $-1497 \pm 20.02$   
p1  $0.01266 \pm 0.01778$

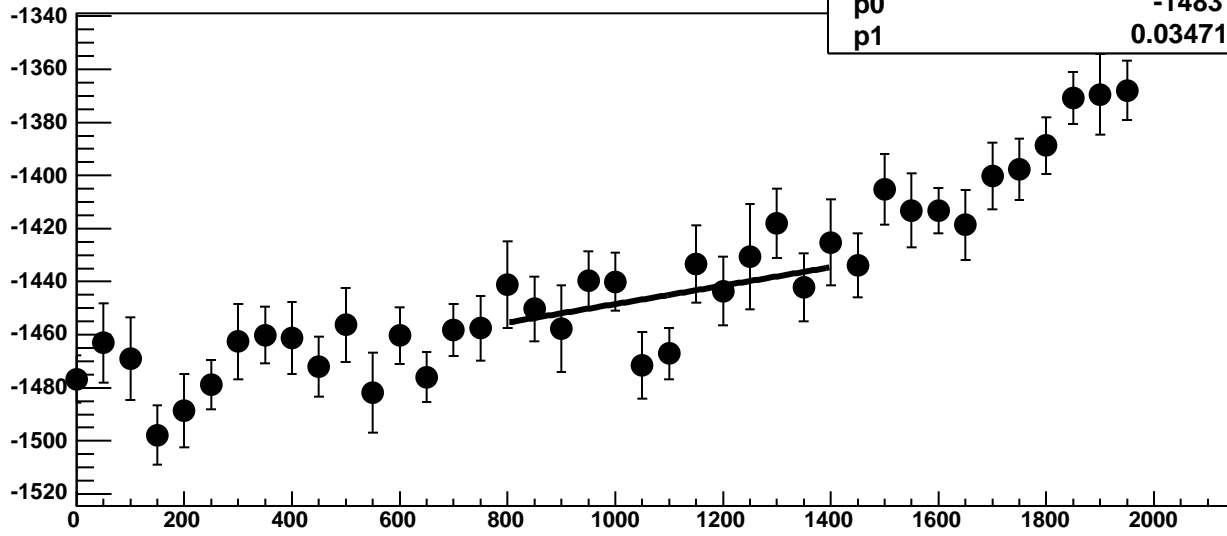
Chip 6, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC



Chip 6, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC

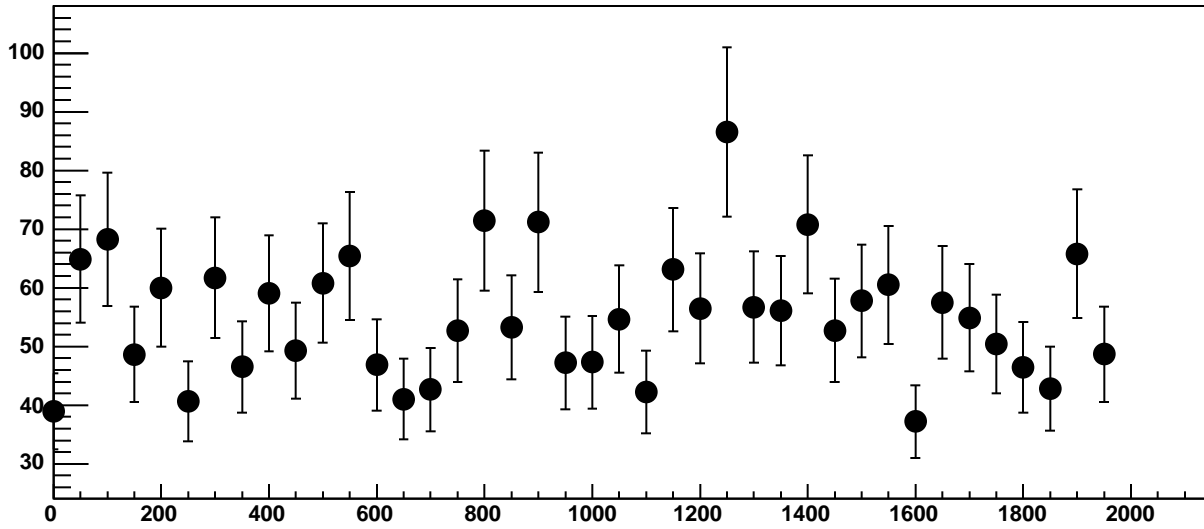


Chip 6, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC

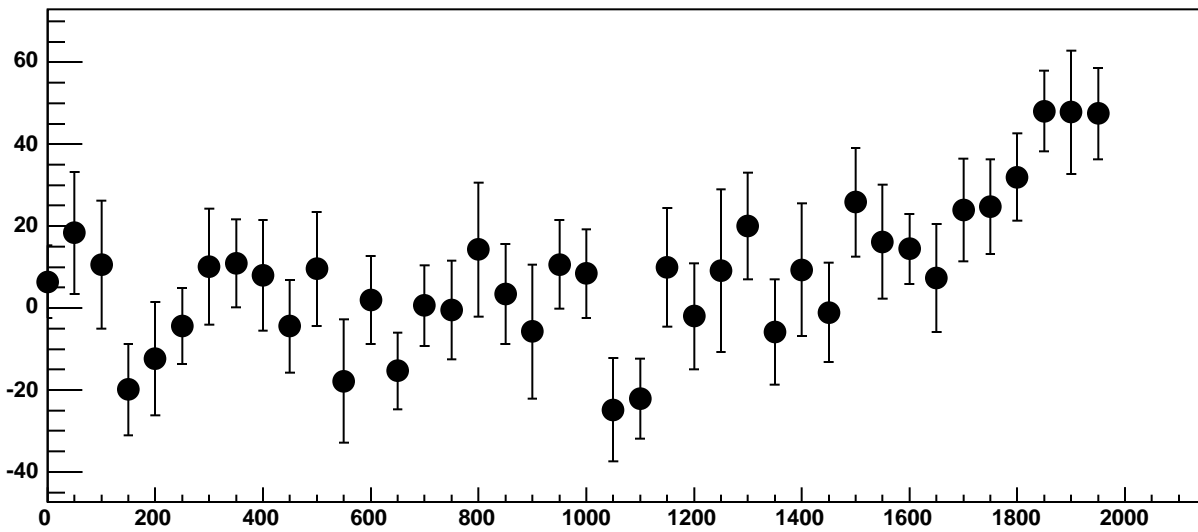


$\chi^2 / \text{ndf}$  15.26 / 11  
p0  $-1483 \pm 23.12$   
p1  $0.03471 \pm 0.021$

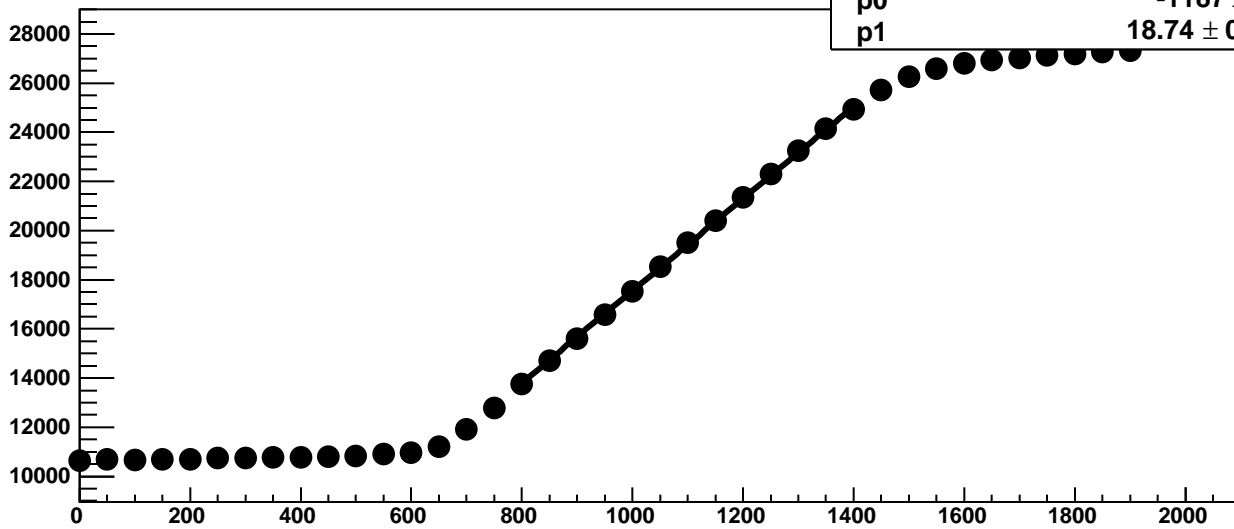
Chip 6, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC



Chip 6, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC

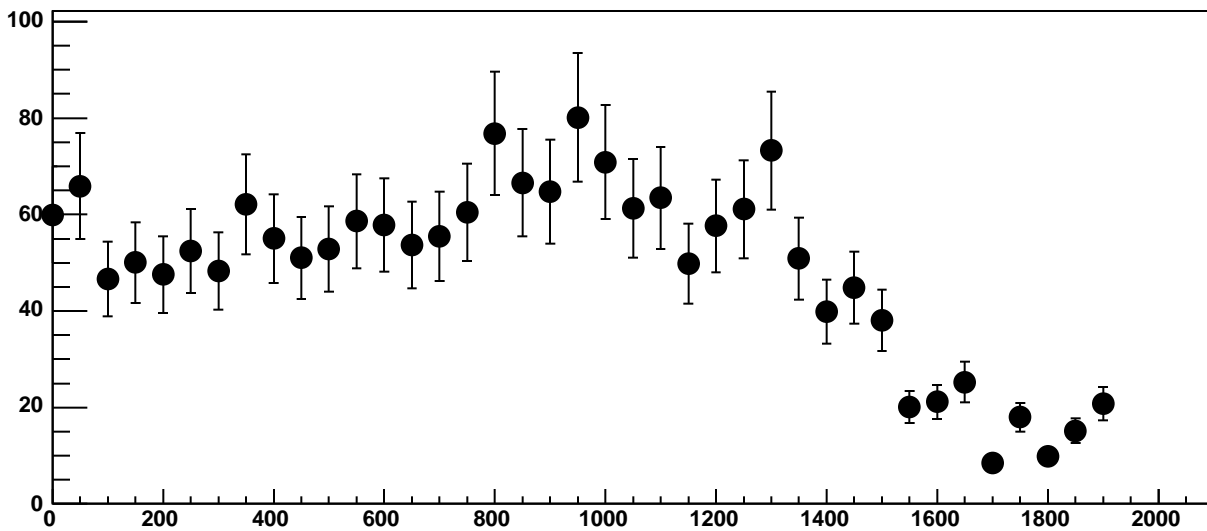


Chip 6, Channel 16, Enable 5!, Hold=30, ADC Mean vs DAC

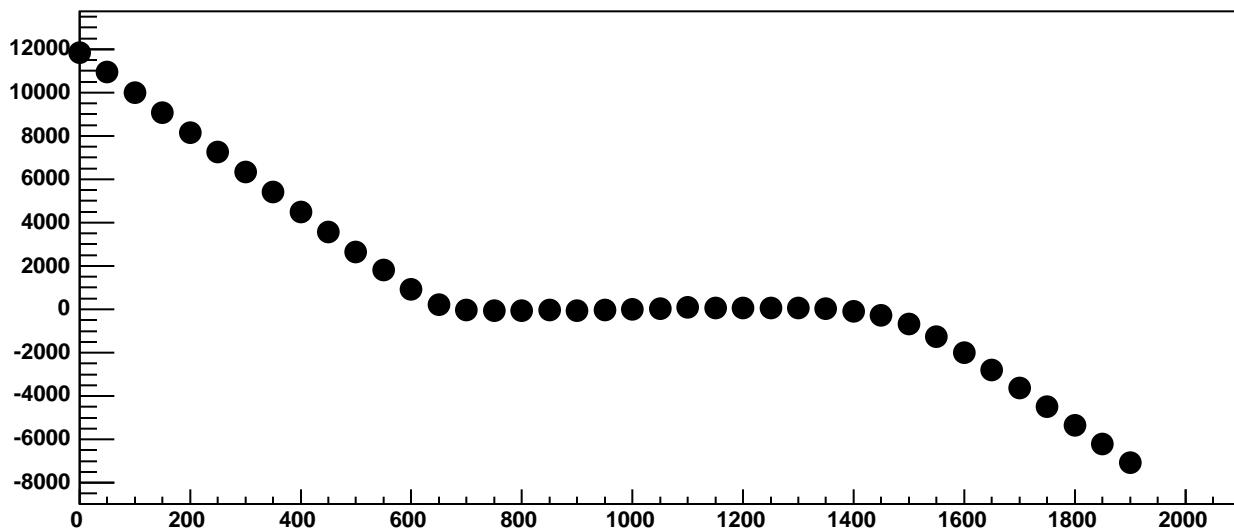


$\chi^2 / \text{ndf}$  287.7 / 11  
p0  $-1187 \pm 23.34$   
p1  $18.74 \pm 0.01994$

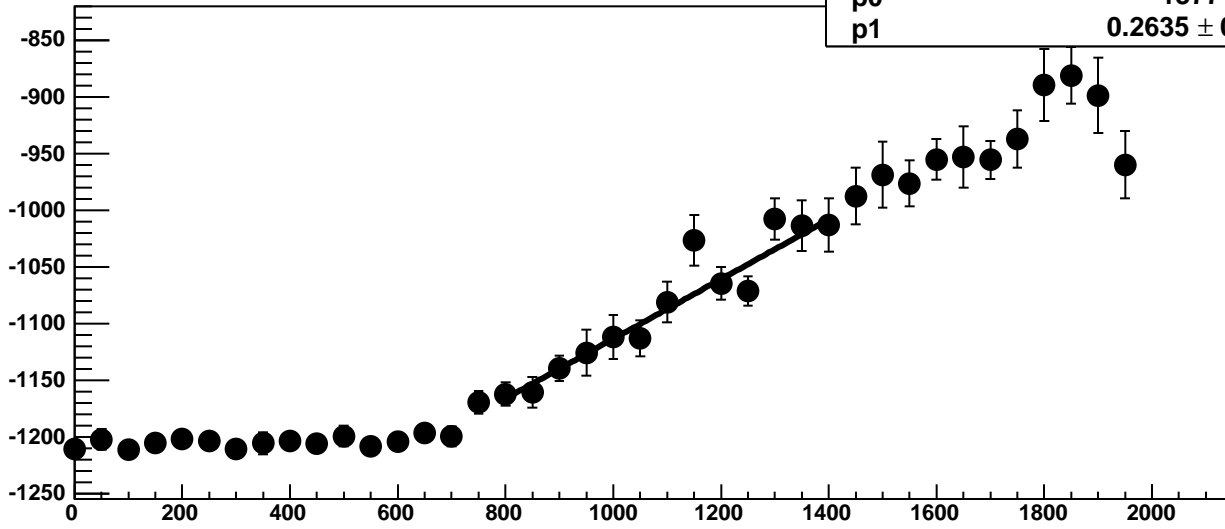
Chip 6, Channel 16, Enable 5!, Hold=30, ADC Noise vs DAC



Chip 6, Channel 16, Enable 5!, Hold=30, ADC Residuals vs DAC

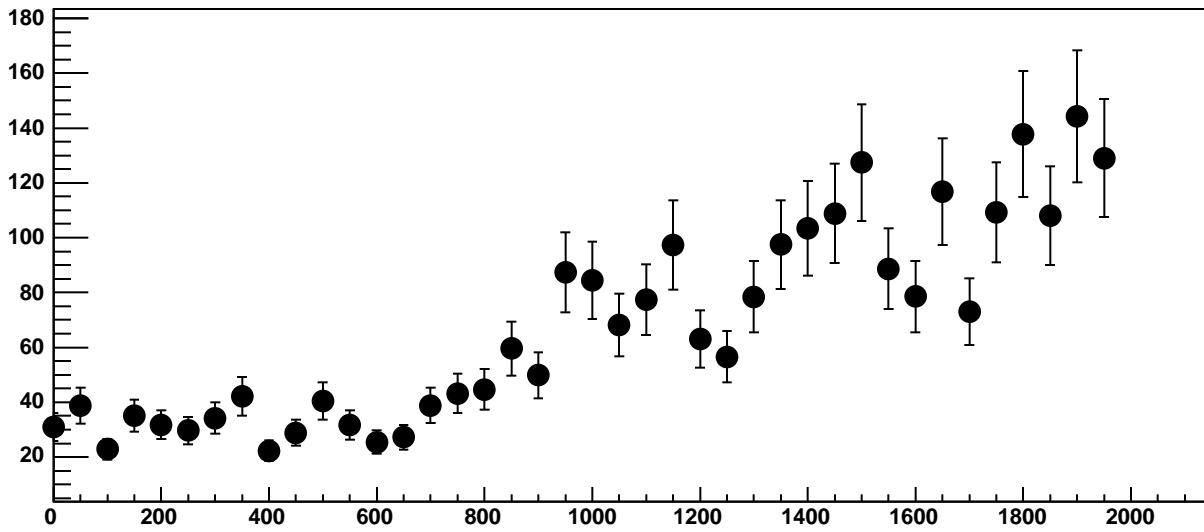


Chip 6, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC

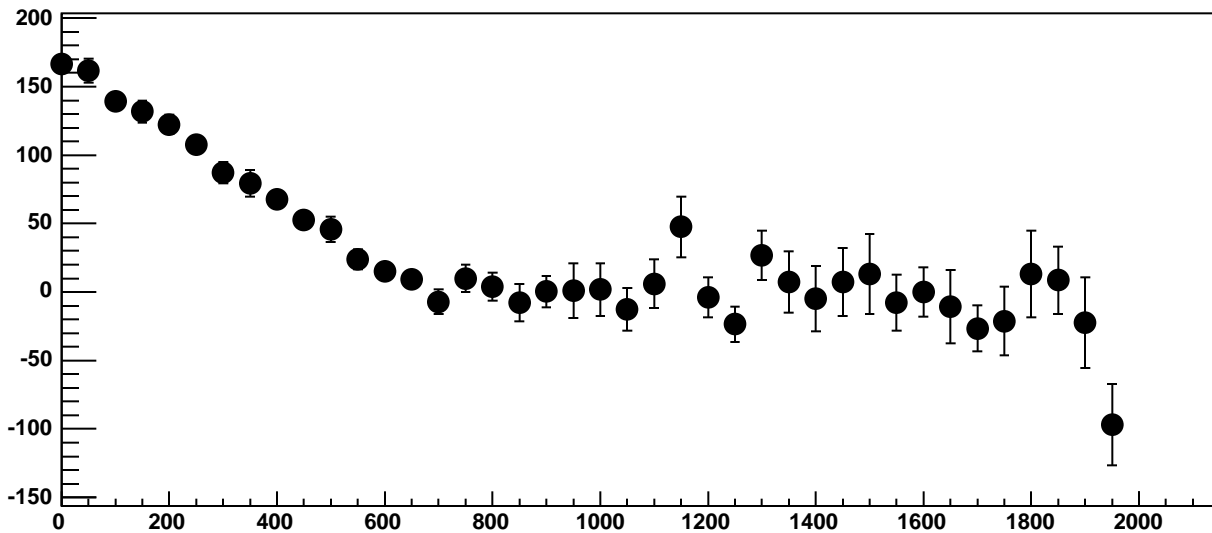


$\chi^2 / \text{ndf}$  11.53 / 11  
p0  $-1377 \pm 23.76$   
p1  $0.2635 \pm 0.02254$

Chip 6, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC

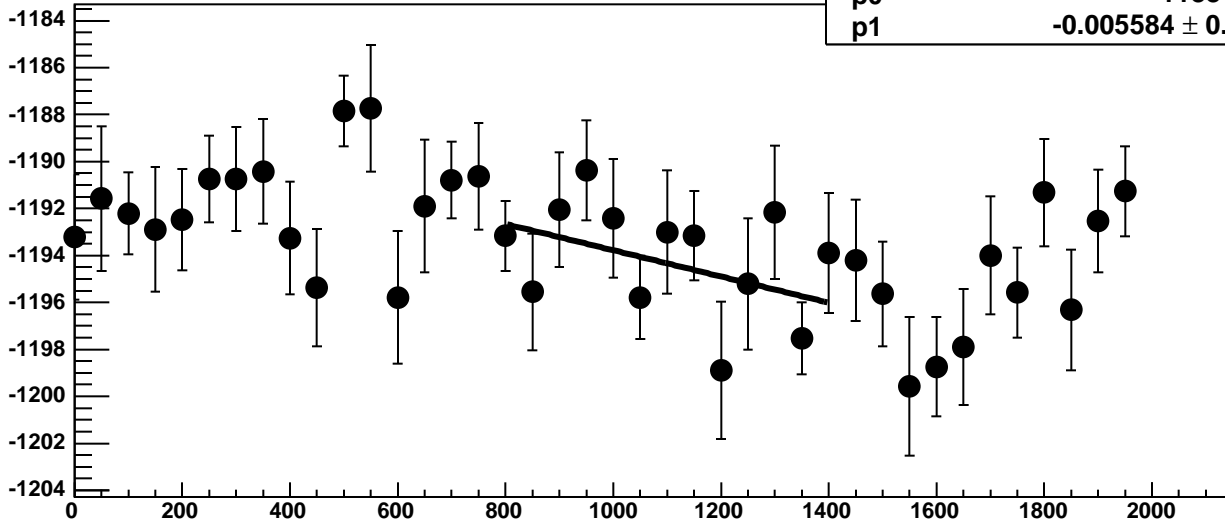


Chip 6, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC

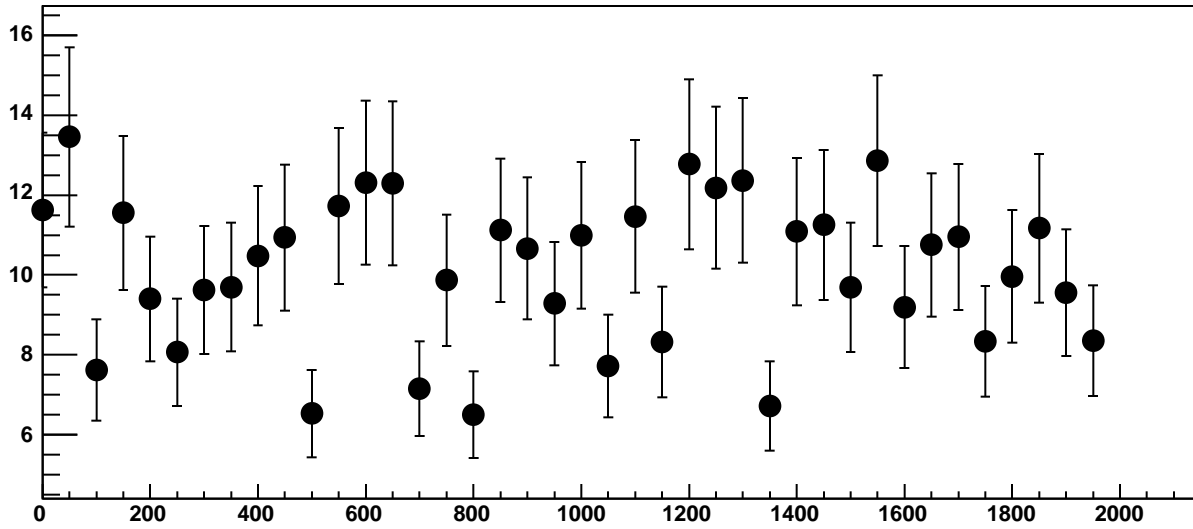


Chip 6, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC

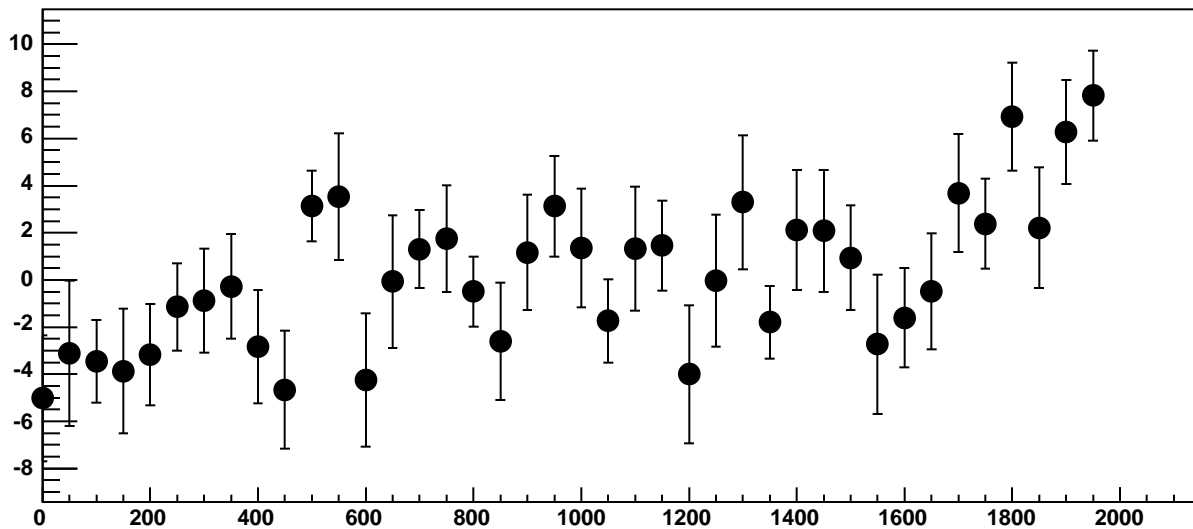
$\chi^2 / \text{ndf}$  10.95 / 11  
p0  $-1188 \pm 3.264$   
p1  $-0.005584 \pm 0.002967$



Chip 6, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC

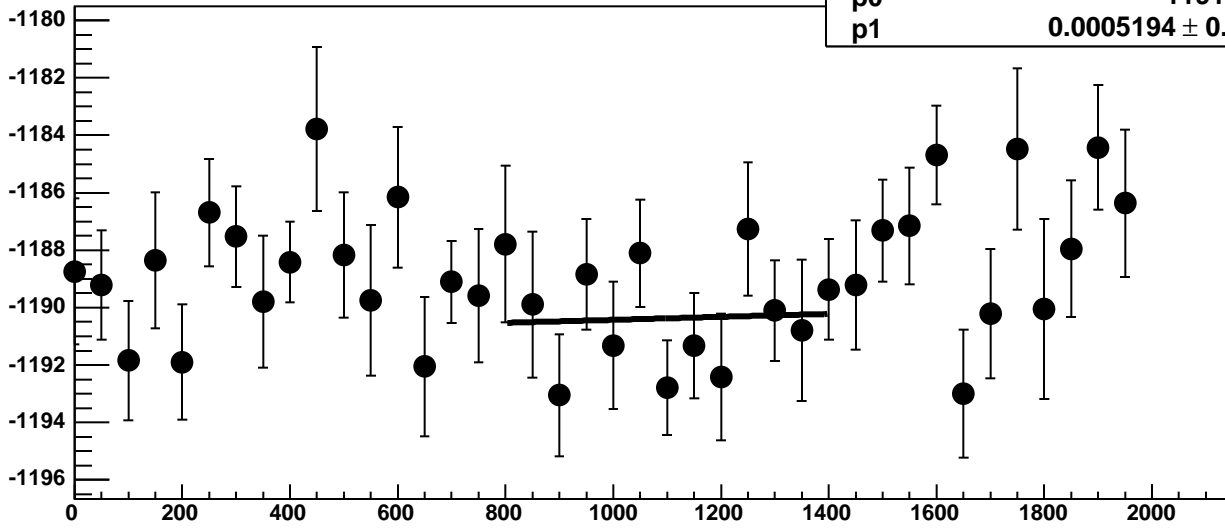


Chip 6, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC

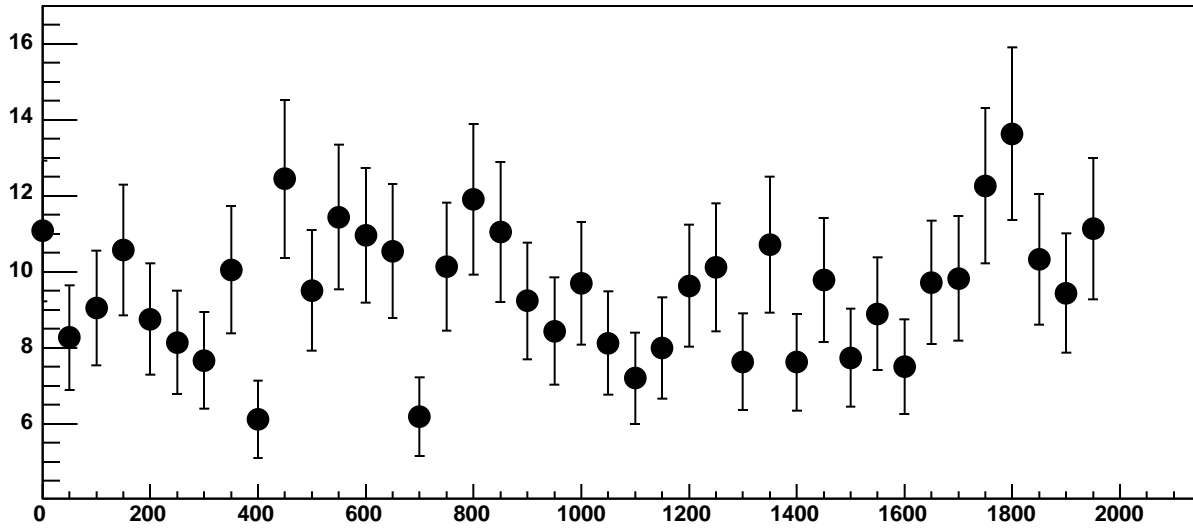


Chip 6, Channel 17, Enable 2!, Hold=30, ADC Mean vs DAC

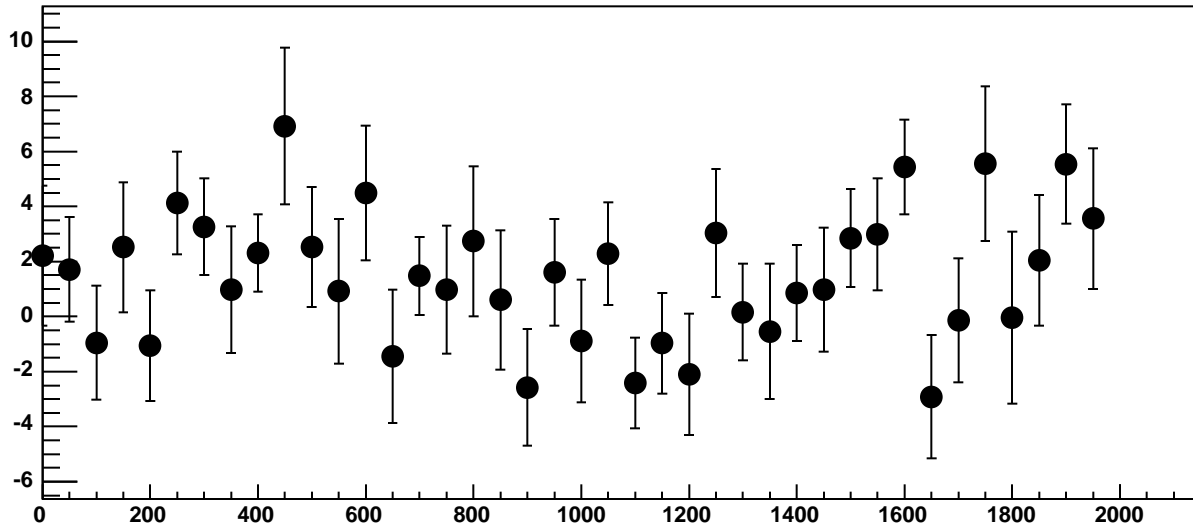
$\chi^2 / \text{ndf}$  10.23 / 11  
p0  $-1191 \pm 3.633$   
p1  $0.0005194 \pm 0.003199$



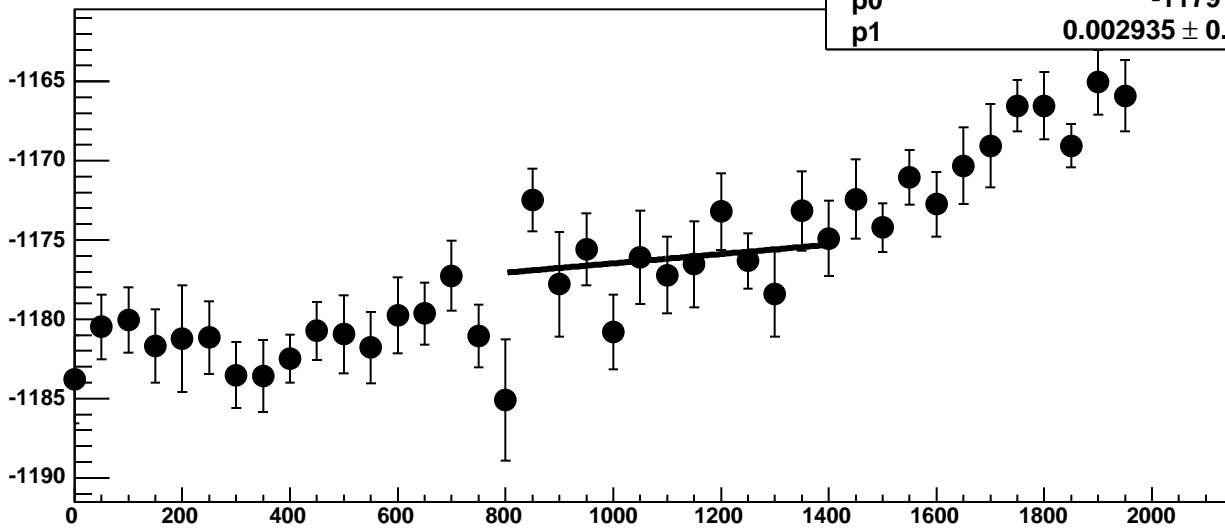
Chip 6, Channel 17, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 6, Channel 17, Enable 2!, Hold=30, ADC Residuals vs DAC

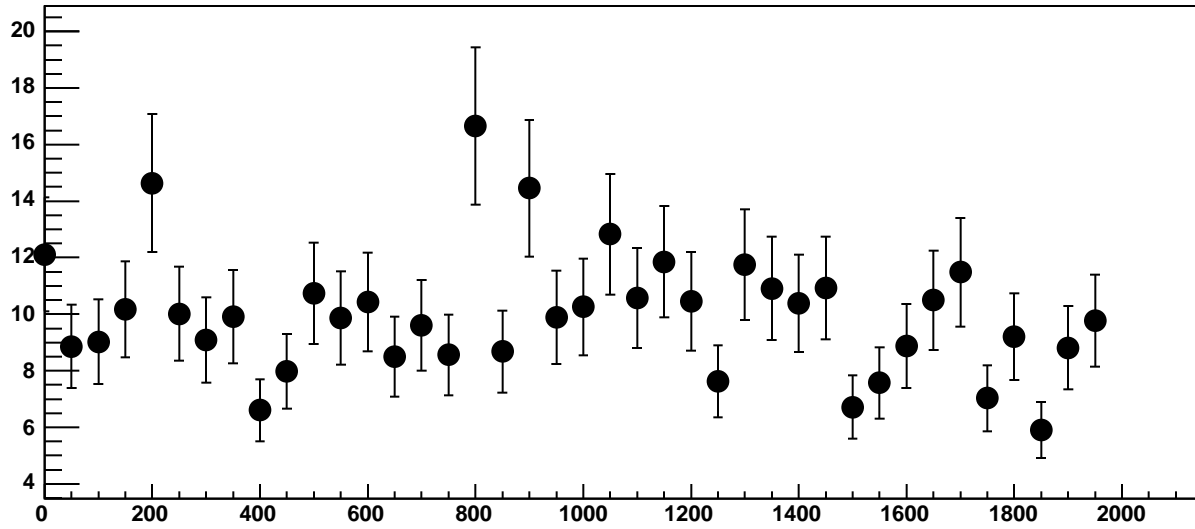


Chip 6, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC

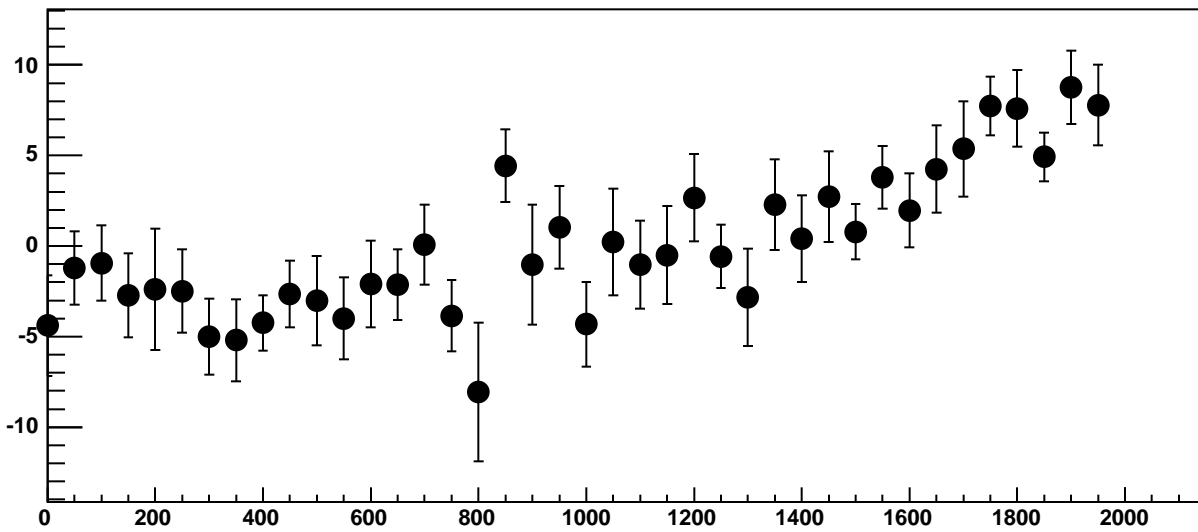


$\chi^2 / \text{ndf}$  16.61 / 11  
p0  $-1179 \pm 4.218$   
p1  $0.002935 \pm 0.003725$

Chip 6, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC

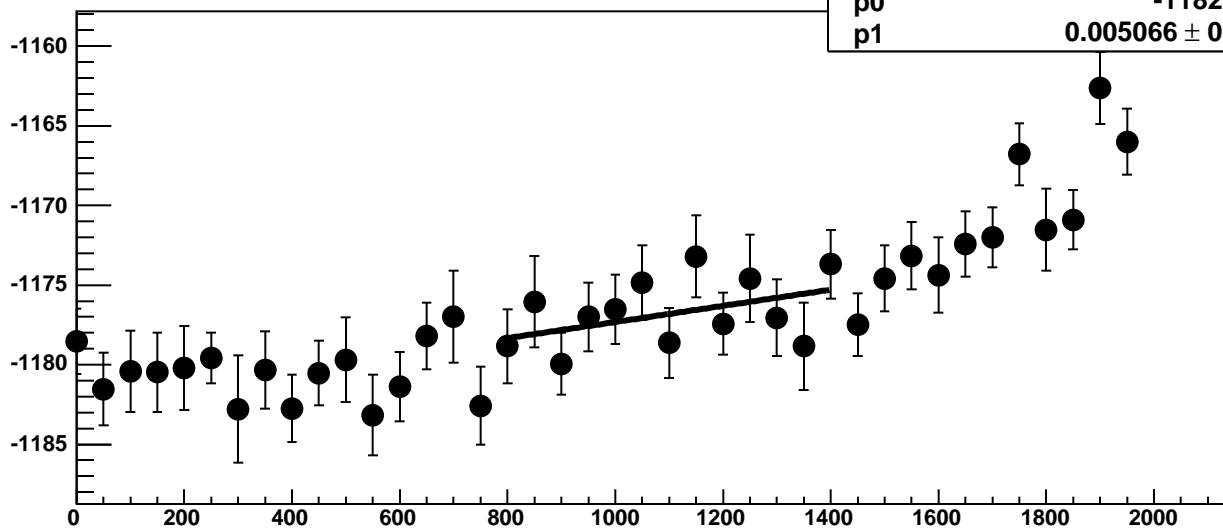


Chip 6, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC

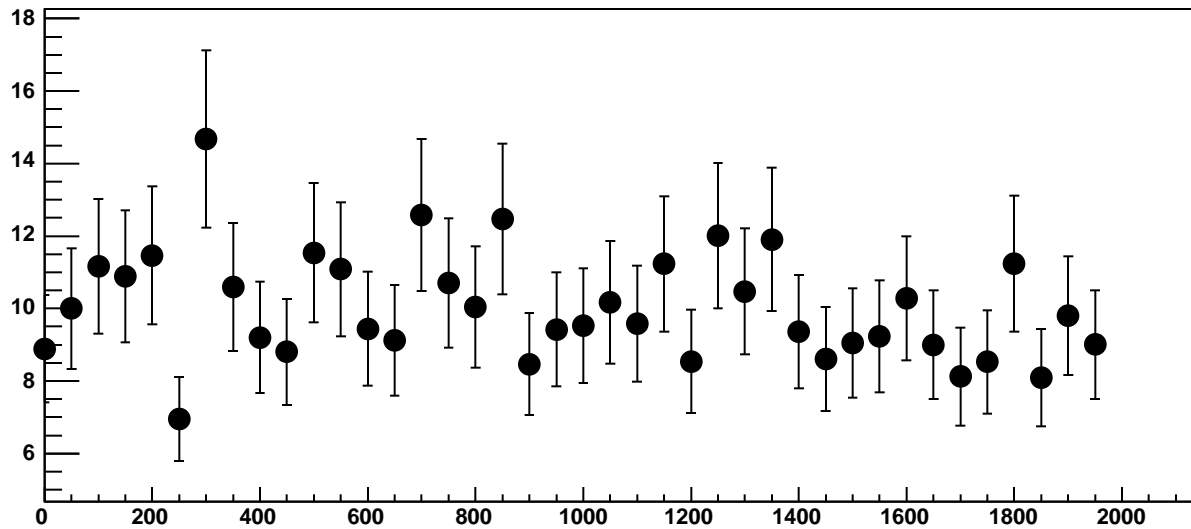




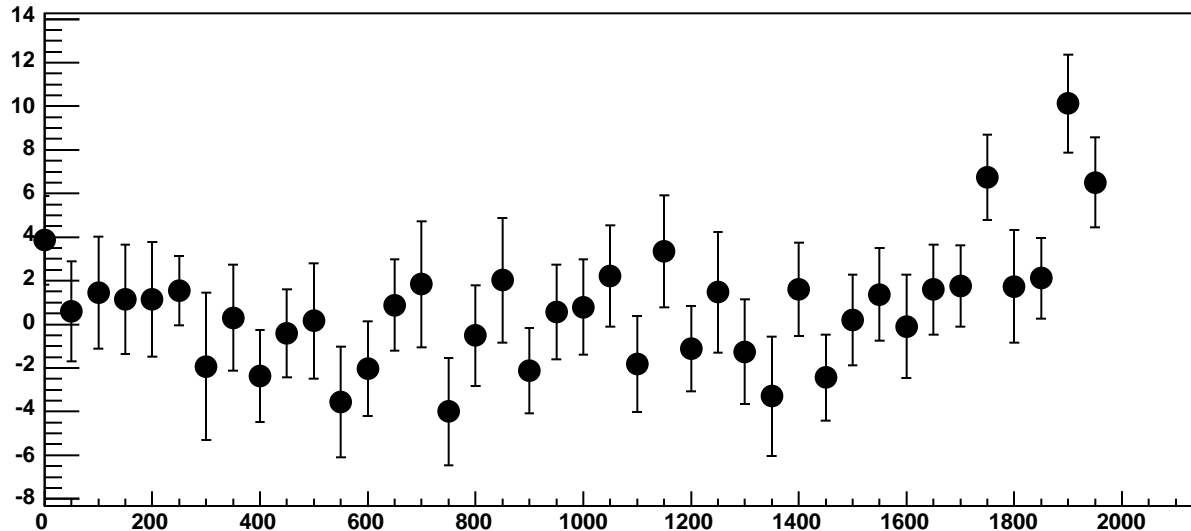
Chip 6, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC



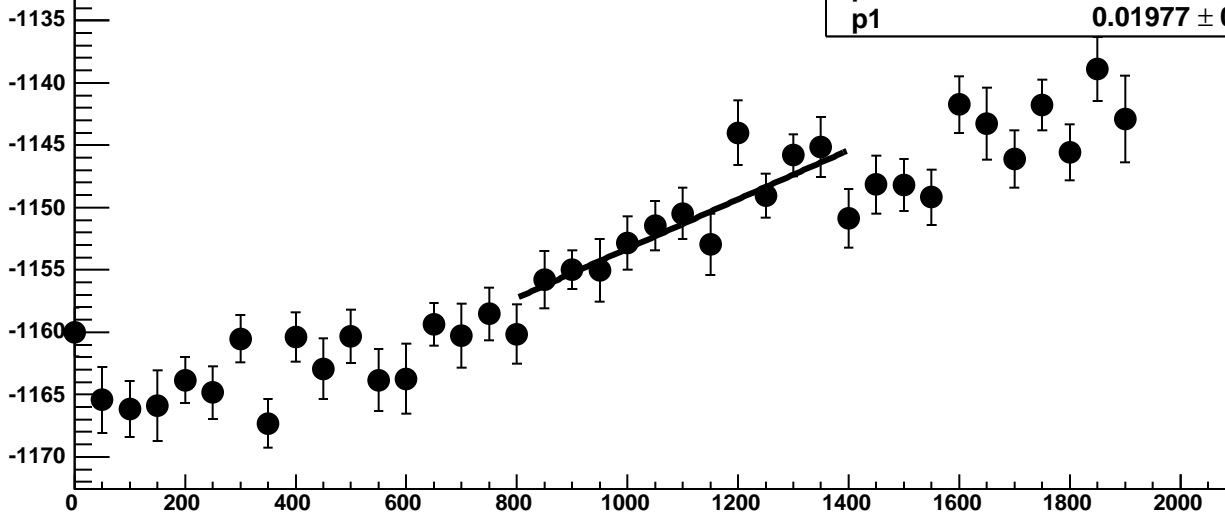
Chip 6, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC



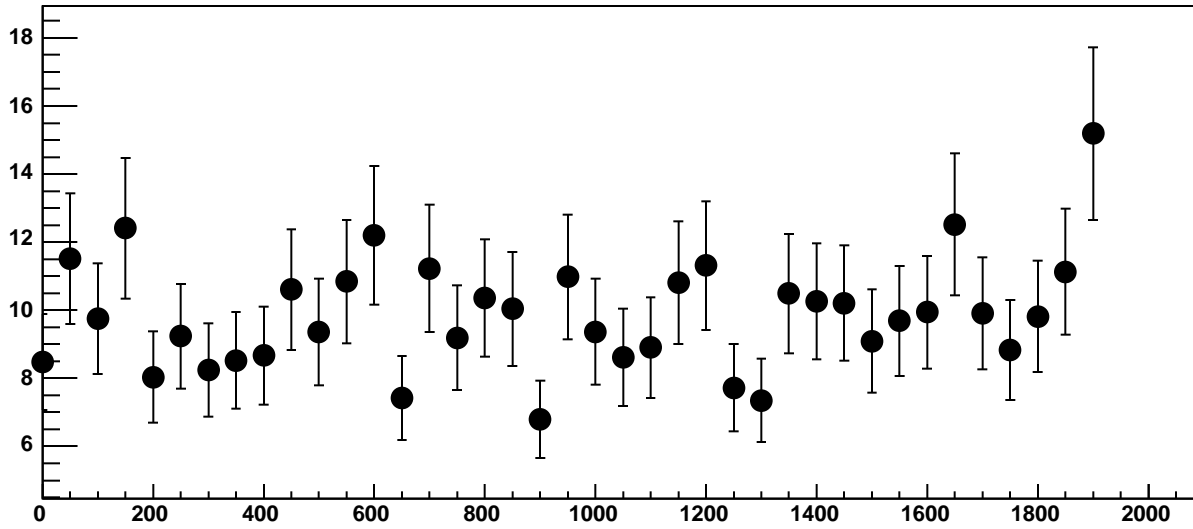
Chip 6, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC



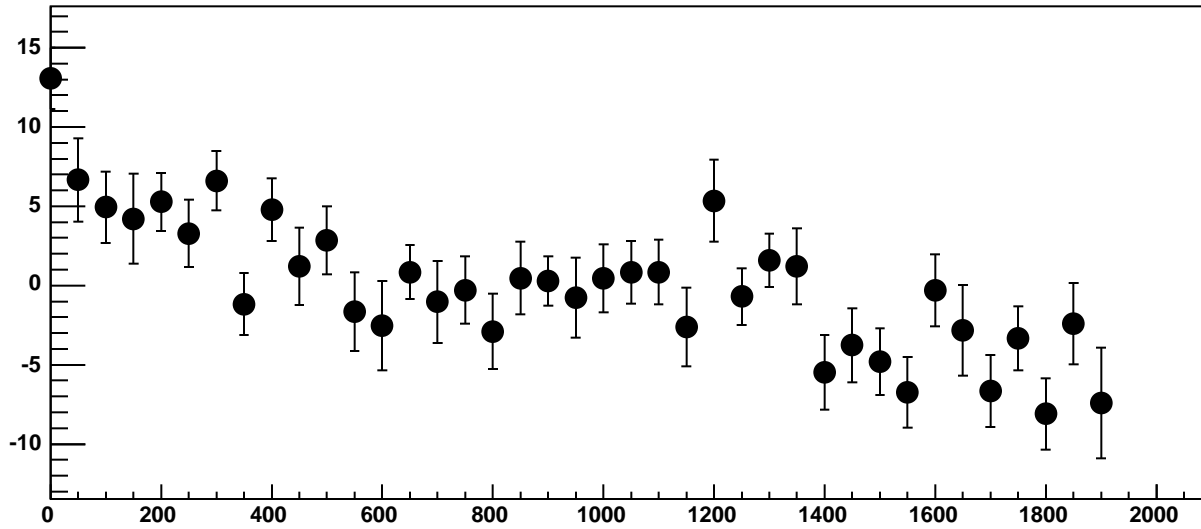
Chip 6, Channel 17, Enable 5, Hold=30, ADC Mean vs DAC



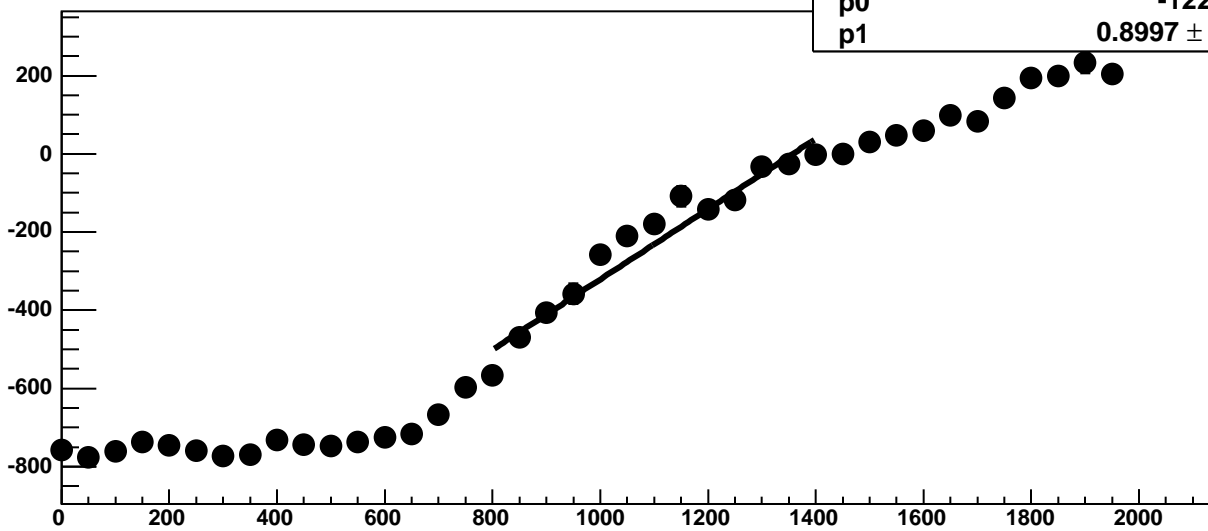
Chip 6, Channel 17, Enable 5, Hold=30, ADC Noise vs DAC



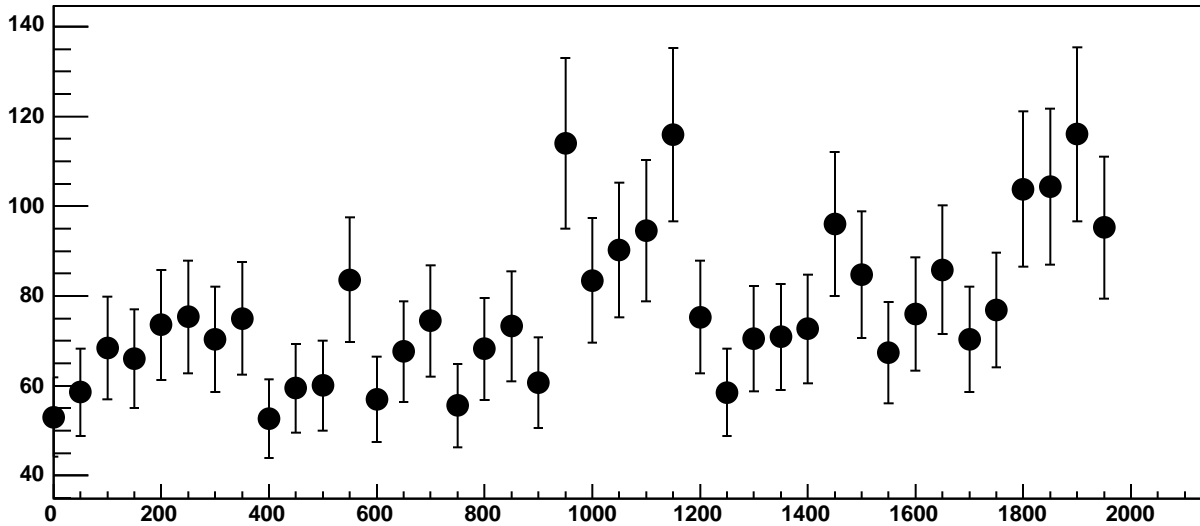
Chip 6, Channel 17, Enable 5, Hold=30, ADC Residuals vs DAC



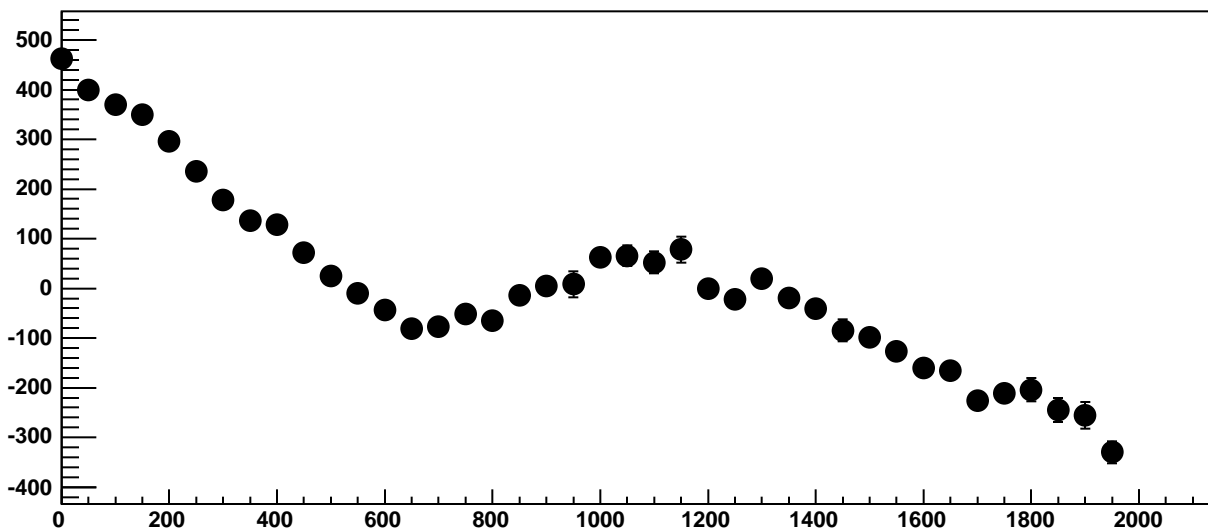
Chip 7, Channel 0, Enable 0, Hold=30, ADC Mean vs DAC



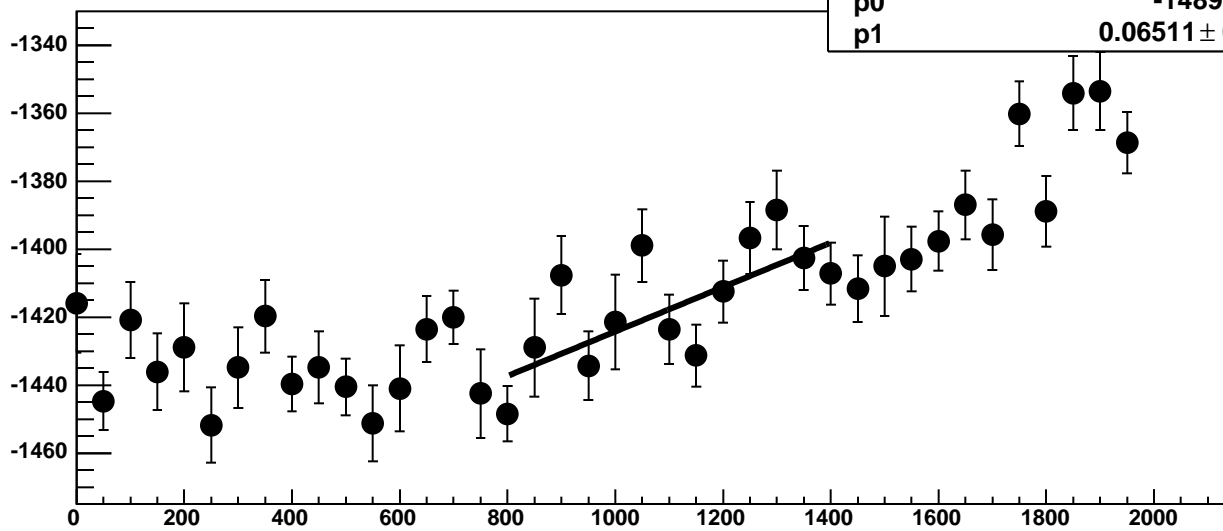
Chip 7, Channel 0, Enable 0, Hold=30, ADC Noise vs DAC



Chip 7, Channel 0, Enable 0, Hold=30, ADC Residuals vs DAC

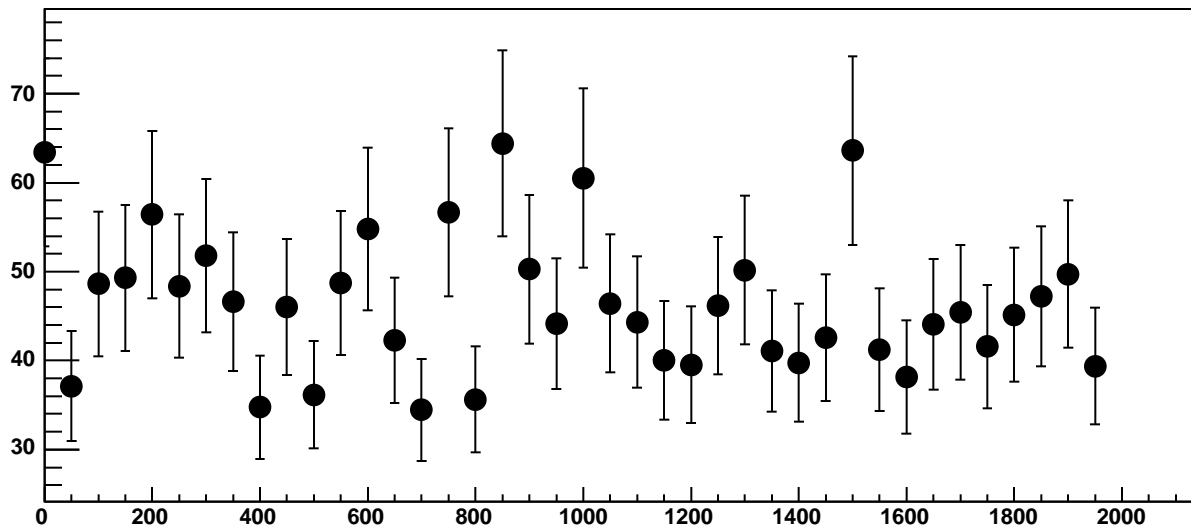


Chip 7, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC

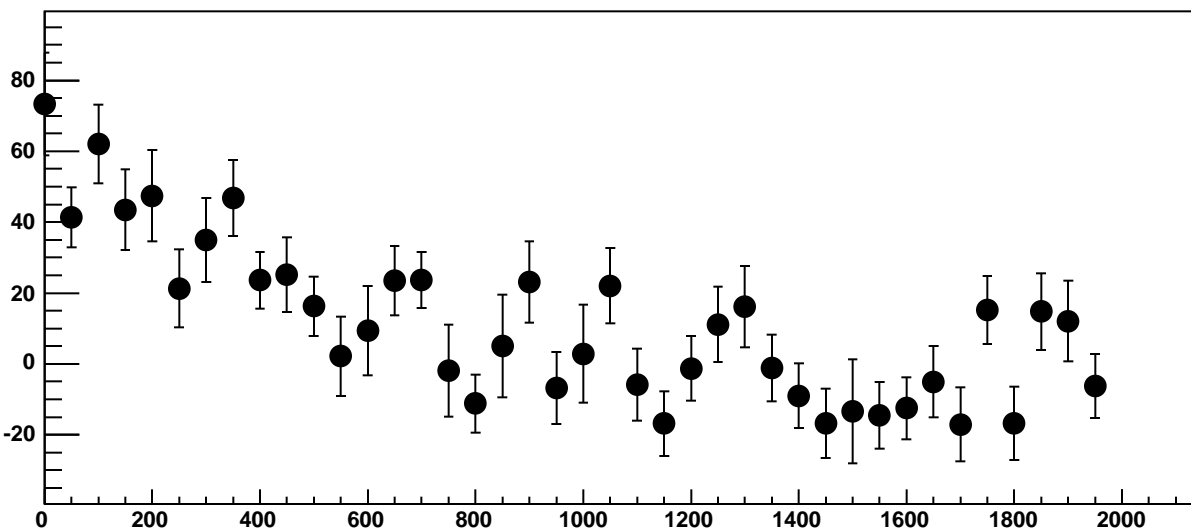


$\chi^2 / \text{ndf}$  18.58 / 11  
p0  $-1489 \pm 16.52$   
p1  $0.06511 \pm 0.01464$

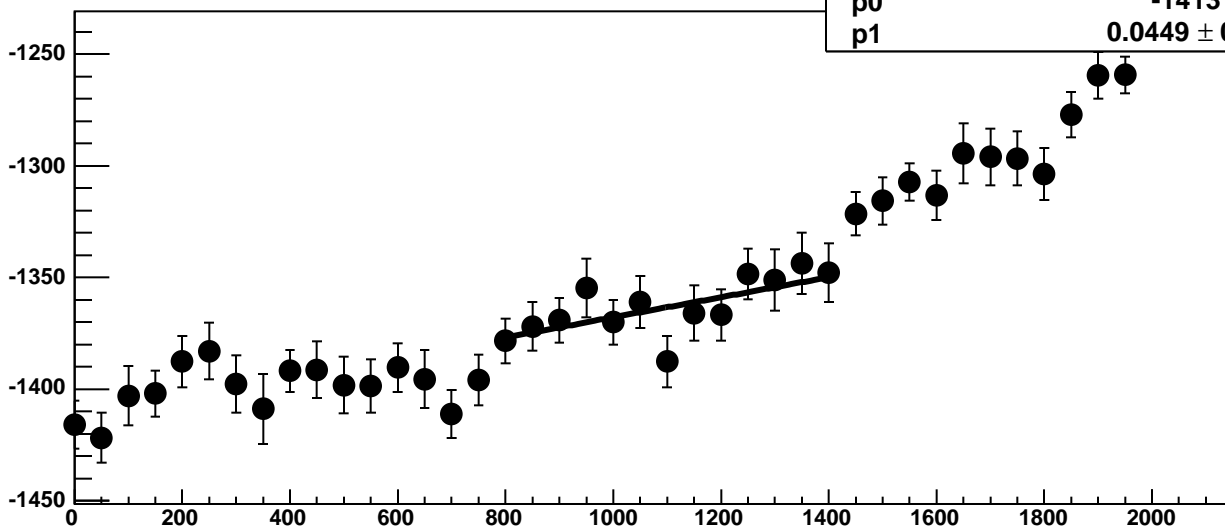
Chip 7, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC



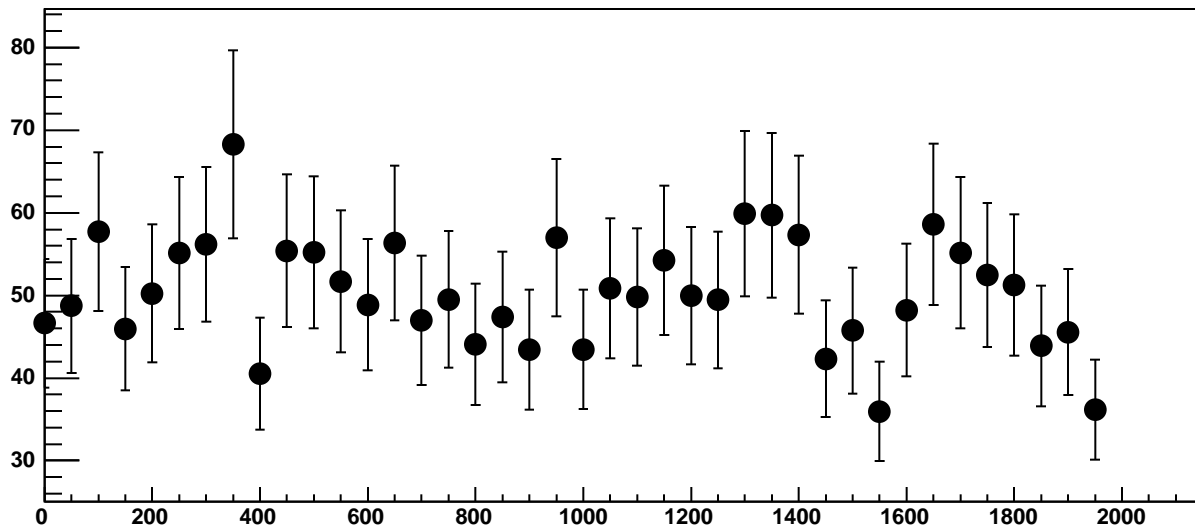
Chip 7, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC



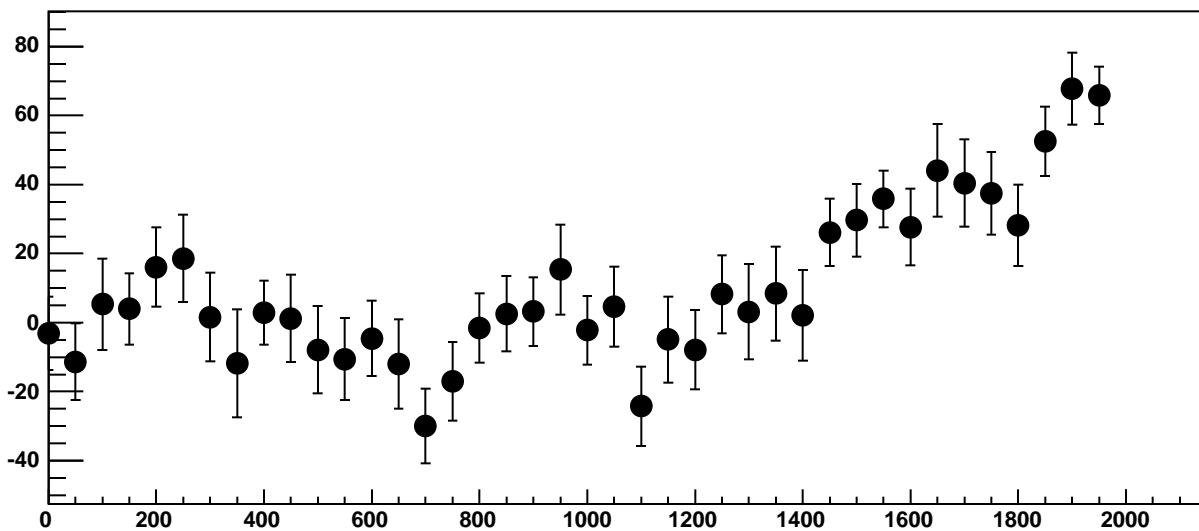
Chip 7, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC



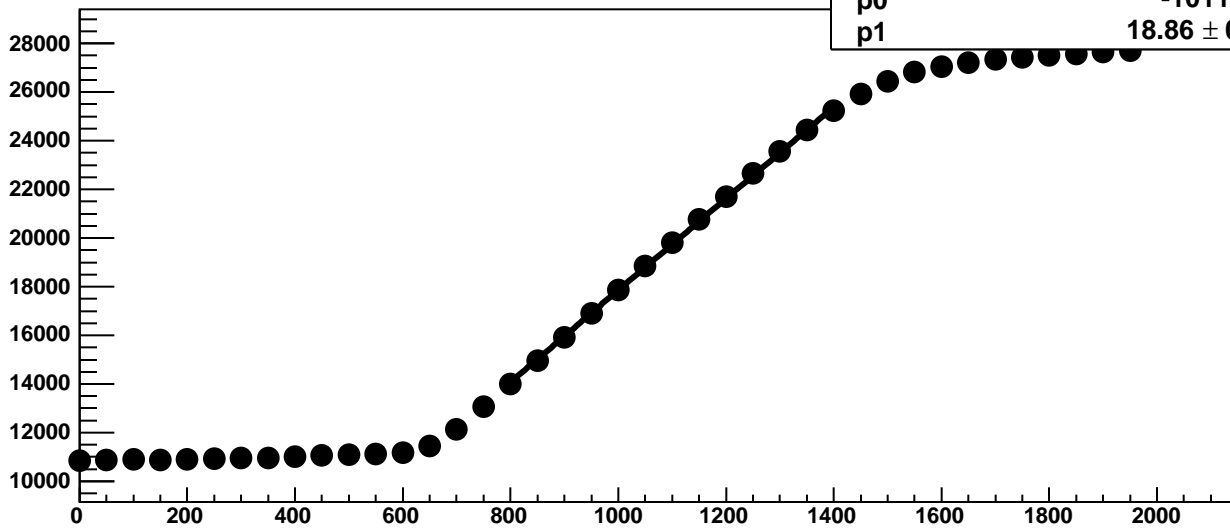
Chip 7, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC



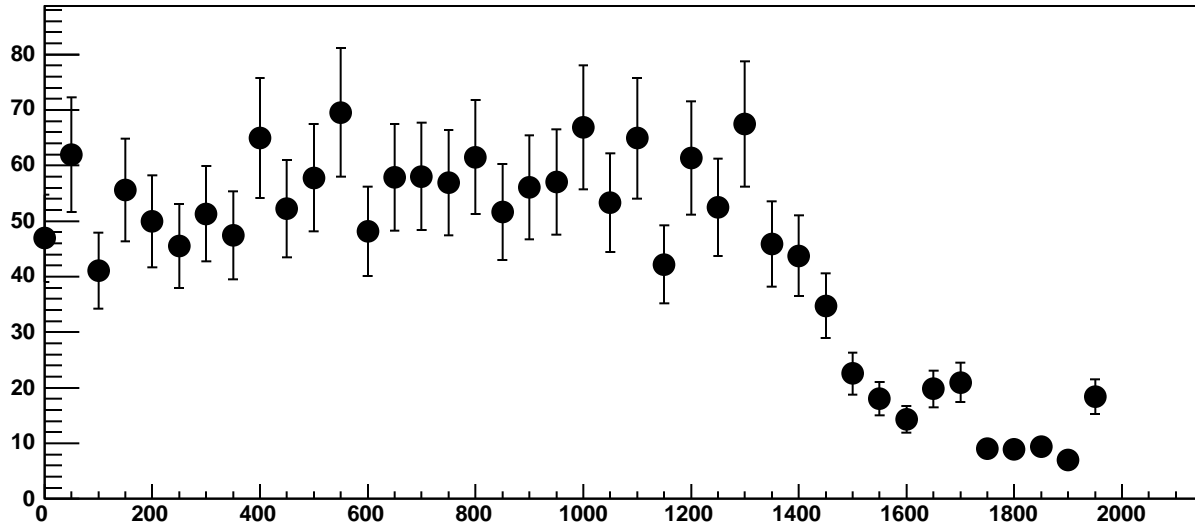
Chip 7, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC



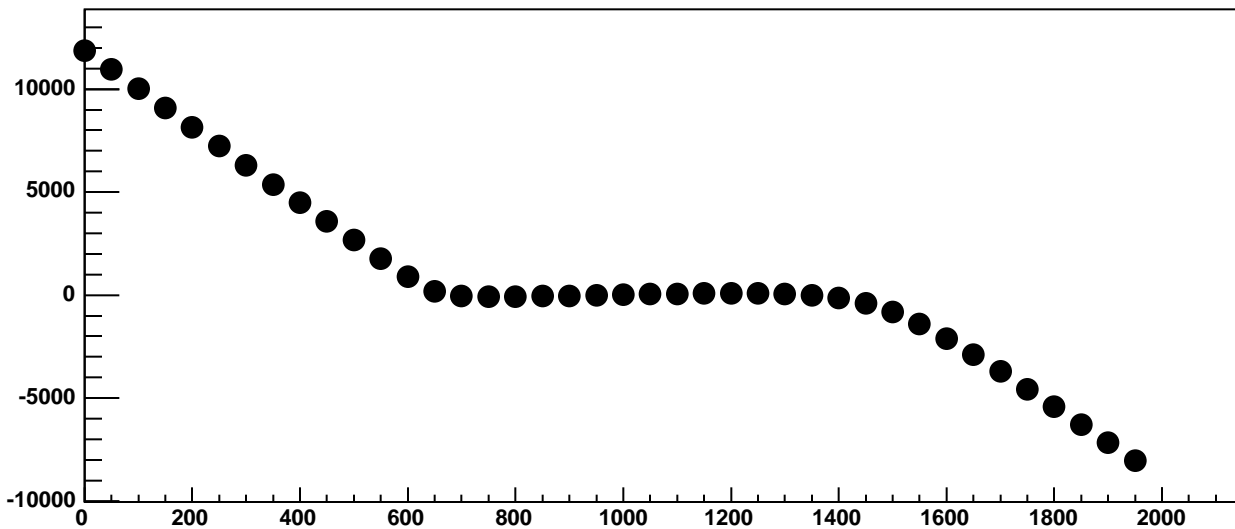
Chip 7, Channel 0, Enable 3!, Hold=30, ADC Mean vs DAC



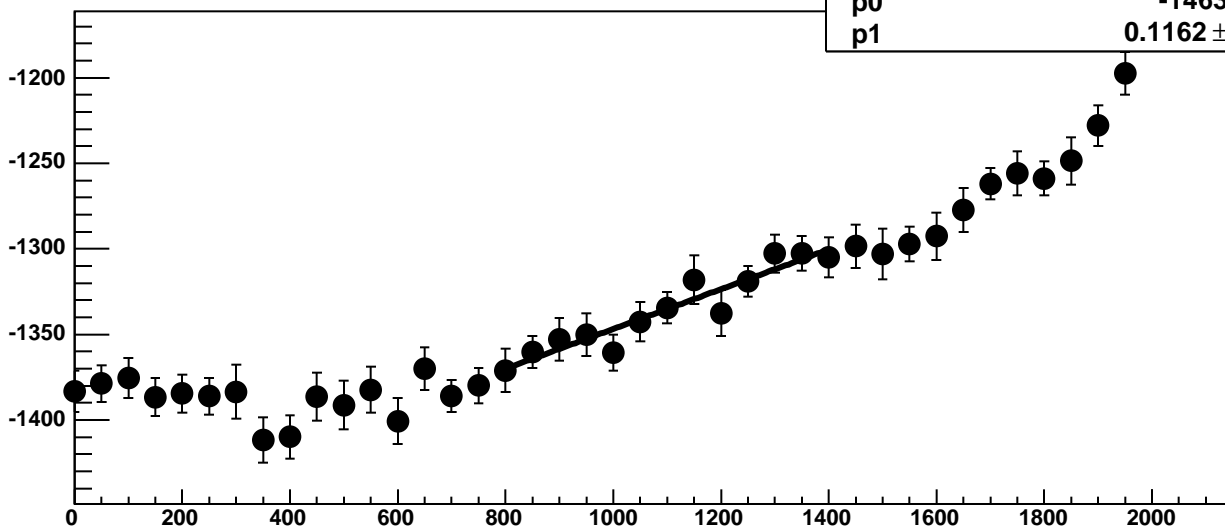
Chip 7, Channel 0, Enable 3!, Hold=30, ADC Noise vs DAC



Chip 7, Channel 0, Enable 3!, Hold=30, ADC Residuals vs DAC

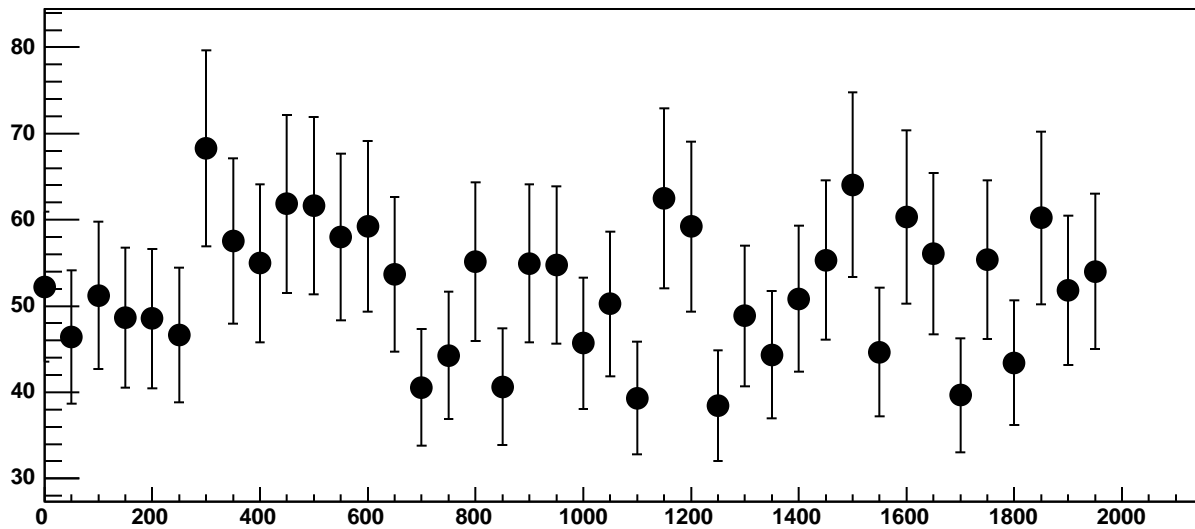


Chip 7, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC

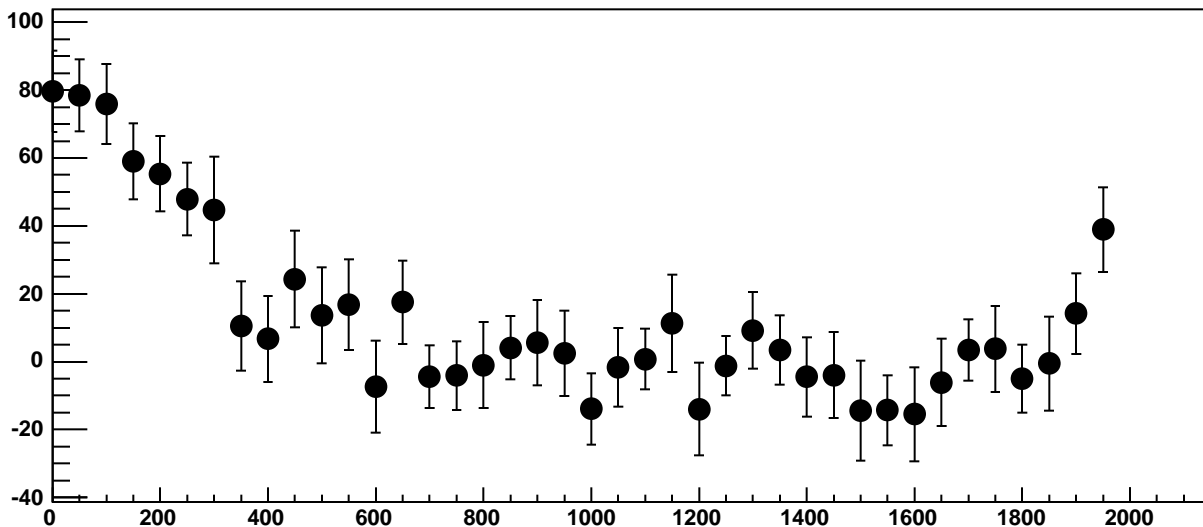


$\chi^2 / \text{ndf}$  4.861 / 11  
p0  $-1463 \pm 18.4$   
p1  $0.1162 \pm 0.0164$

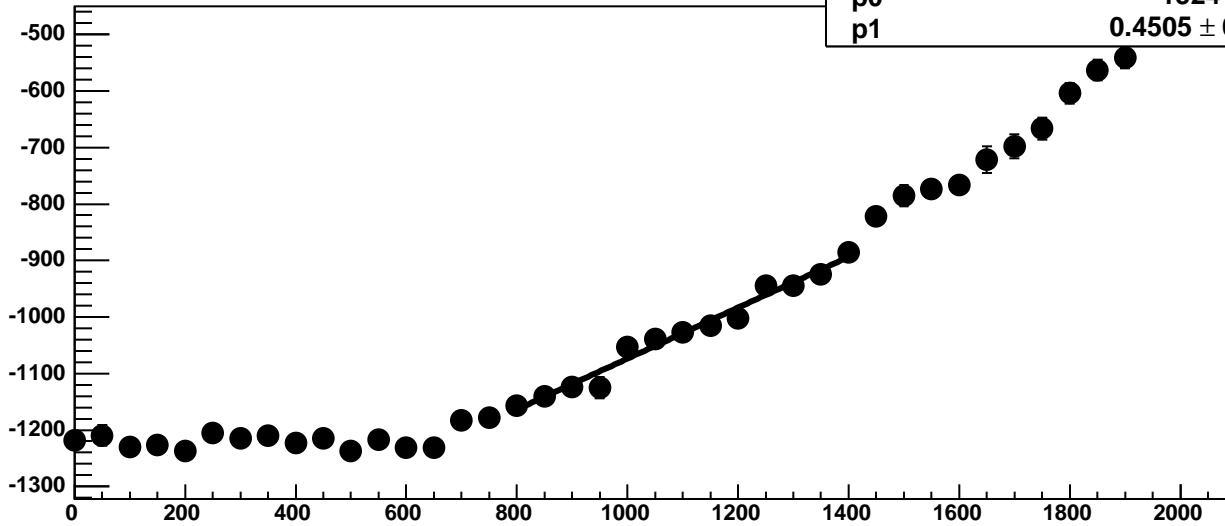
Chip 7, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC



Chip 7, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC

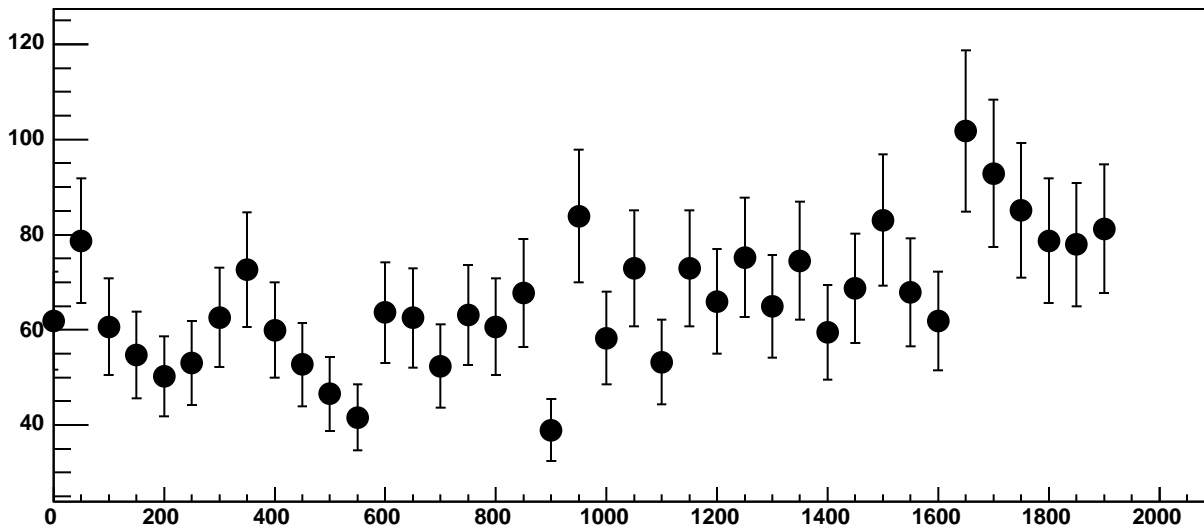


Chip 7, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC

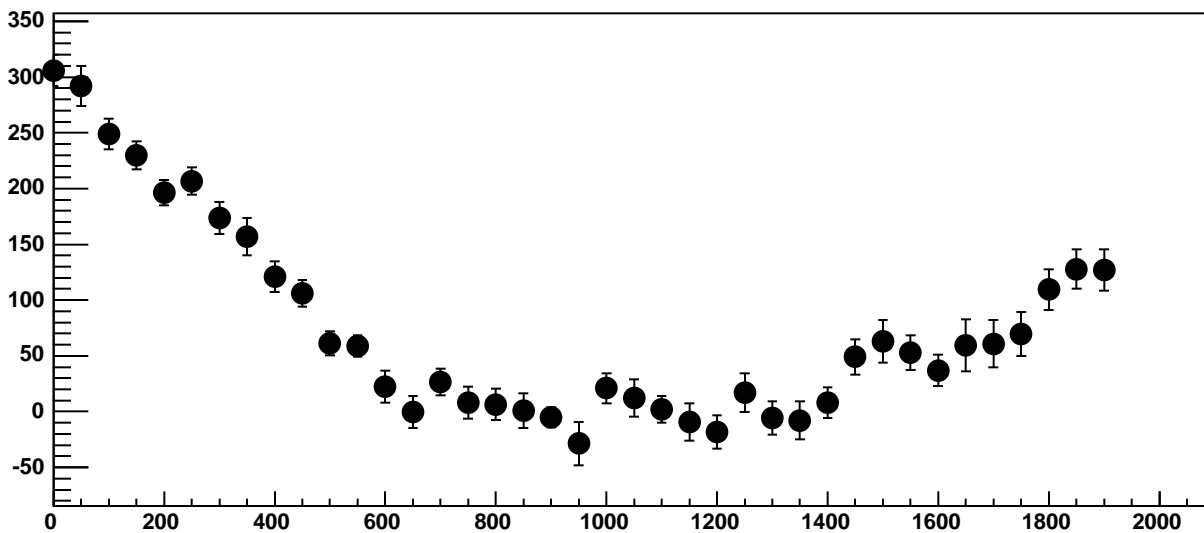


$\chi^2 / \text{ndf}$  9.233 / 11  
p0  $-1524 \pm 22.68$   
p1  $0.4505 \pm 0.02081$

Chip 7, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC

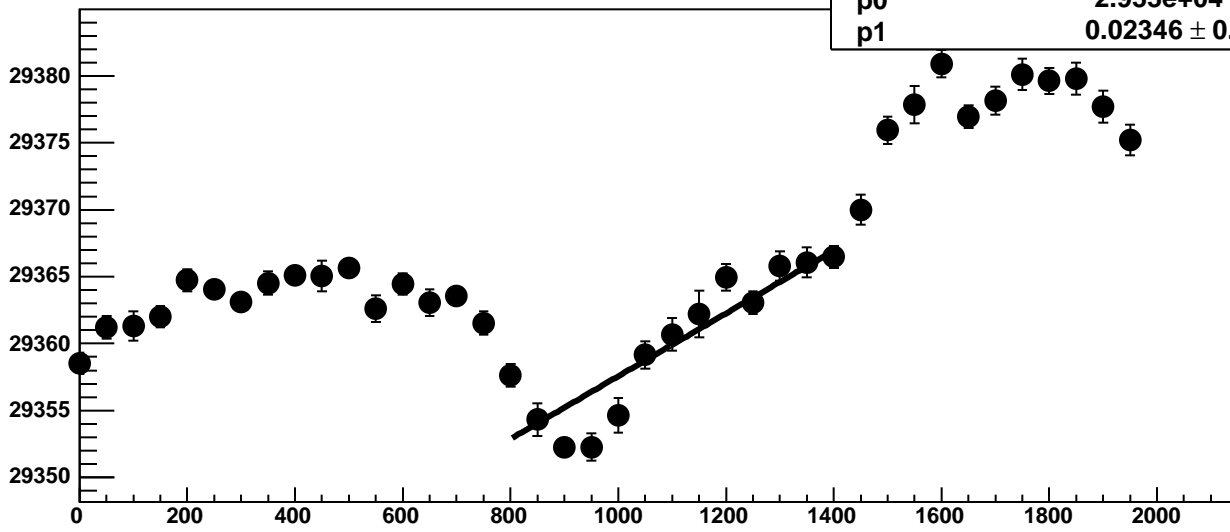


Chip 7, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC

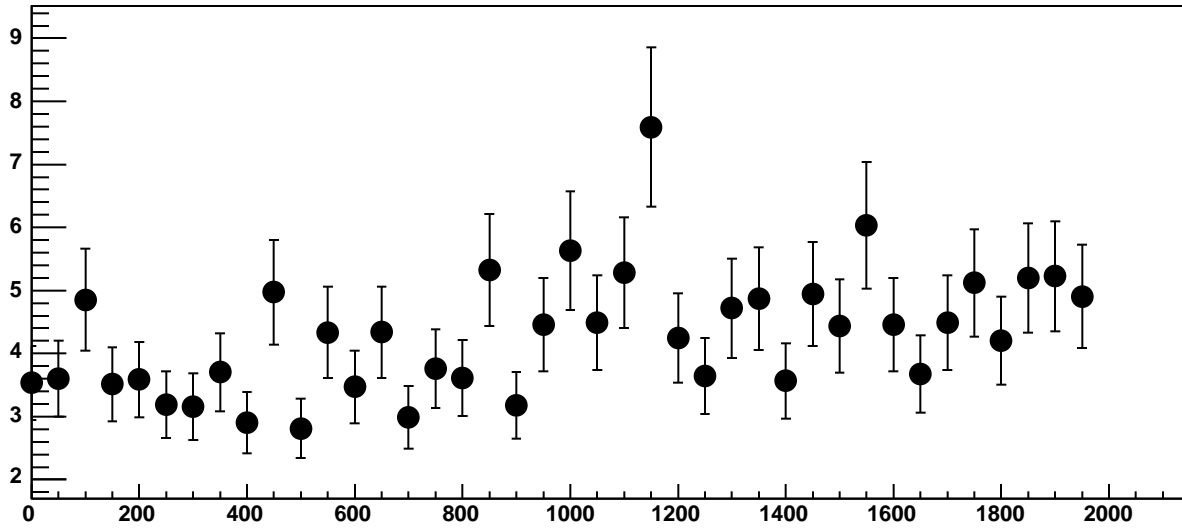




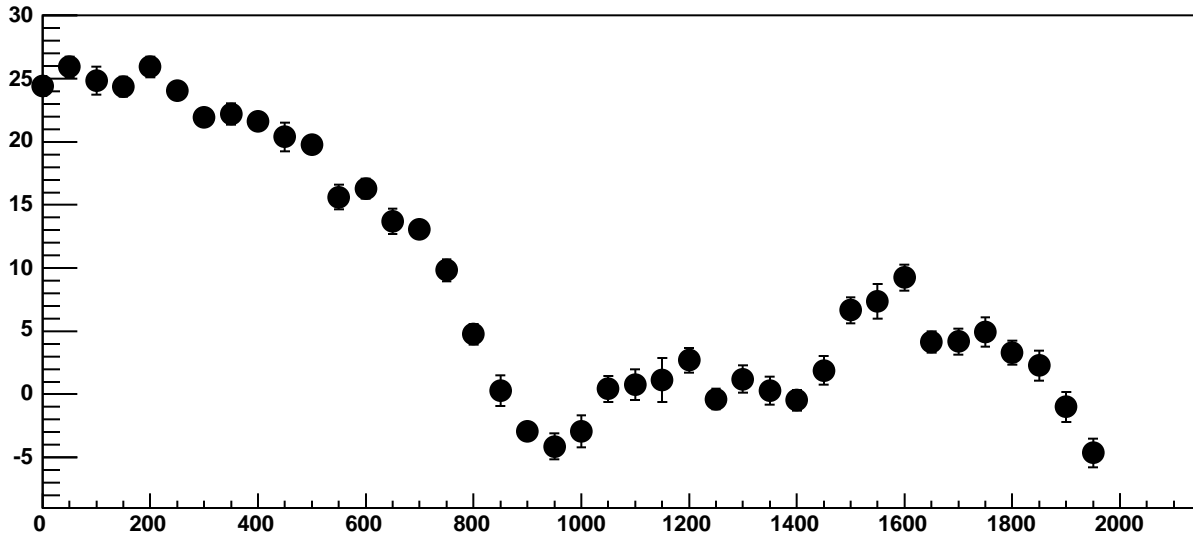
Chip 7, Channel 1, Enable 0!, Hold=30, ADC Mean vs DAC



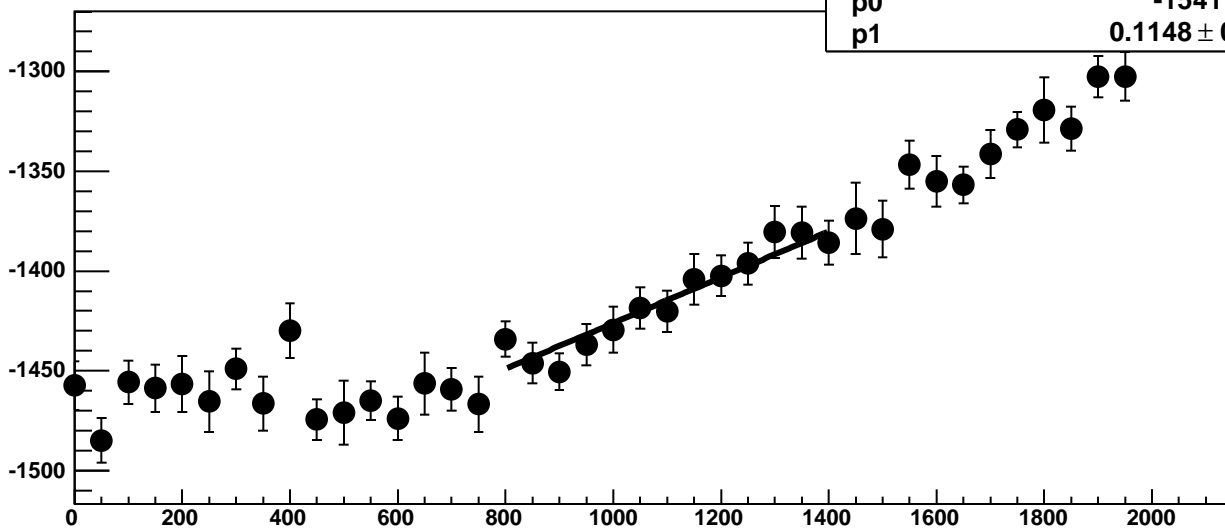
Chip 7, Channel 1, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 7, Channel 1, Enable 0!, Hold=30, ADC Residuals vs DAC

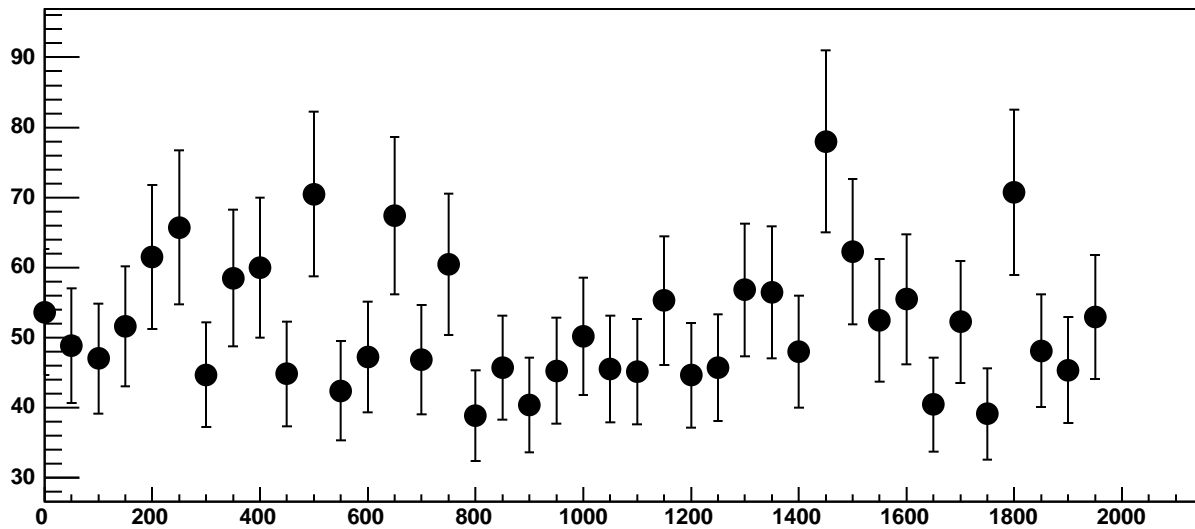


Chip 7, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC

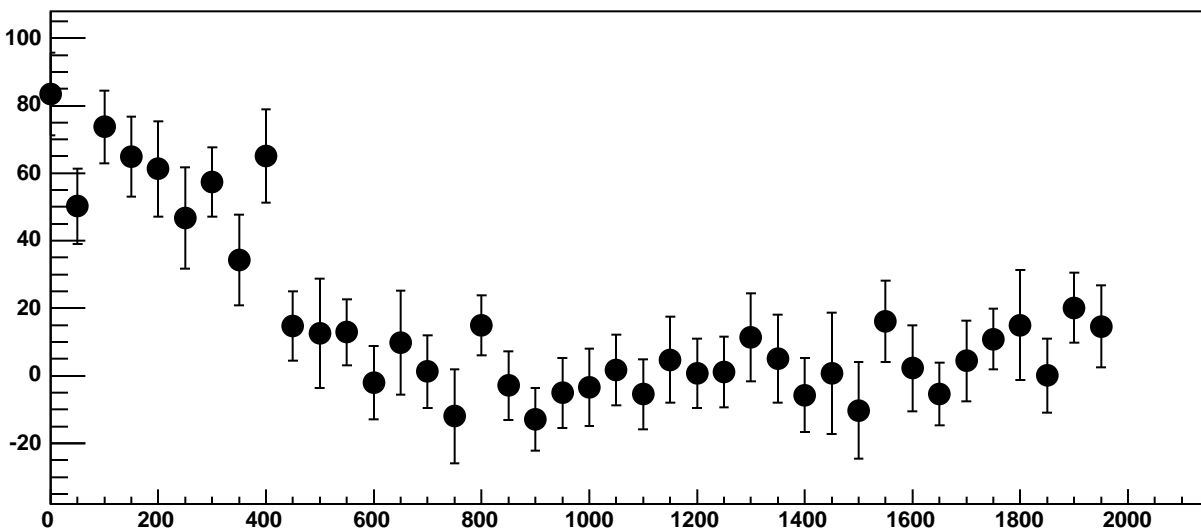


$\chi^2 / \text{ndf}$  6.777 / 11  
p0 -1541 ± 17.05  
p1 0.1148 ± 0.01567

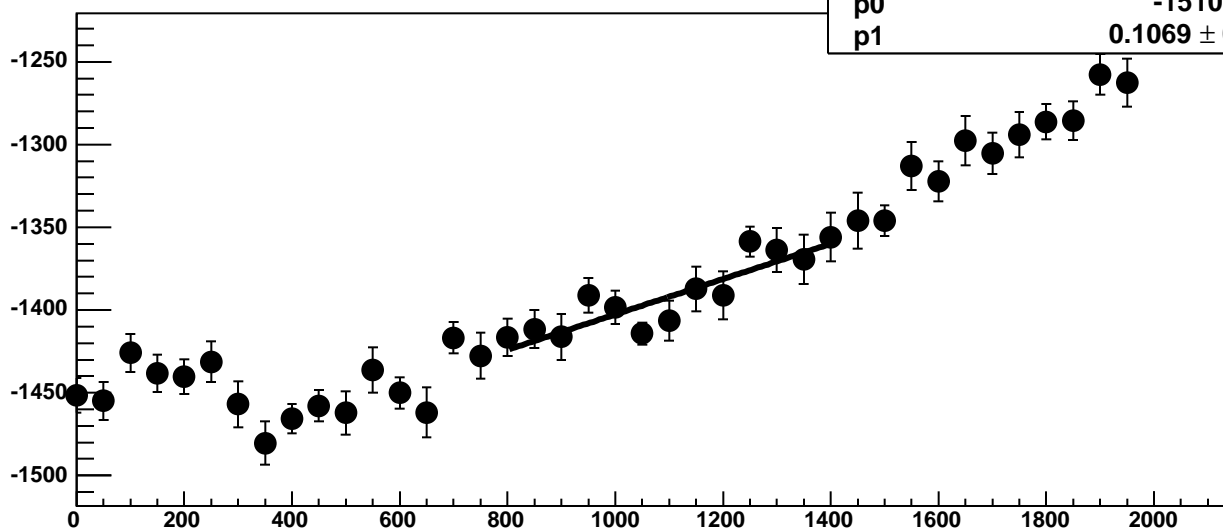
Chip 7, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC



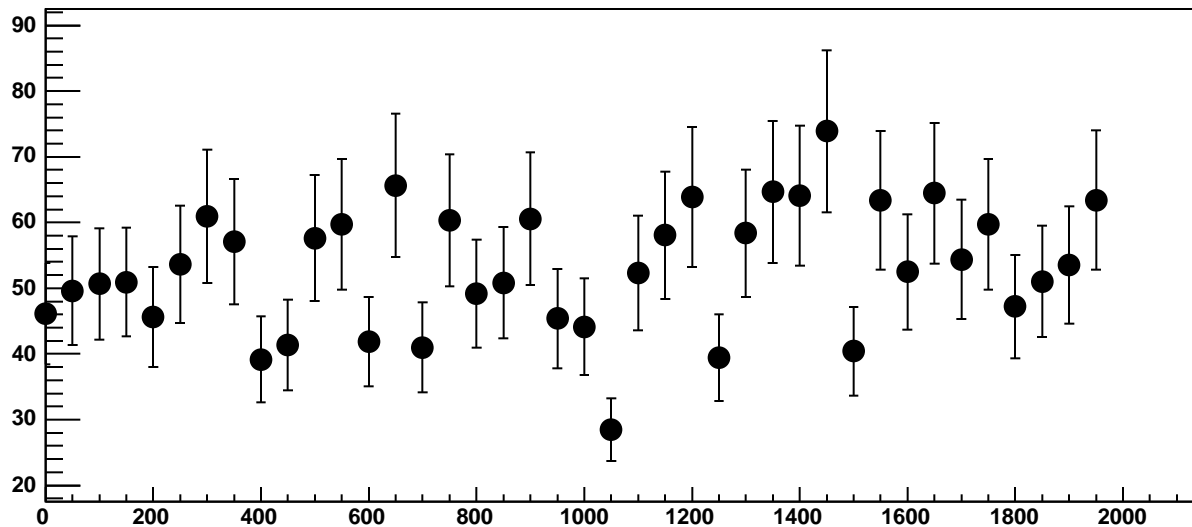
Chip 7, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC



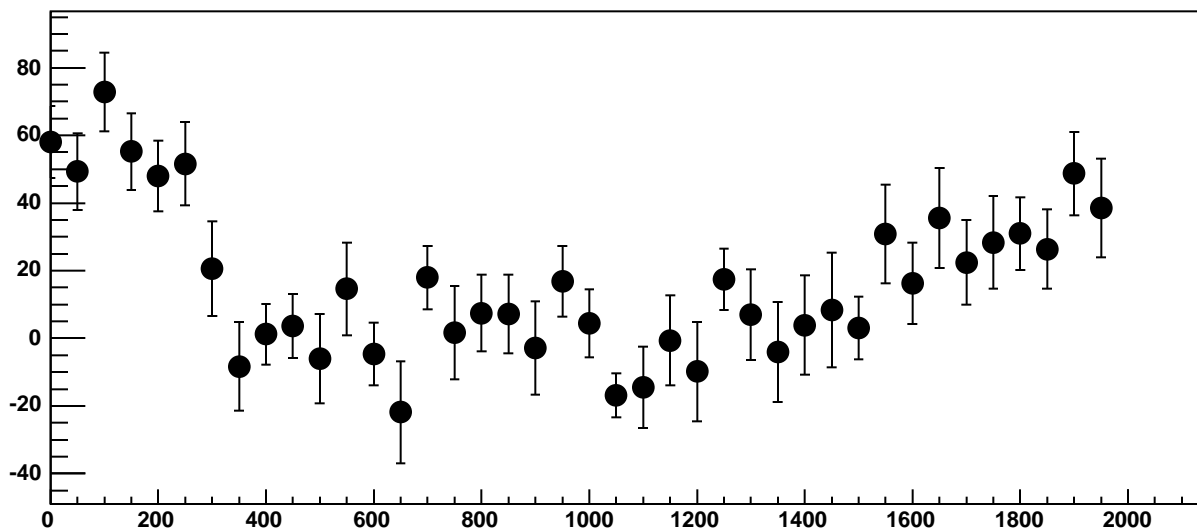
Chip 7, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC



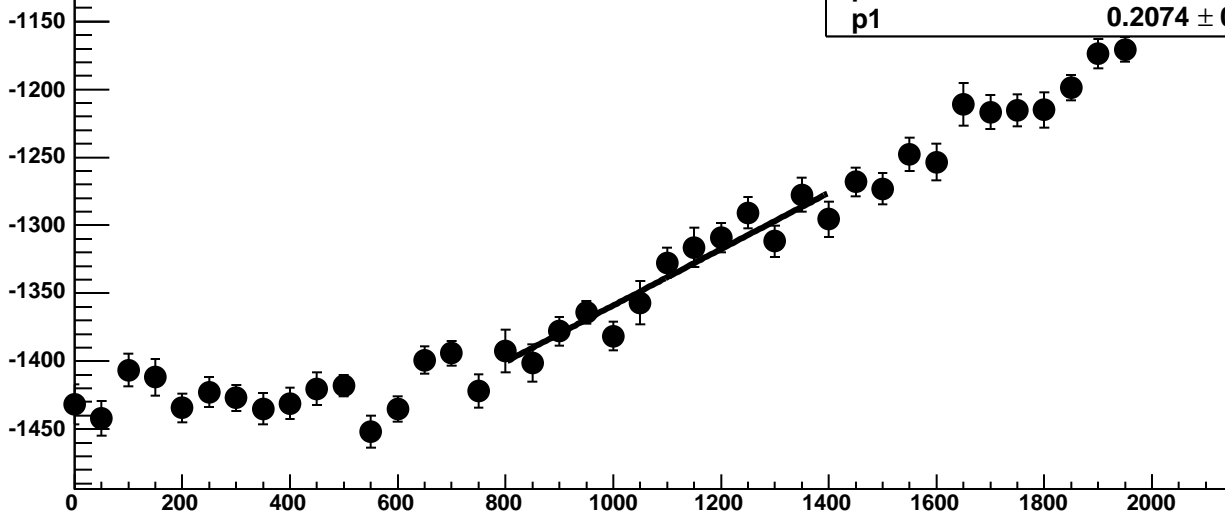
Chip 7, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC



Chip 7, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC

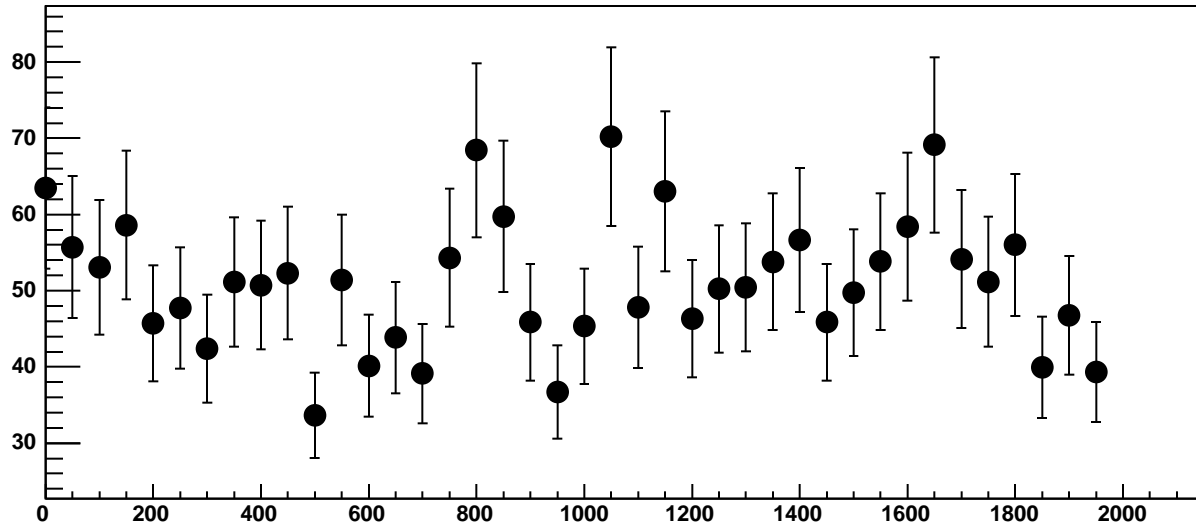


Chip 7, Channel 1, Enable 3, Hold=30, ADC Mean vs DAC

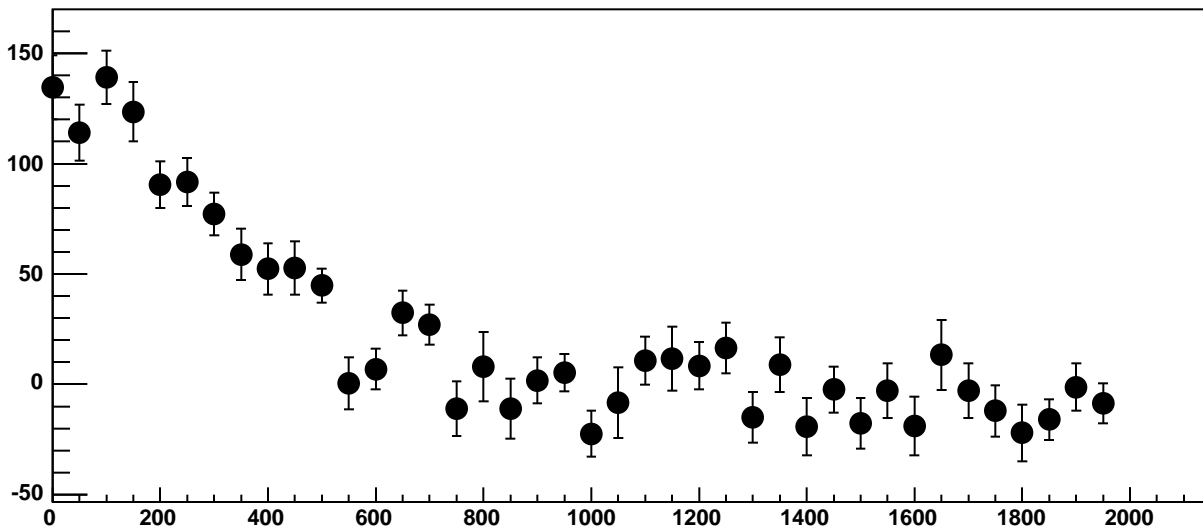


$\chi^2 / \text{ndf}$  14.94 / 11  
p0  $-1566 \pm 20.3$   
p1  $0.2074 \pm 0.01829$

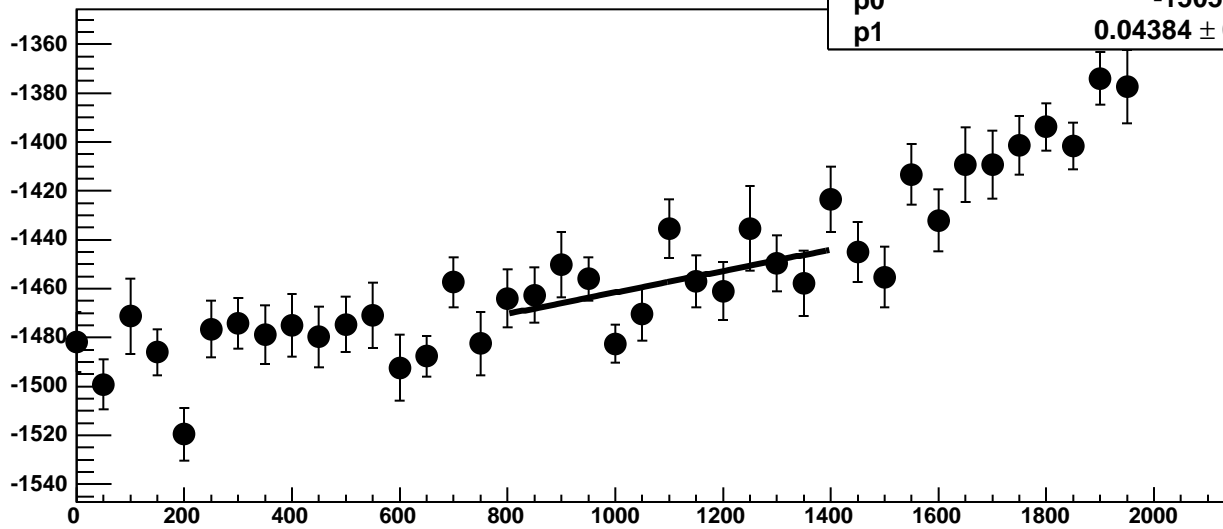
Chip 7, Channel 1, Enable 3, Hold=30, ADC Noise vs DAC



Chip 7, Channel 1, Enable 3, Hold=30, ADC Residuals vs DAC

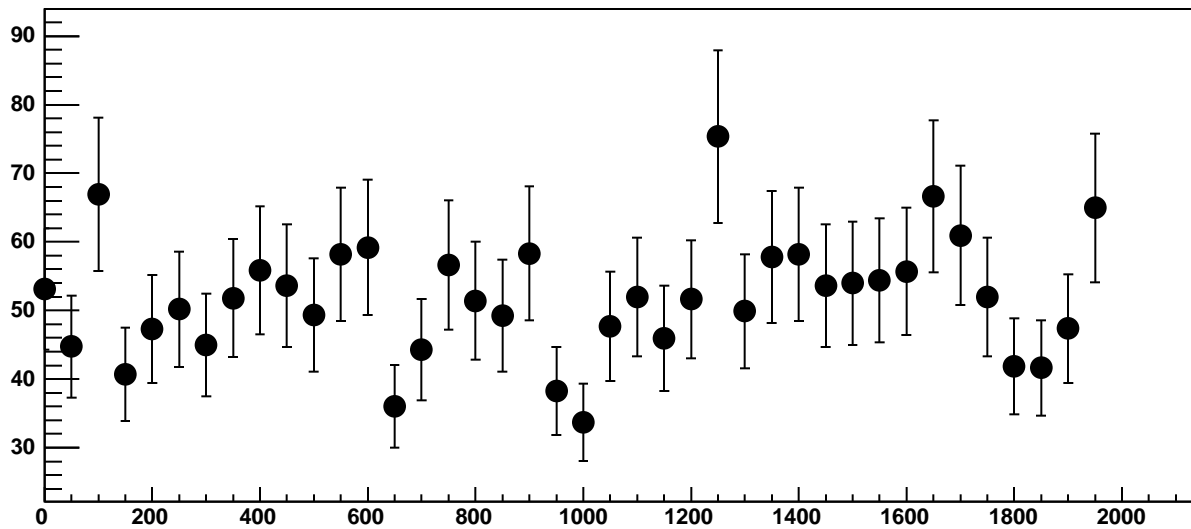


Chip 7, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC

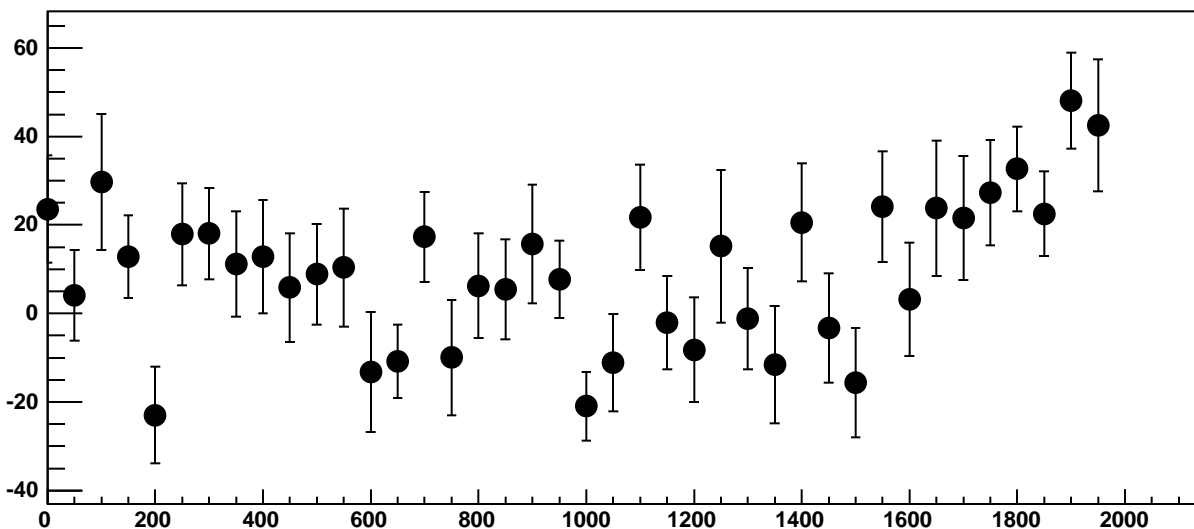


$\chi^2 / \text{ndf}$  18.81 / 11  
p0  $-1505 \pm 19.52$   
p1  $0.04384 \pm 0.01799$

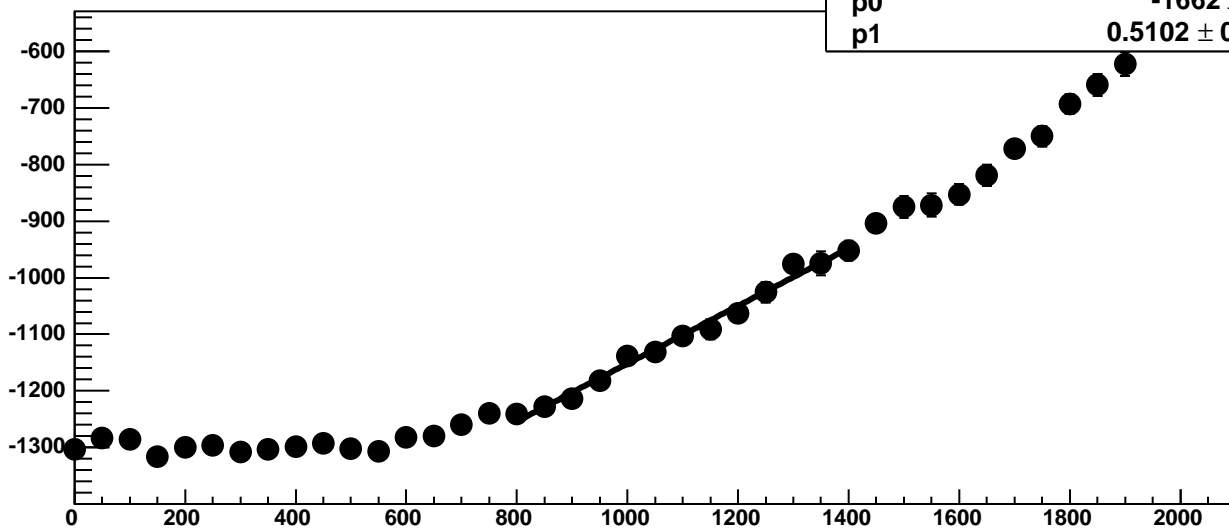
Chip 7, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC



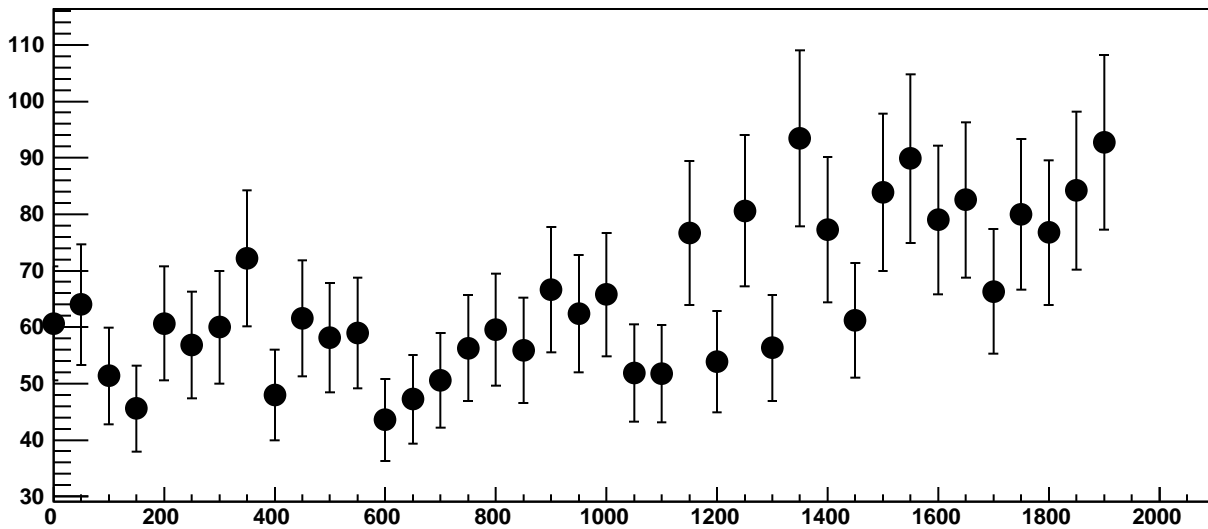
Chip 7, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC



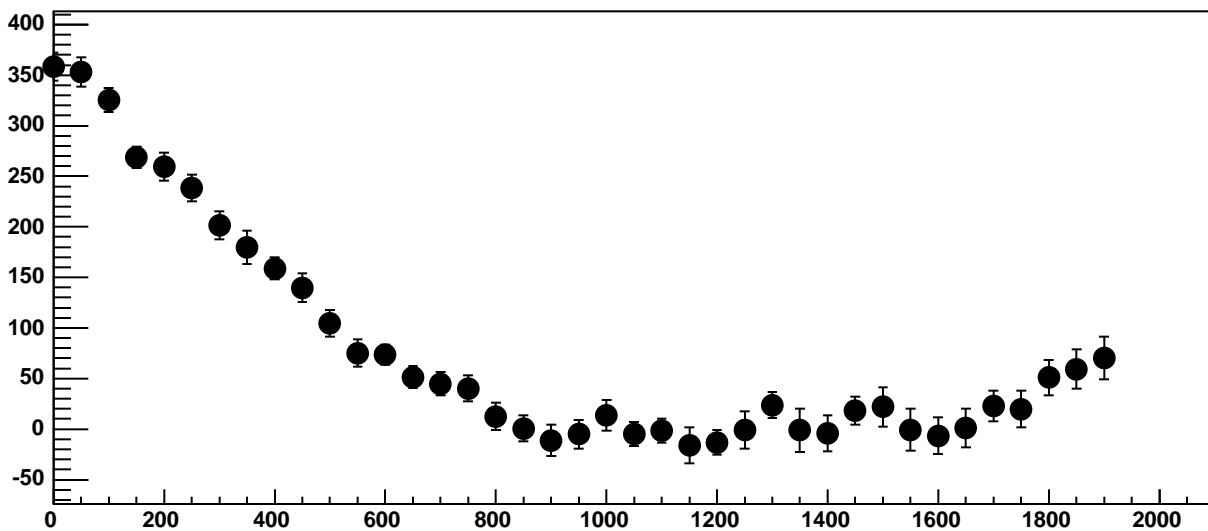
Chip 7, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC



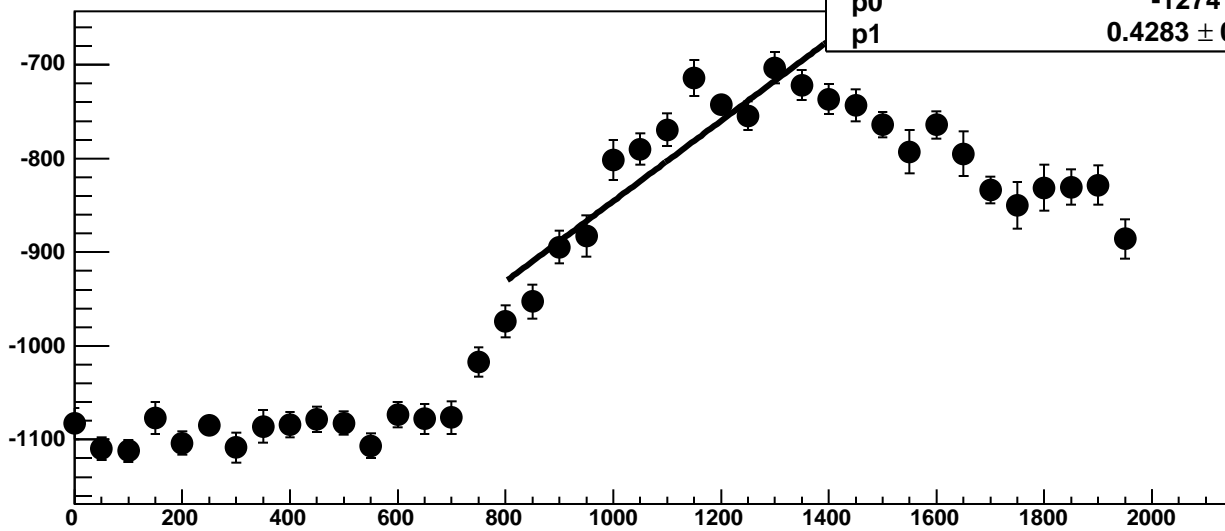
Chip 7, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC



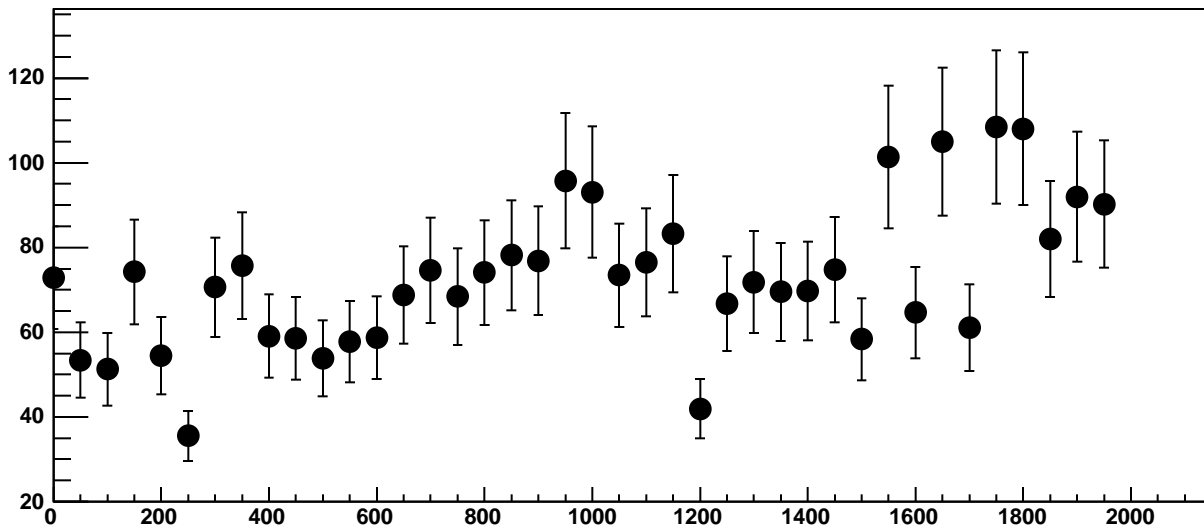
Chip 7, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC



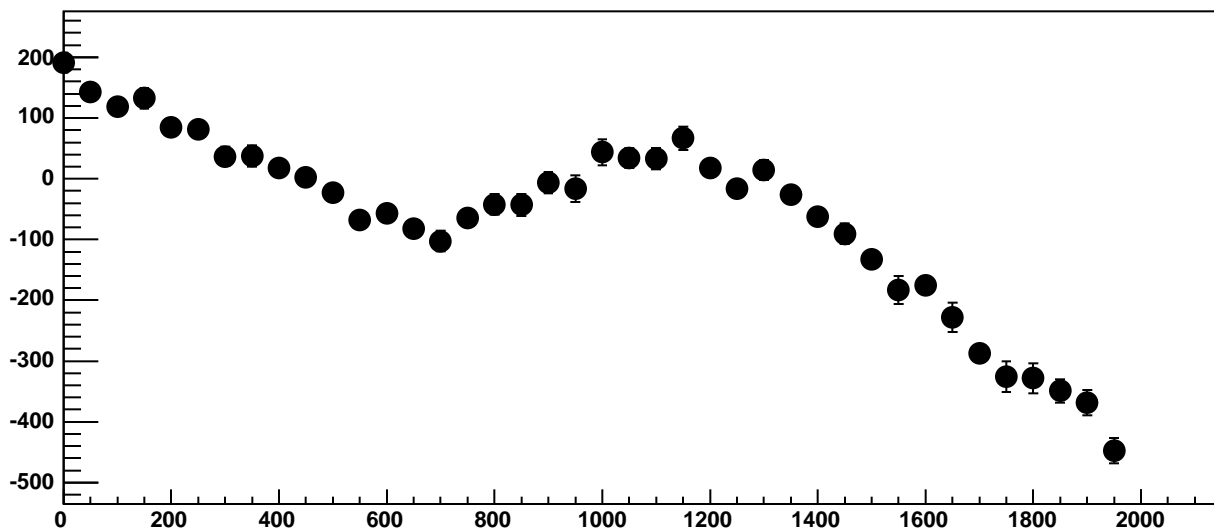
Chip 7, Channel 2, Enable 0, Hold=30, ADC Mean vs DAC



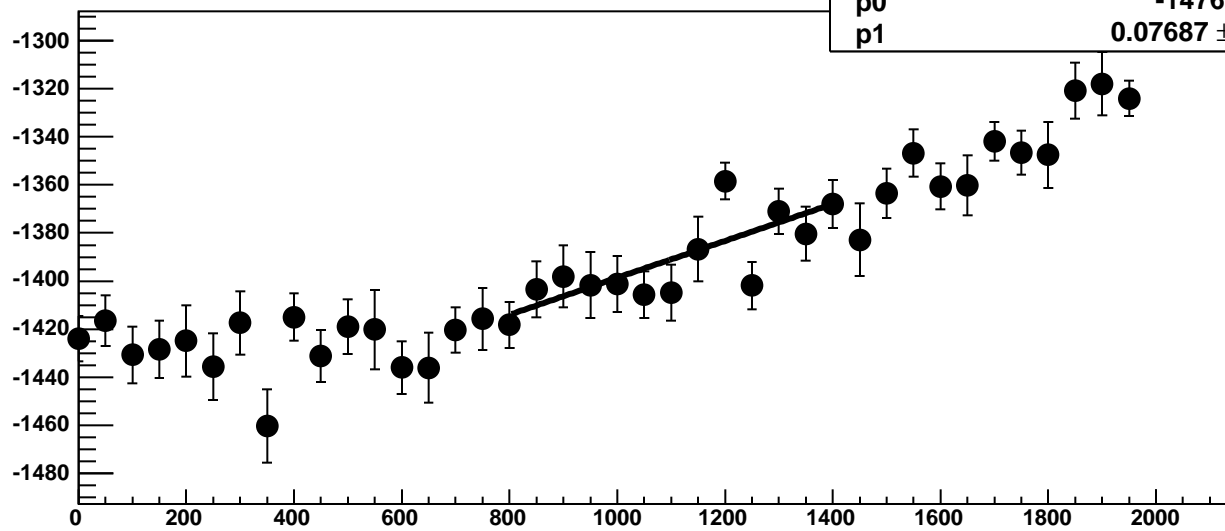
Chip 7, Channel 2, Enable 0, Hold=30, ADC Noise vs DAC



Chip 7, Channel 2, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 7, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

19.95 / 11

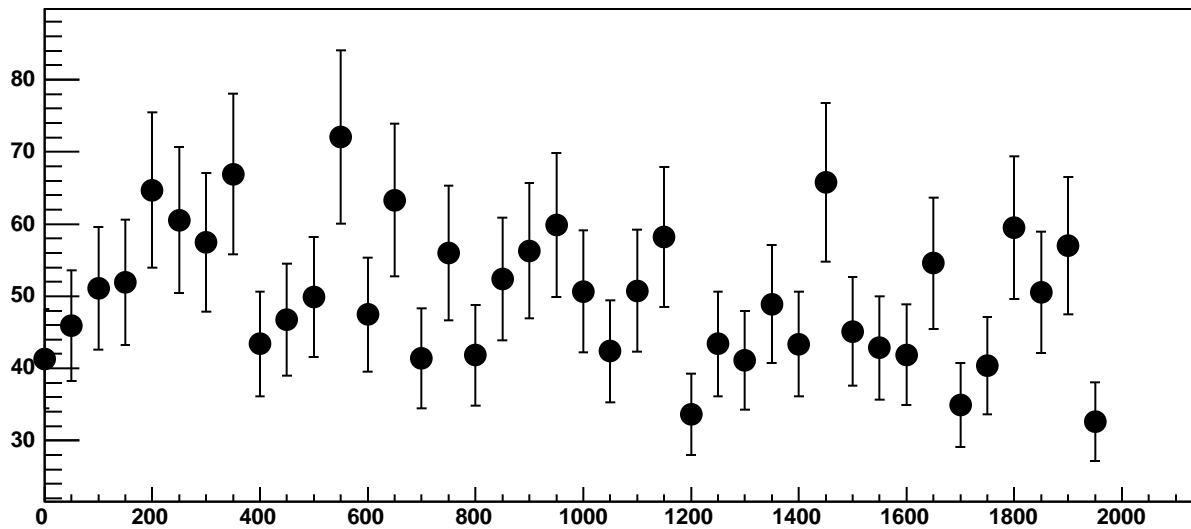
p0

$-1476 \pm 17.73$

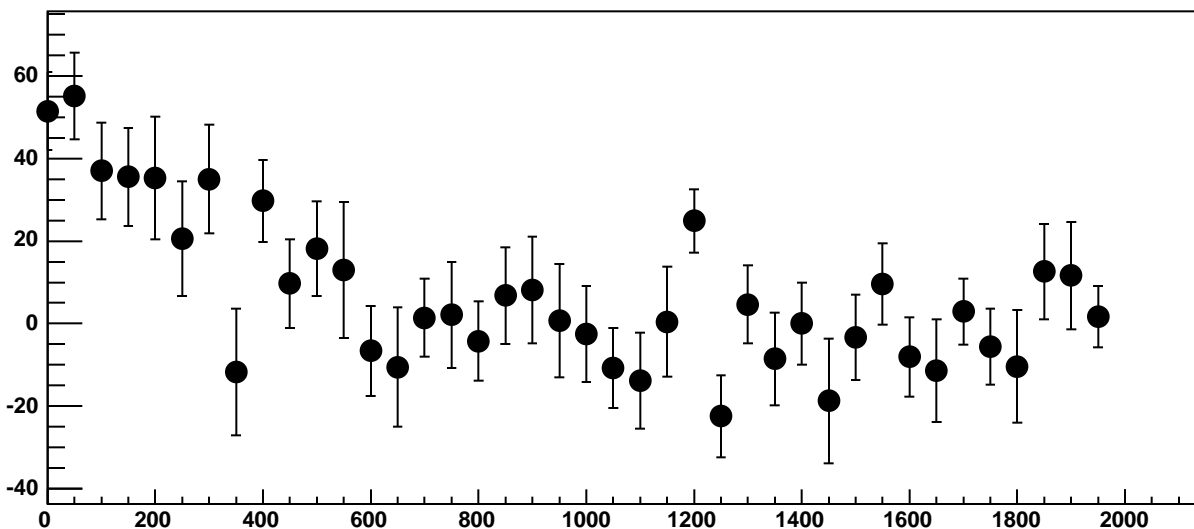
p1

$0.07687 \pm 0.0156$

Chip 7, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC

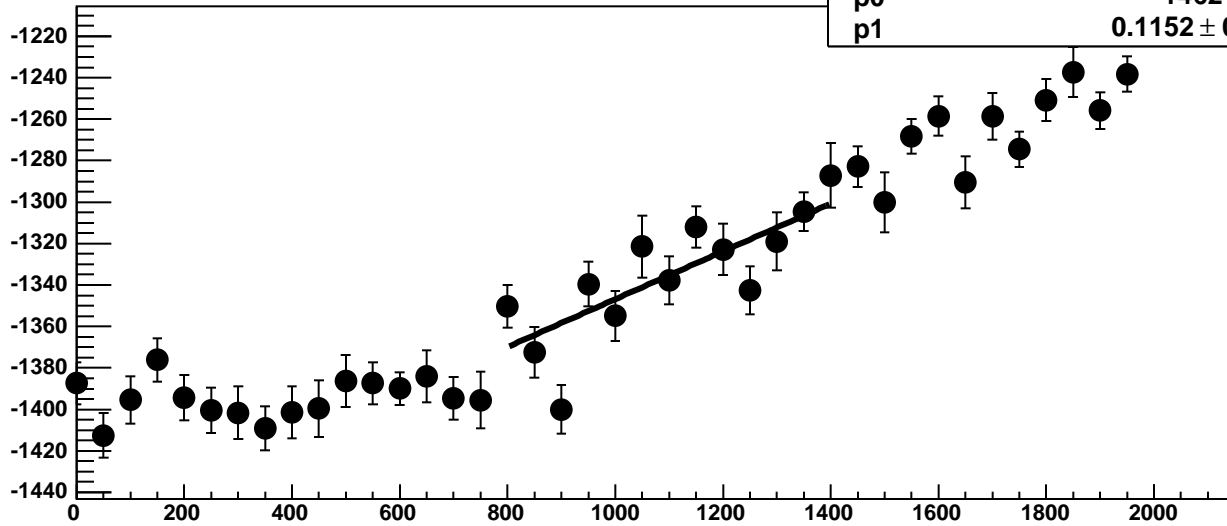


Chip 7, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC

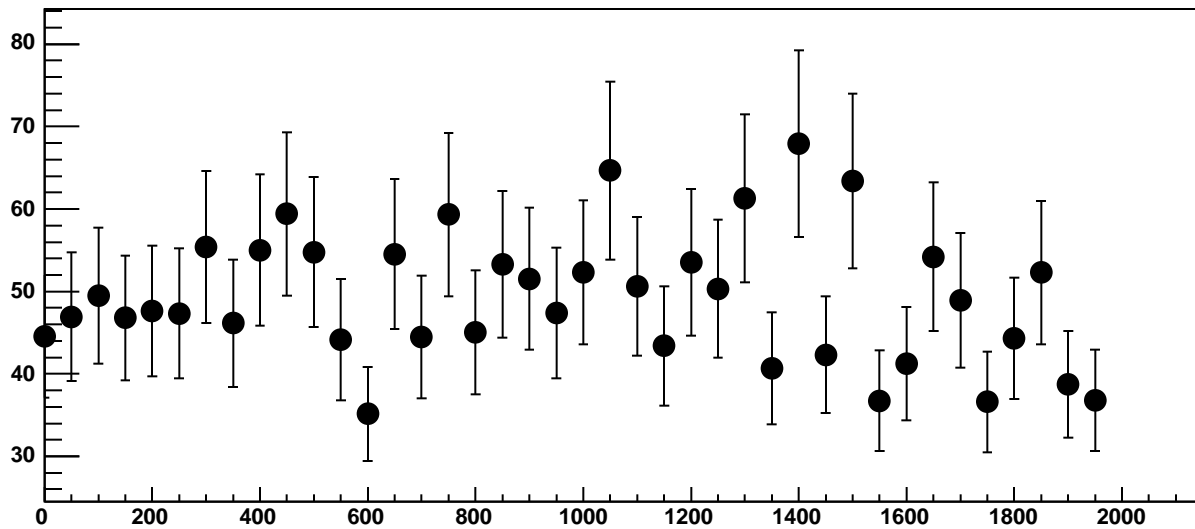




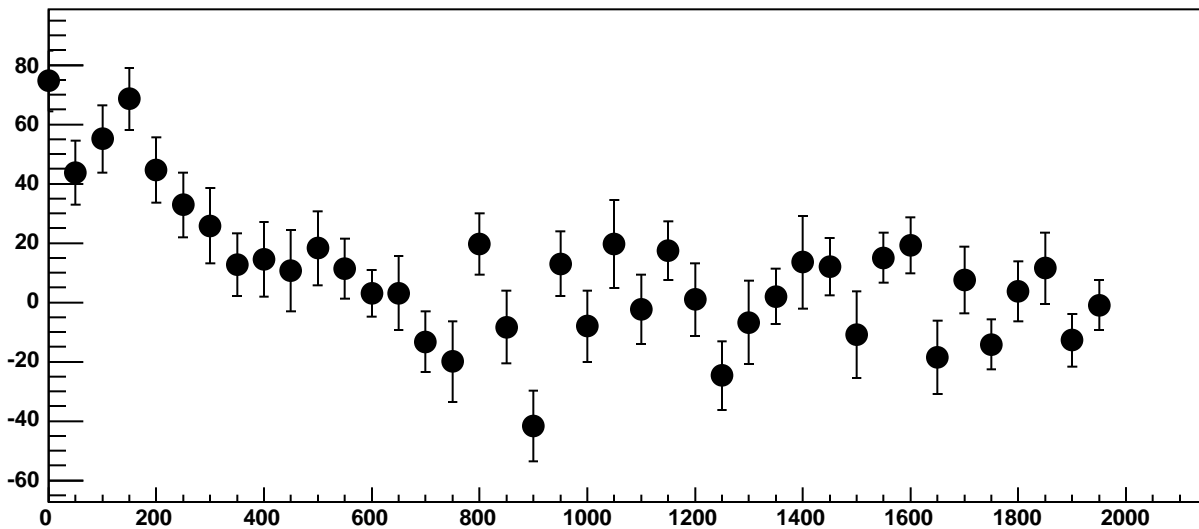
Chip 7, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC



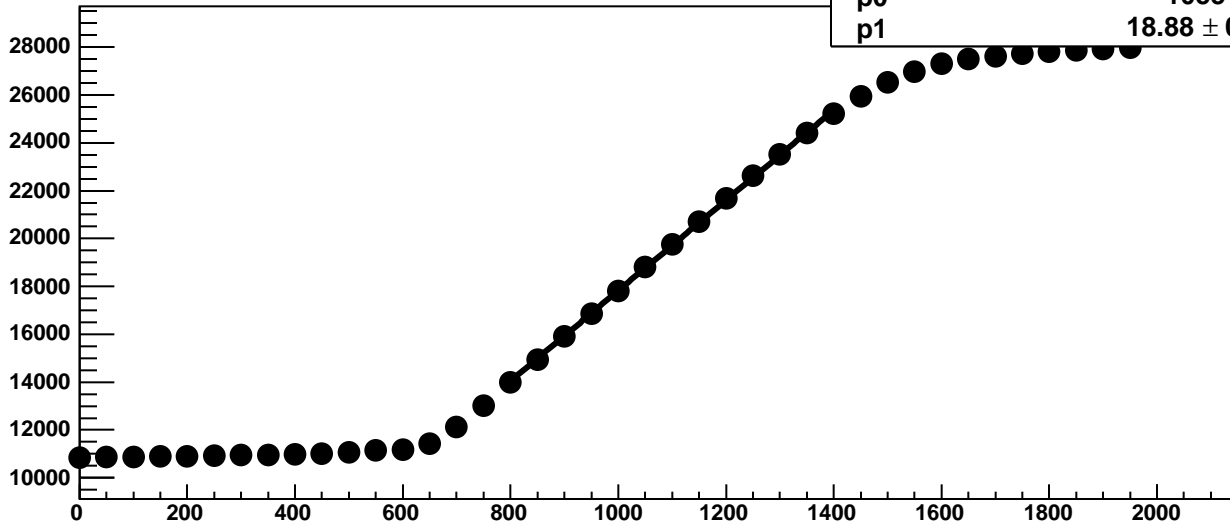
Chip 7, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC



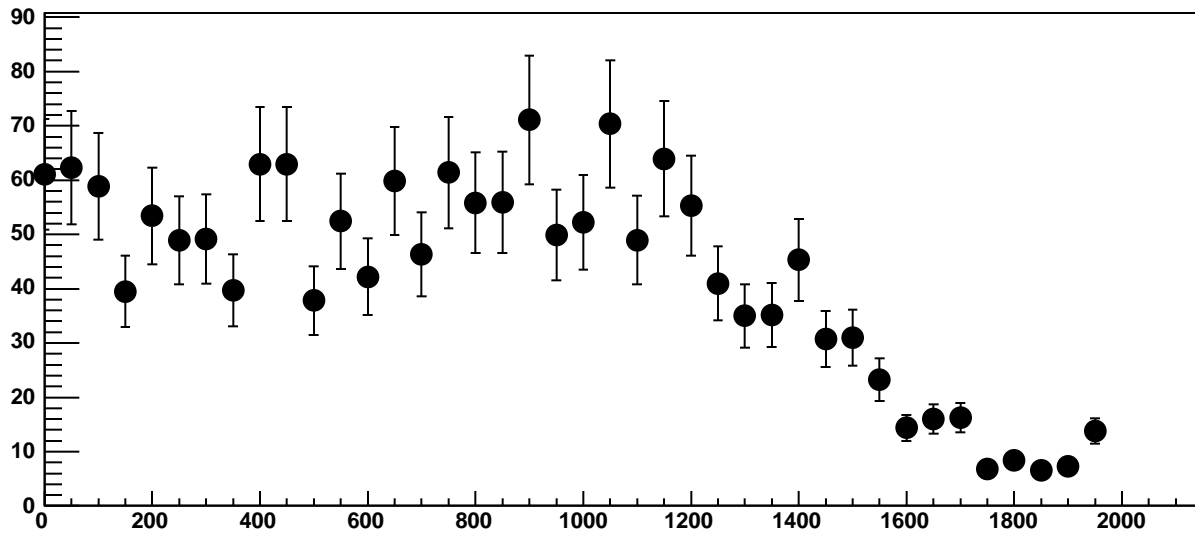
Chip 7, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC



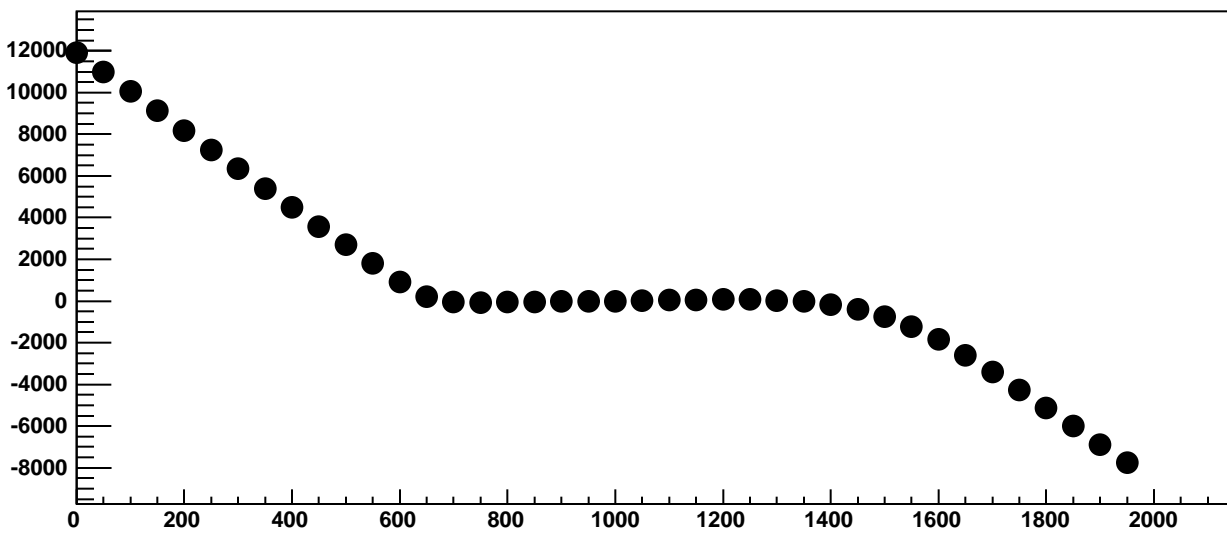
Chip 7, Channel 2, Enable 3!, Hold=30, ADC Mean vs DAC



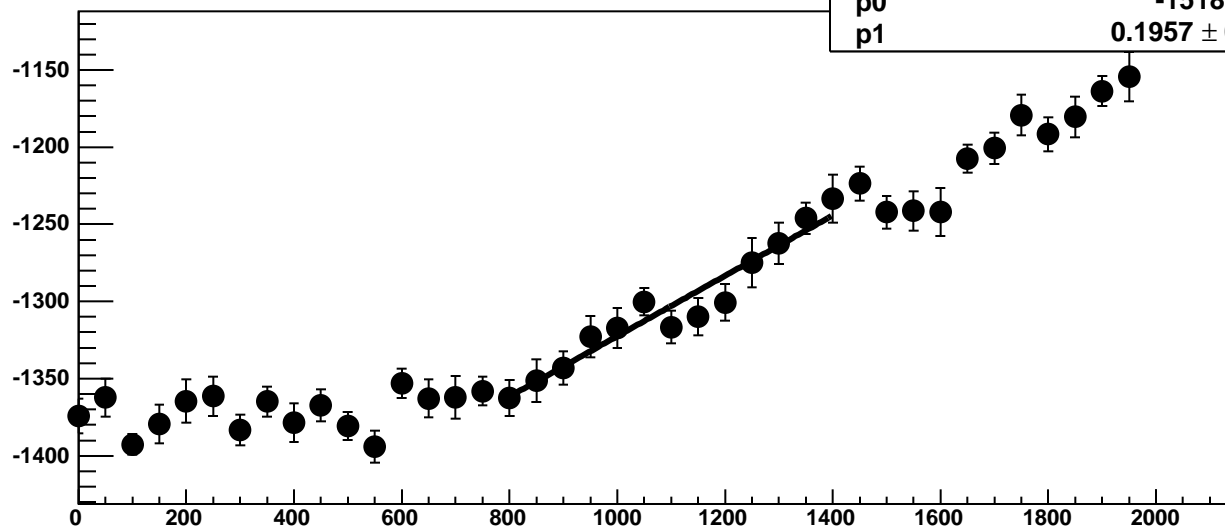
Chip 7, Channel 2, Enable 3!, Hold=30, ADC Noise vs DAC



Chip 7, Channel 2, Enable 3!, Hold=30, ADC Residuals vs DAC

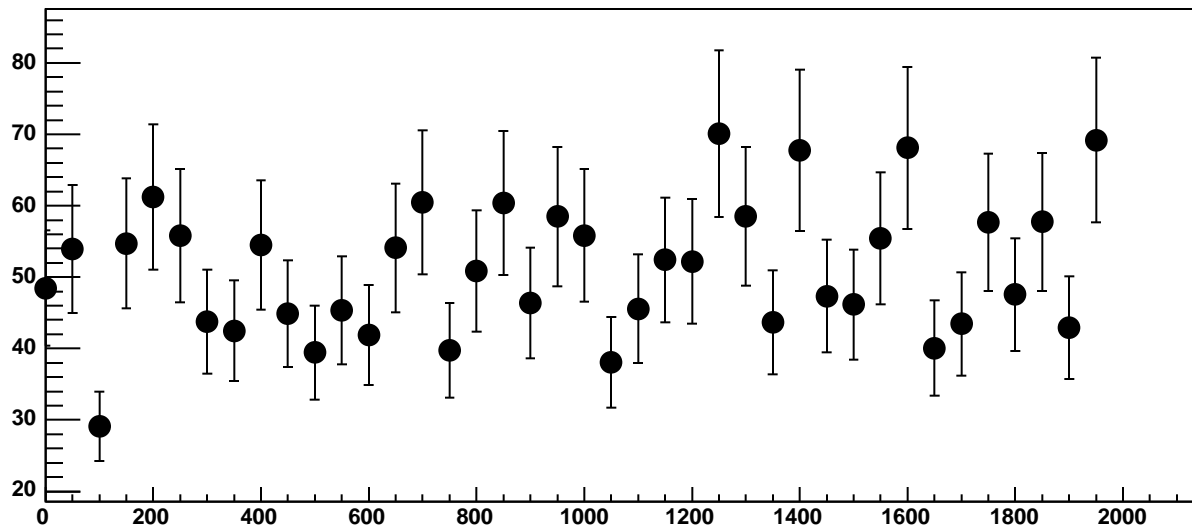


Chip 7, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC

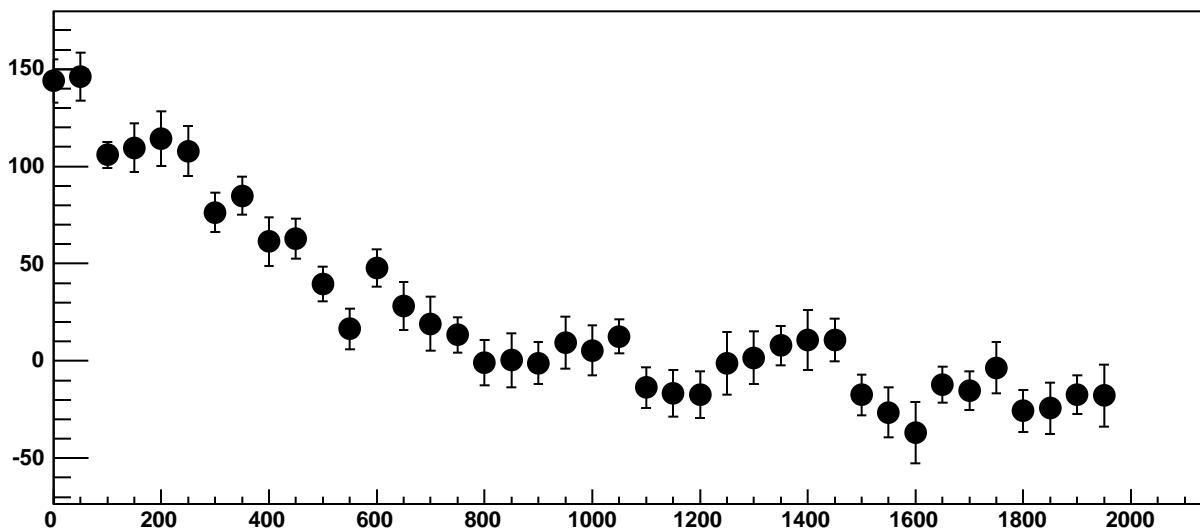


$\chi^2 / \text{ndf}$  9.593 / 11  
p0  $-1518 \pm 20.37$   
p1  $0.1957 \pm 0.01844$

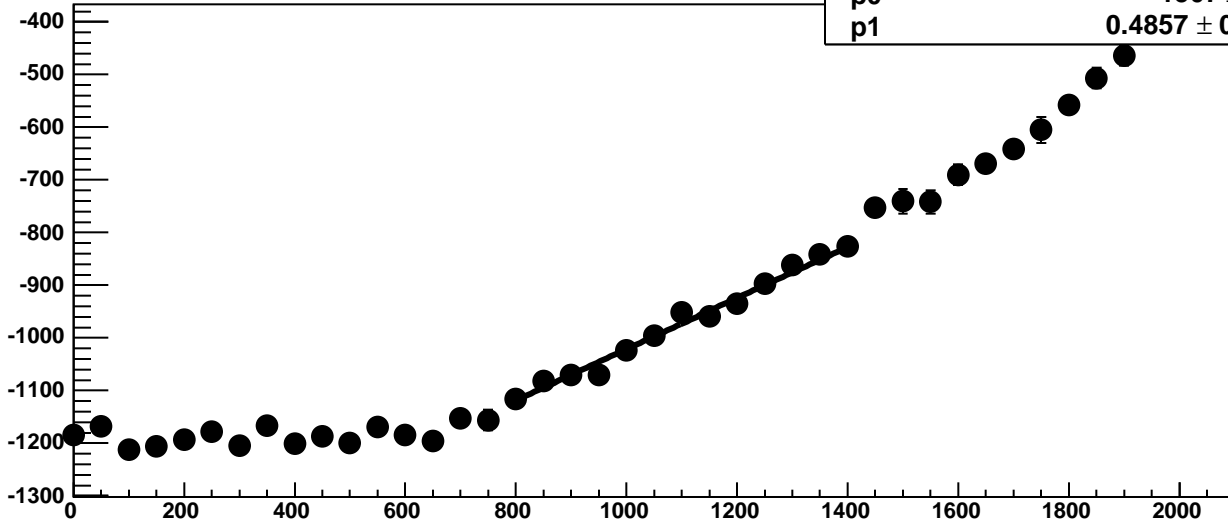
Chip 7, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC



Chip 7, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC

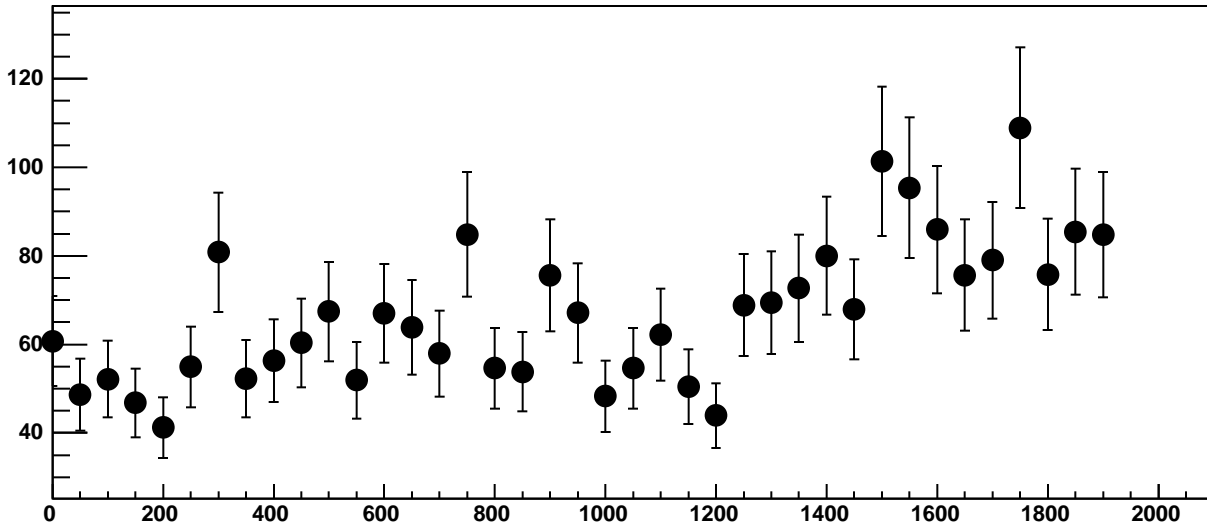


Chip 7, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC

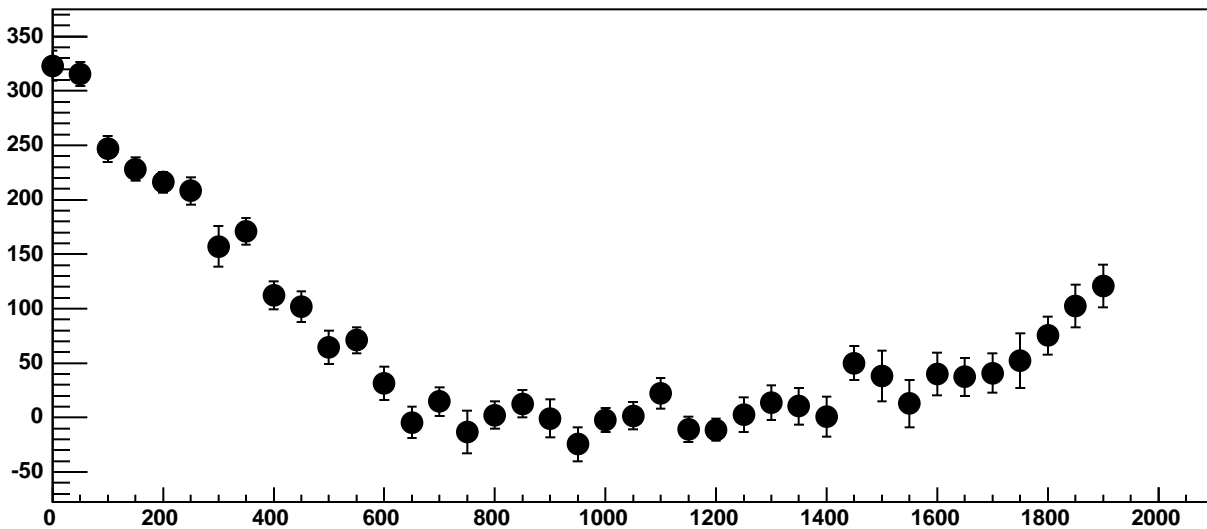


$\chi^2 / \text{ndf}$  9.371 / 11  
p0  $-1507 \pm 23.67$   
p1  $0.4857 \pm 0.02164$

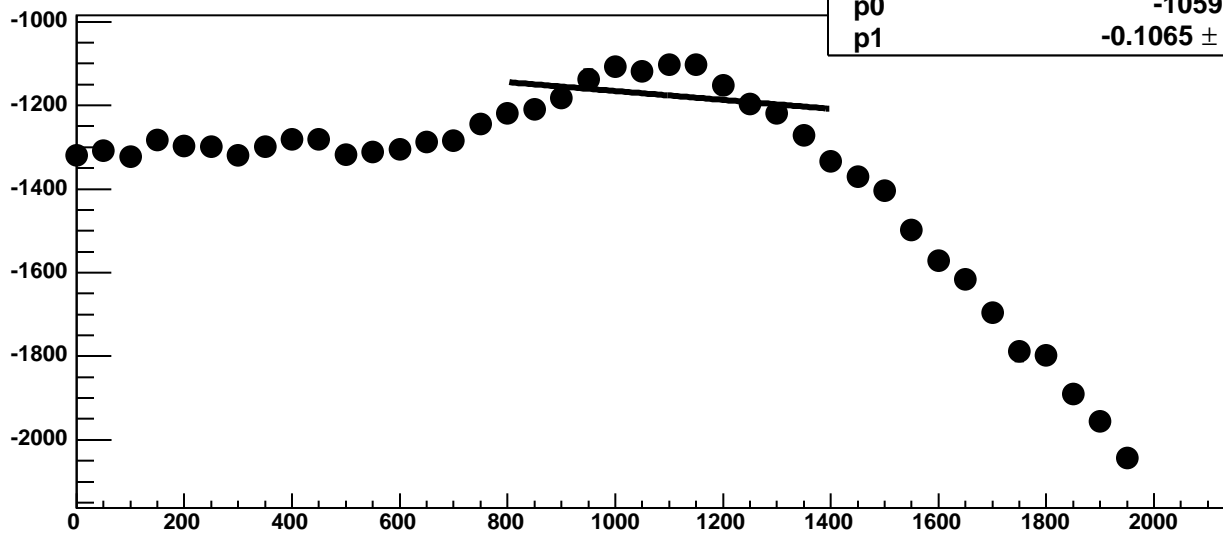
Chip 7, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC



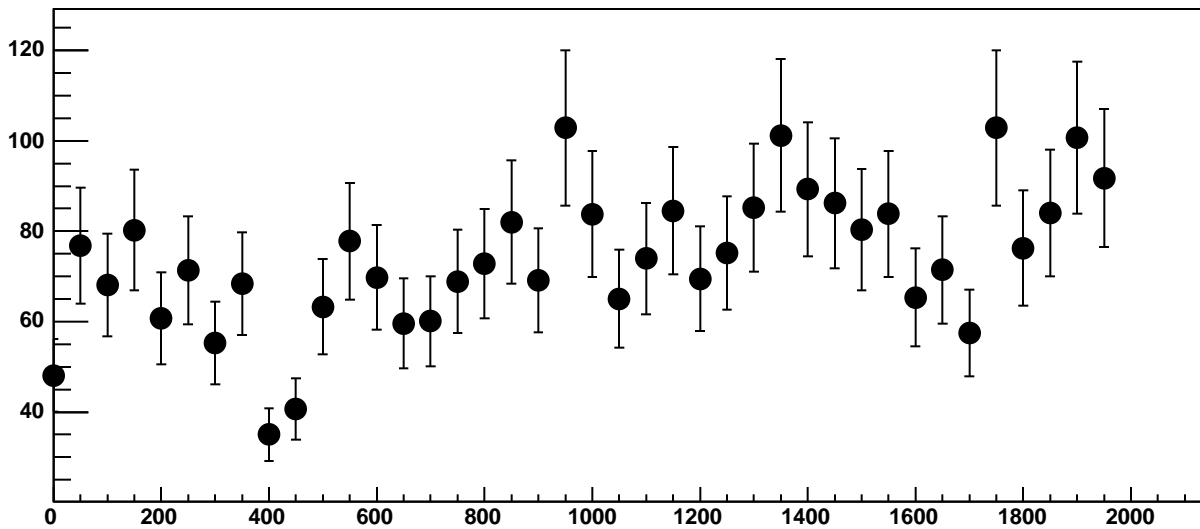
Chip 7, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC



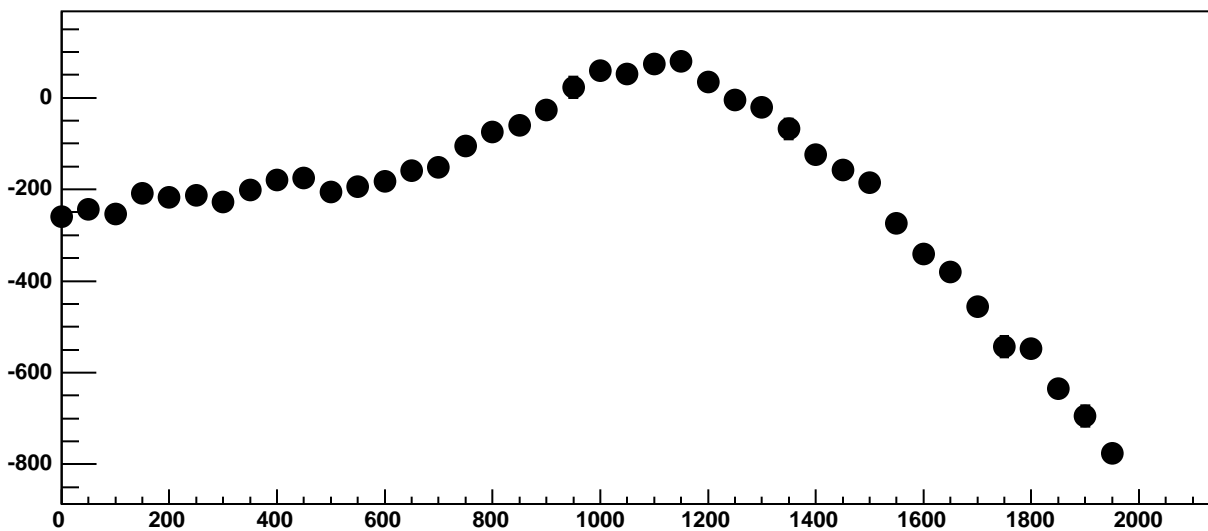
Chip 7, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC



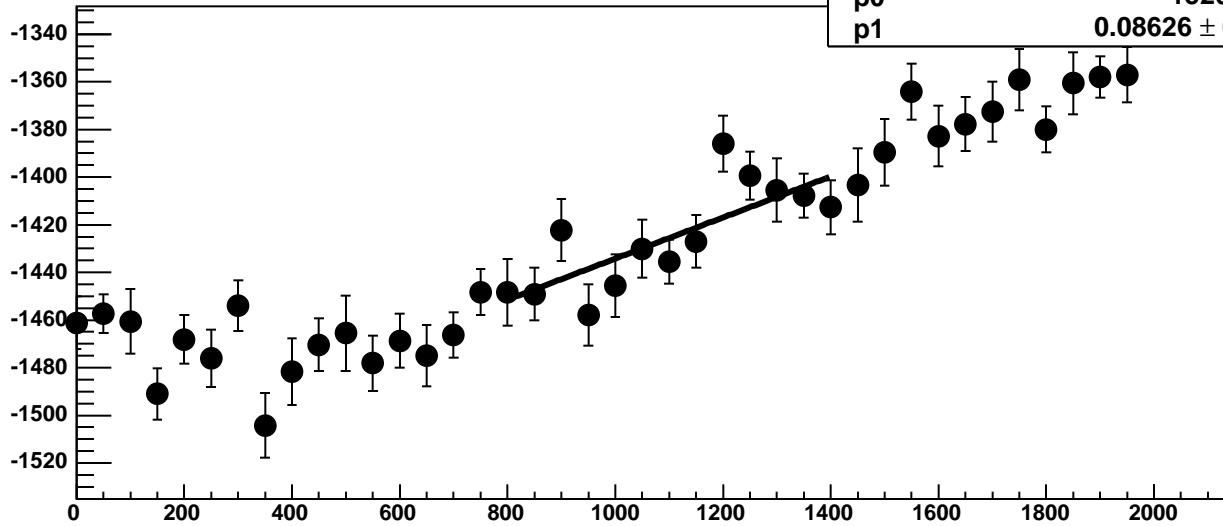
Chip 7, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC



Chip 7, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC

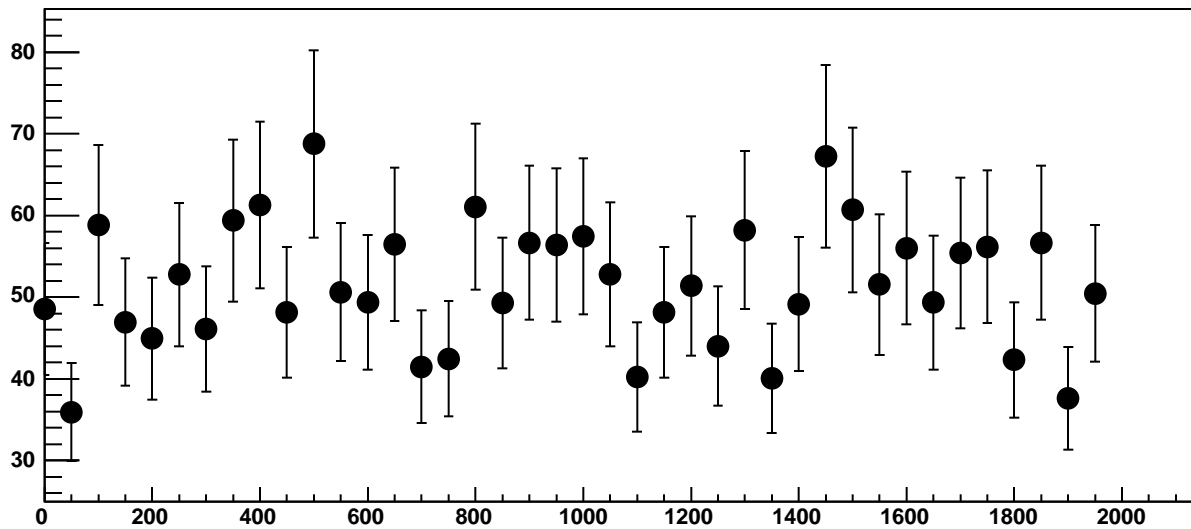


Chip 7, Channel 3, Enable 1, Hold=30, ADC Mean vs DAC

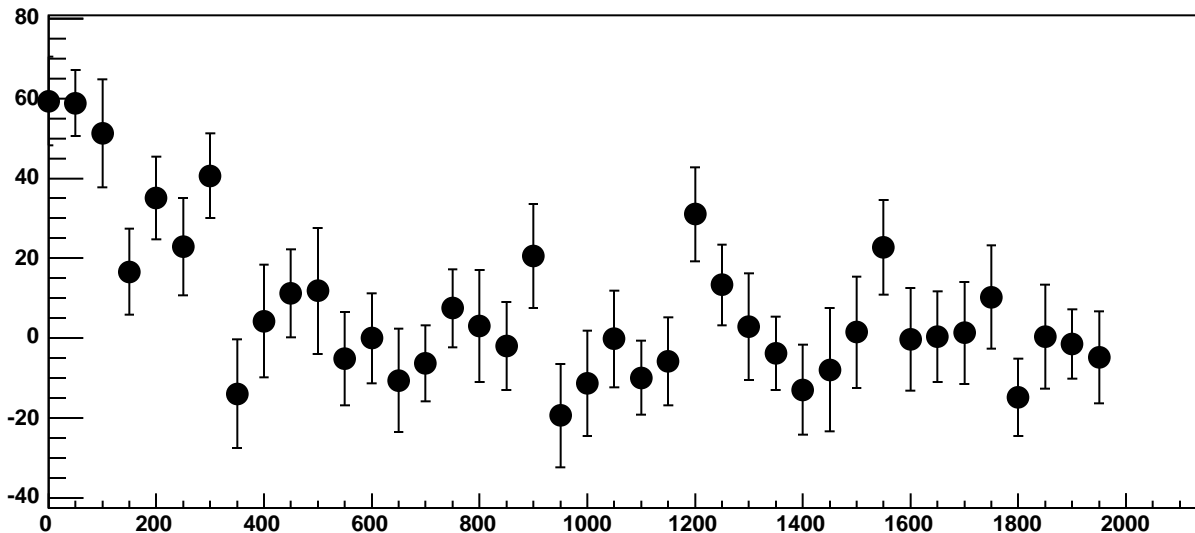


$\chi^2 / \text{ndf}$  17.22 / 11  
p0  $-1520 \pm 19.7$   
p1  $0.08626 \pm 0.01729$

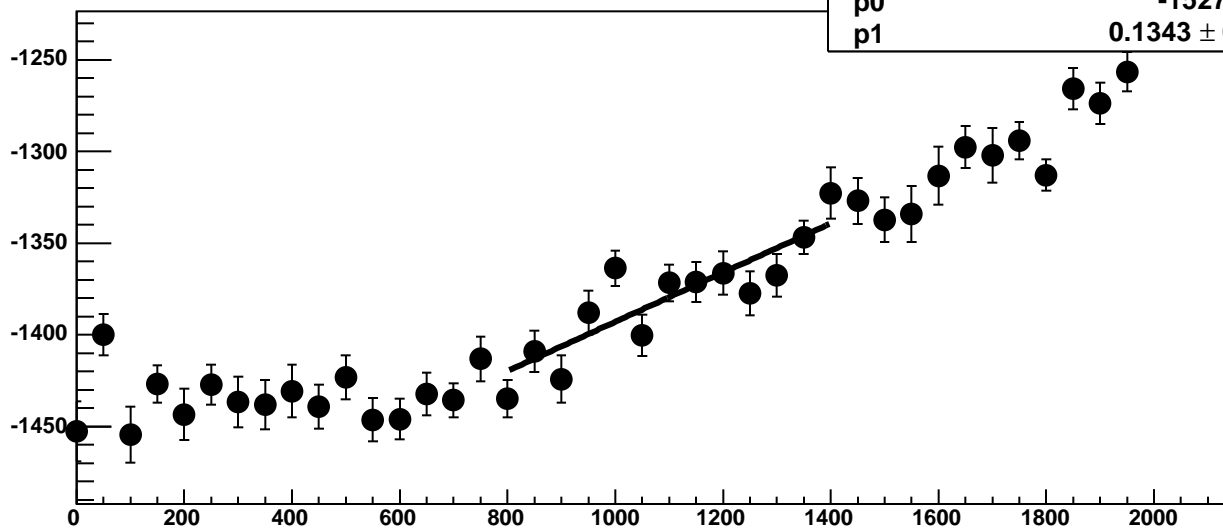
Chip 7, Channel 3, Enable 1, Hold=30, ADC Noise vs DAC



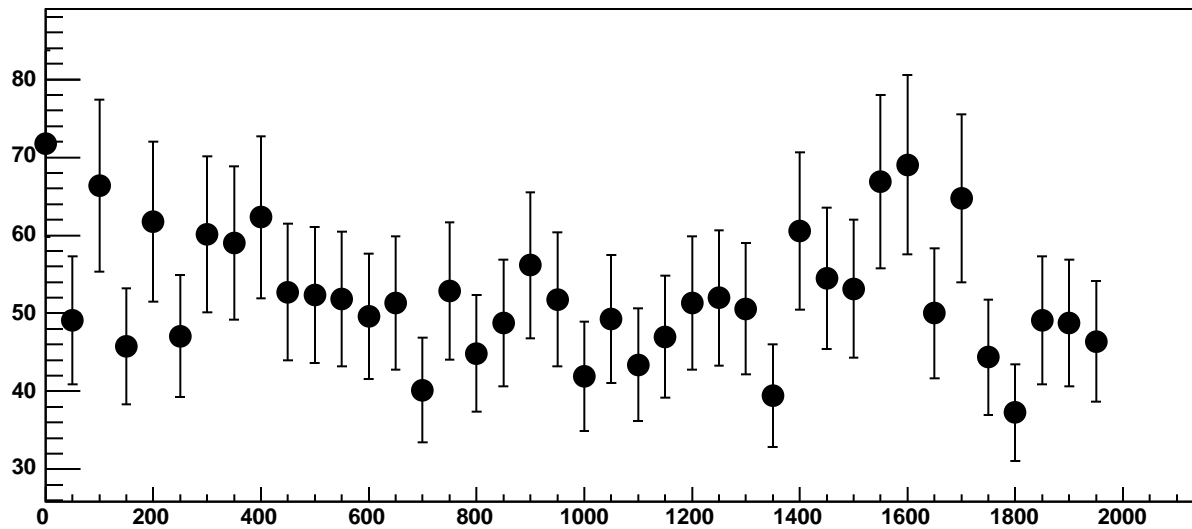
Chip 7, Channel 3, Enable 1, Hold=30, ADC Residuals vs DAC



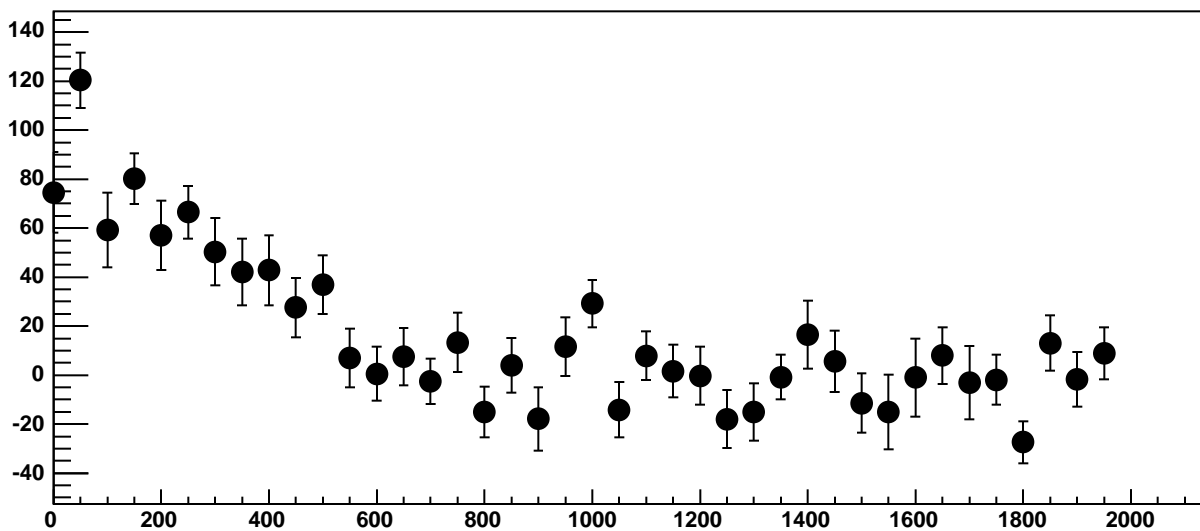
Chip 7, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC



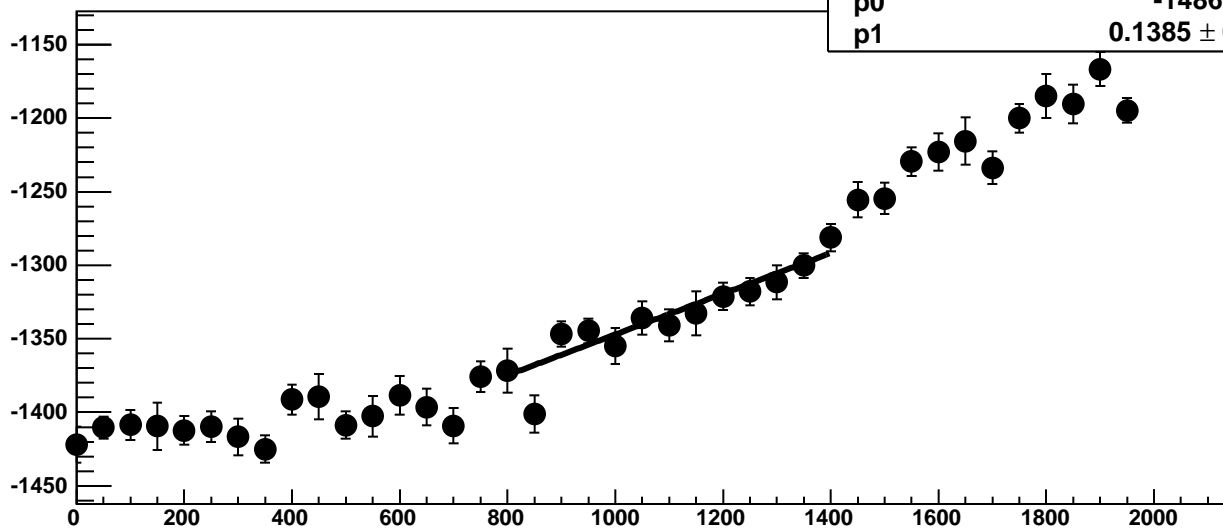
Chip 7, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC



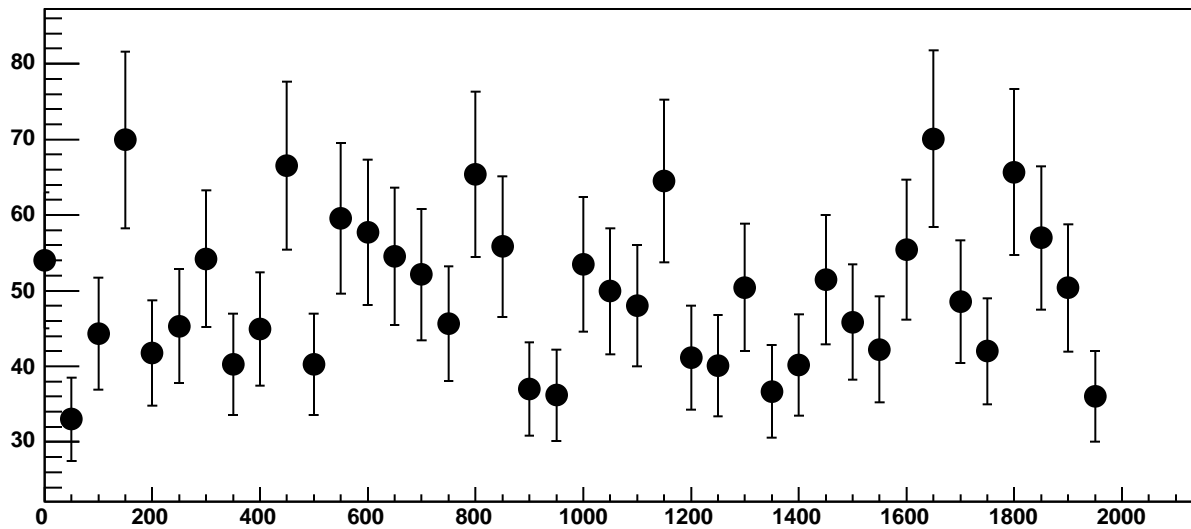
Chip 7, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC



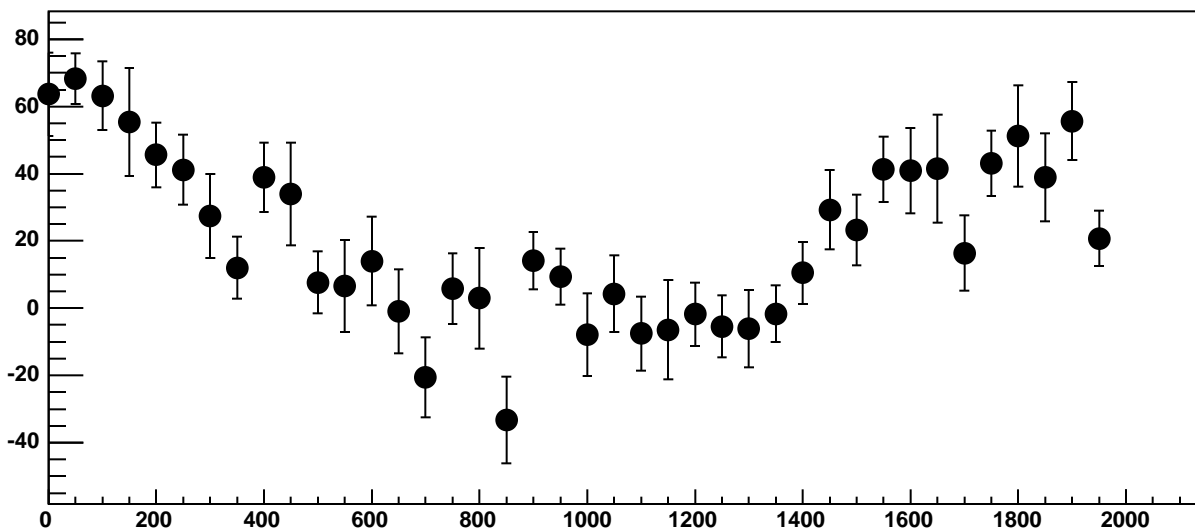
Chip 7, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC



Chip 7, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC

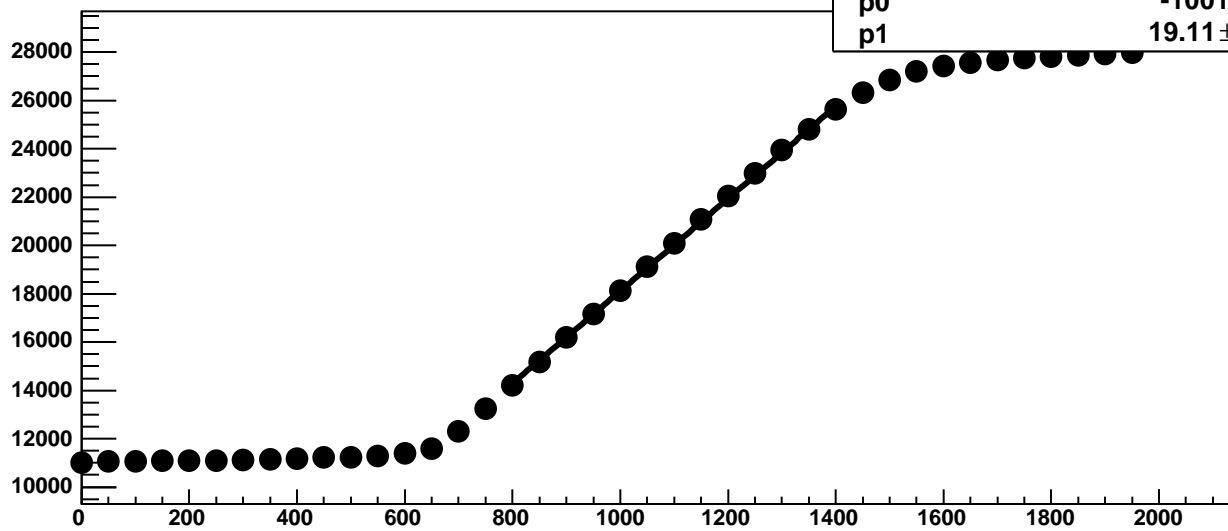


Chip 7, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC



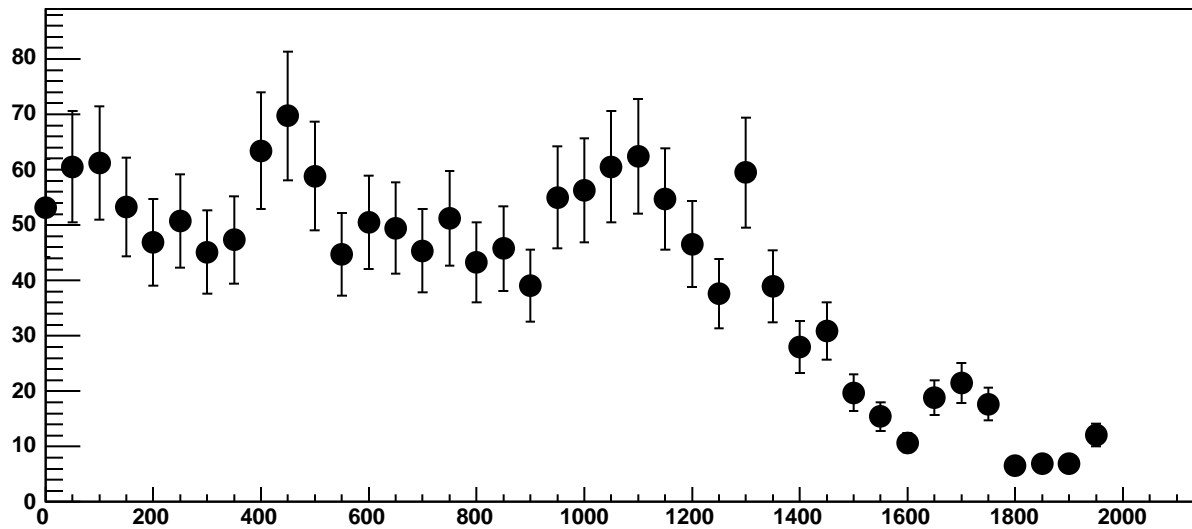


Chip 7, Channel 3, Enable 4!, Hold=30, ADC Mean vs DAC

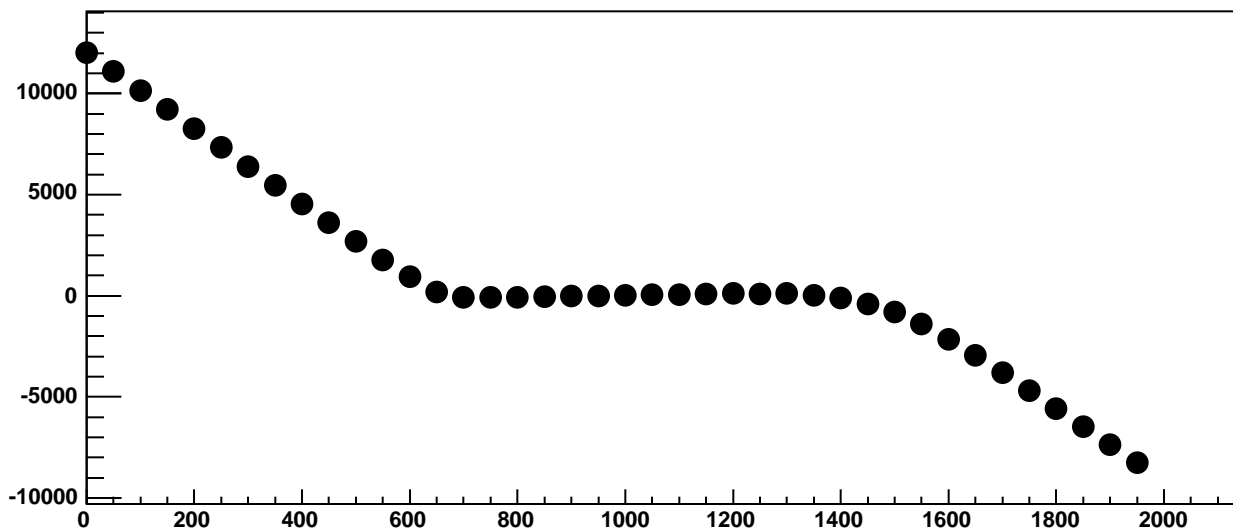


$\chi^2 / \text{ndf}$	894.2 / 11
p0	-1001 ± 15.54
p1	19.11 ± 0.0134

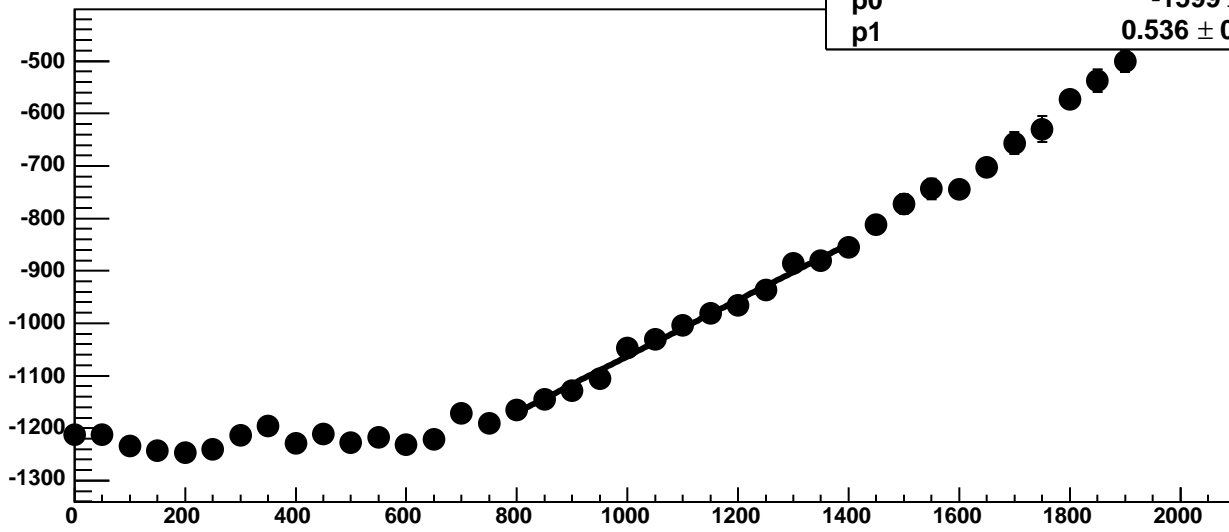
Chip 7, Channel 3, Enable 4!, Hold=30, ADC Noise vs DAC



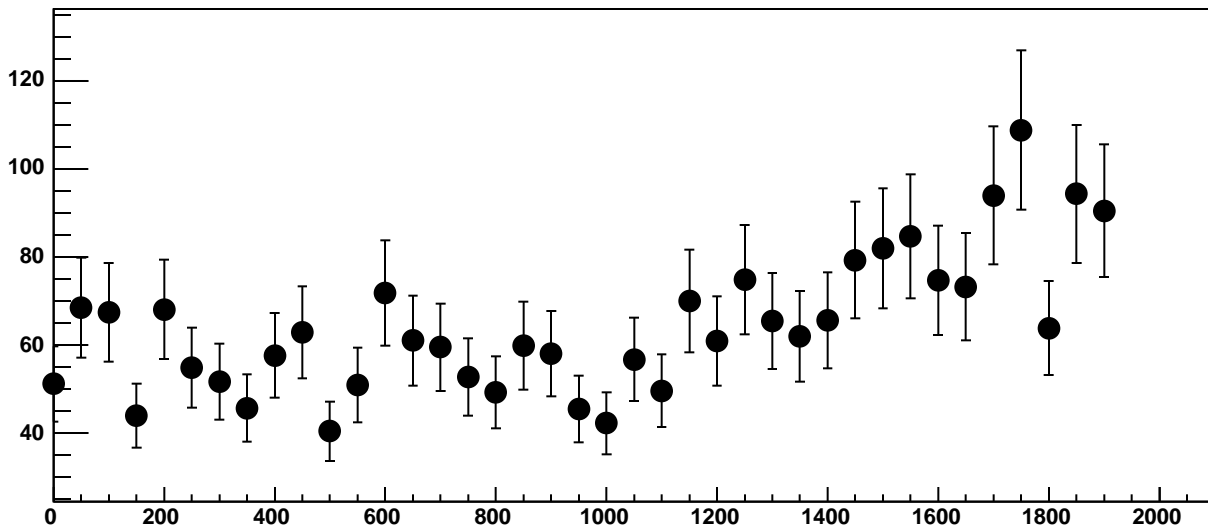
Chip 7, Channel 3, Enable 4!, Hold=30, ADC Residuals vs DAC



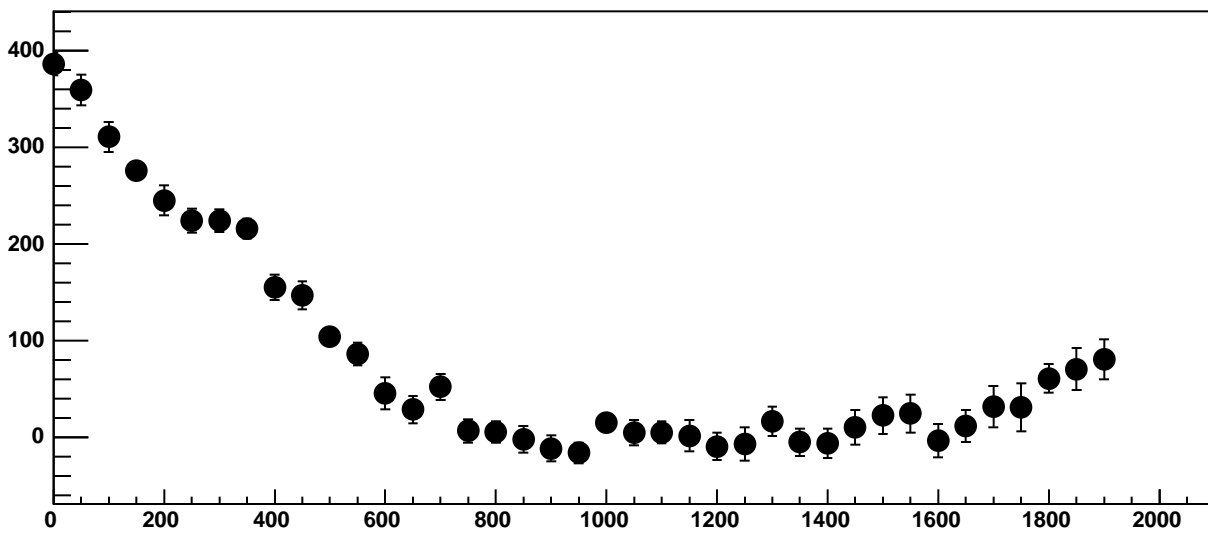
Chip 7, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC



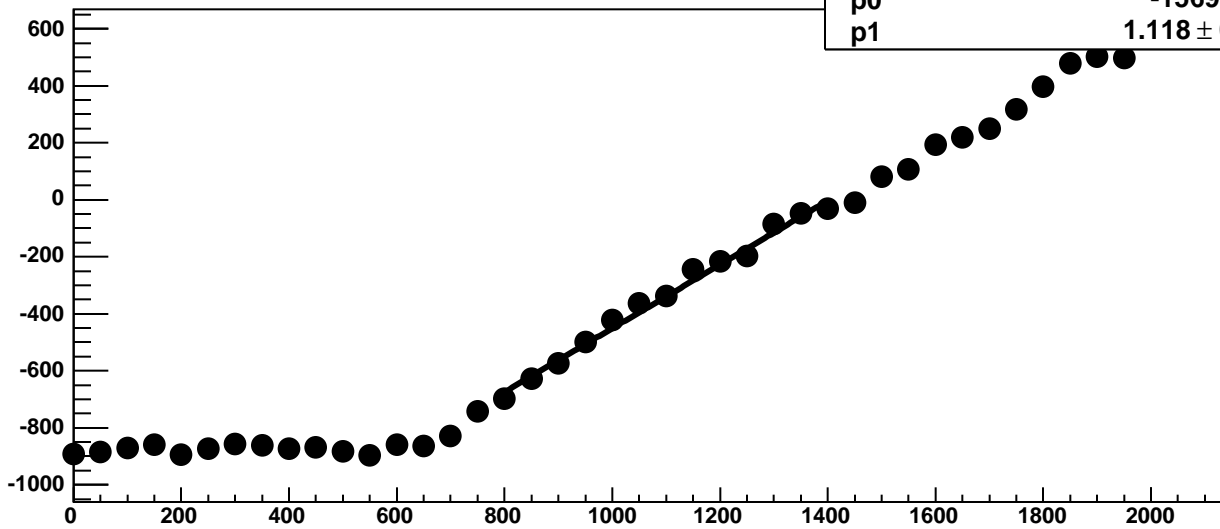
Chip 7, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC



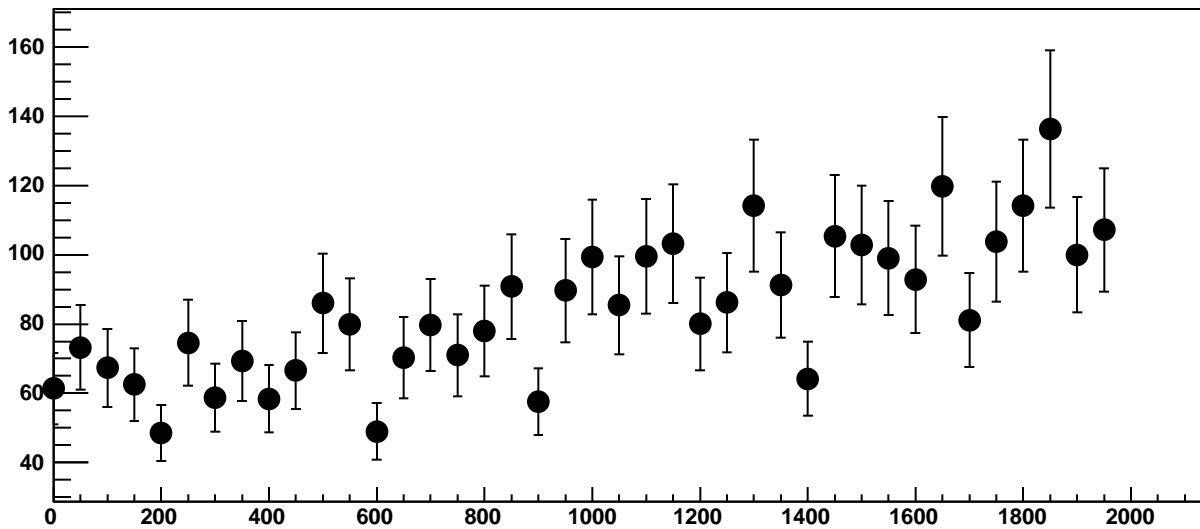
Chip 7, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC



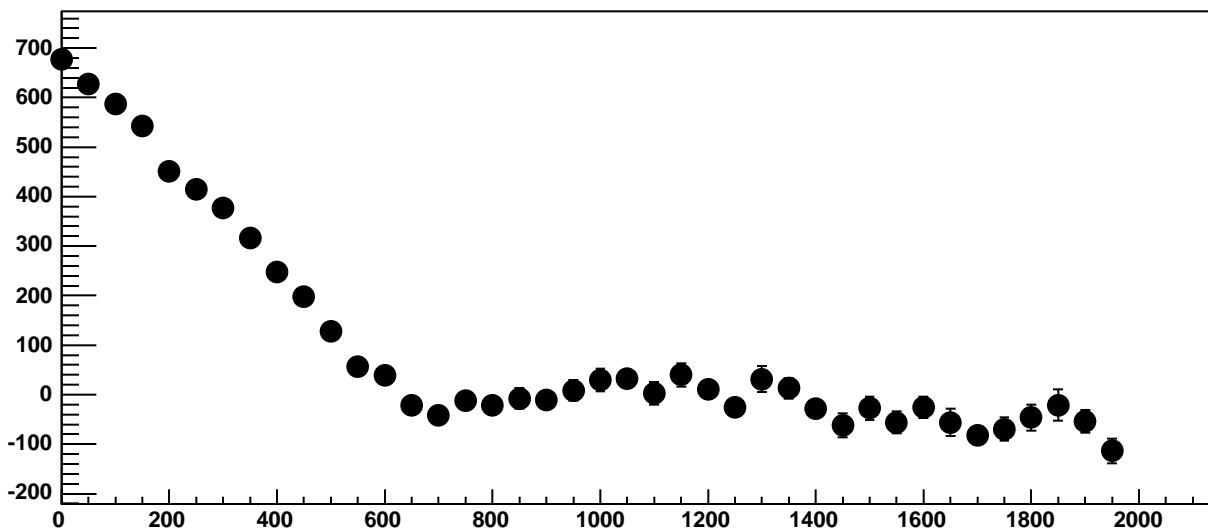
Chip 7, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC



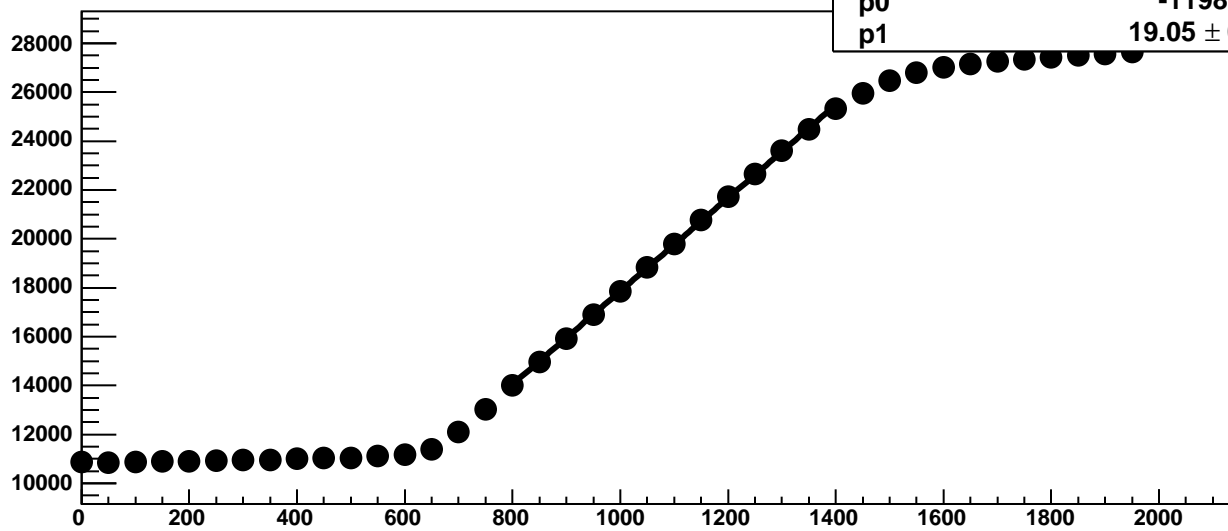
Chip 7, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC



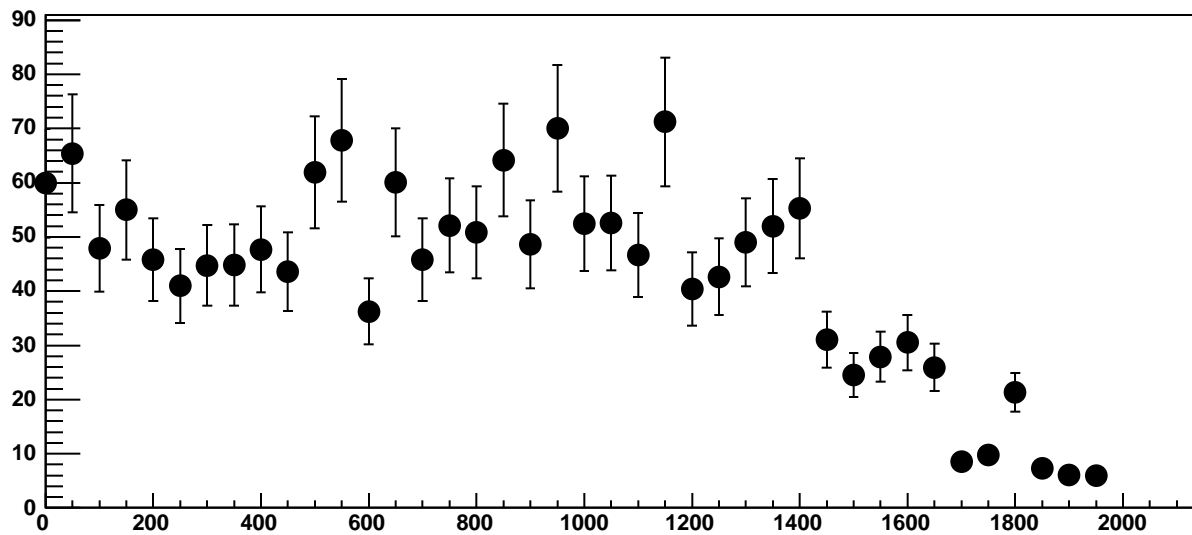
Chip 7, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC



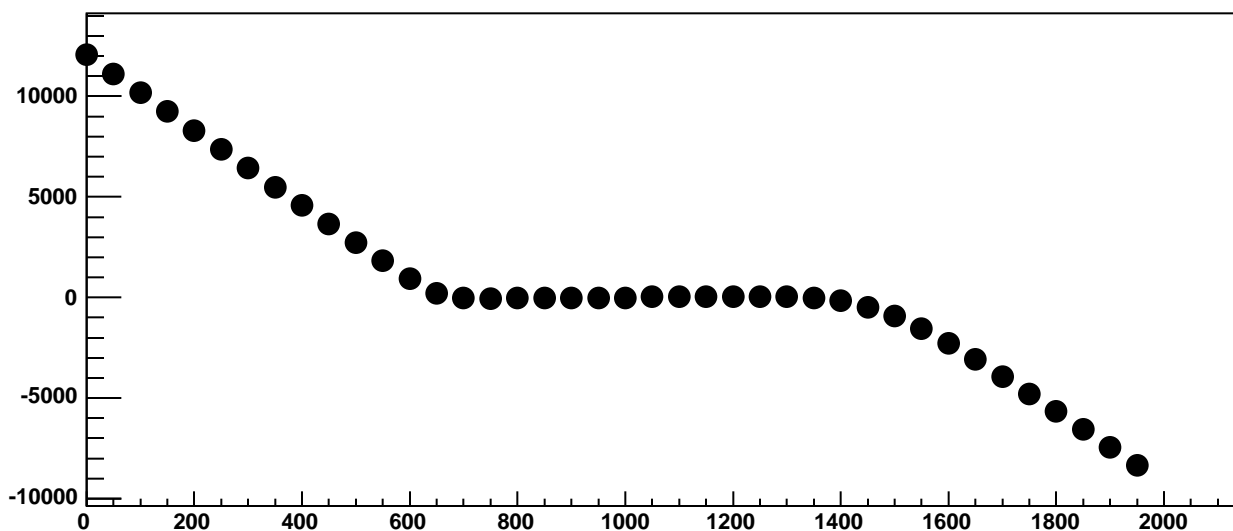
Chip 7, Channel 4, Enable 1!, Hold=30, ADC Mean vs DAC



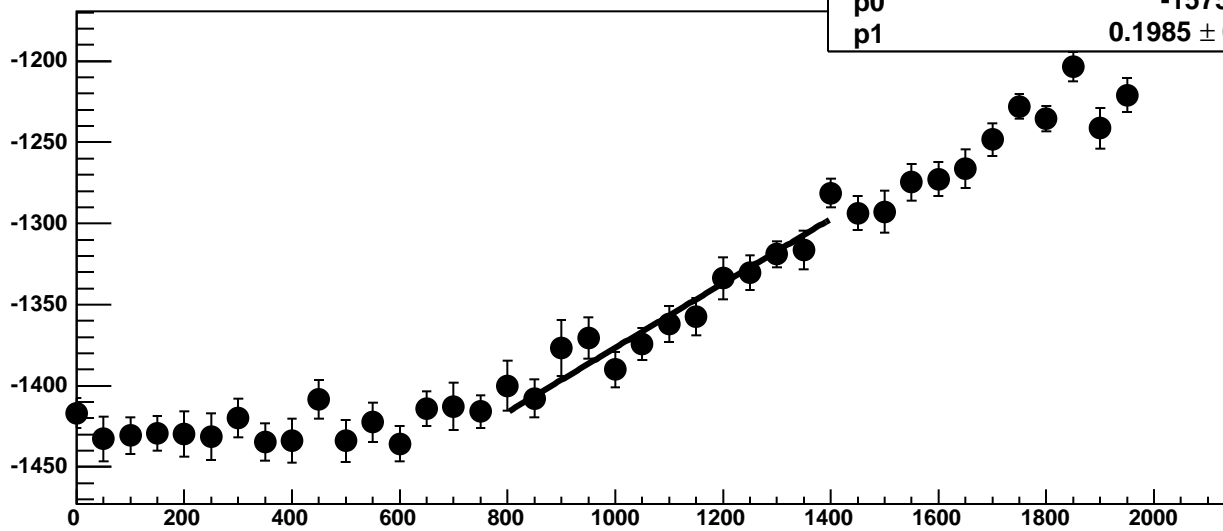
Chip 7, Channel 4, Enable 1!, Hold=30, ADC Noise vs DAC



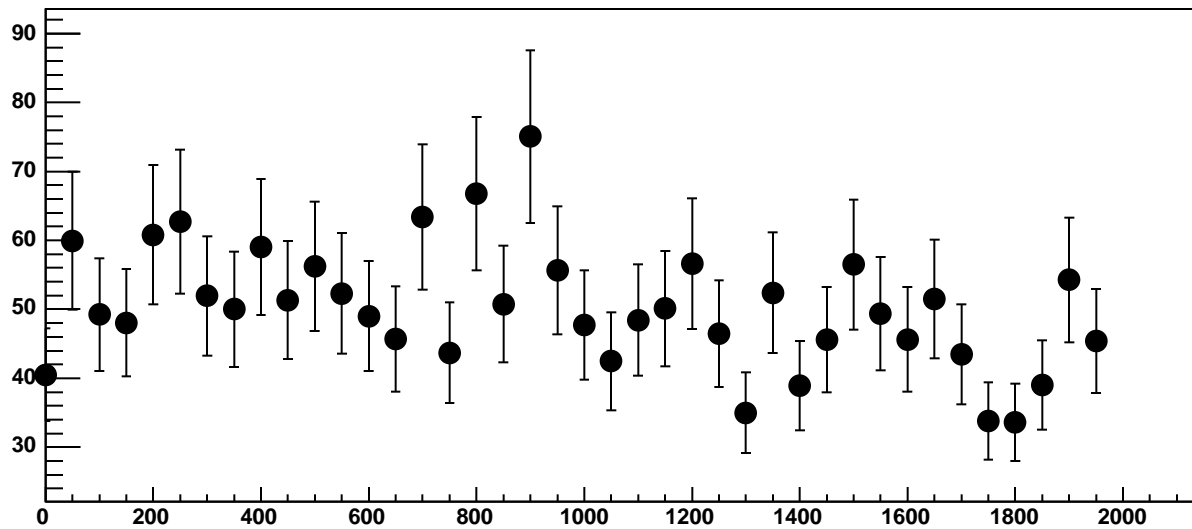
Chip 7, Channel 4, Enable 1!, Hold=30, ADC Residuals vs DAC



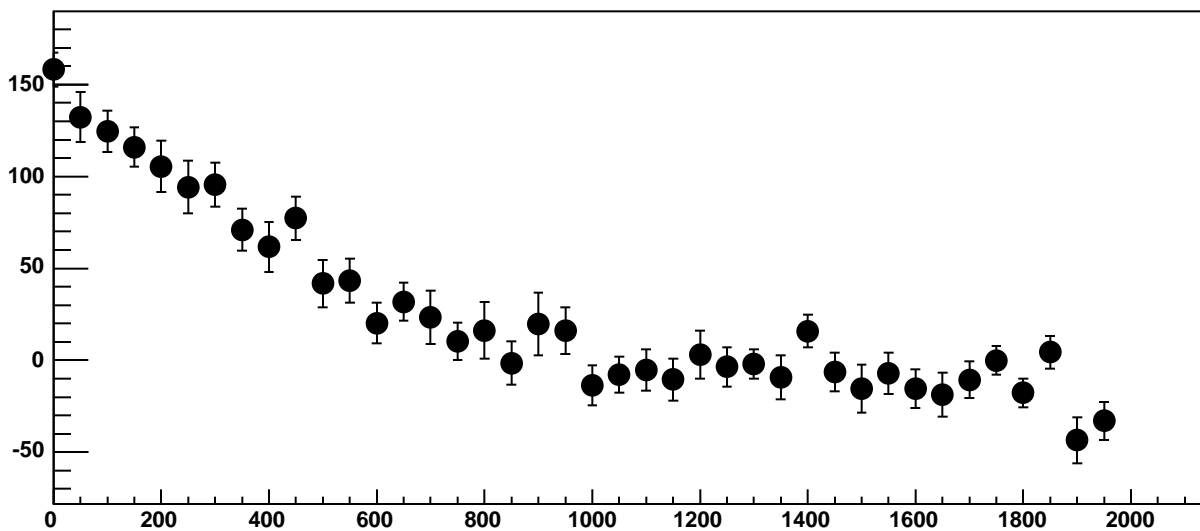
Chip 7, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC



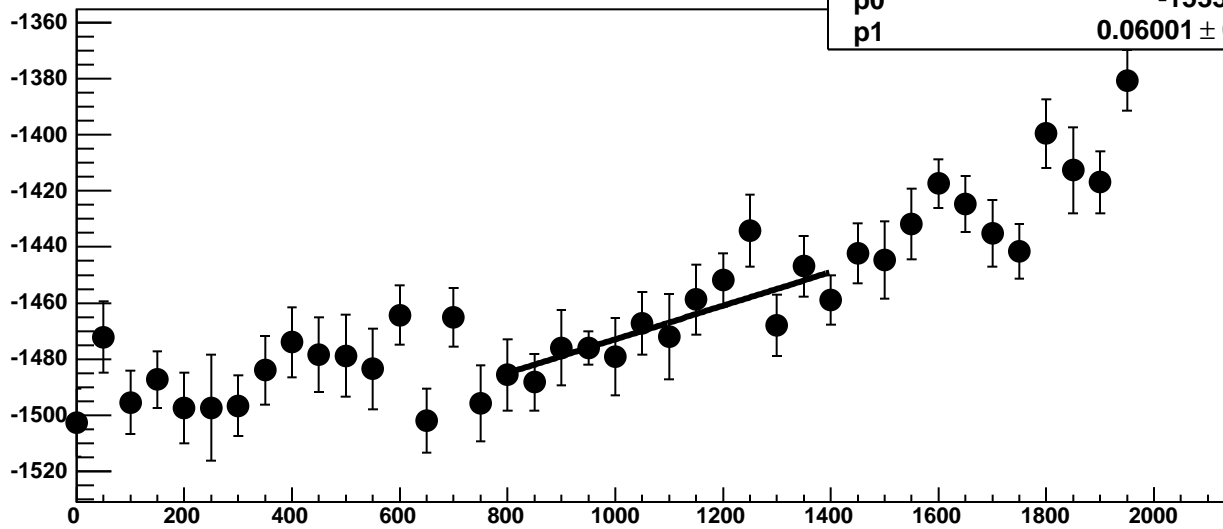
Chip 7, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC



Chip 7, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 7, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

8.182 / 11

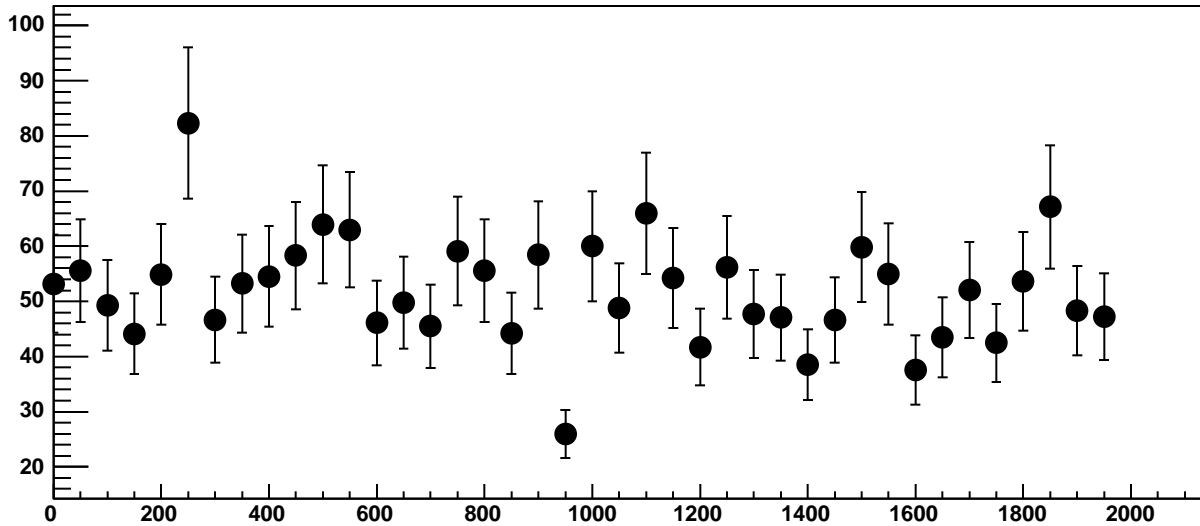
p0

$-1533 \pm 16.81$

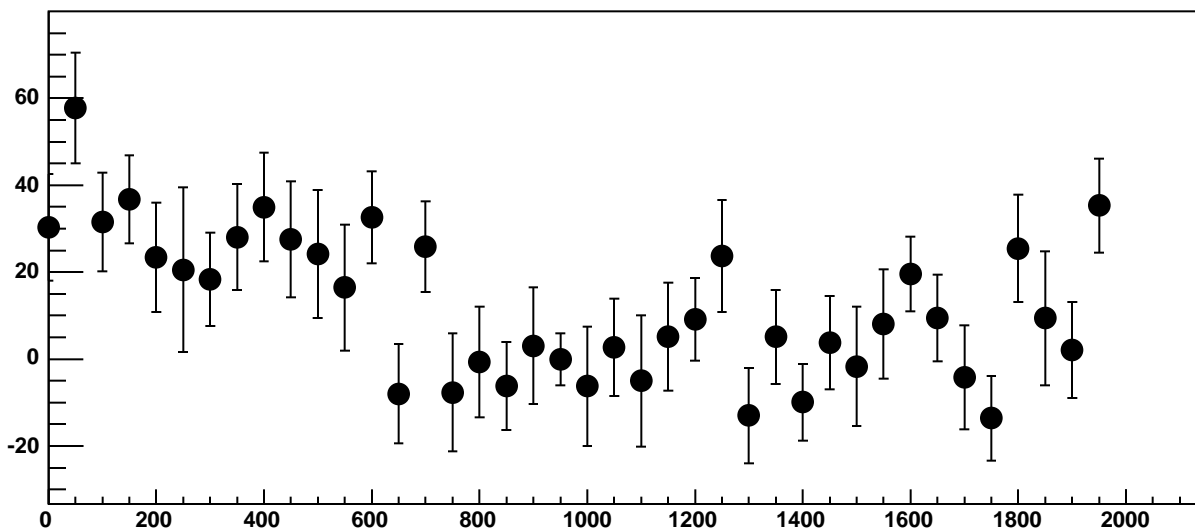
p1

$0.06001 \pm 0.01512$

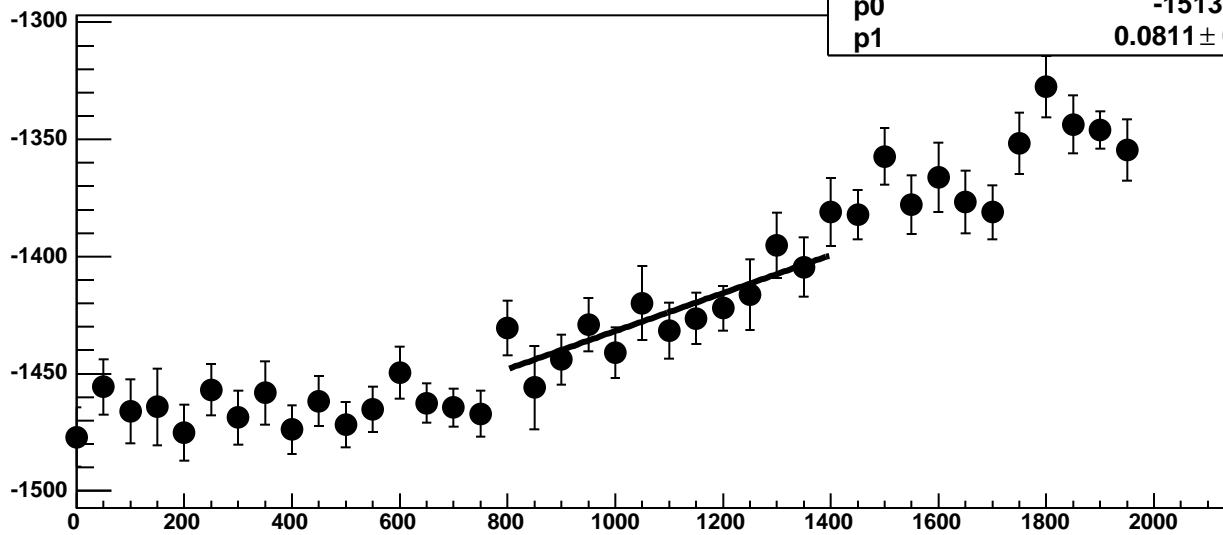
Chip 7, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC



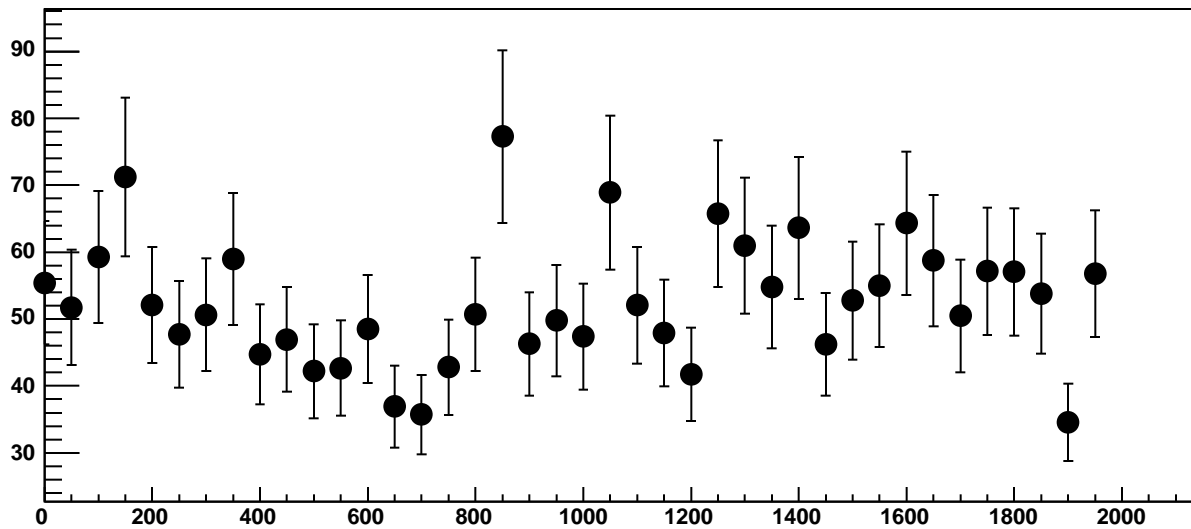
Chip 7, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC



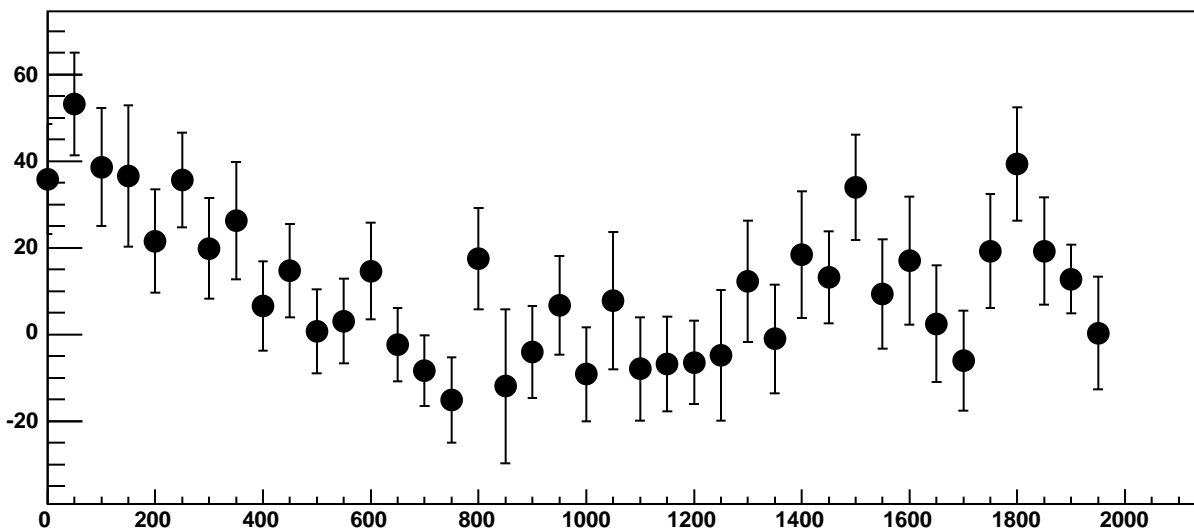
Chip 7, Channel 4, Enable 4, Hold=30, ADC Mean vs DAC



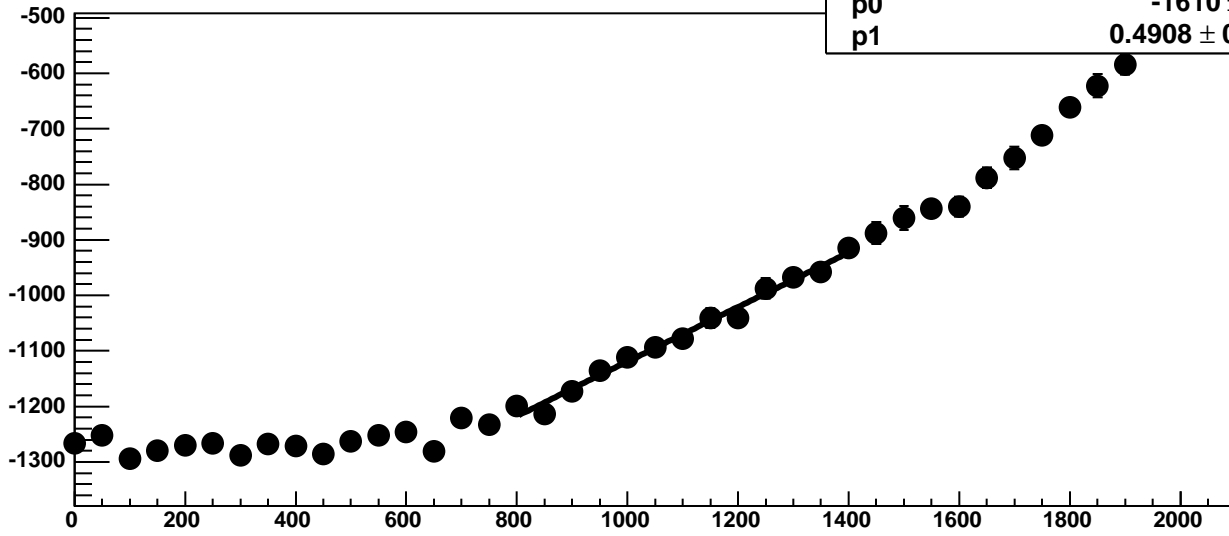
Chip 7, Channel 4, Enable 4, Hold=30, ADC Noise vs DAC



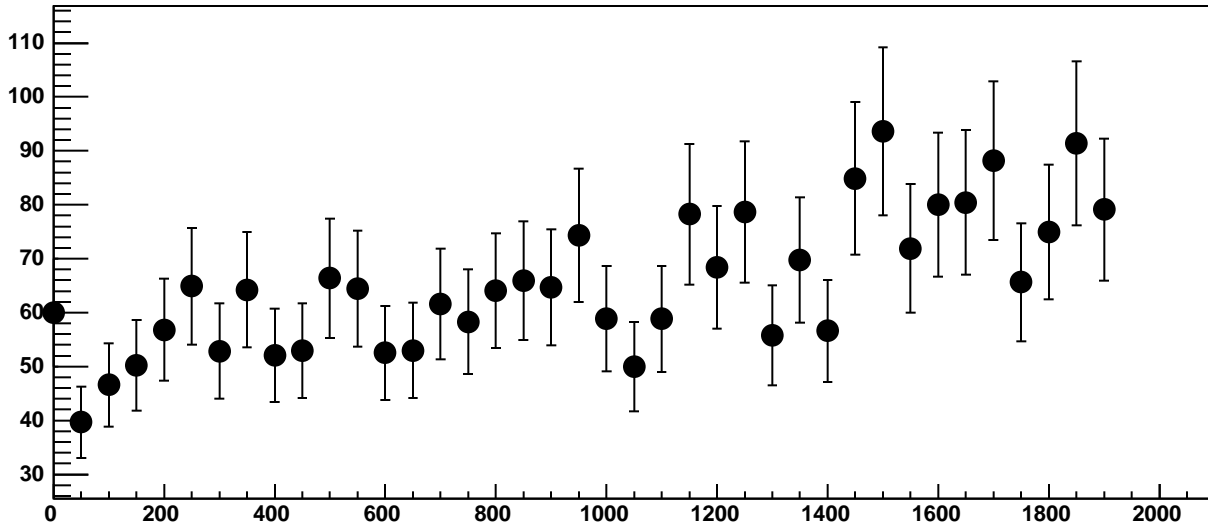
Chip 7, Channel 4, Enable 4, Hold=30, ADC Residuals vs DAC



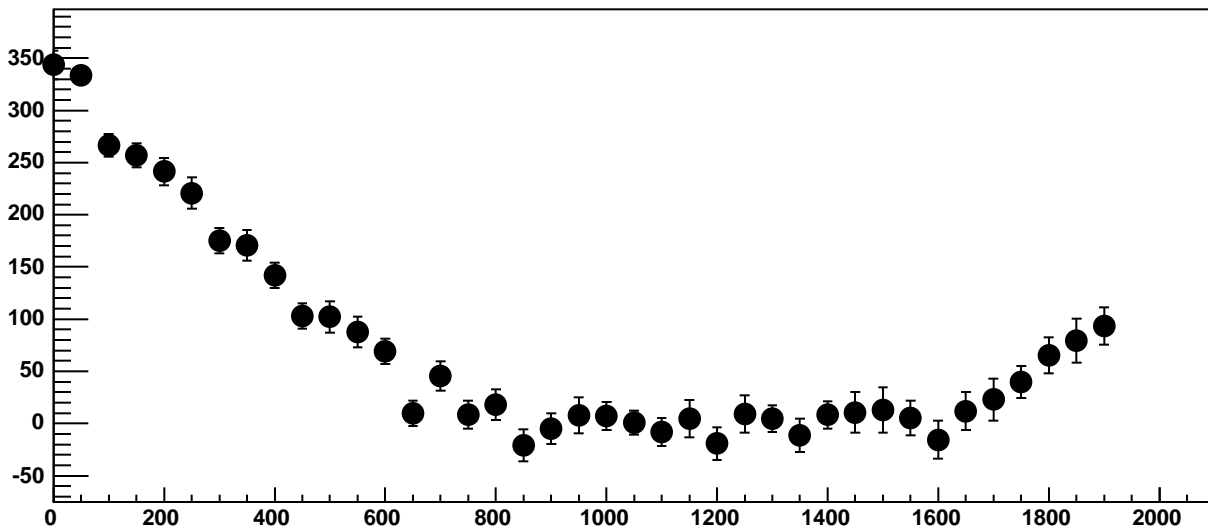
Chip 7, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC



Chip 7, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC

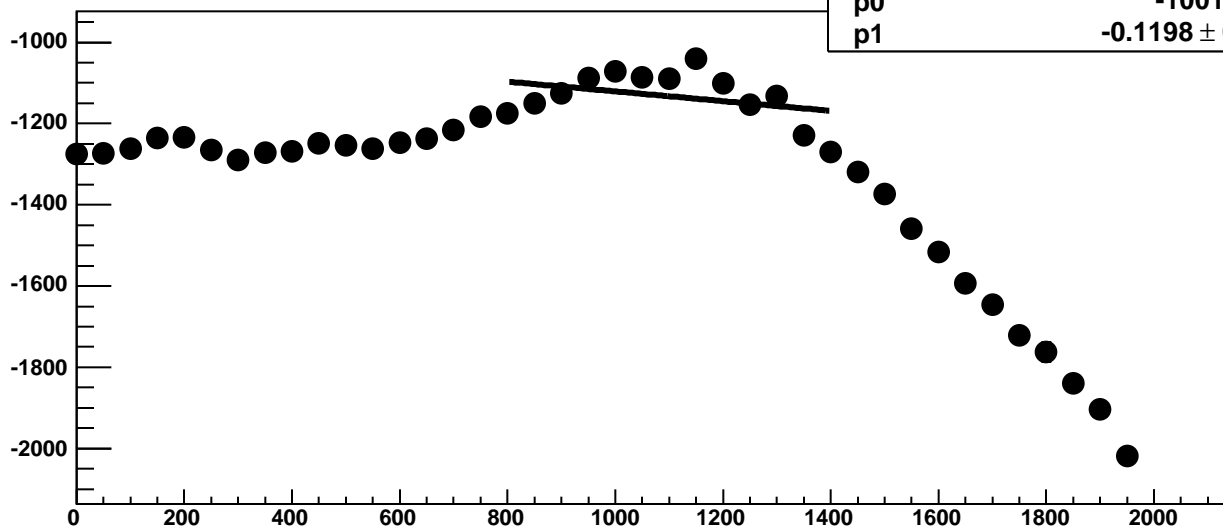


Chip 7, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC





Chip 7, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

168.5 / 11

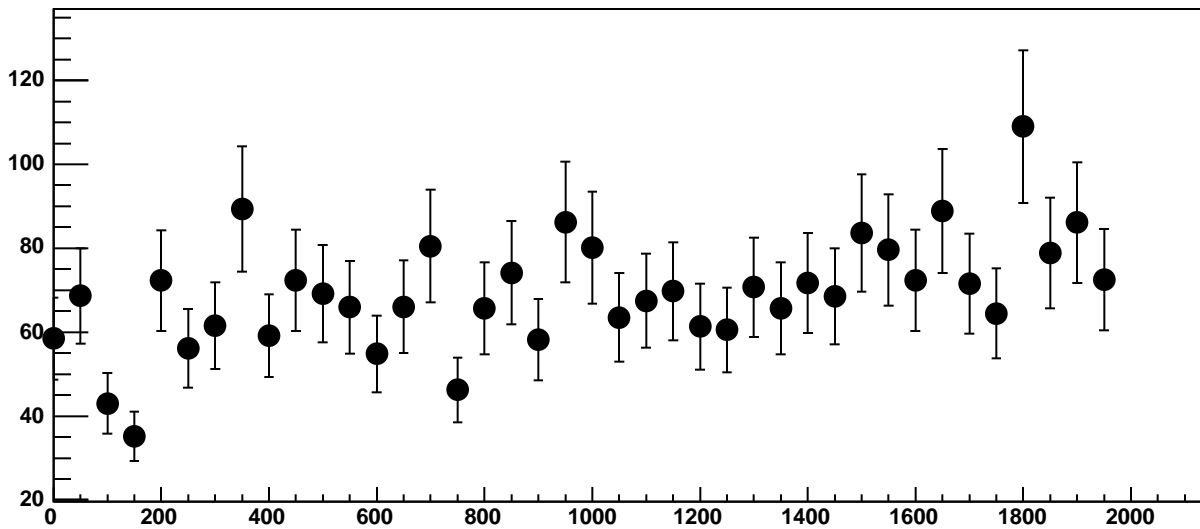
p0

$-1001 \pm 25.89$

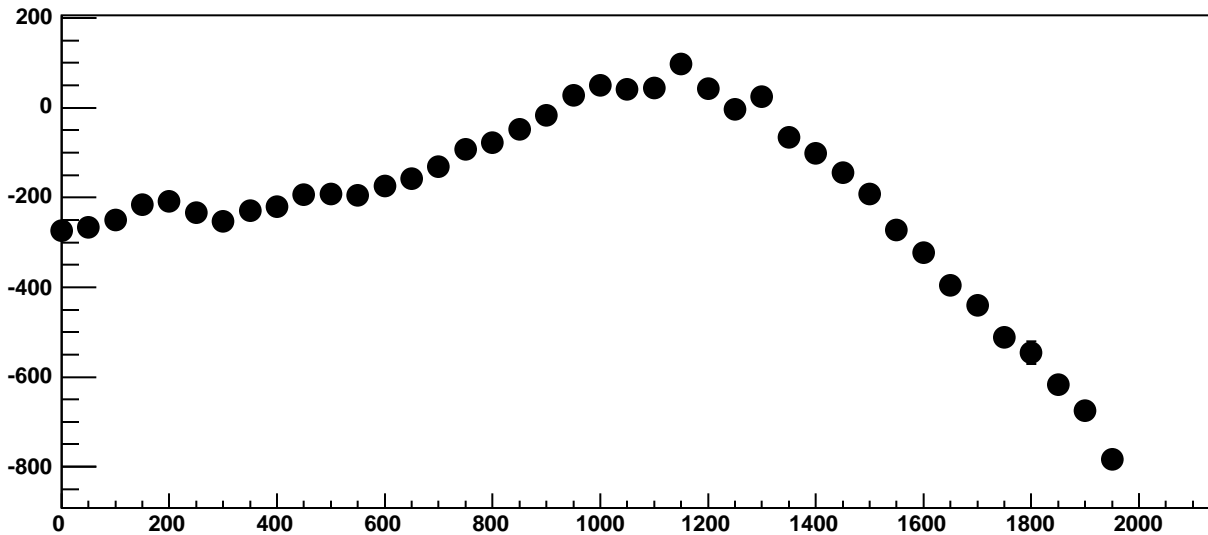
p1

$-0.1198 \pm 0.02312$

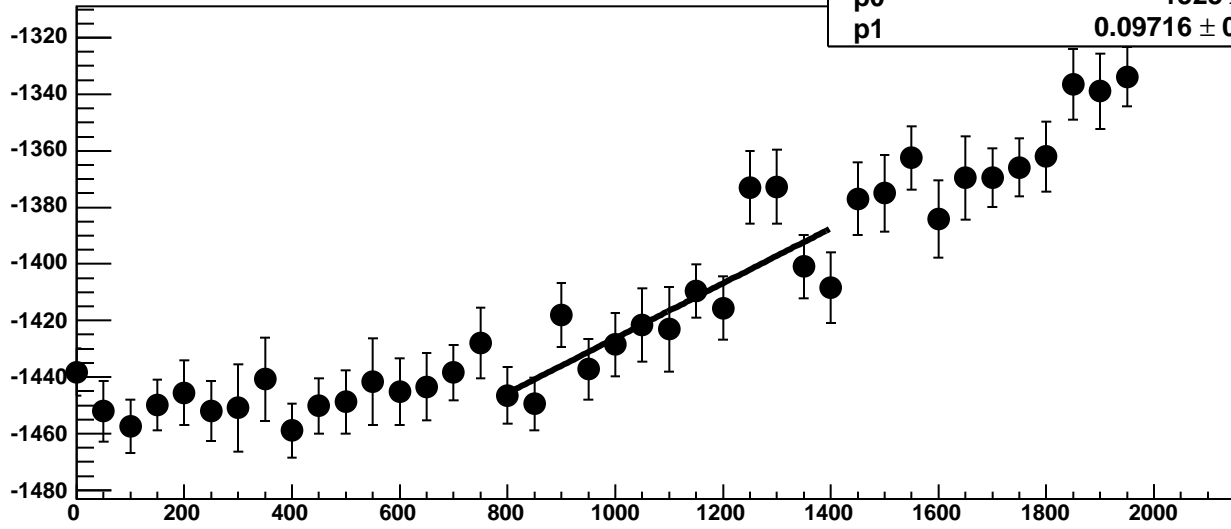
Chip 7, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC



Chip 7, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC

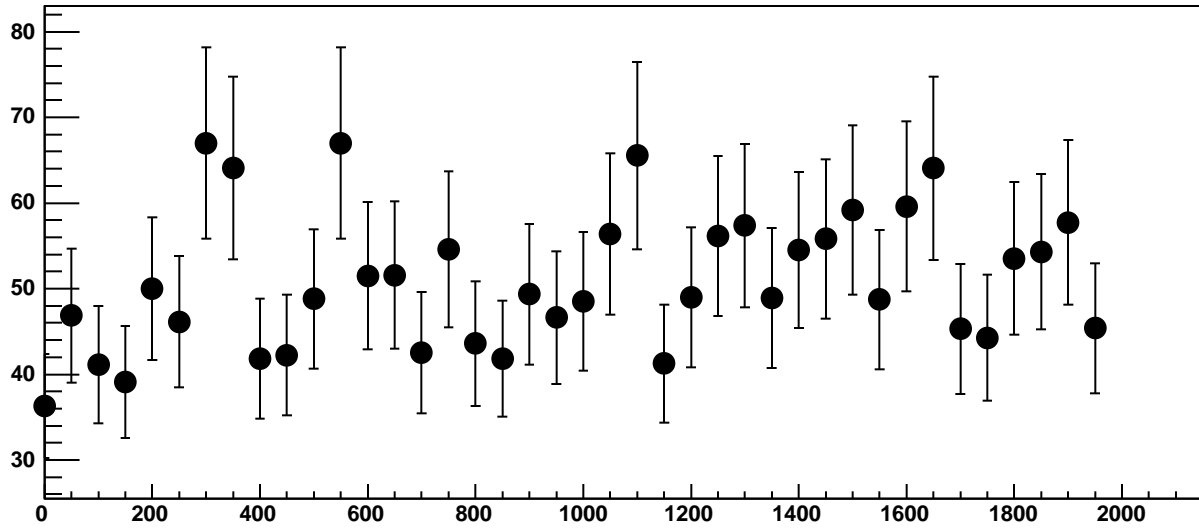


Chip 7, Channel 5, Enable 1, Hold=30, ADC Mean vs DAC

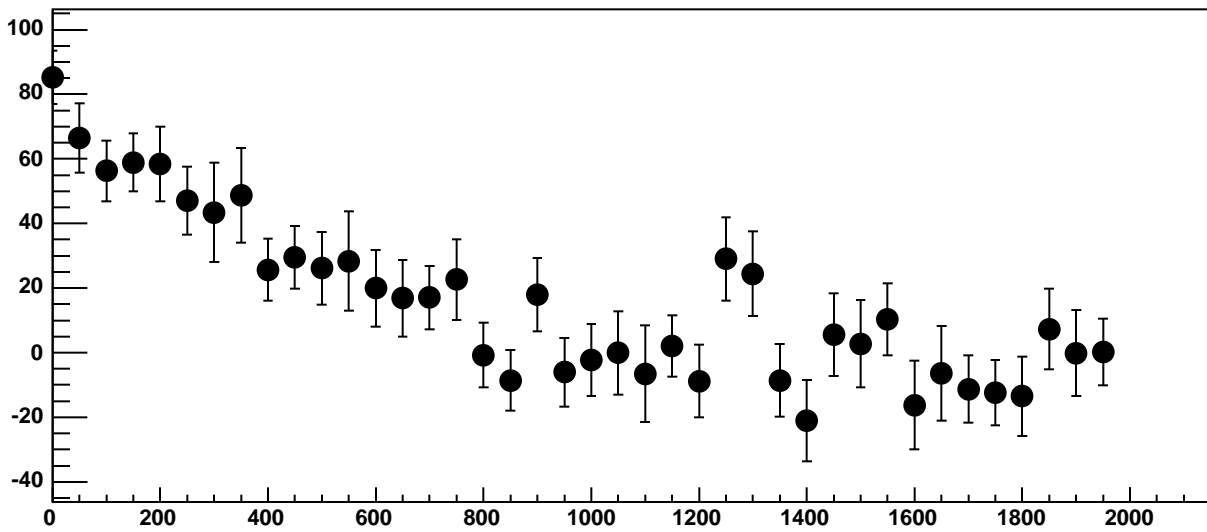


$\chi^2 / \text{ndf}$  16.52 / 11  
p0  $-1523 \pm 17.95$   
p1  $0.09716 \pm 0.01644$

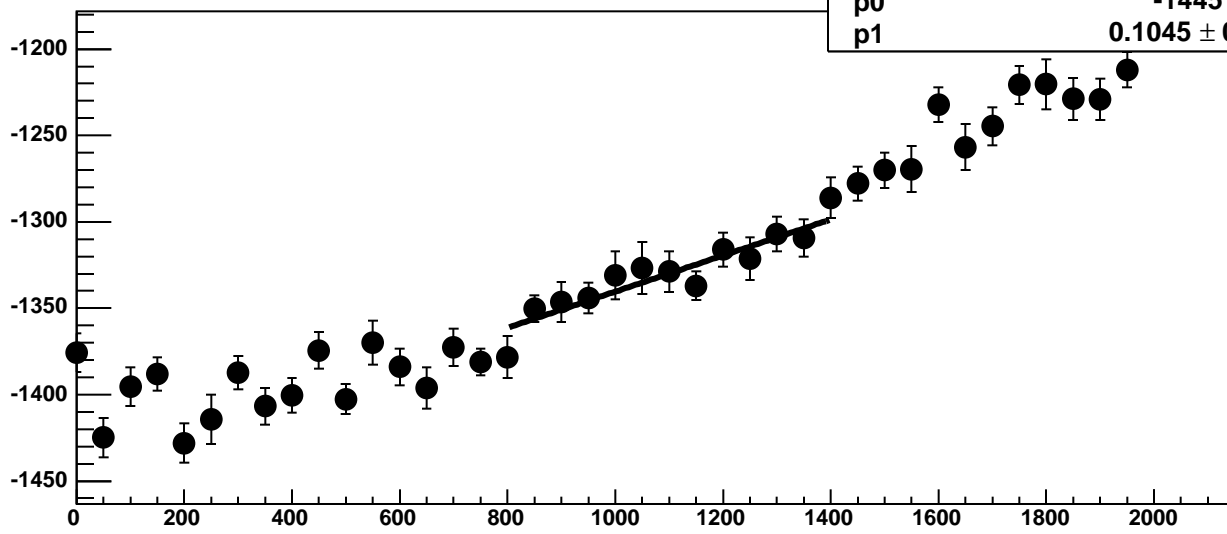
Chip 7, Channel 5, Enable 1, Hold=30, ADC Noise vs DAC



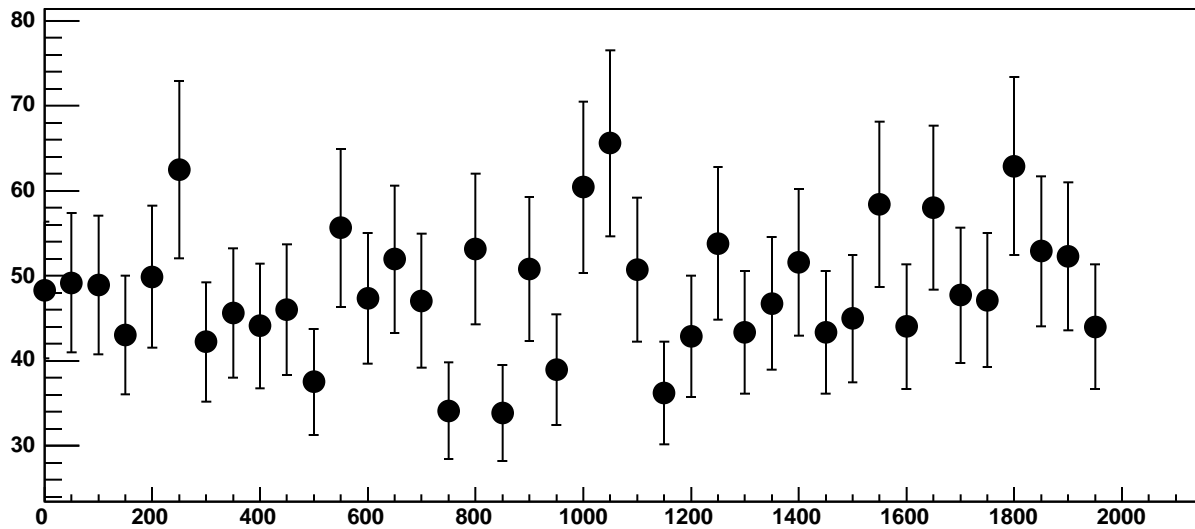
Chip 7, Channel 5, Enable 1, Hold=30, ADC Residuals vs DAC



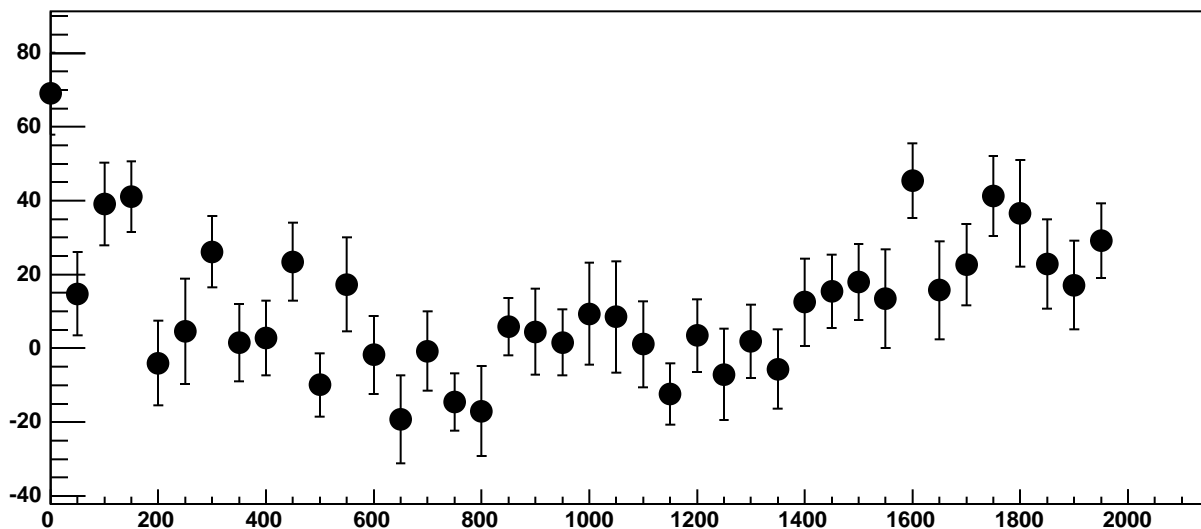
Chip 7, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC



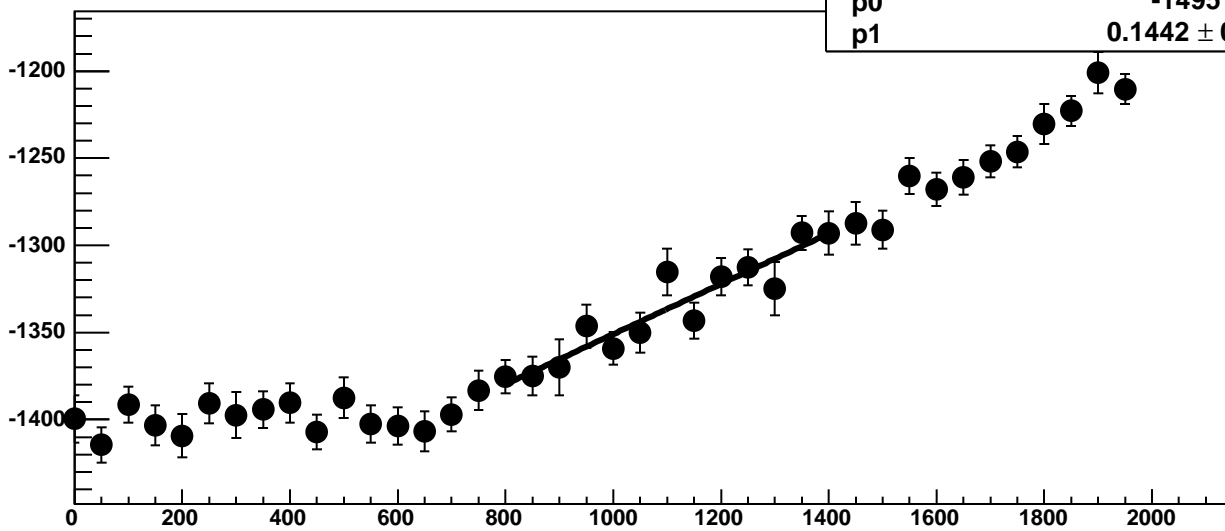
Chip 7, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC



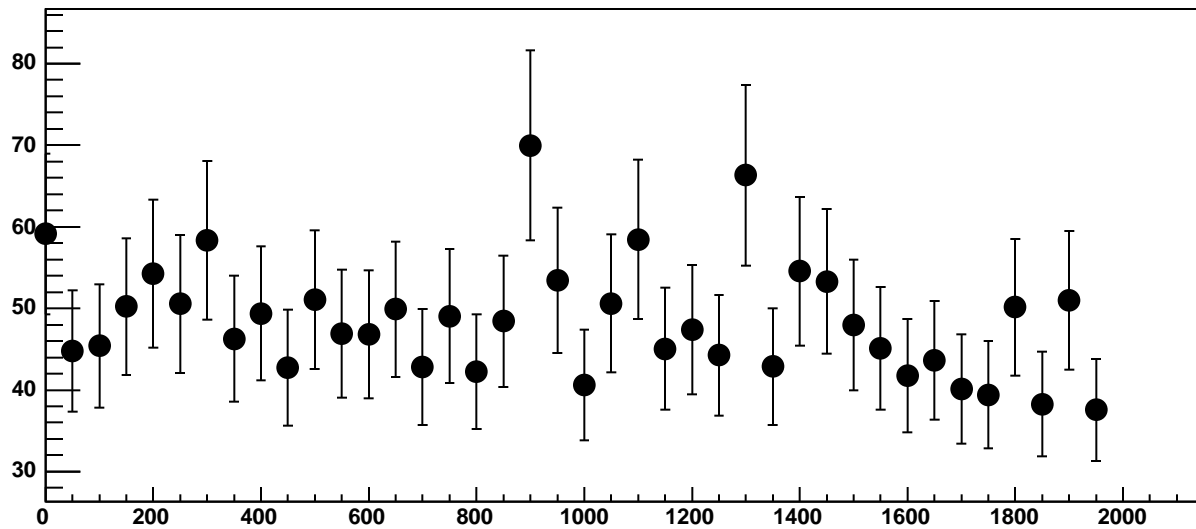
Chip 7, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC



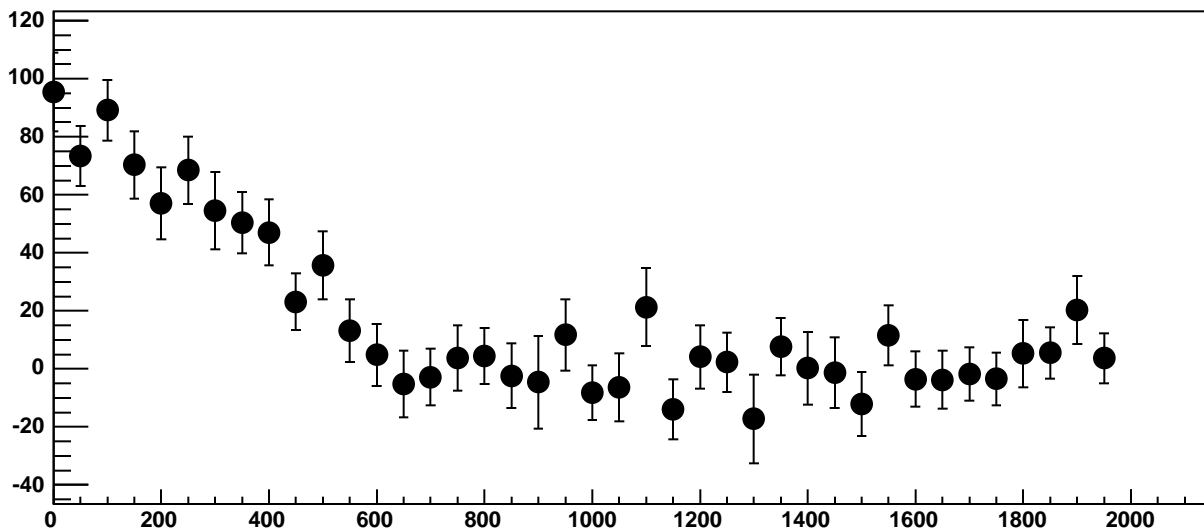
Chip 7, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC



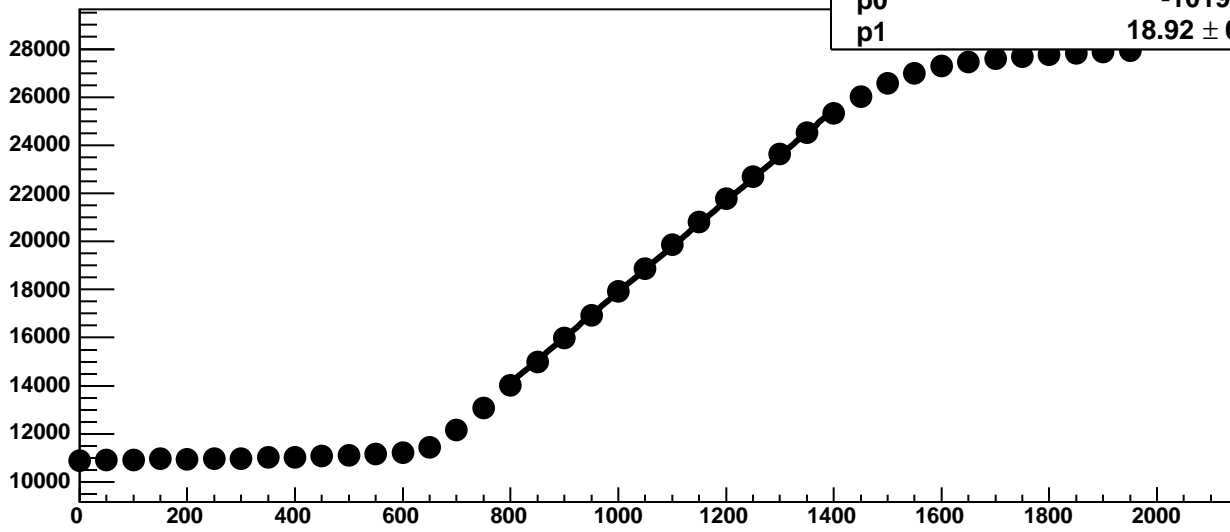
Chip 7, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC



Chip 7, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC

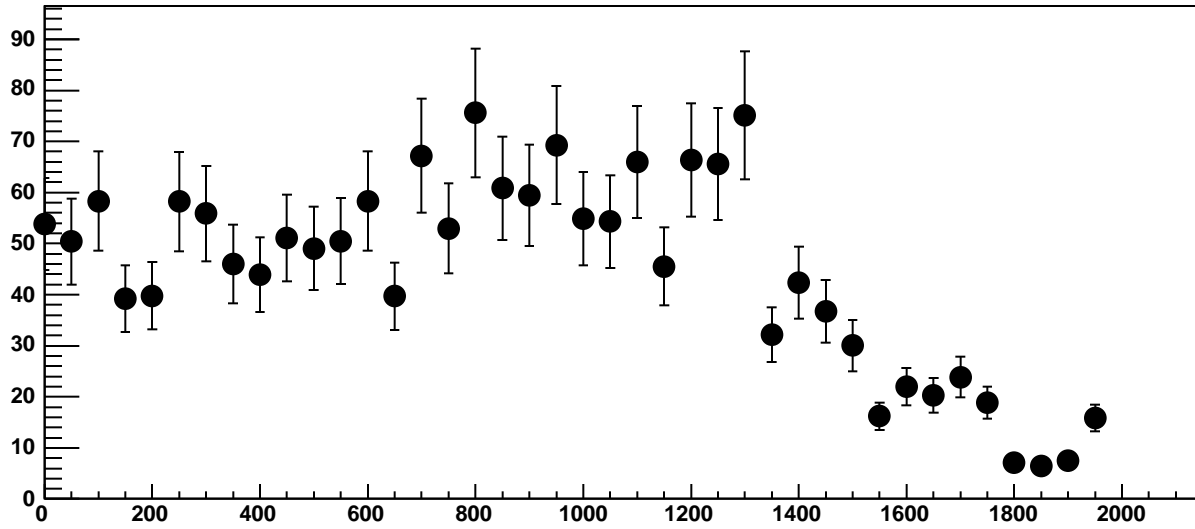


Chip 7, Channel 5, Enable 4!, Hold=30, ADC Mean vs DAC

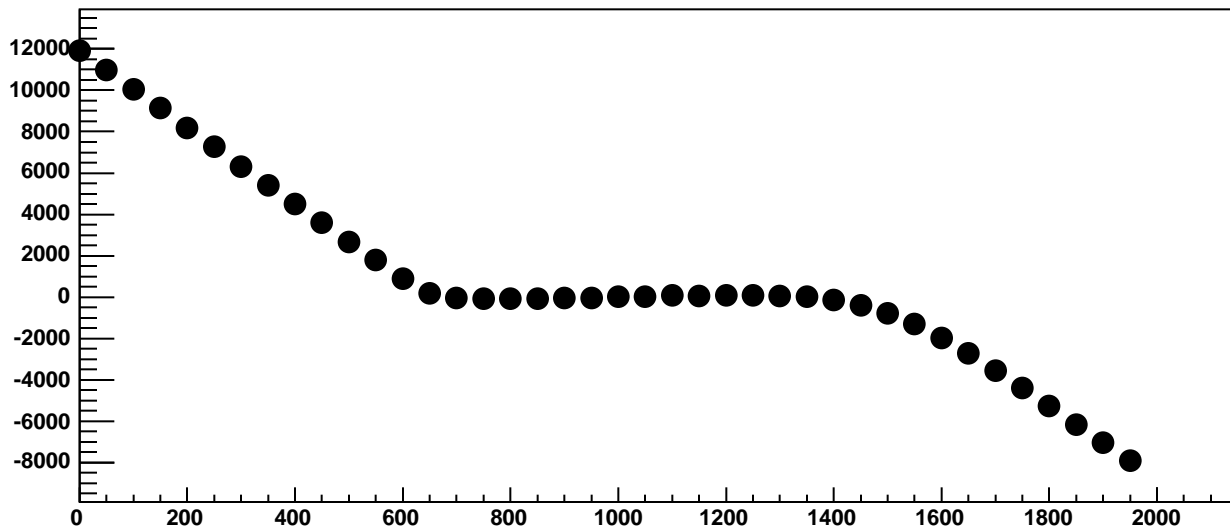


$\chi^2 / \text{ndf}$  446.9 / 11  
p0  $-1019 \pm 21.11$   
p1  $18.92 \pm 0.01796$

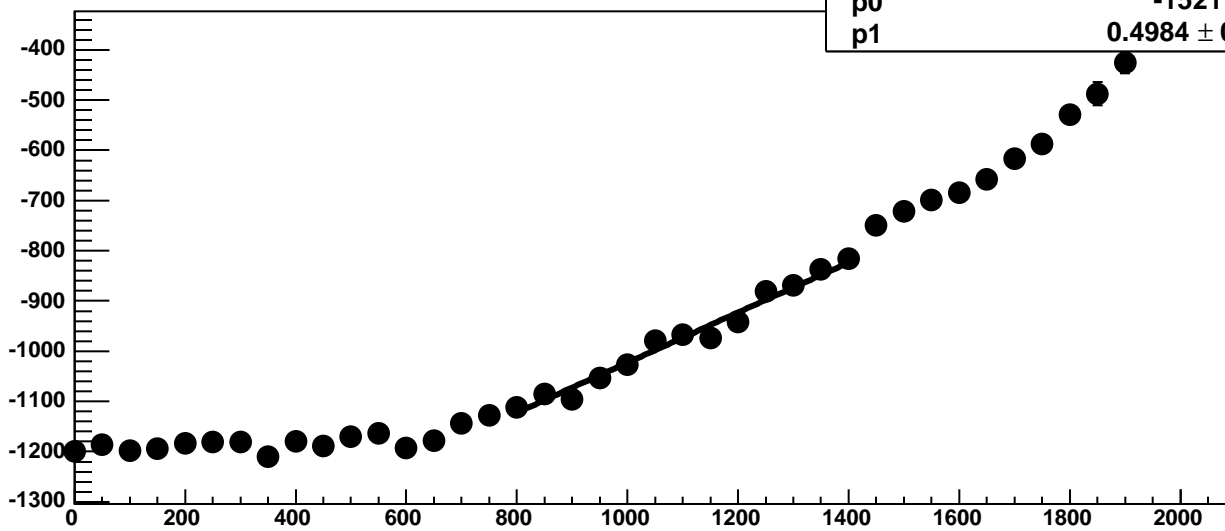
Chip 7, Channel 5, Enable 4!, Hold=30, ADC Noise vs DAC



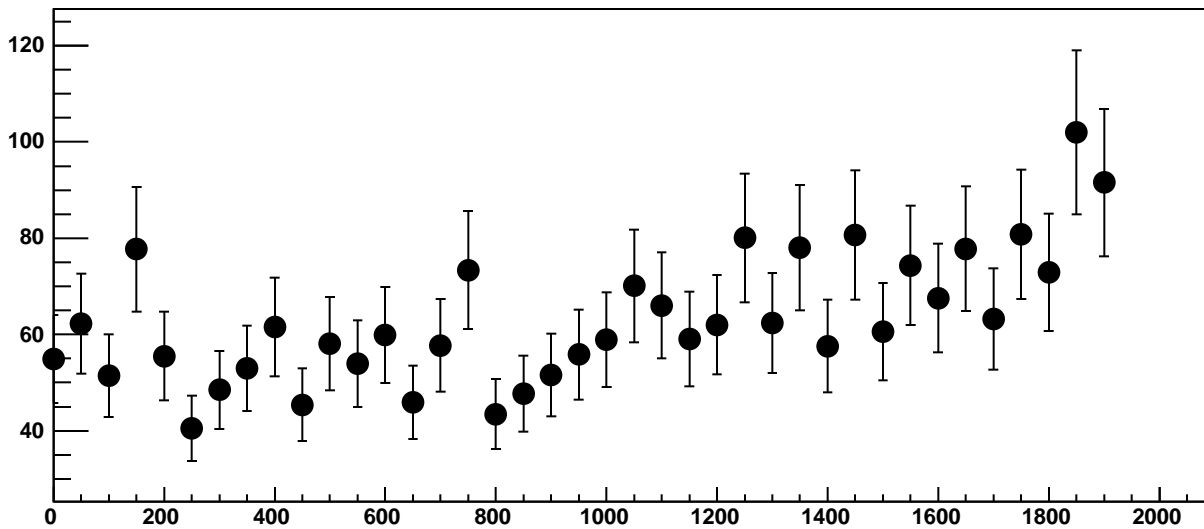
Chip 7, Channel 5, Enable 4!, Hold=30, ADC Residuals vs DAC



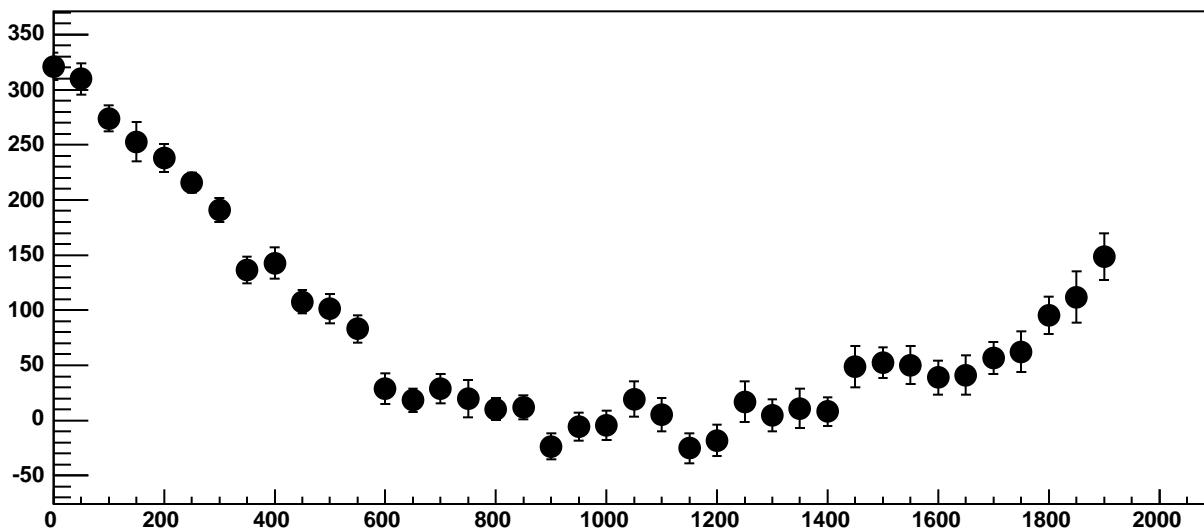
Chip 7, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC



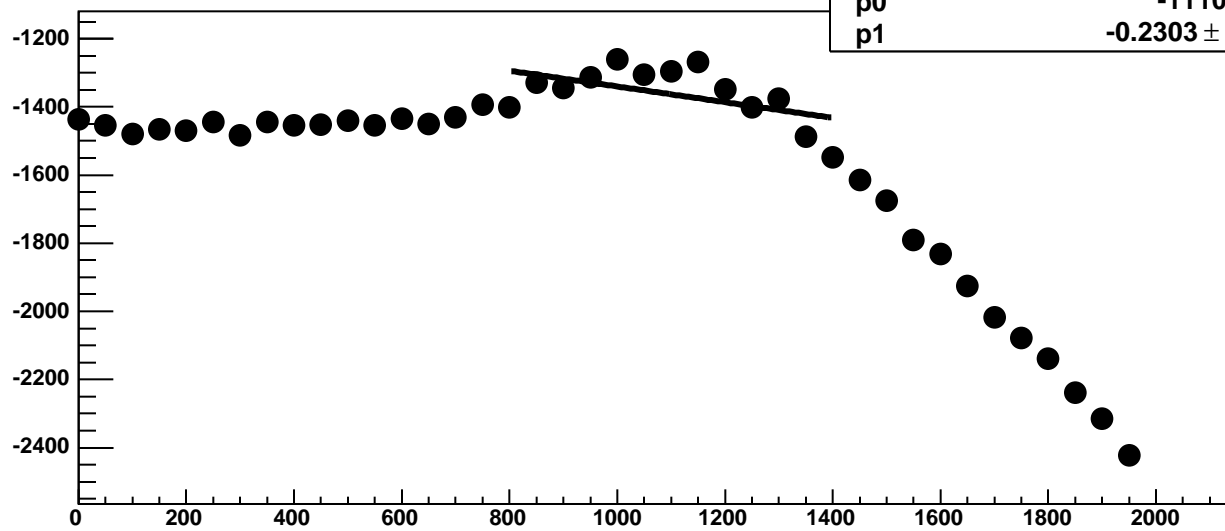
Chip 7, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC



Chip 7, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 7, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

159 / 11

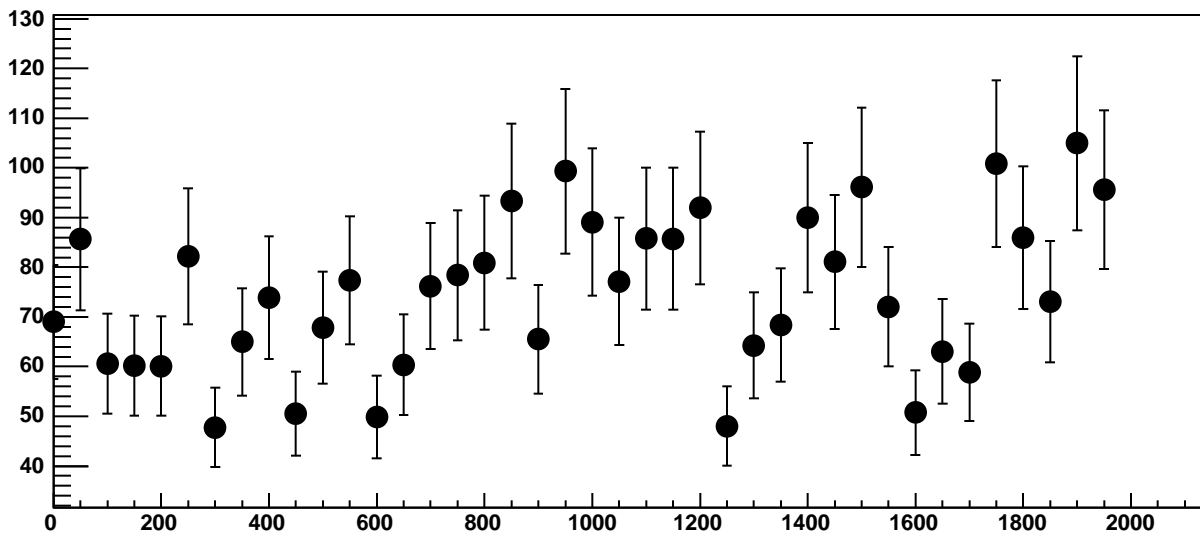
p0

$-1110 \pm 29.74$

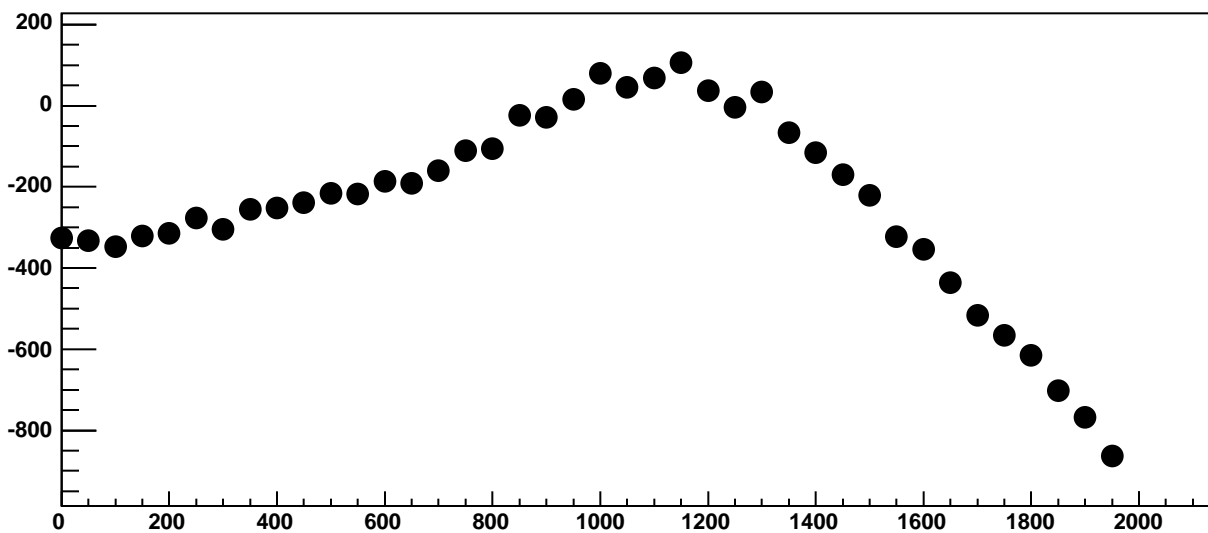
p1

$-0.2303 \pm 0.02601$

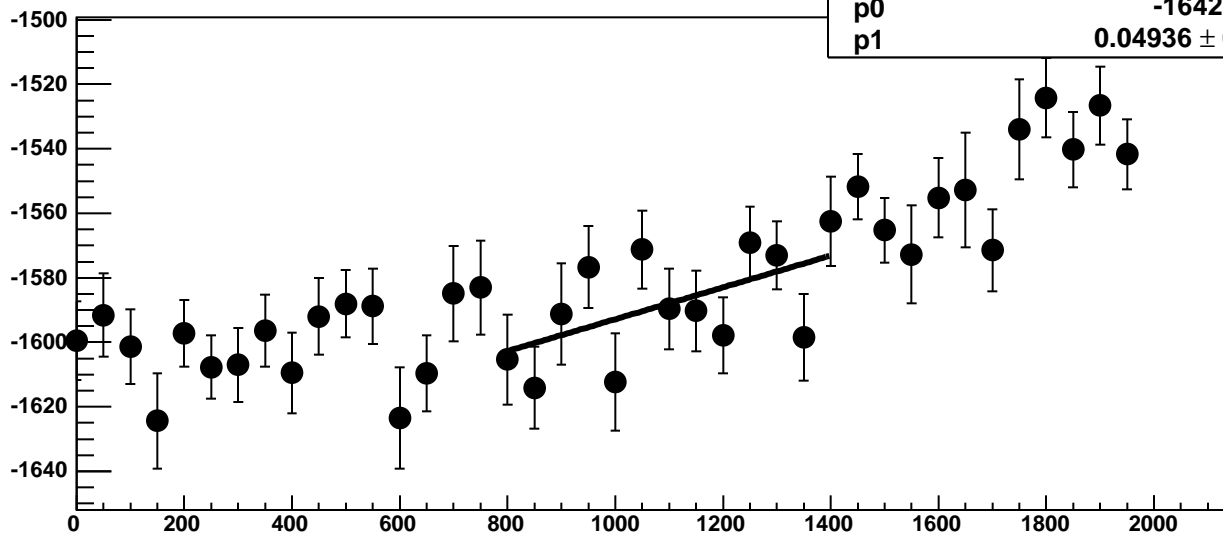
Chip 7, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC



Chip 7, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC

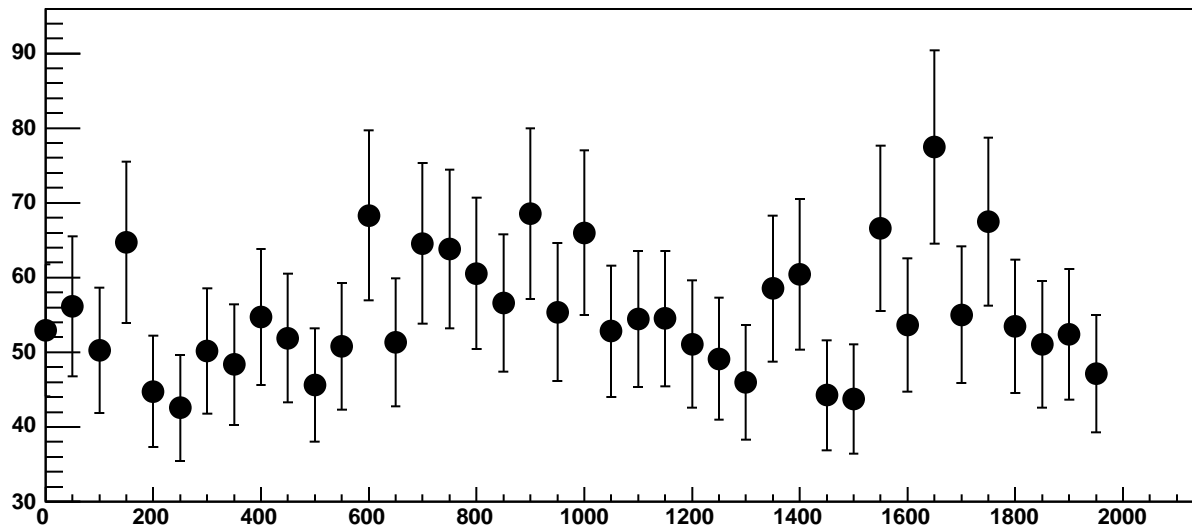


Chip 7, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC

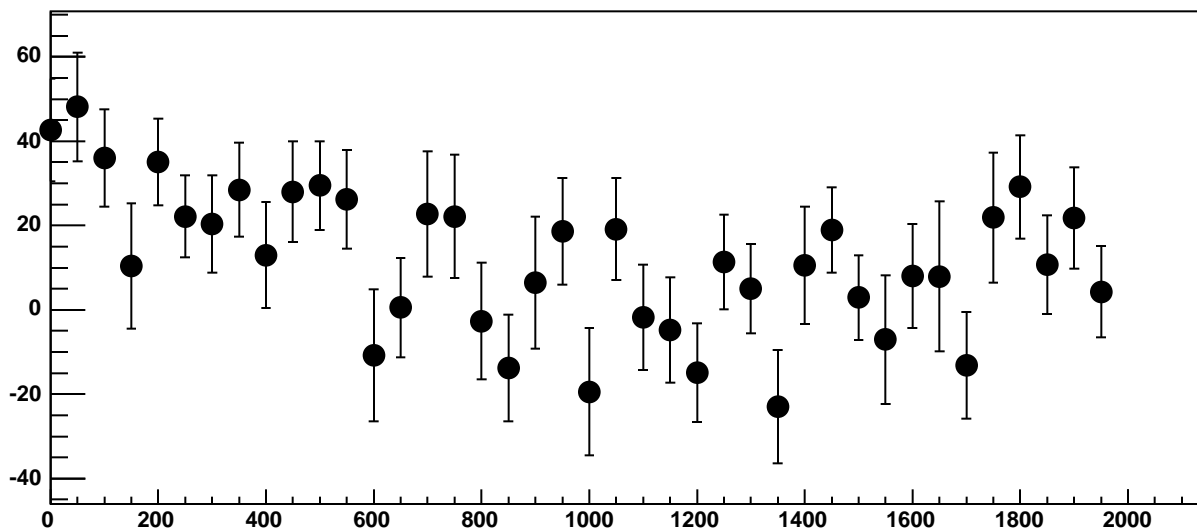


$\chi^2 / \text{ndf}$  14.21 / 11  
p0  $-1642 \pm 22.03$   
p1  $0.04936 \pm 0.01947$

Chip 7, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC

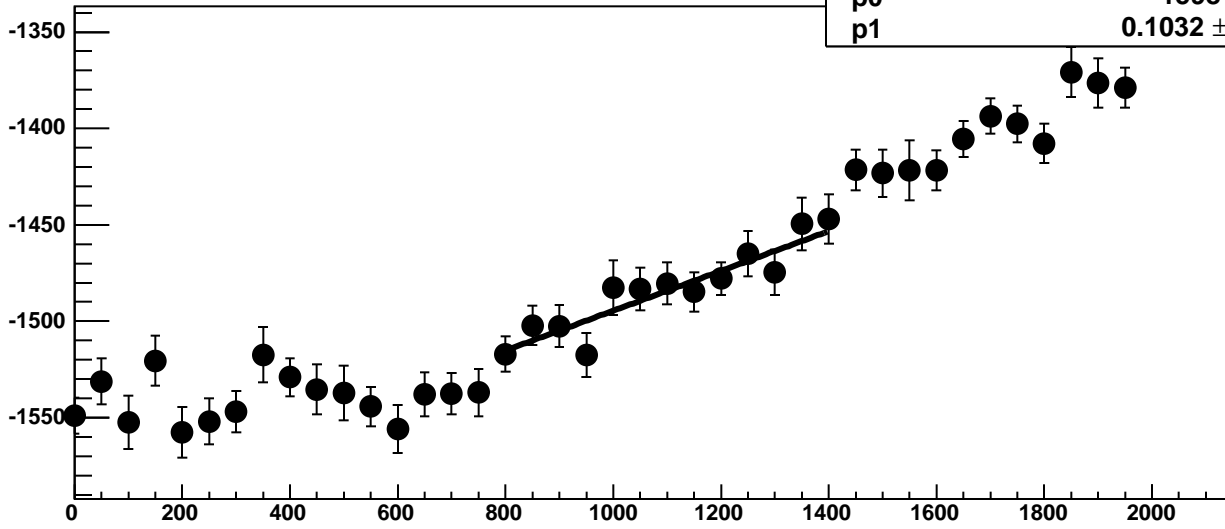


Chip 7, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC



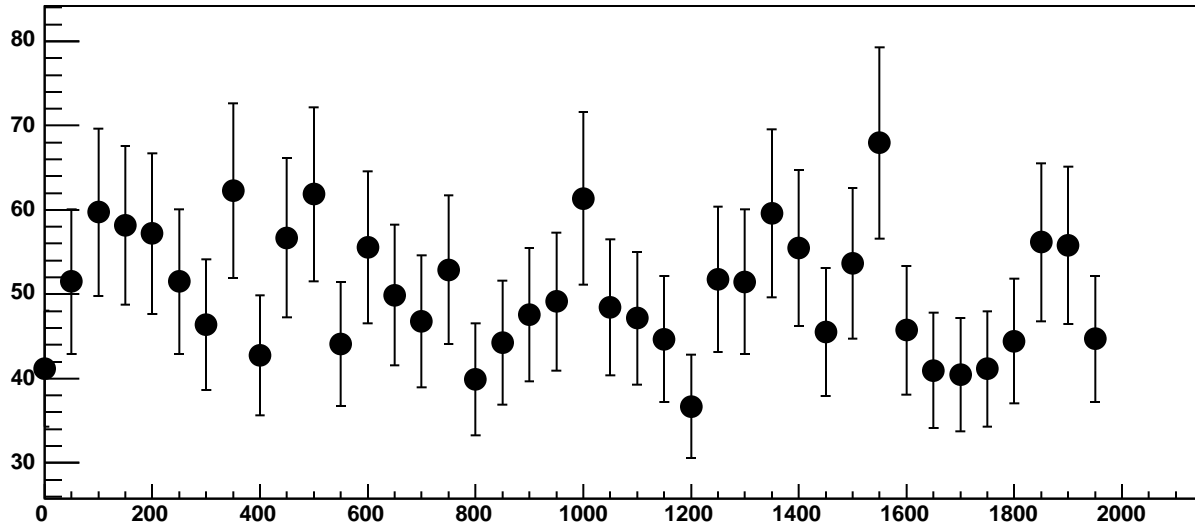


Chip 7, Channel 6, Enable 2, Hold=30, ADC Mean vs DAC

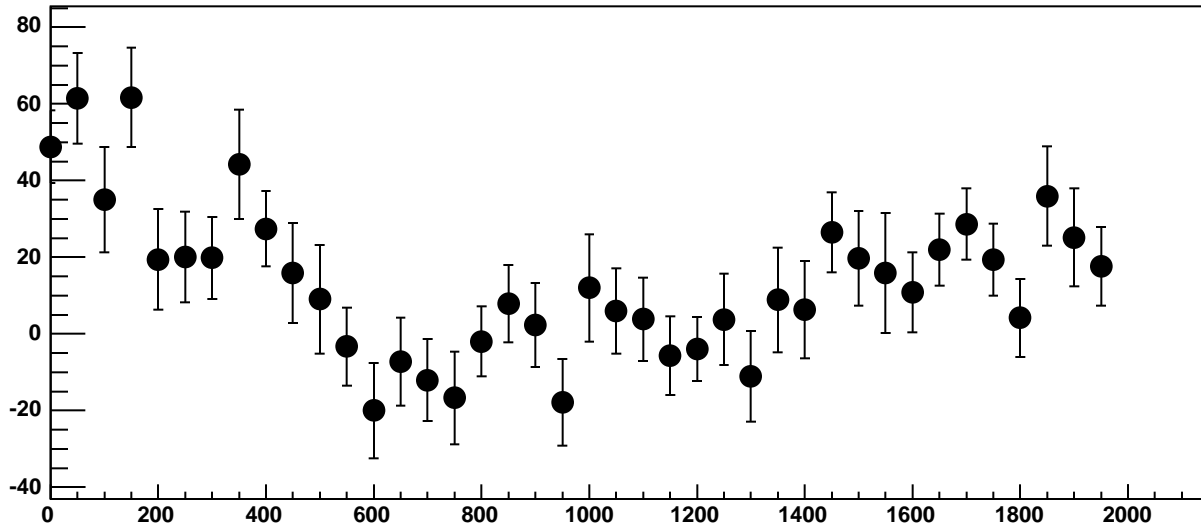


$\chi^2 / \text{ndf}$  6.498 / 11  
p0  $-1598 \pm 17.96$   
p1  $0.1032 \pm 0.0164$

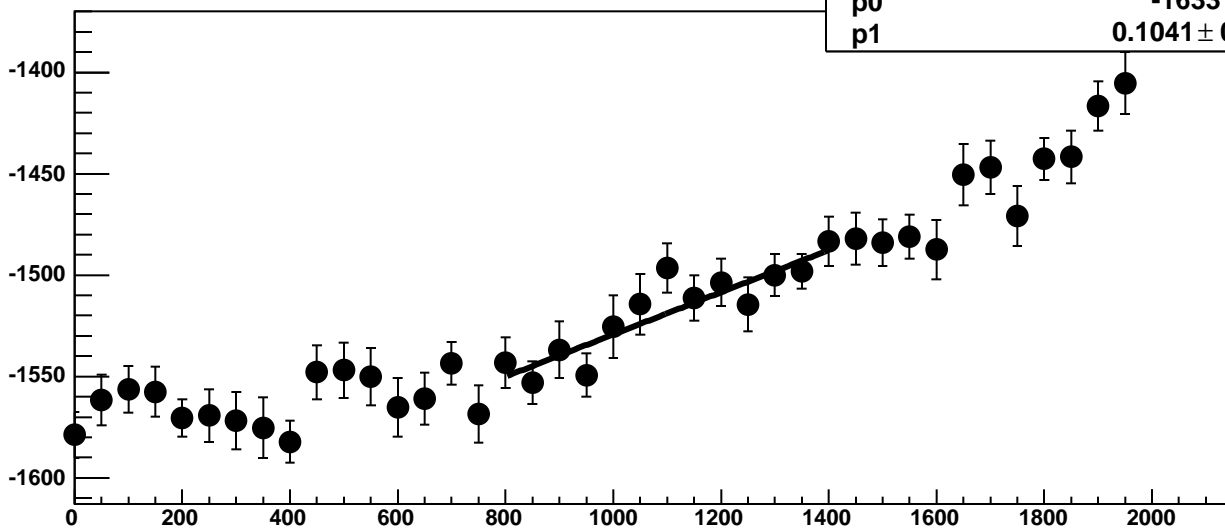
Chip 7, Channel 6, Enable 2, Hold=30, ADC Noise vs DAC



Chip 7, Channel 6, Enable 2, Hold=30, ADC Residuals vs DAC

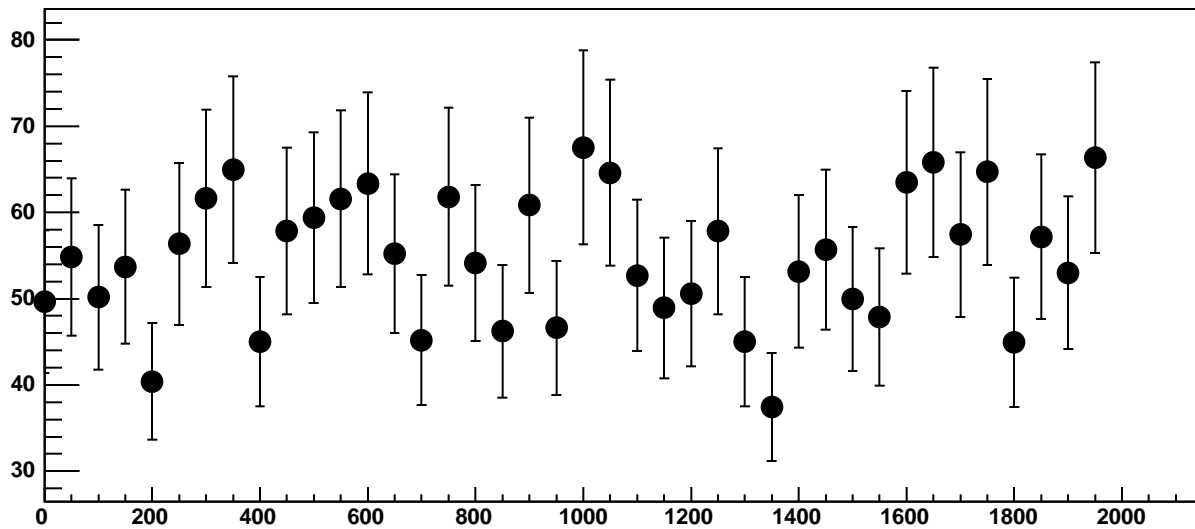


Chip 7, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC

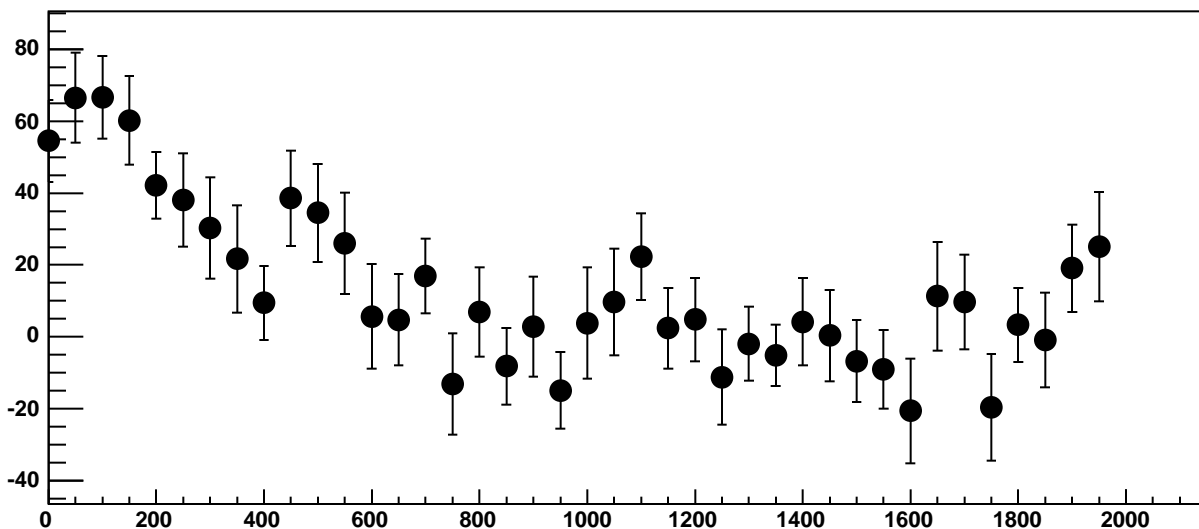


$\chi^2 / \text{ndf}$  8.251 / 11  
p0  $-1633 \pm 18.99$   
p1  $0.1041 \pm 0.01668$

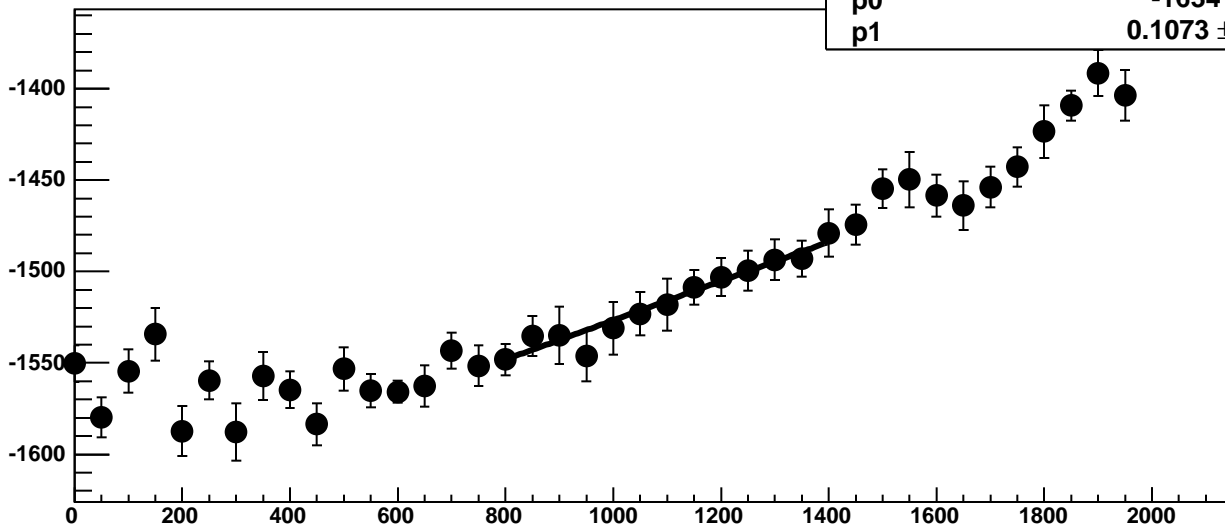
Chip 7, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC



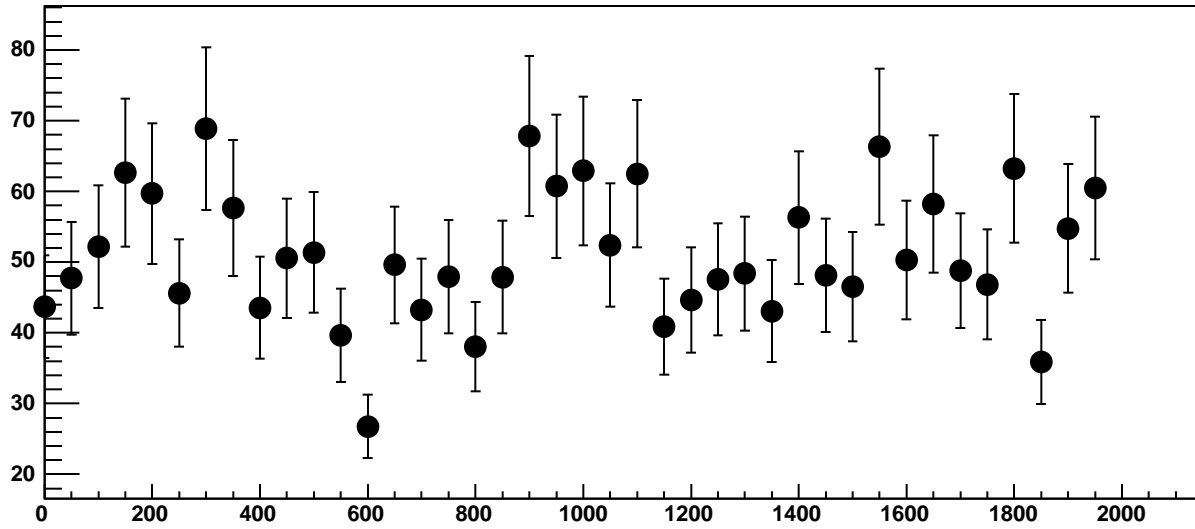
Chip 7, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC



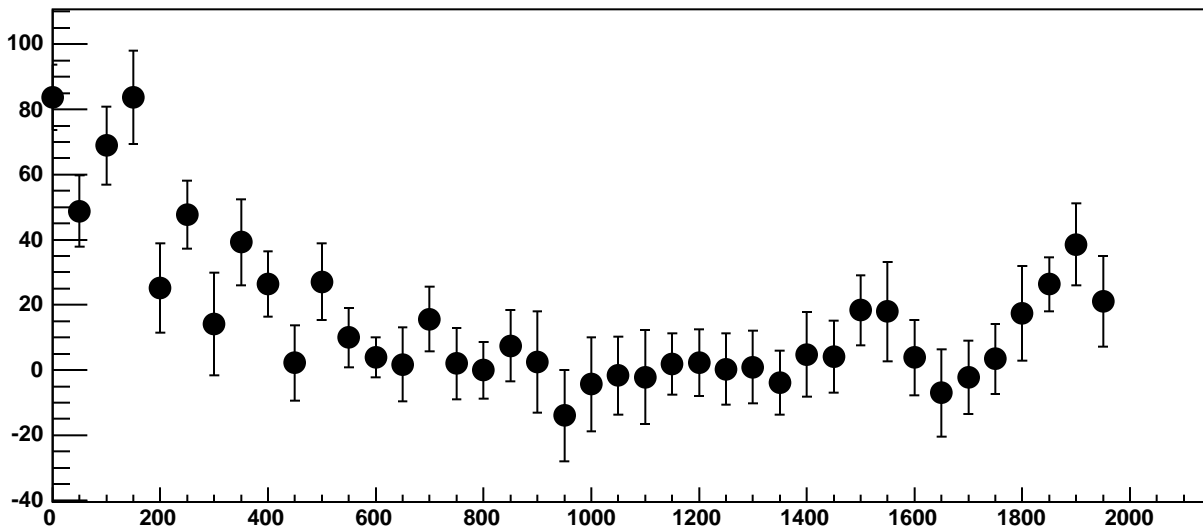
Chip 7, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC



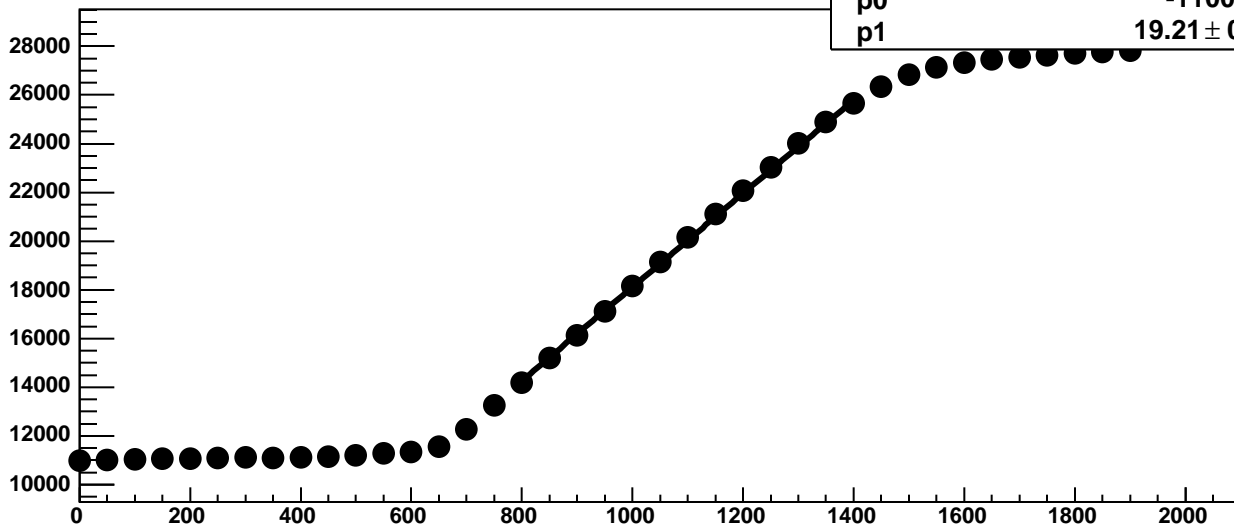
Chip 7, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC



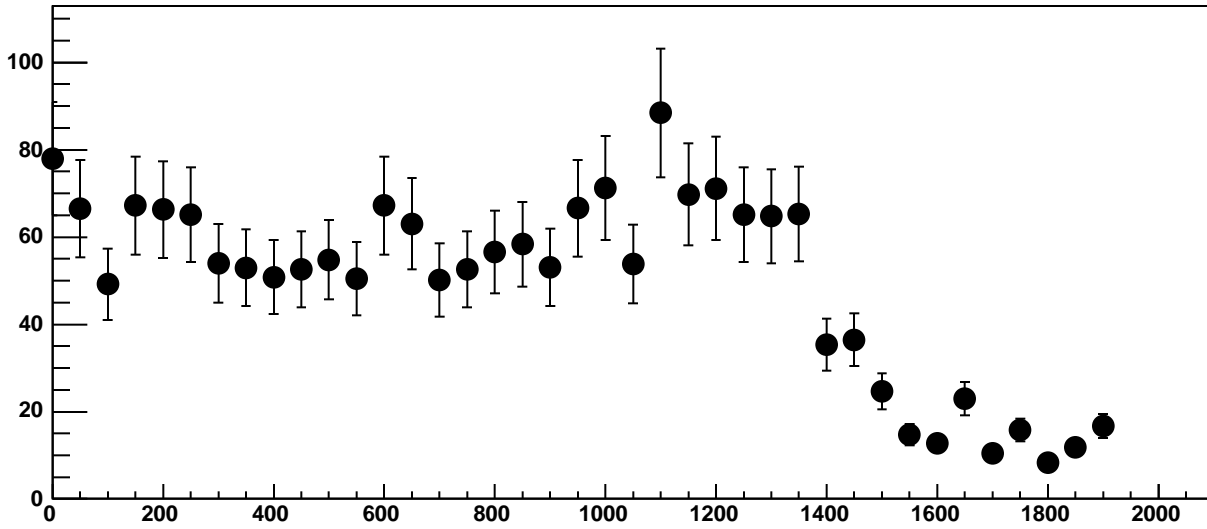
Chip 7, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC



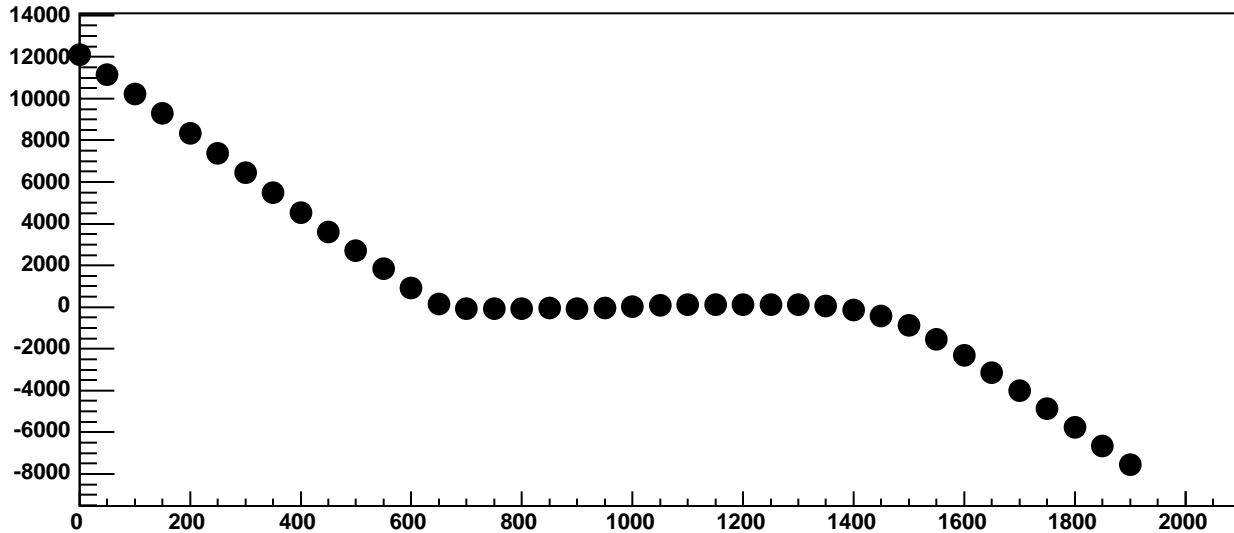
Chip 7, Channel 6, Enable 5!, Hold=30, ADC Mean vs DAC



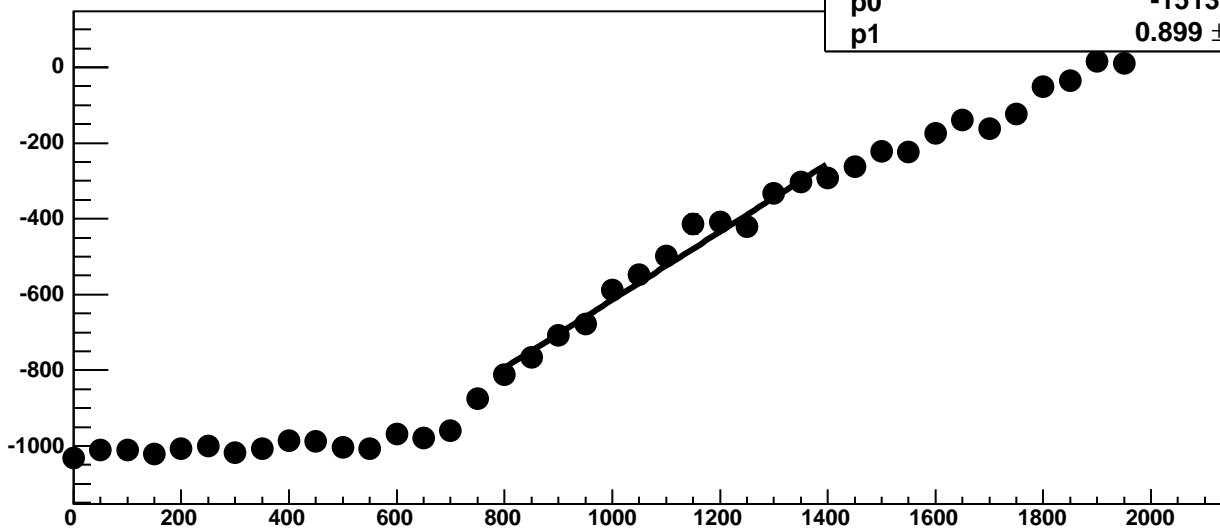
Chip 7, Channel 6, Enable 5!, Hold=30, ADC Noise vs DAC



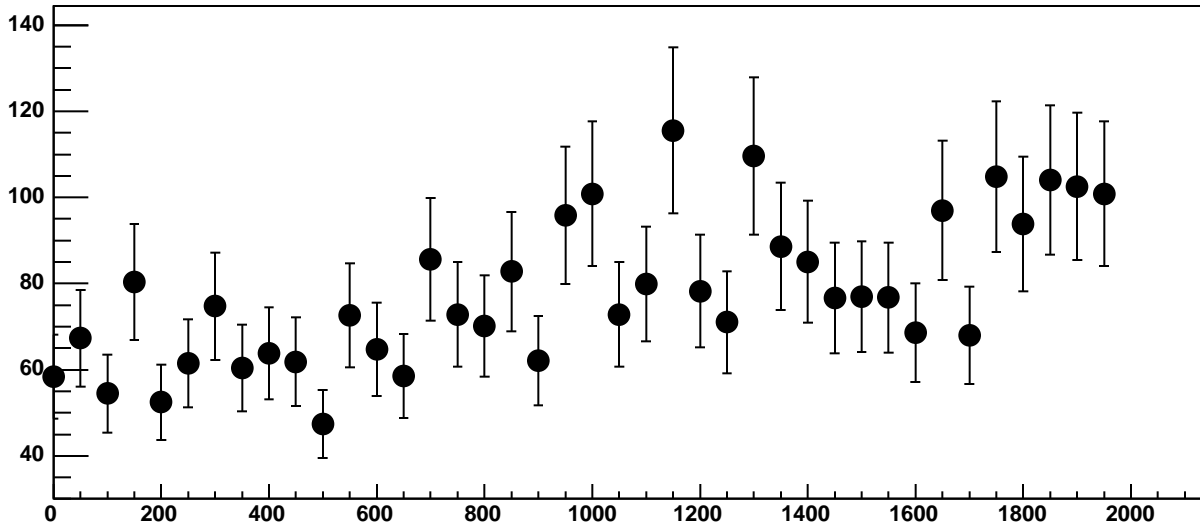
Chip 7, Channel 6, Enable 5!, Hold=30, ADC Residuals vs DAC



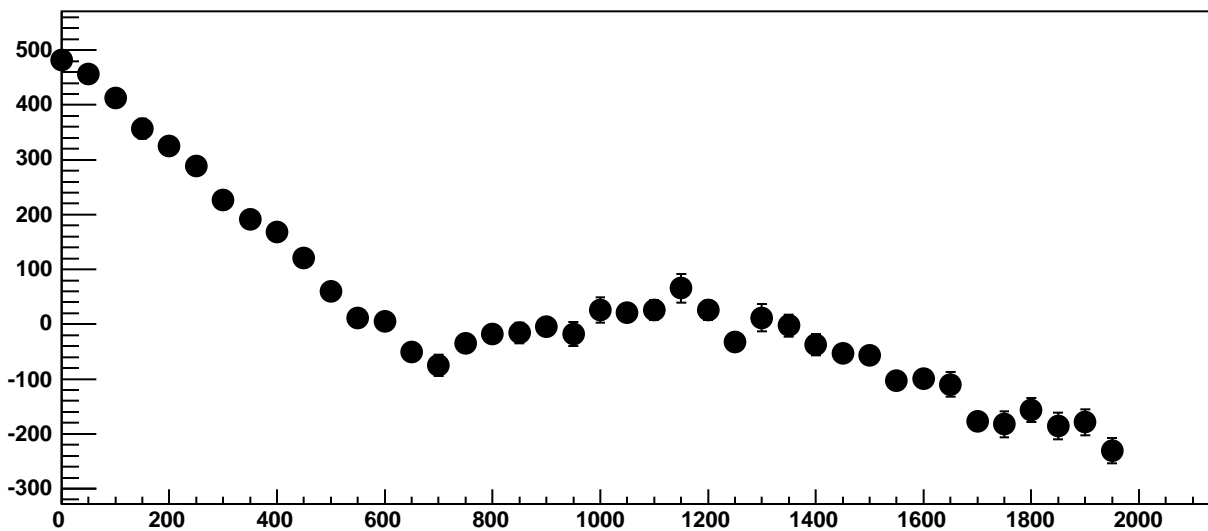
Chip 7, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC



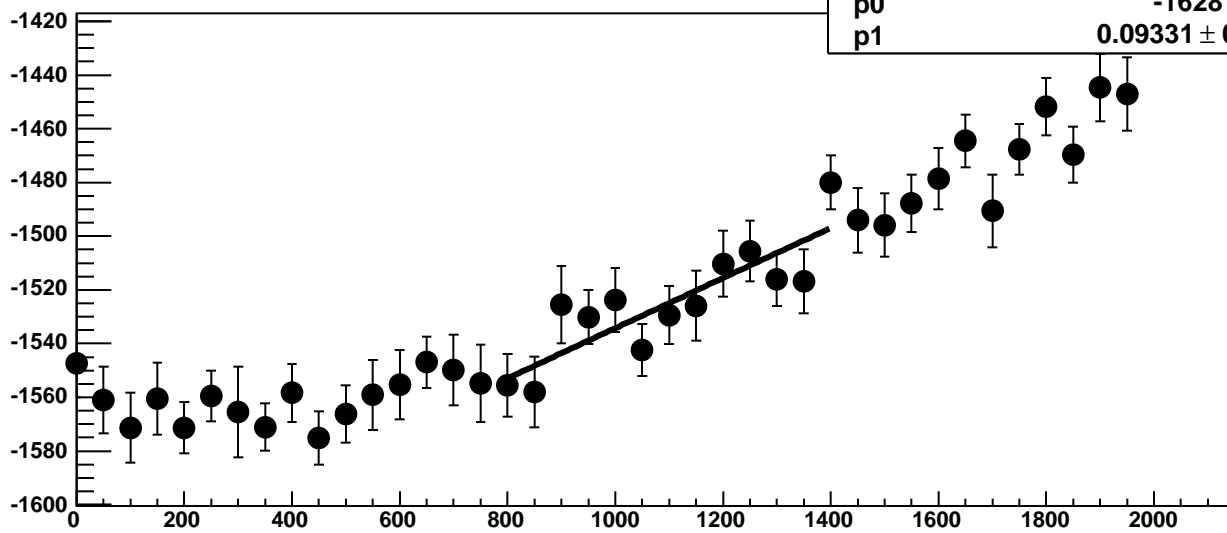
Chip 7, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC



Chip 7, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC

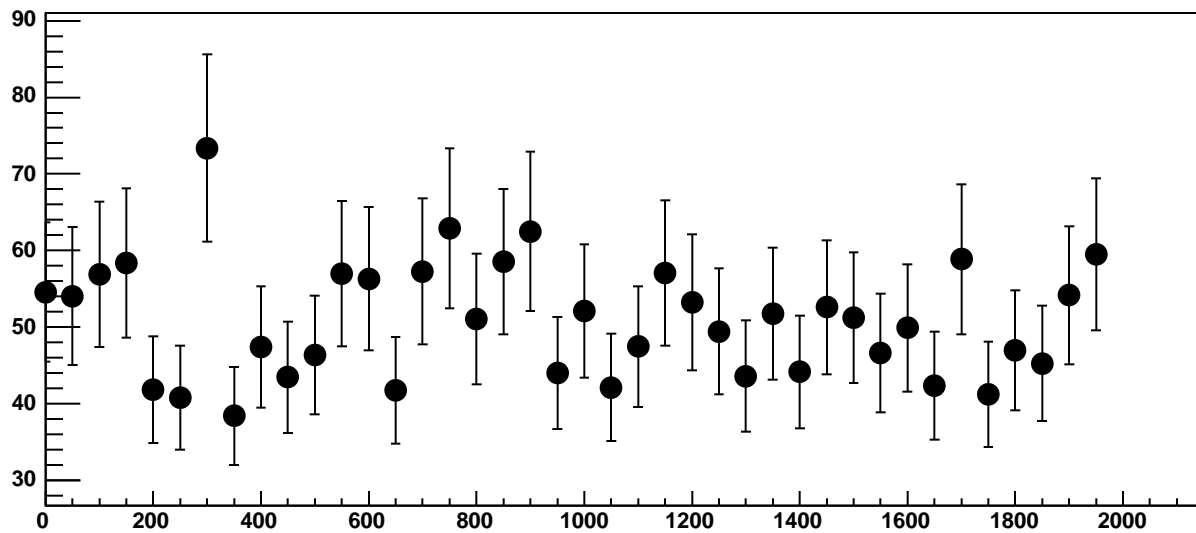


Chip 7, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC

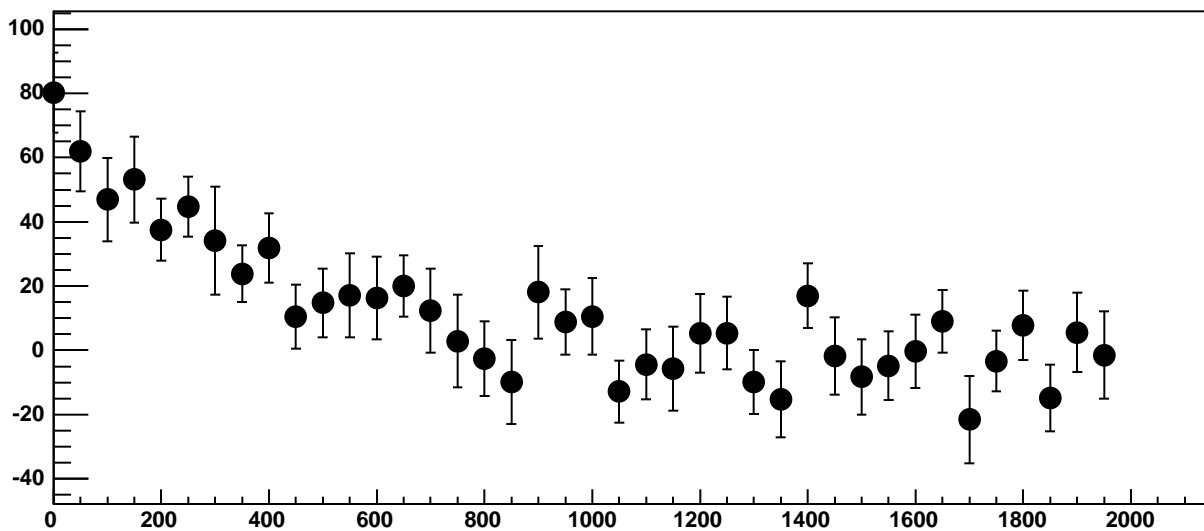


$\chi^2 / \text{ndf}$  11.7 / 11  
p0  $-1628 \pm 19.13$   
p1  $0.09331 \pm 0.01693$

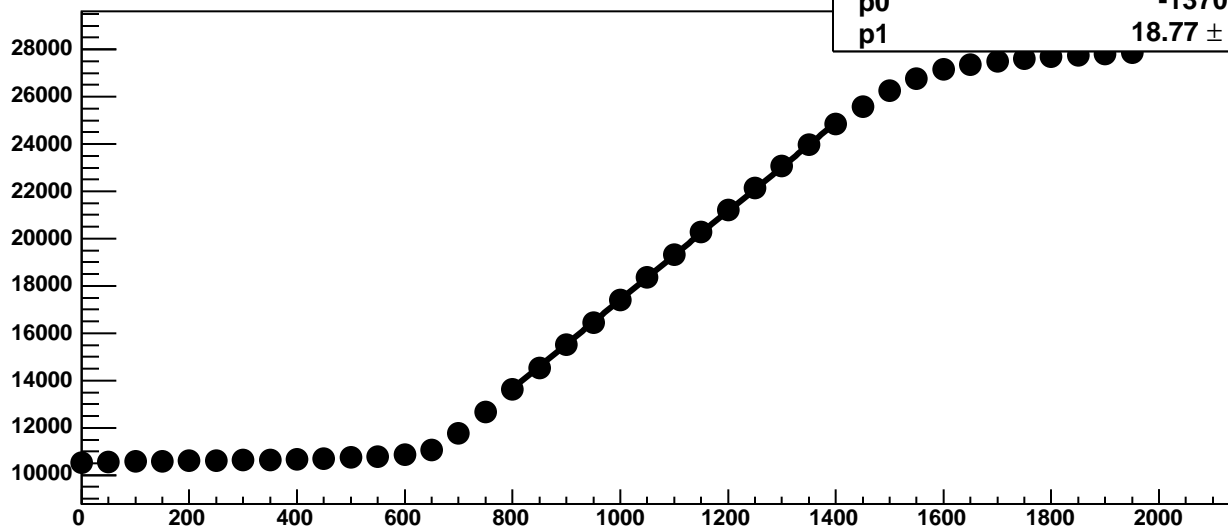
Chip 7, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC



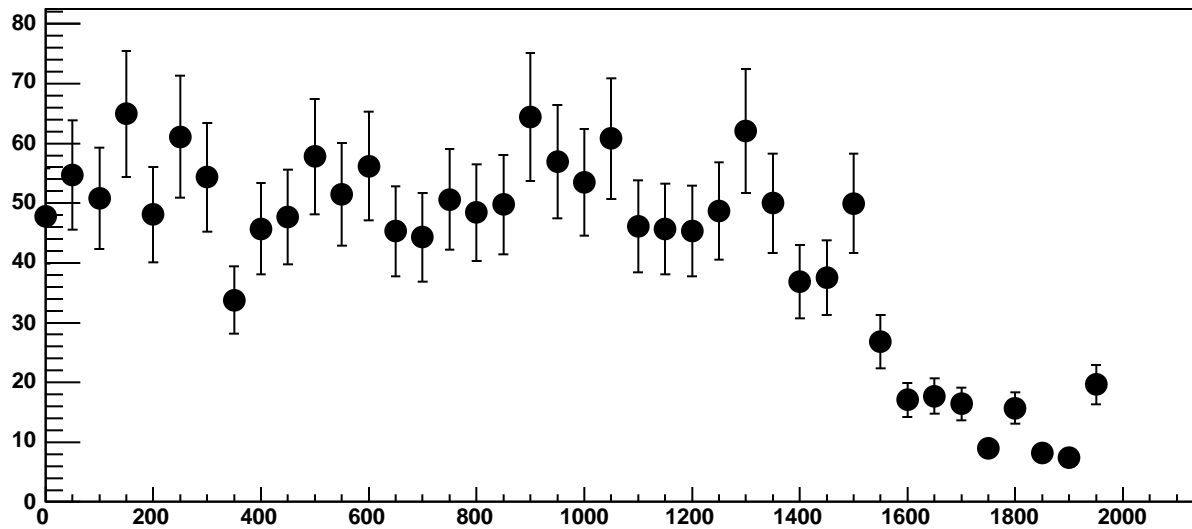
Chip 7, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC



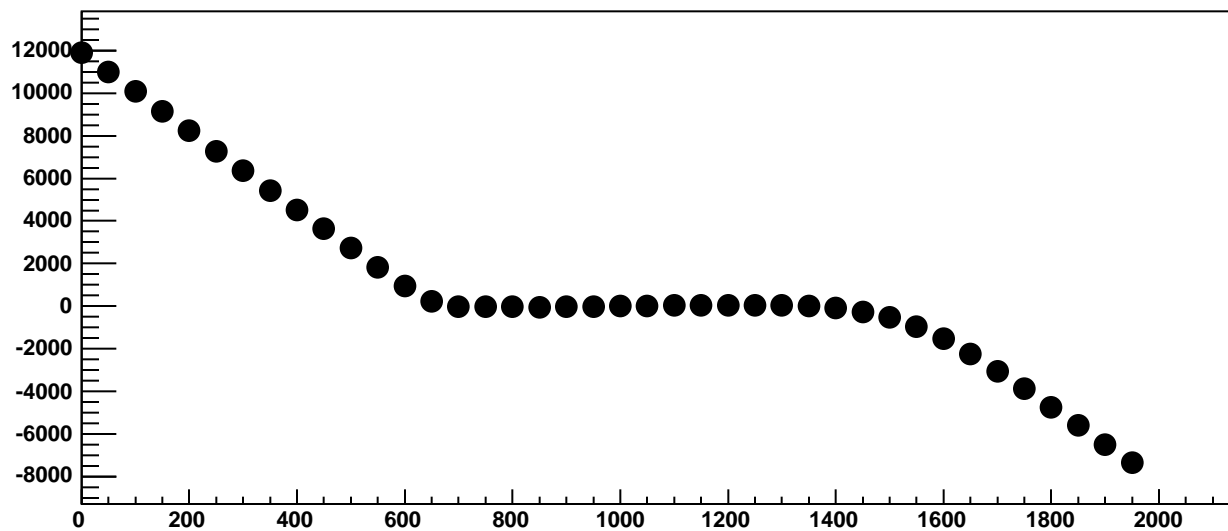
Chip 7, Channel 7, Enable 2!, Hold=30, ADC Mean vs DAC



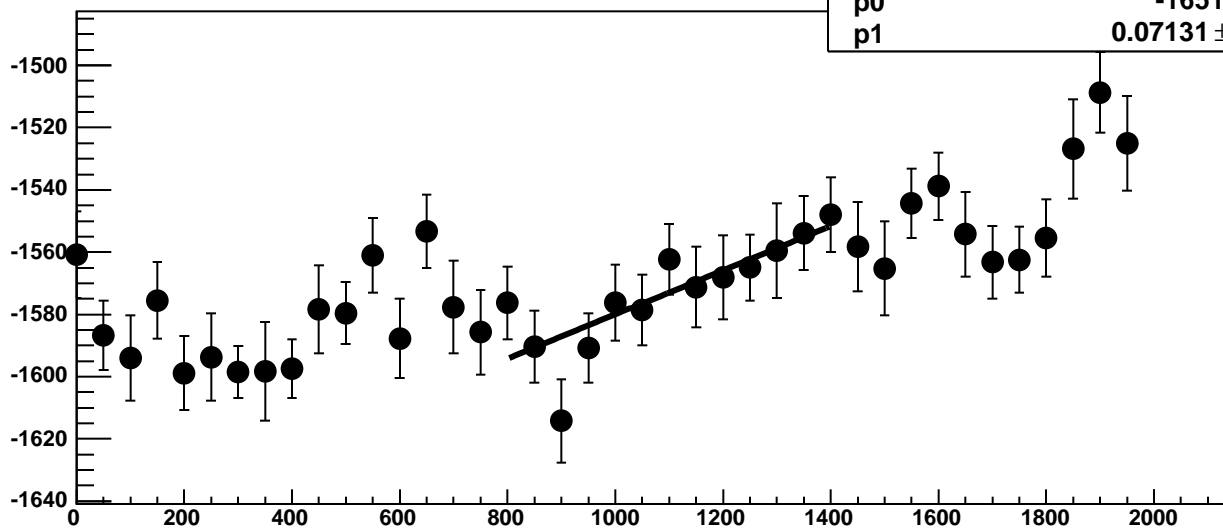
Chip 7, Channel 7, Enable 2!, Hold=30, ADC Noise vs DAC



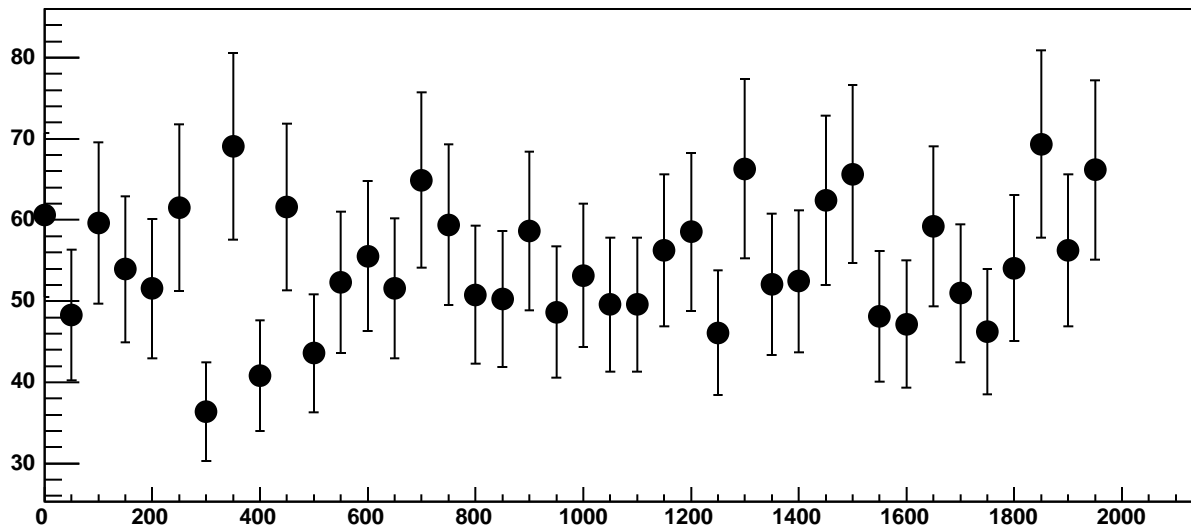
Chip 7, Channel 7, Enable 2!, Hold=30, ADC Residuals vs DAC



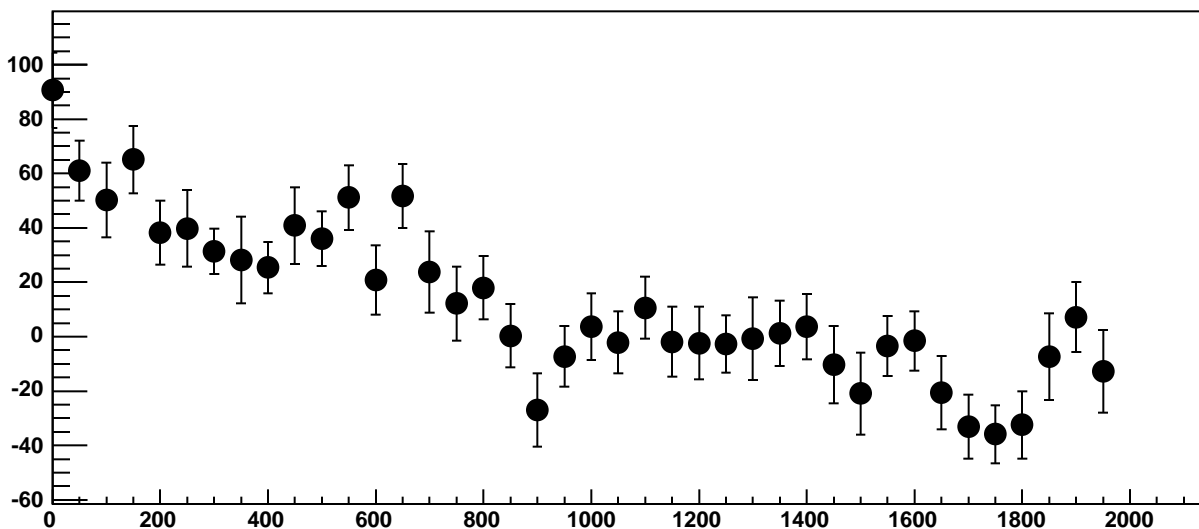
Chip 7, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC



Chip 7, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC

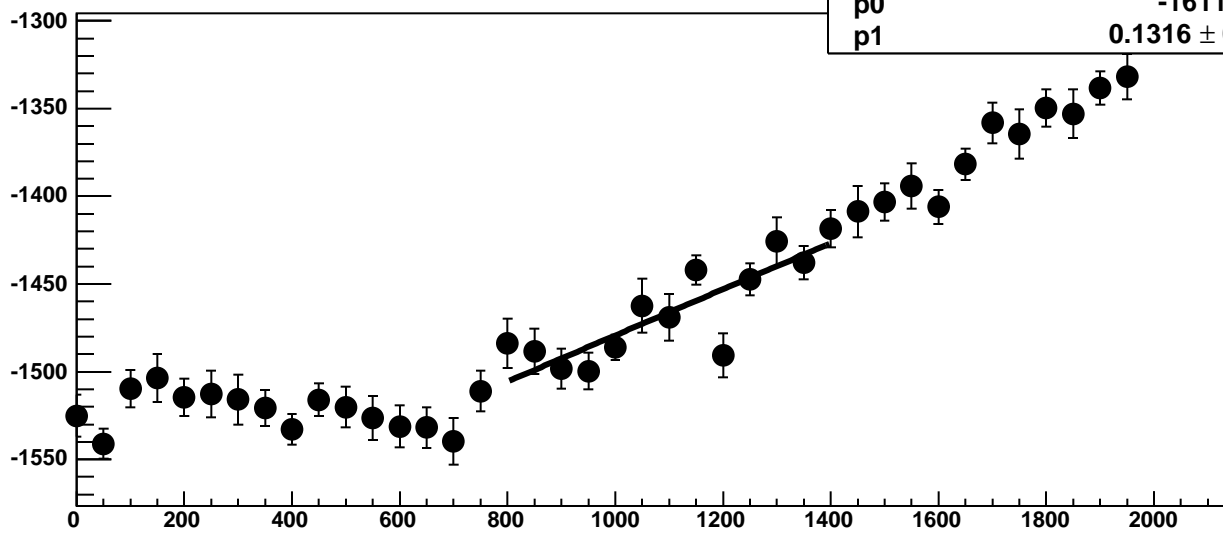


Chip 7, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC



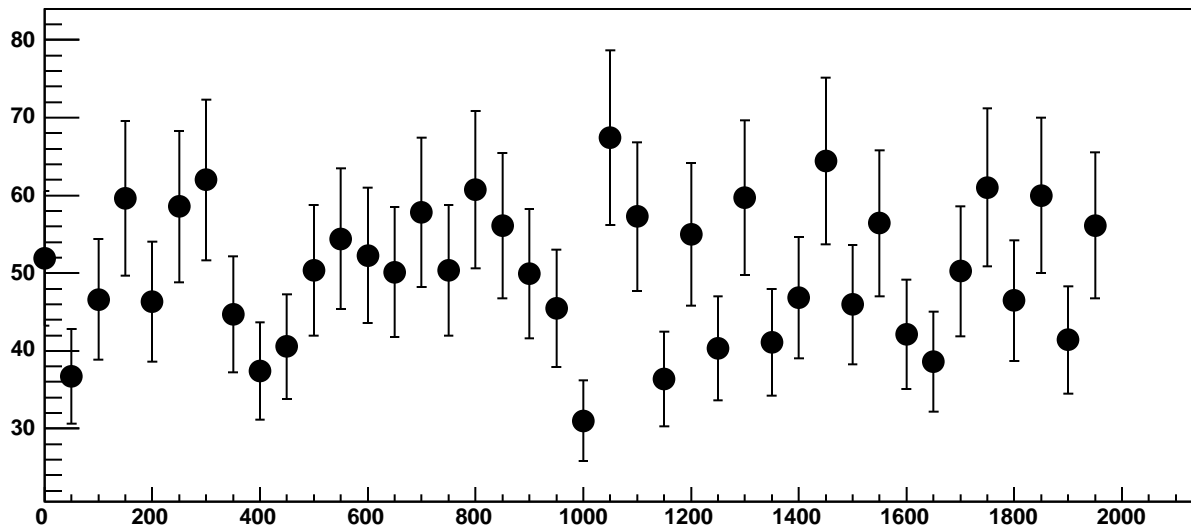


Chip 7, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC

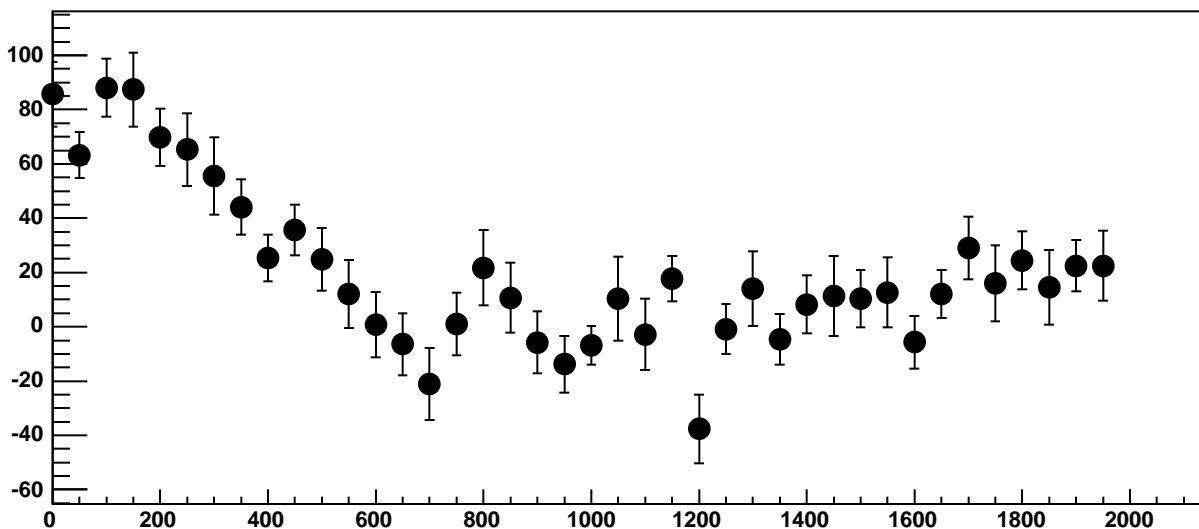


$\chi^2 / \text{ndf}$  21.74 / 11  
p0 -1611 ± 18.78  
p1 0.1316 ± 0.01667

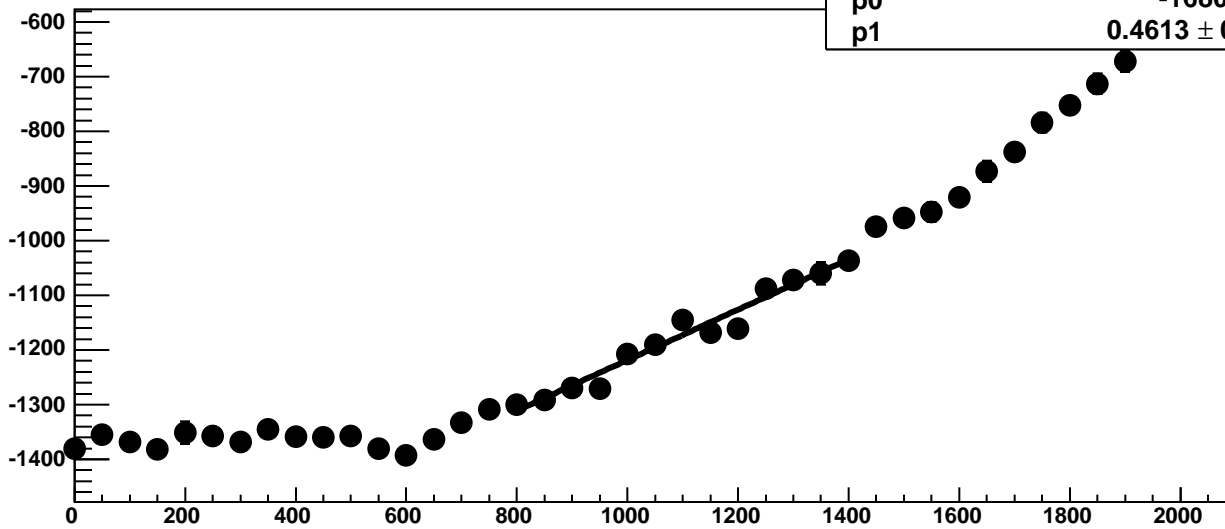
Chip 7, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC



Chip 7, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC

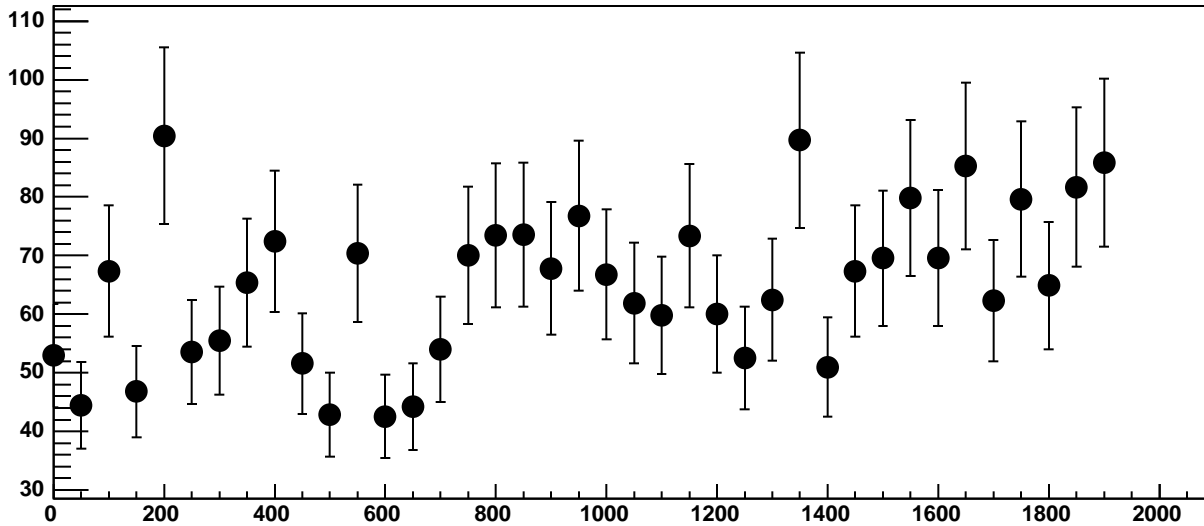


Chip 7, Channel 7, Enable 5, Hold=30, ADC Mean vs DAC

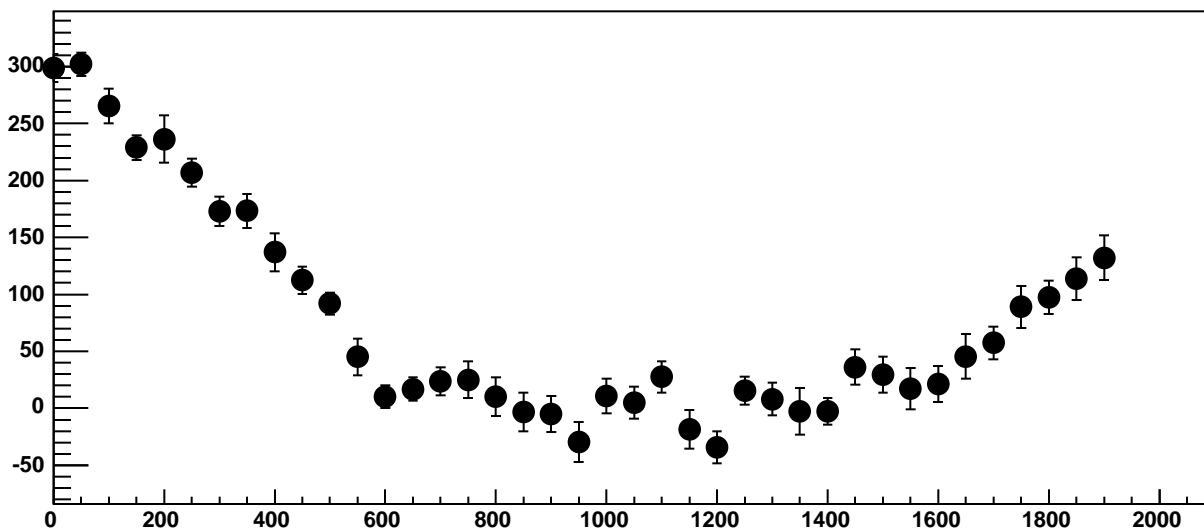


$\chi^2 / \text{ndf}$  17.42 / 11  
p0  $-1680 \pm 25.5$   
p1  $0.4613 \pm 0.02234$

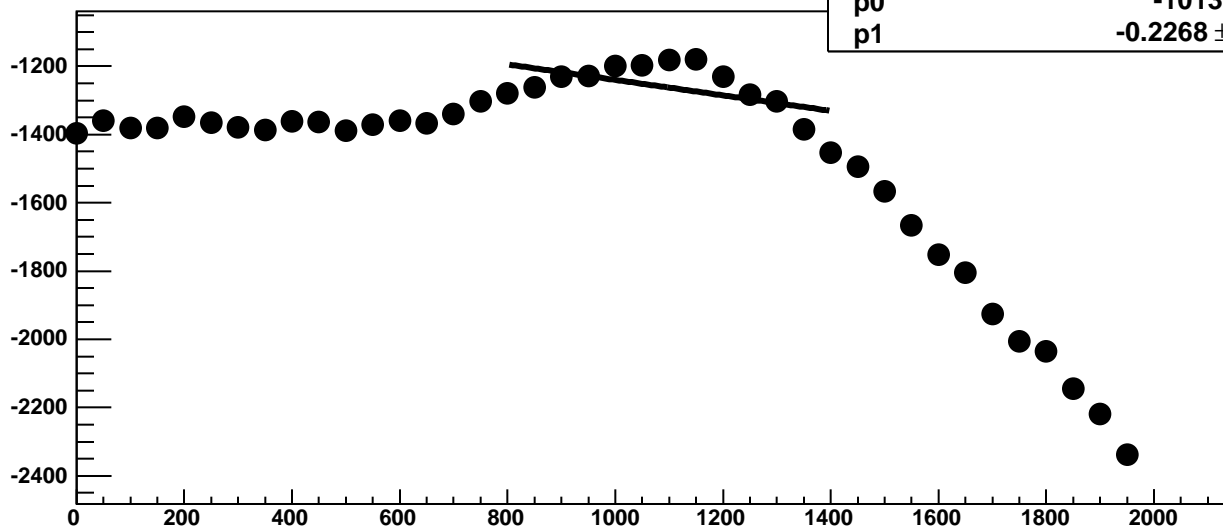
Chip 7, Channel 7, Enable 5, Hold=30, ADC Noise vs DAC



Chip 7, Channel 7, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 7, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

151.3 / 11

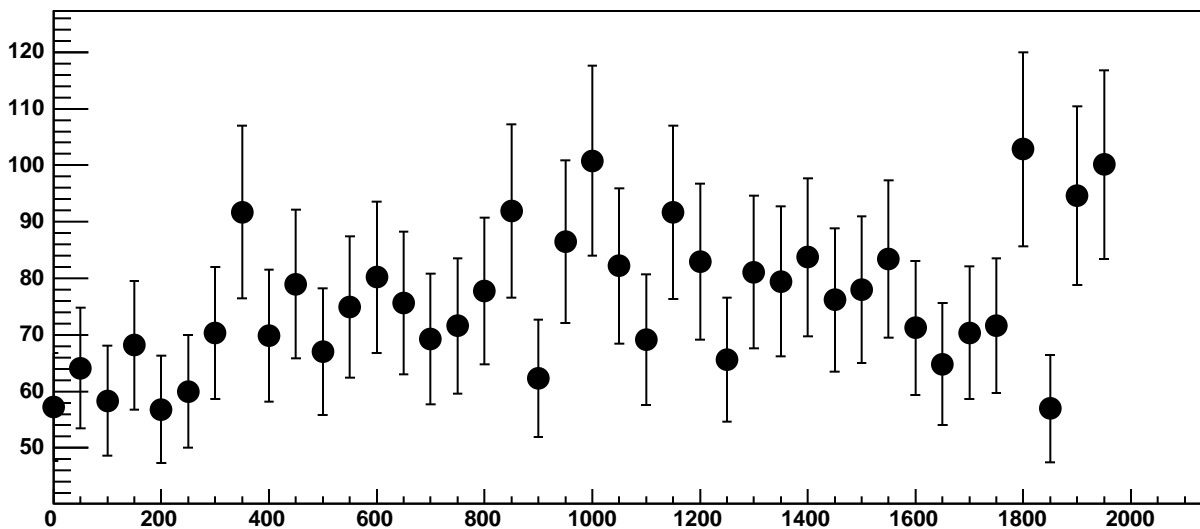
p0

$-1013 \pm 30.02$

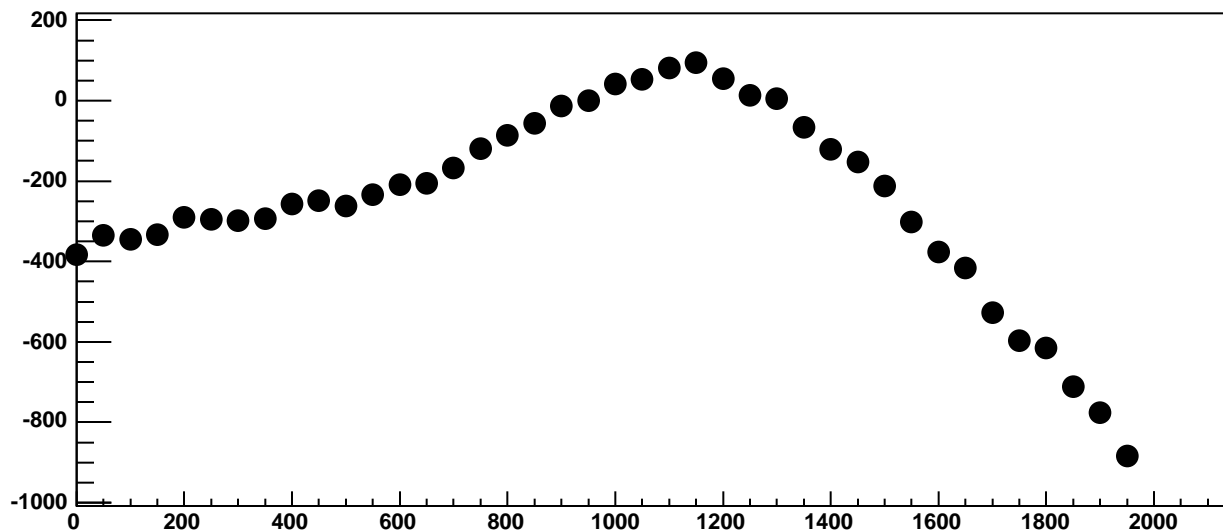
p1

$-0.2268 \pm 0.0269$

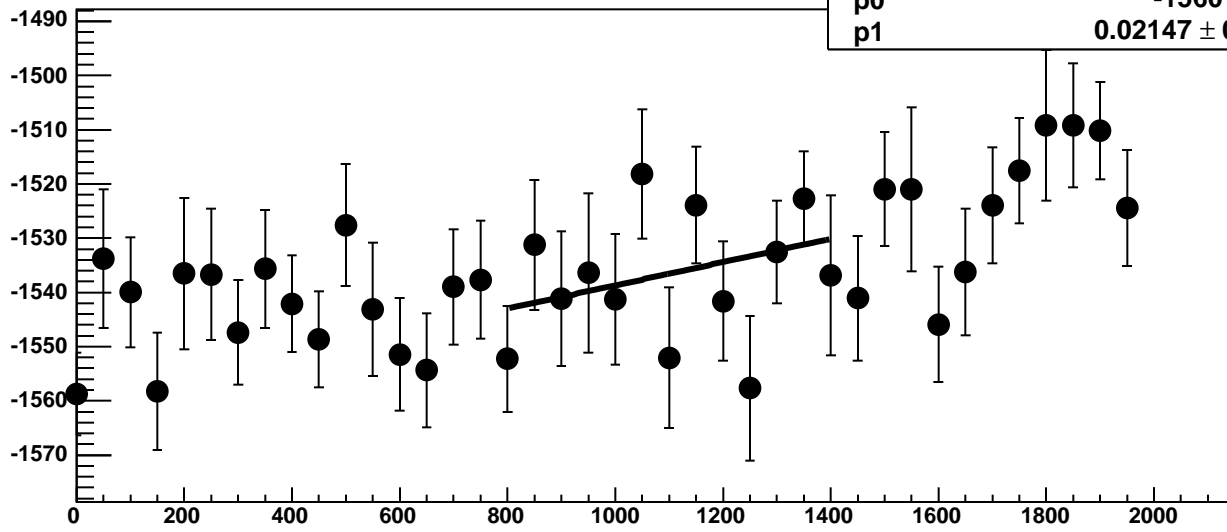
Chip 7, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC



Chip 7, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC

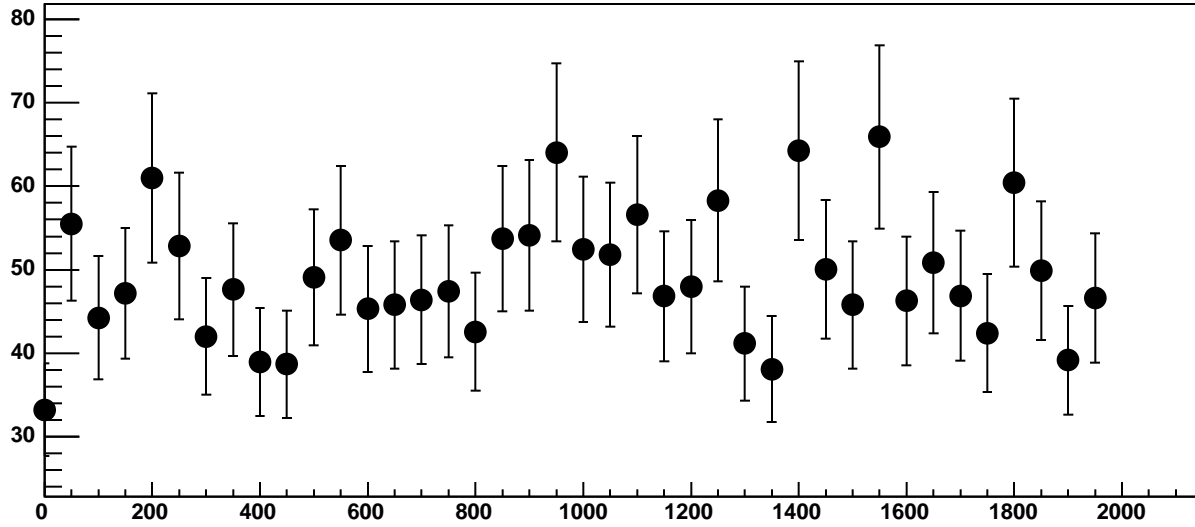


Chip 7, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC

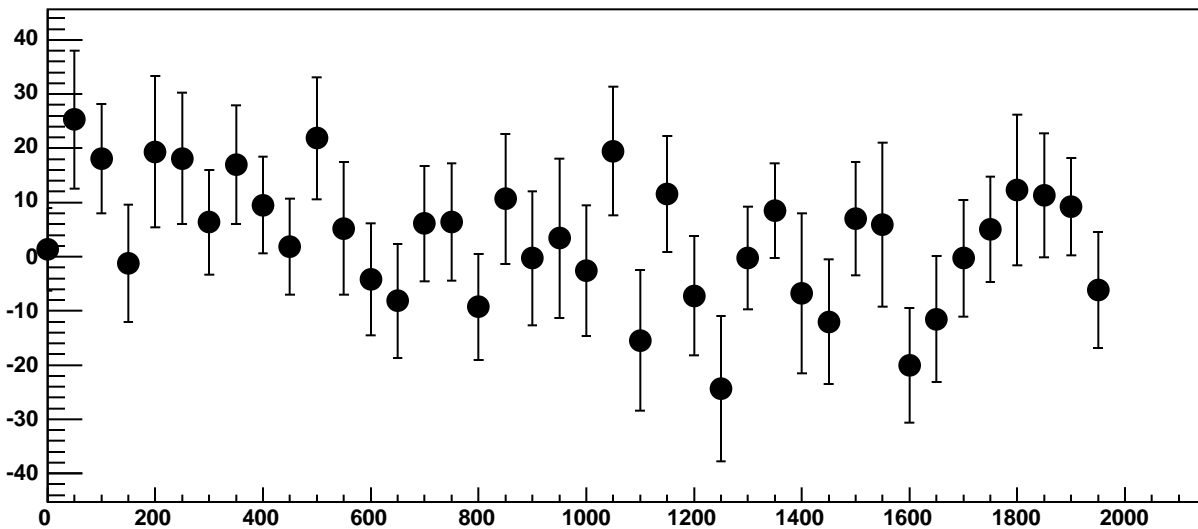


$\chi^2 / \text{ndf}$  11.95 / 11  
p0  $-1560 \pm 18.47$   
p1  $0.02147 \pm 0.01638$

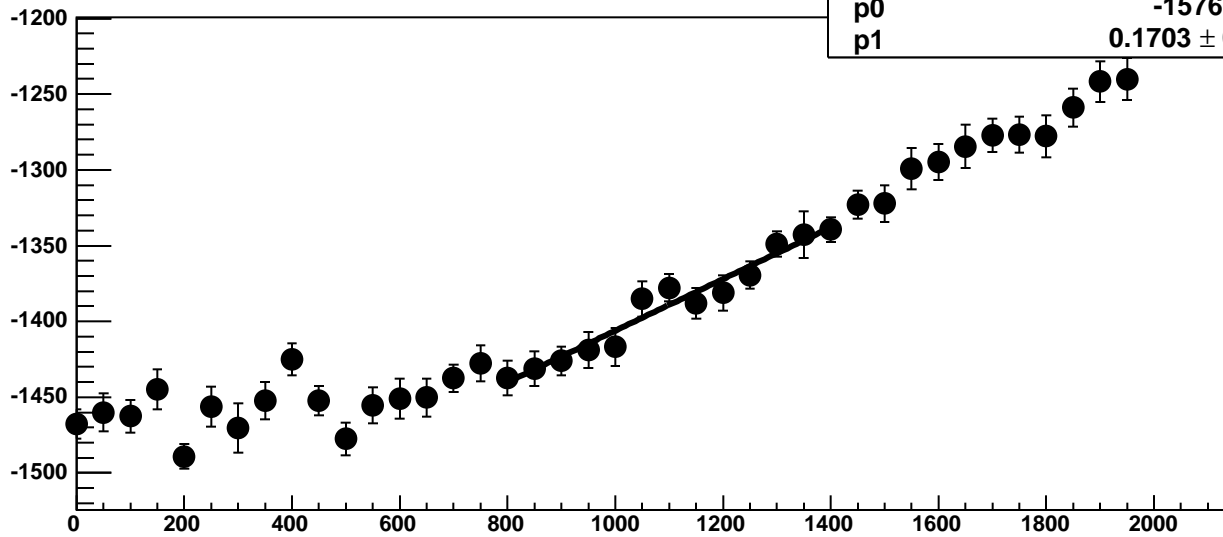
Chip 7, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC



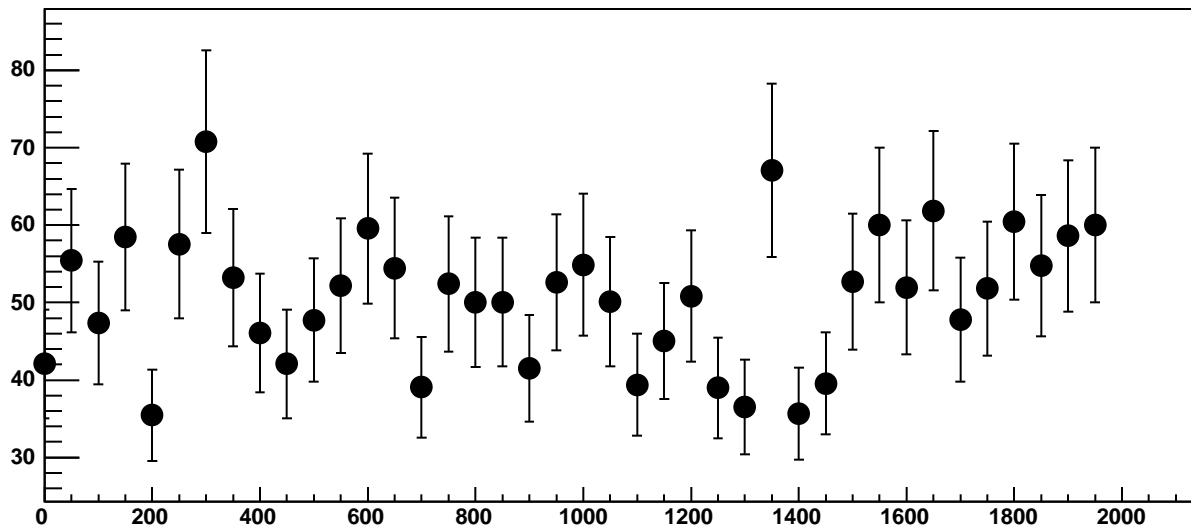
Chip 7, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC



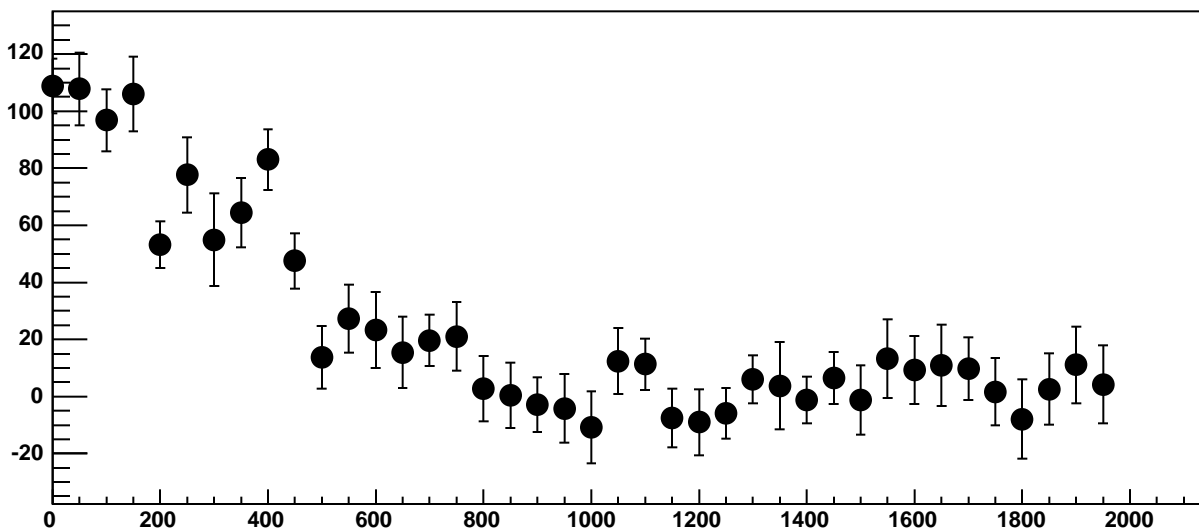
Chip 7, Channel 8, Enable 2, Hold=30, ADC Mean vs DAC



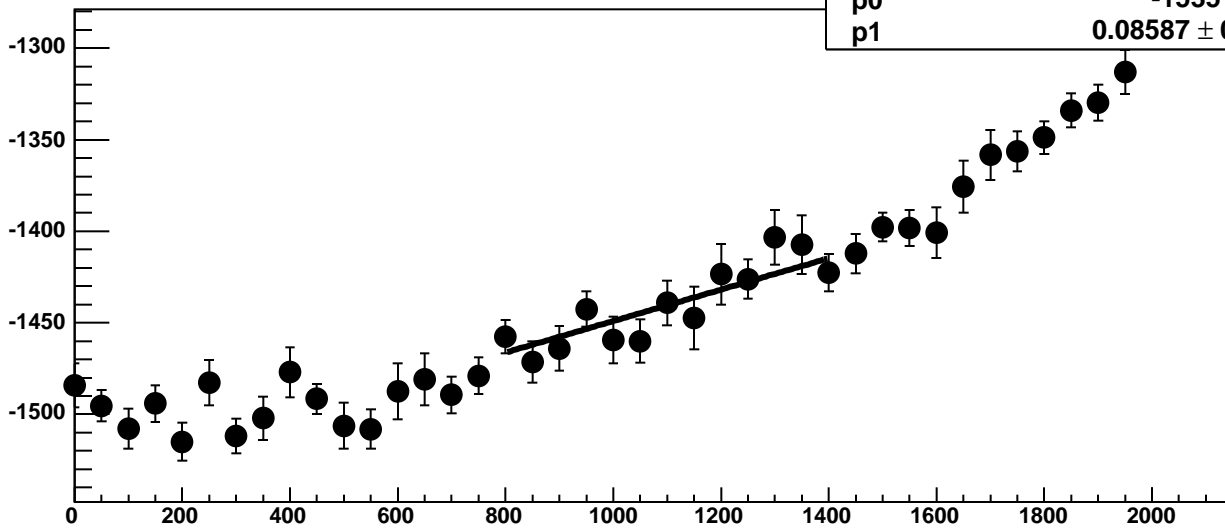
Chip 7, Channel 8, Enable 2, Hold=30, ADC Noise vs DAC



Chip 7, Channel 8, Enable 2, Hold=30, ADC Residuals vs DAC

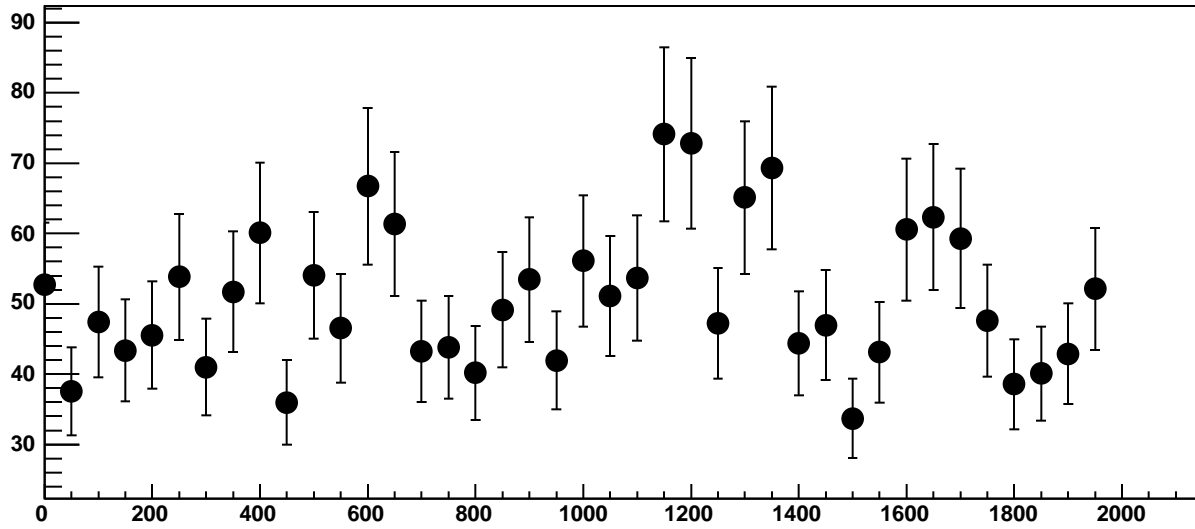


Chip 7, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC

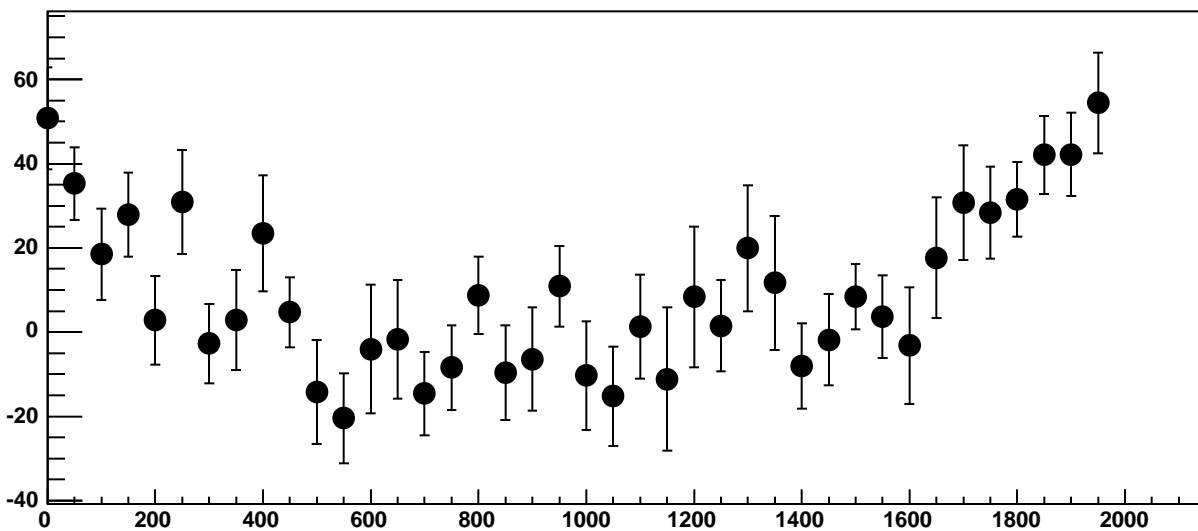


$\chi^2 / \text{ndf}$  9.147 / 11  
p0  $-1535 \pm 18.17$   
p1  $0.08587 \pm 0.01672$

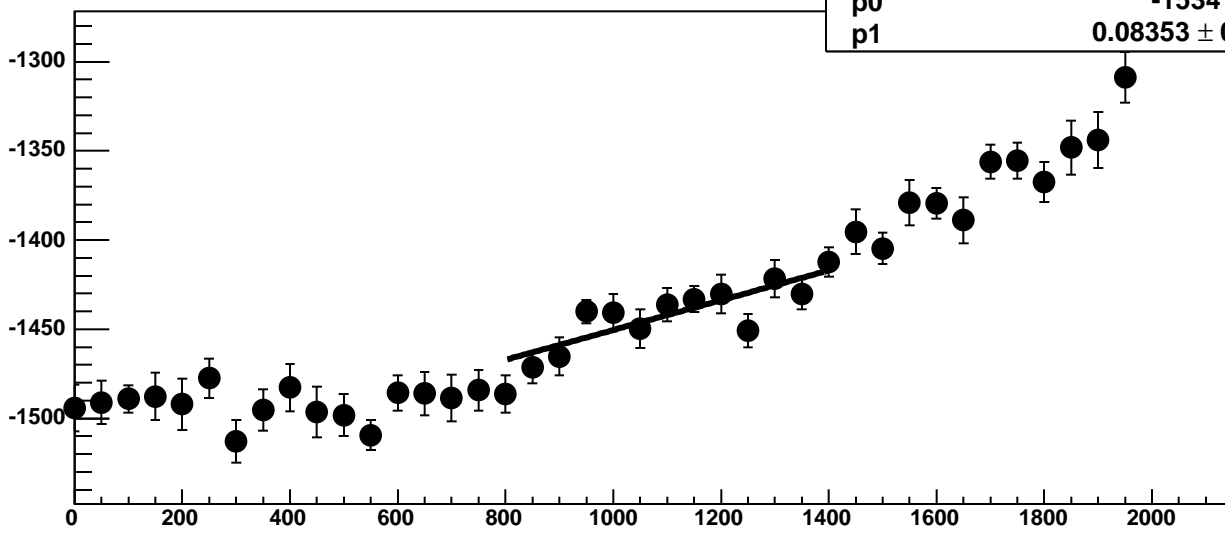
Chip 7, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC



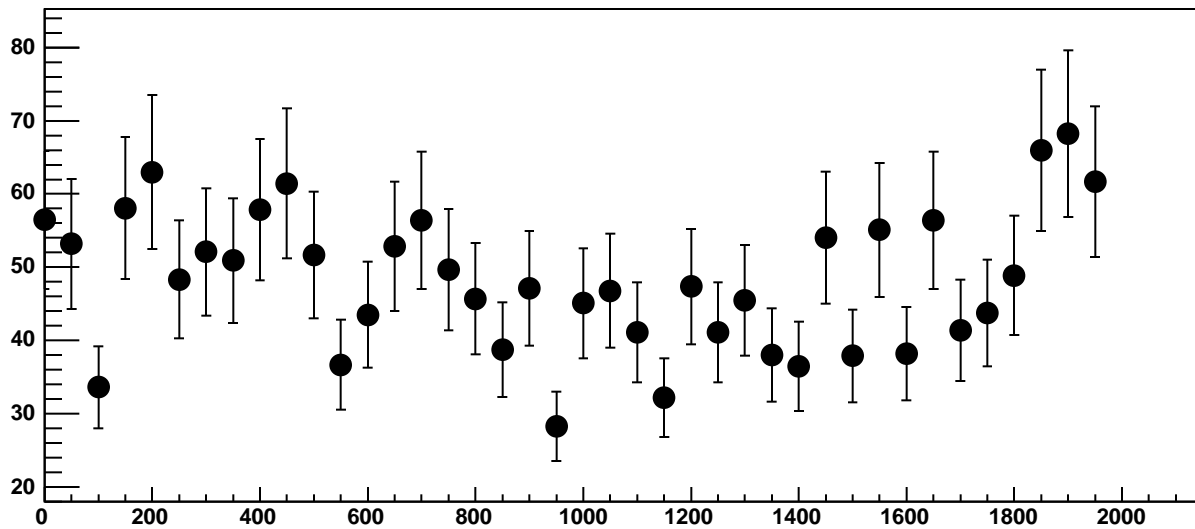
Chip 7, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC



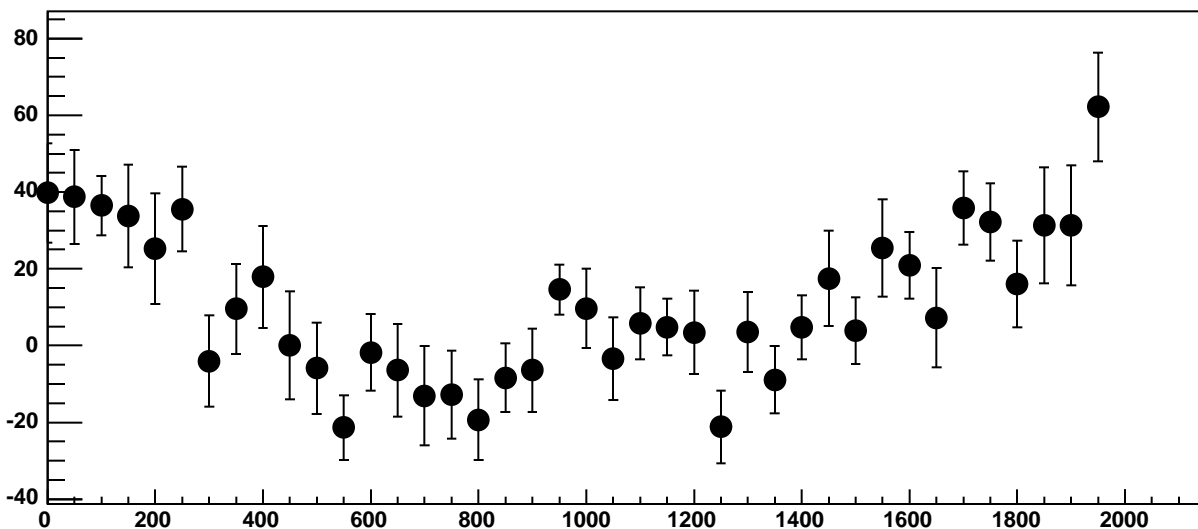
Chip 7, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC



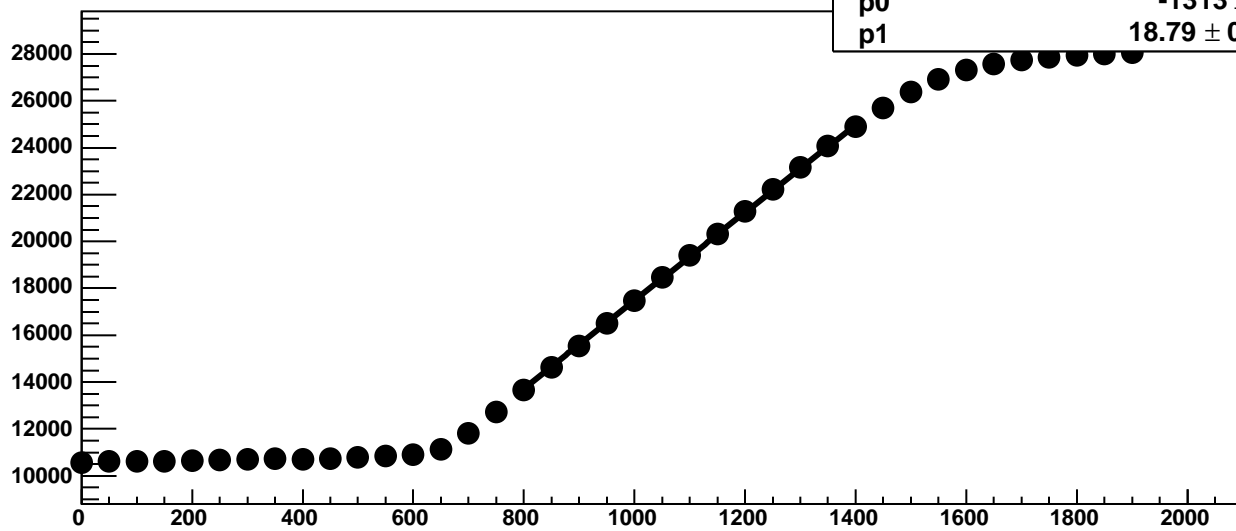
Chip 7, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC



Chip 7, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC

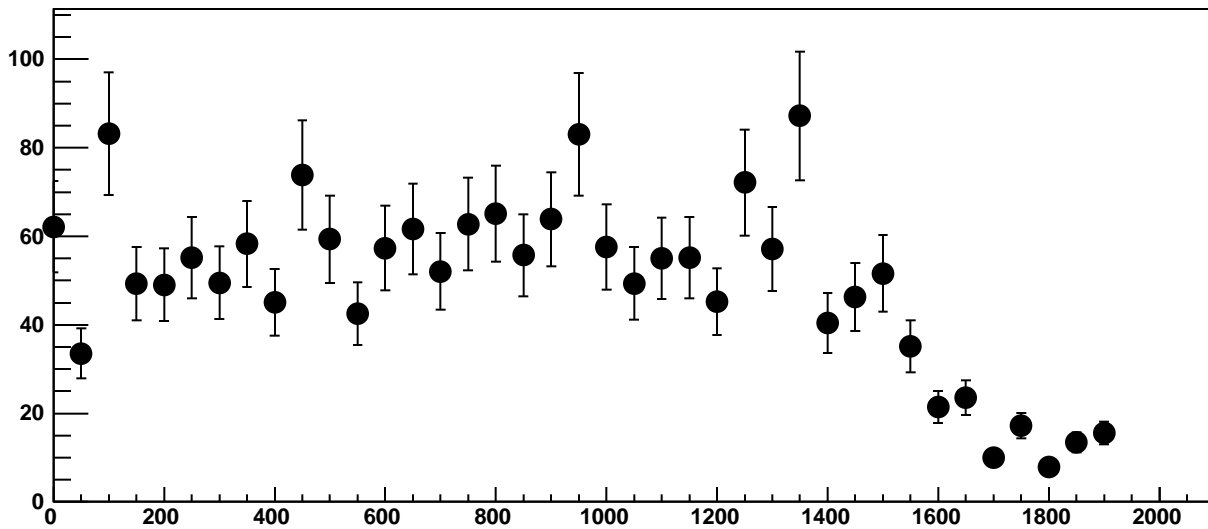


Chip 7, Channel 8, Enable 5!, Hold=30, ADC Mean vs DAC

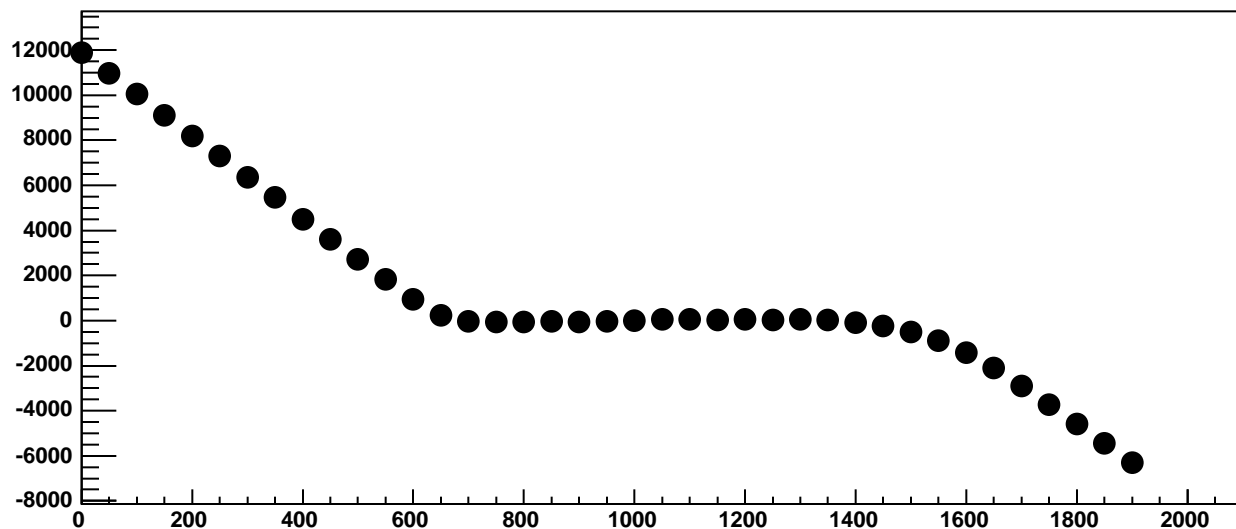


$\chi^2 / \text{ndf}$  201.3 / 11  
p0  $-1313 \pm 21.98$   
p1  $18.79 \pm 0.01929$

Chip 7, Channel 8, Enable 5!, Hold=30, ADC Noise vs DAC

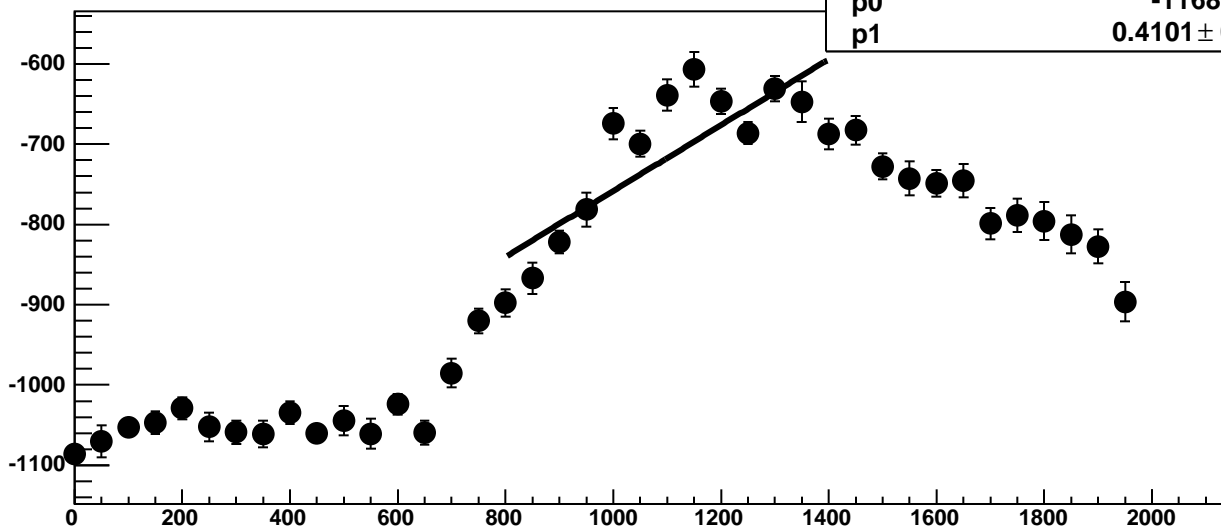


Chip 7, Channel 8, Enable 5!, Hold=30, ADC Residuals vs DAC

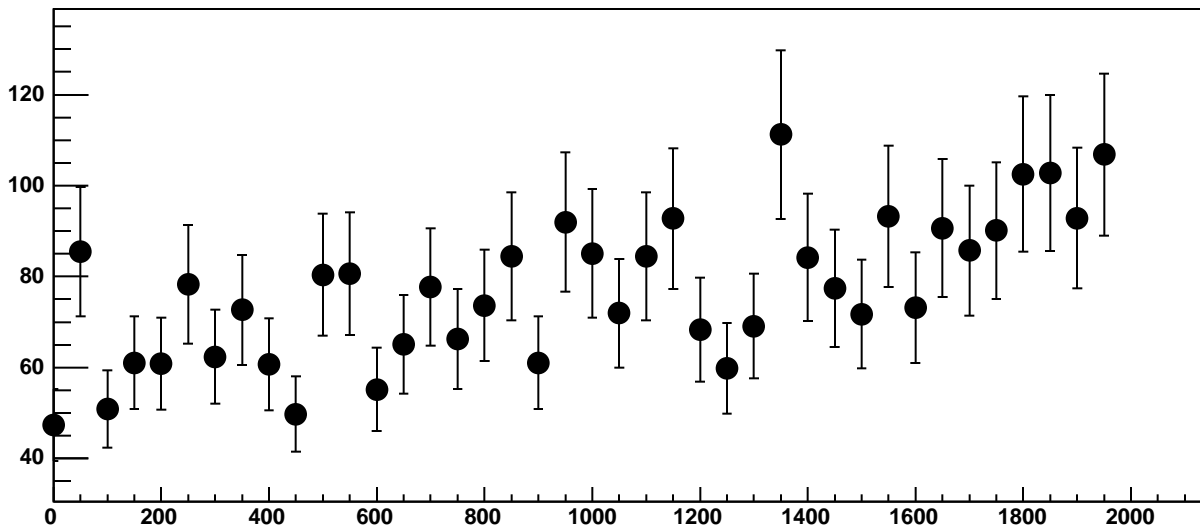




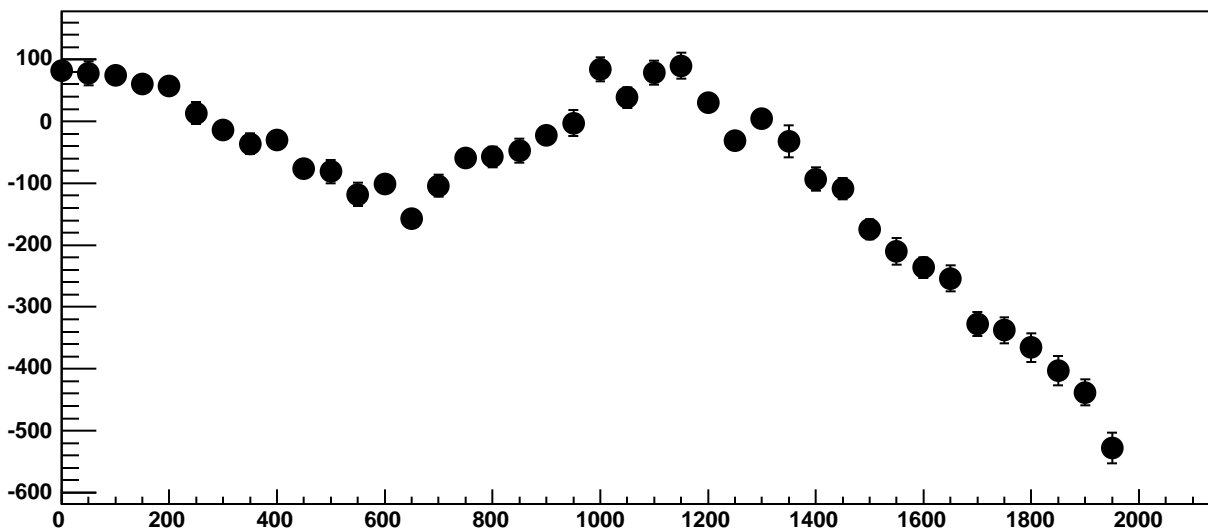
Chip 7, Channel 9, Enable 0, Hold=30, ADC Mean vs DAC



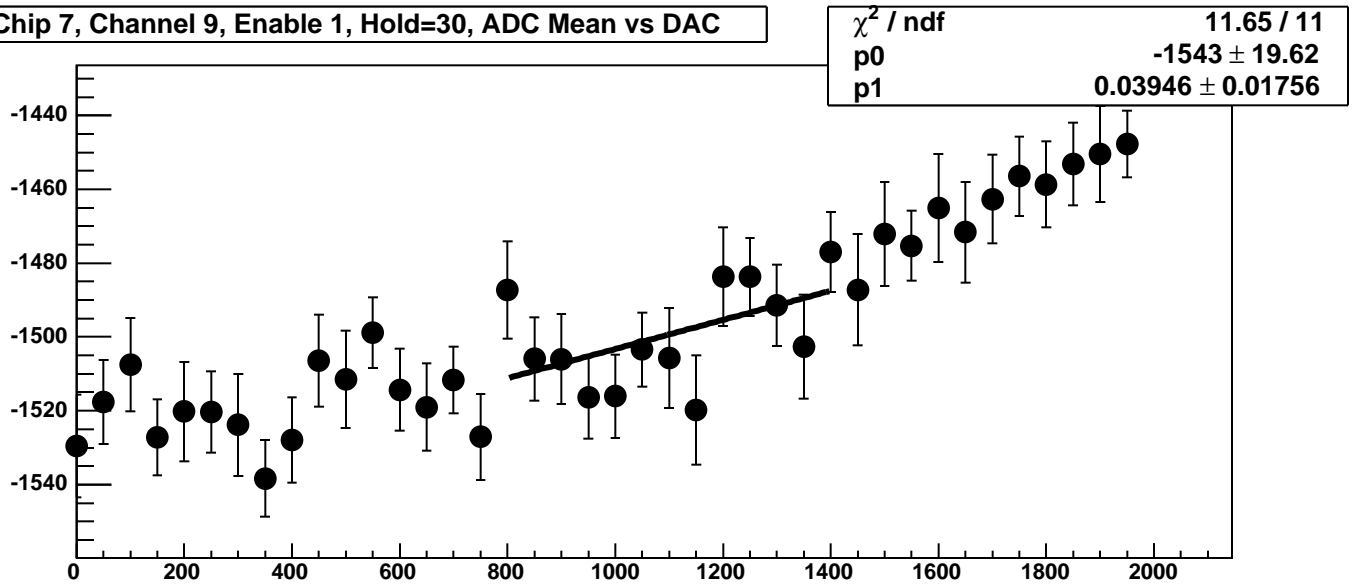
Chip 7, Channel 9, Enable 0, Hold=30, ADC Noise vs DAC



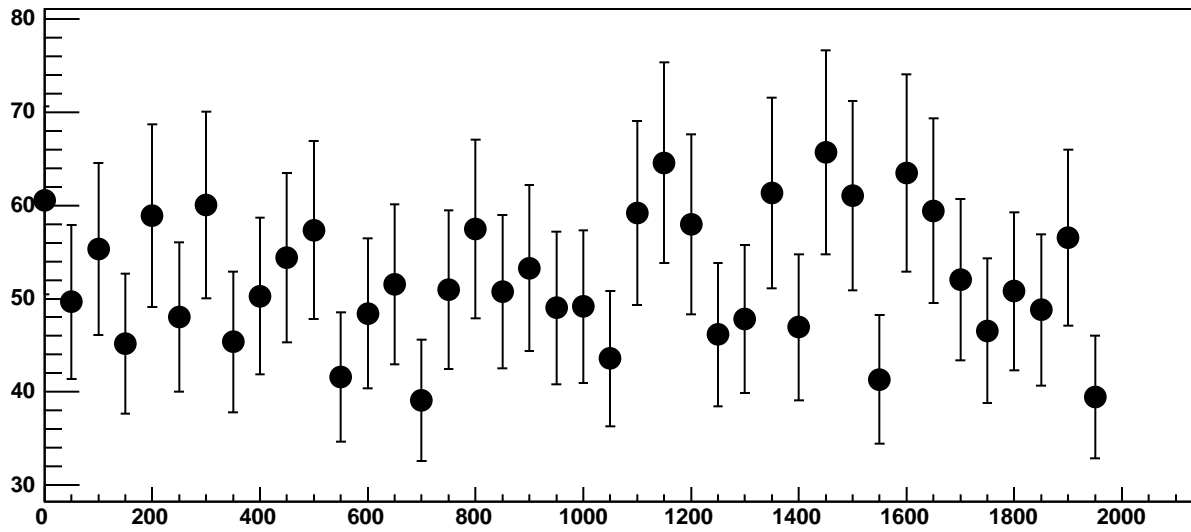
Chip 7, Channel 9, Enable 0, Hold=30, ADC Residuals vs DAC



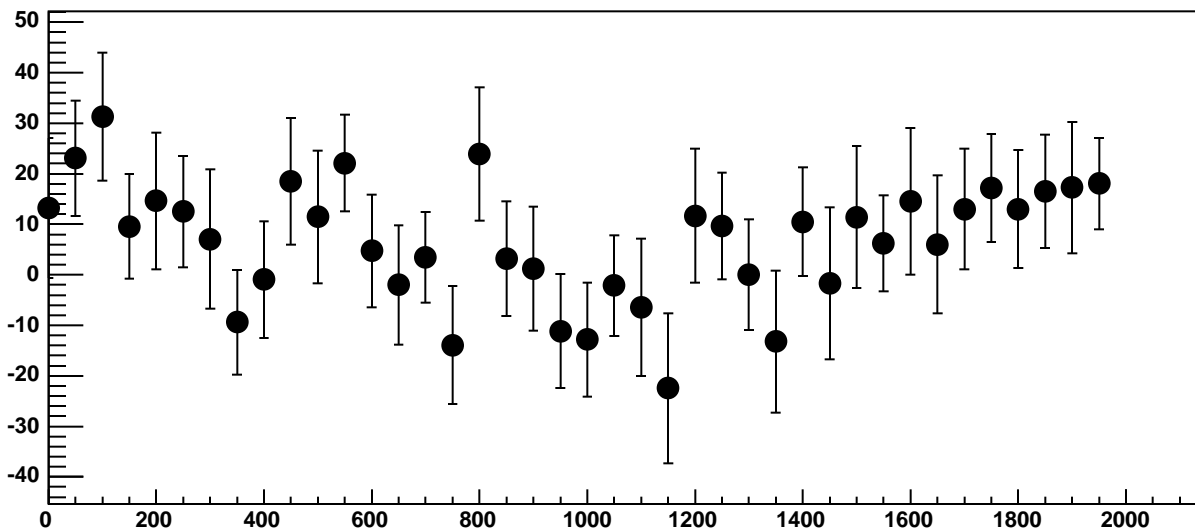
Chip 7, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC



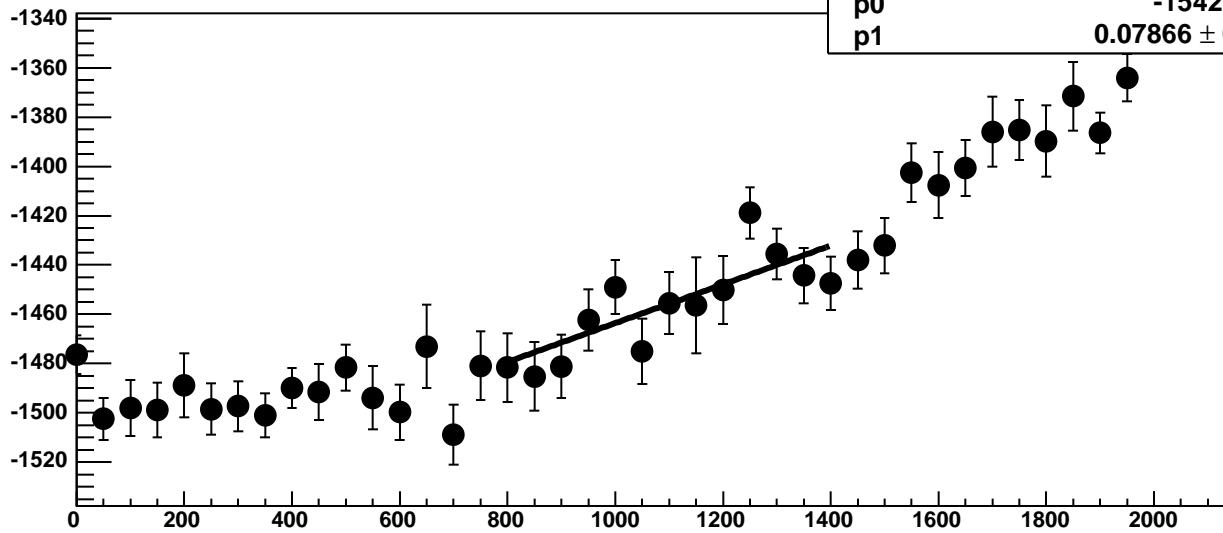
Chip 7, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC



Chip 7, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC



Chip 7, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

13.03 / 11

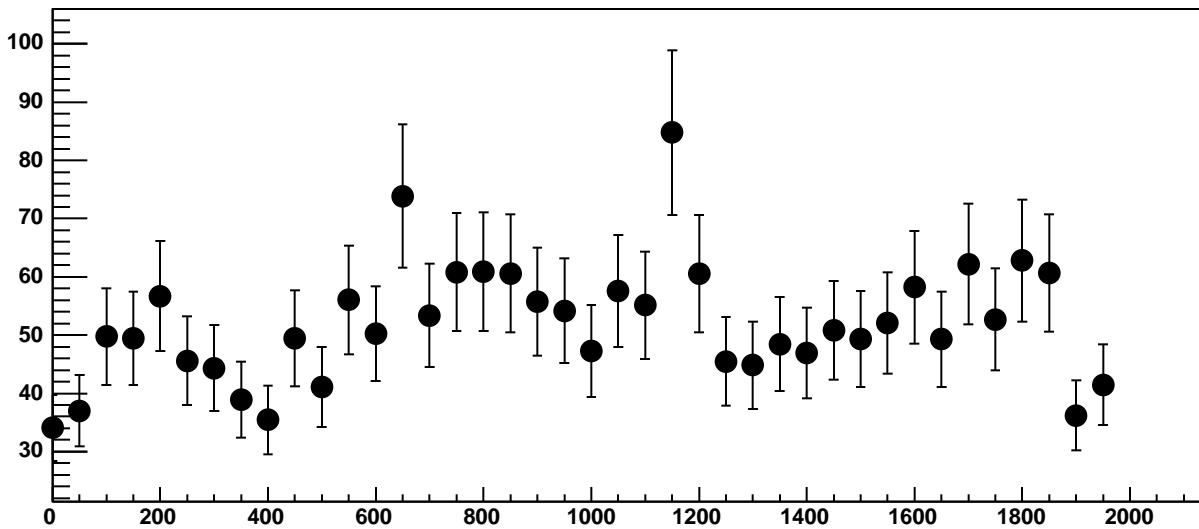
p0

$-1542 \pm 20.42$

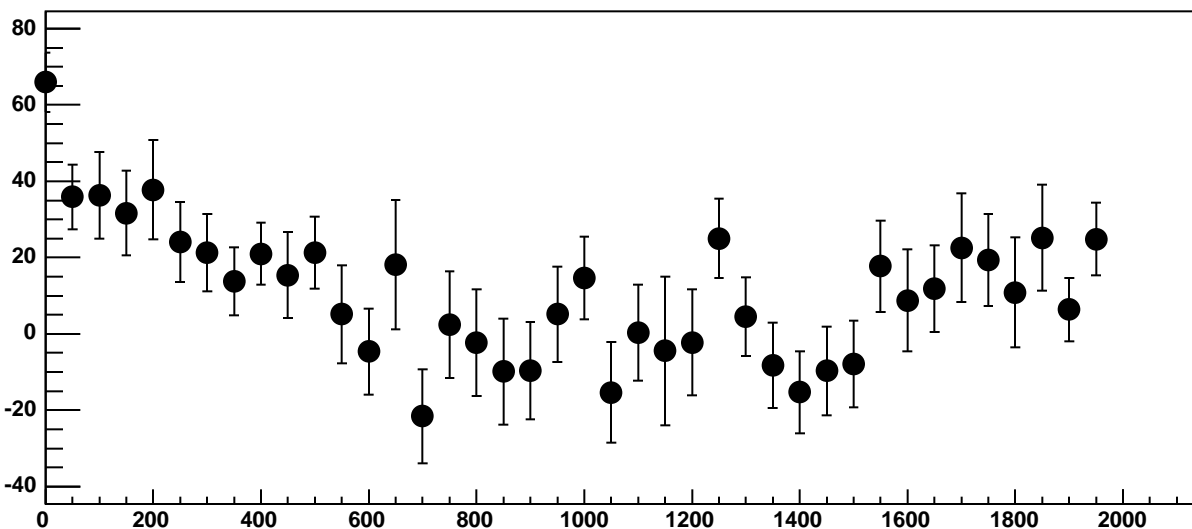
p1

$0.07866 \pm 0.01786$

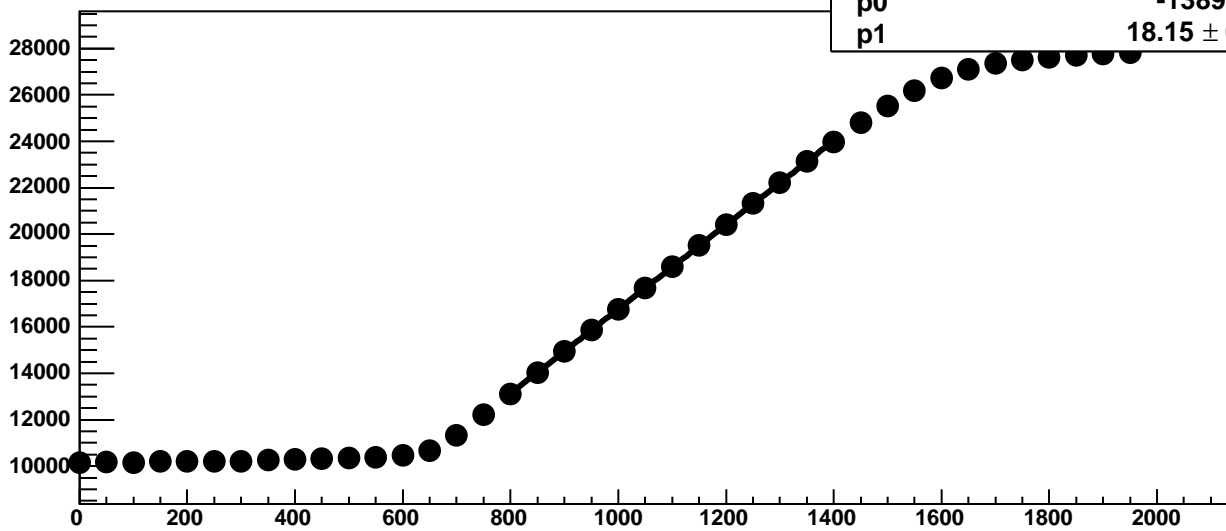
Chip 7, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC



Chip 7, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC

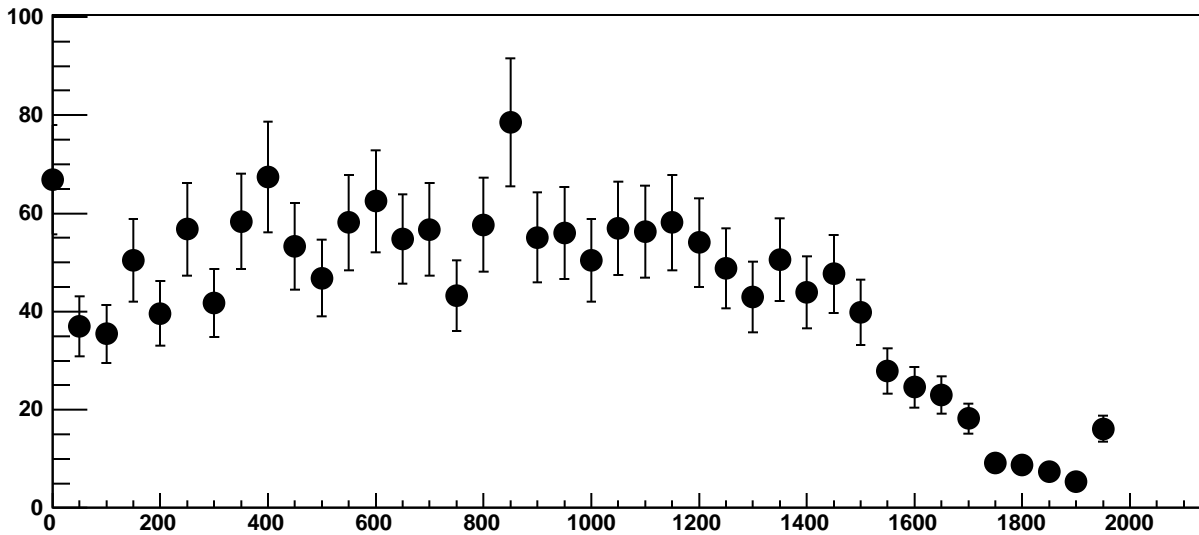


Chip 7, Channel 9, Enable 3!, Hold=30, ADC Mean vs DAC

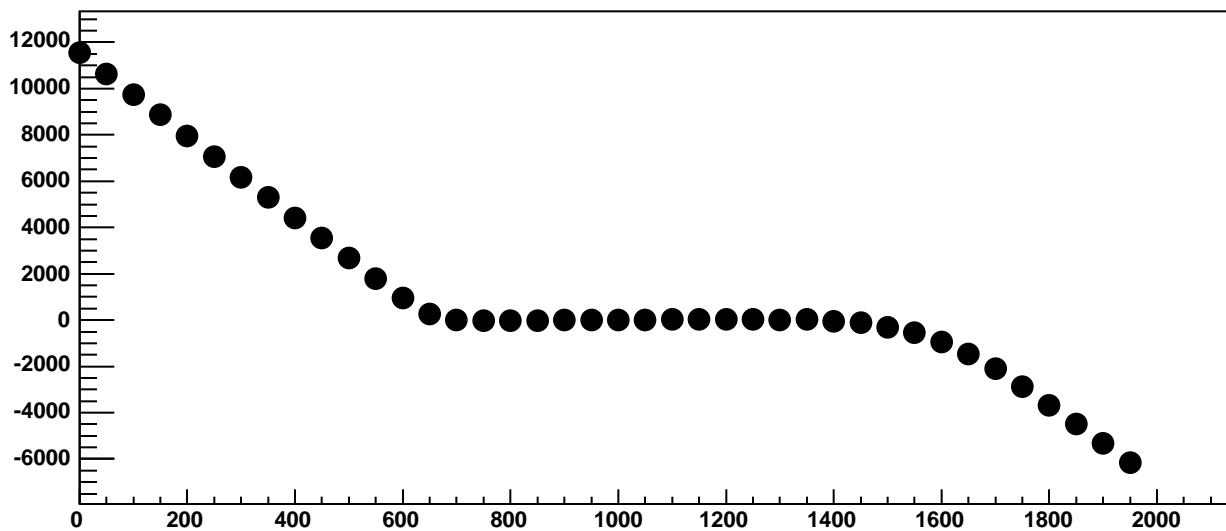


$\chi^2 / \text{ndf}$  50.17 / 11  
p0  $-1389 \pm 20.77$   
p1  $18.15 \pm 0.01802$

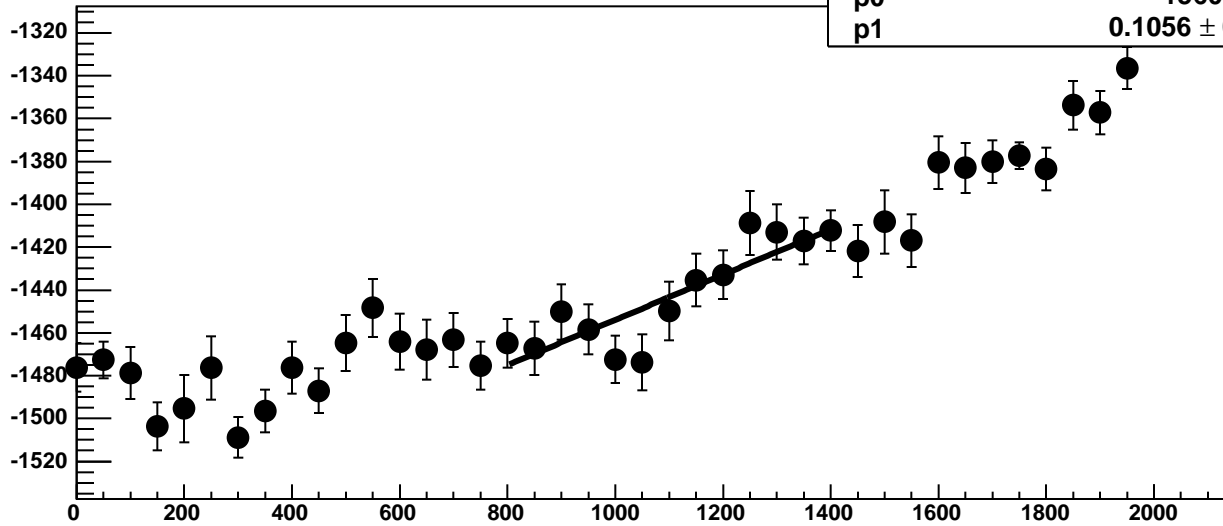
Chip 7, Channel 9, Enable 3!, Hold=30, ADC Noise vs DAC



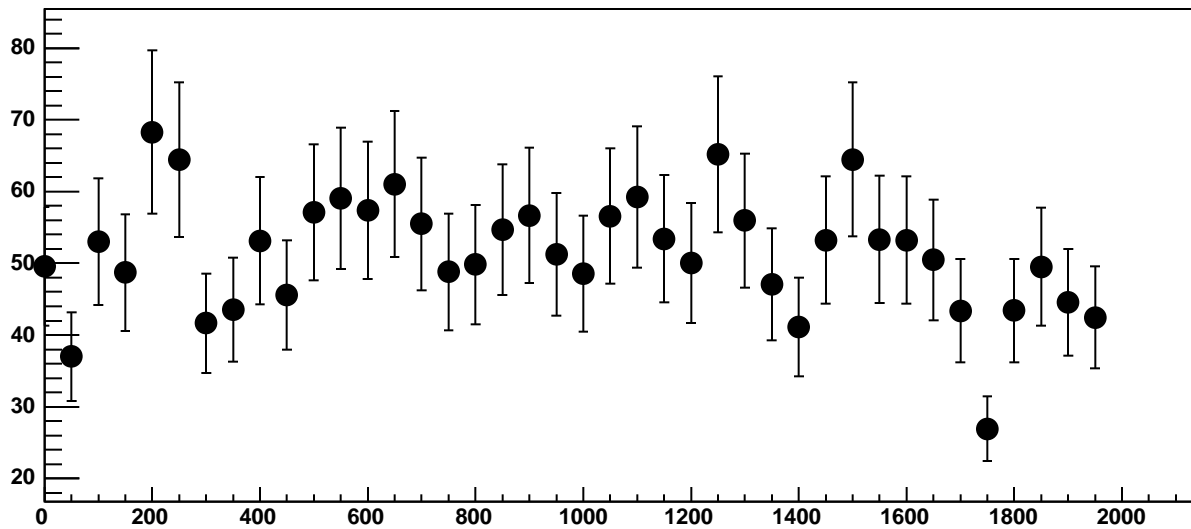
Chip 7, Channel 9, Enable 3!, Hold=30, ADC Residuals vs DAC



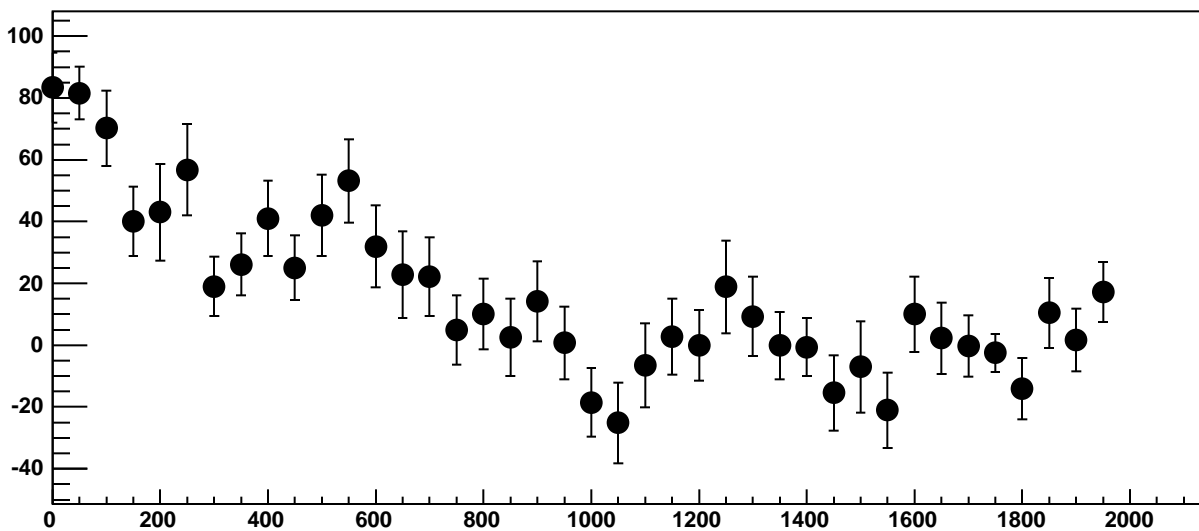
Chip 7, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC



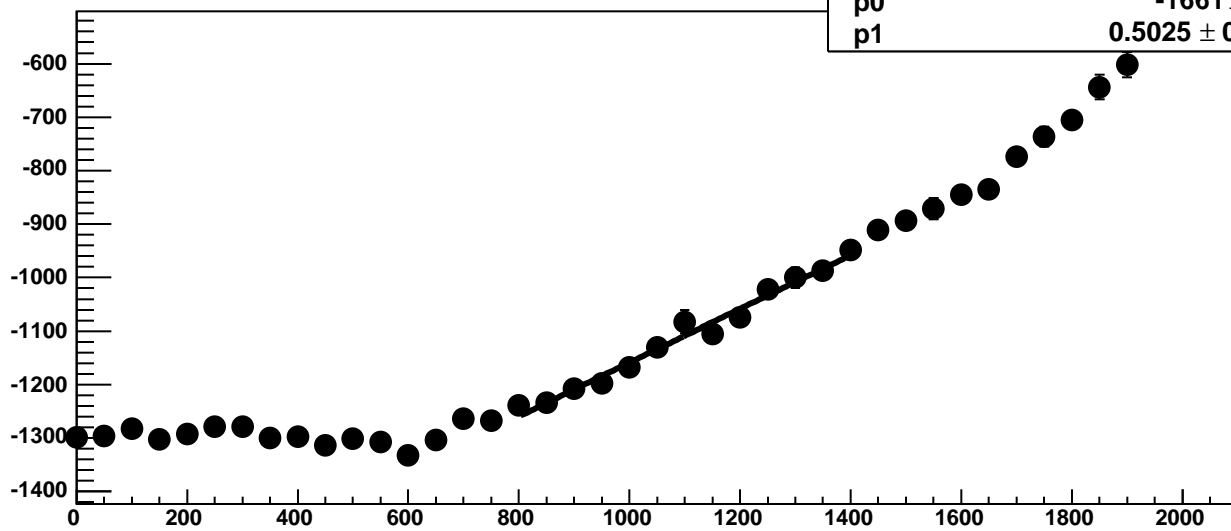
Chip 7, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC



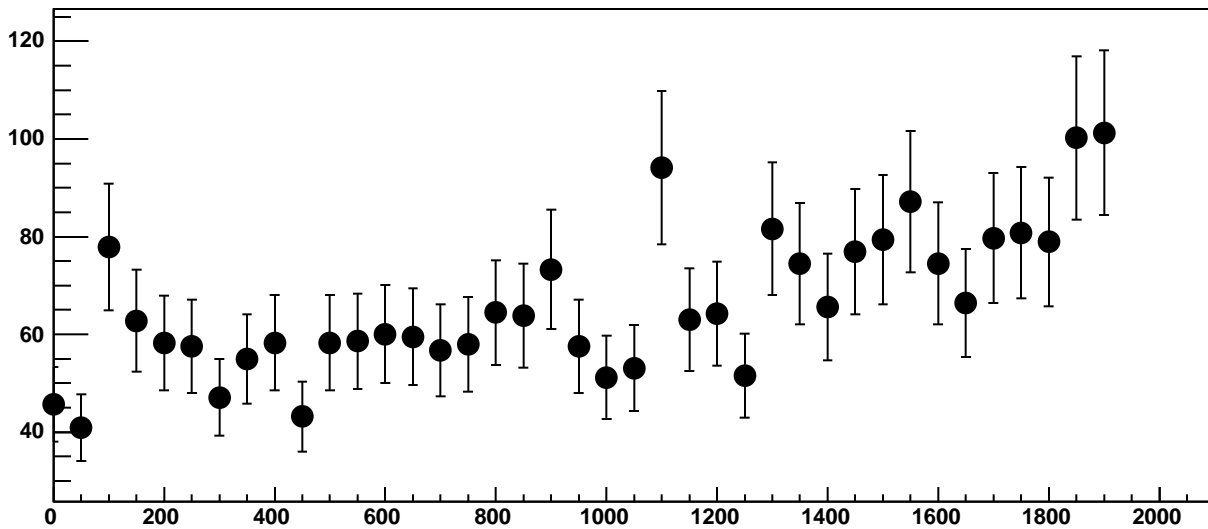
Chip 7, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC



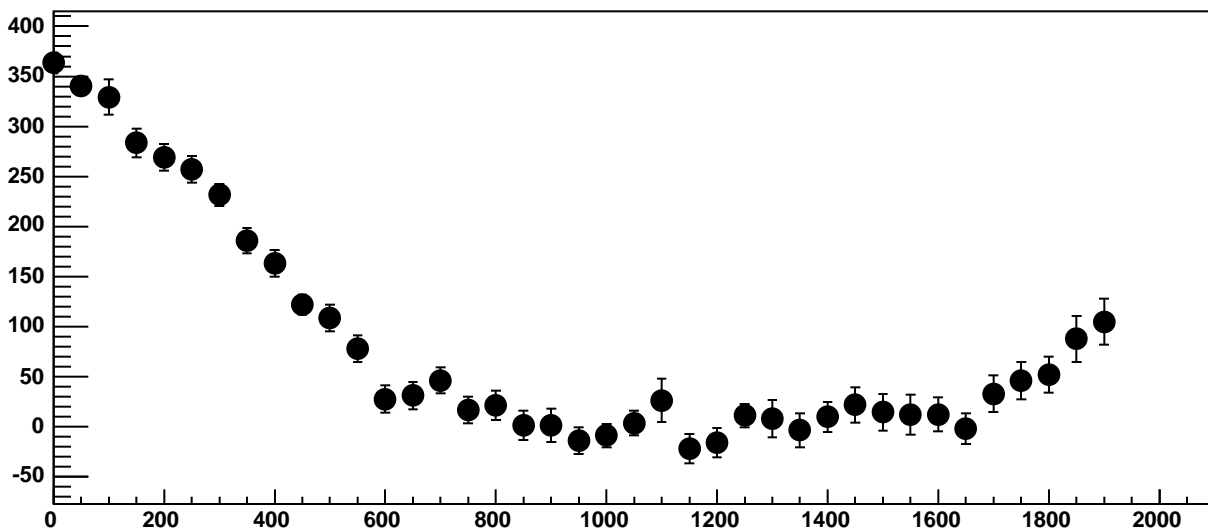
Chip 7, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC



Chip 7, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC



Chip 7, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 7, Channel 10, Enable 0!, Hold=30, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

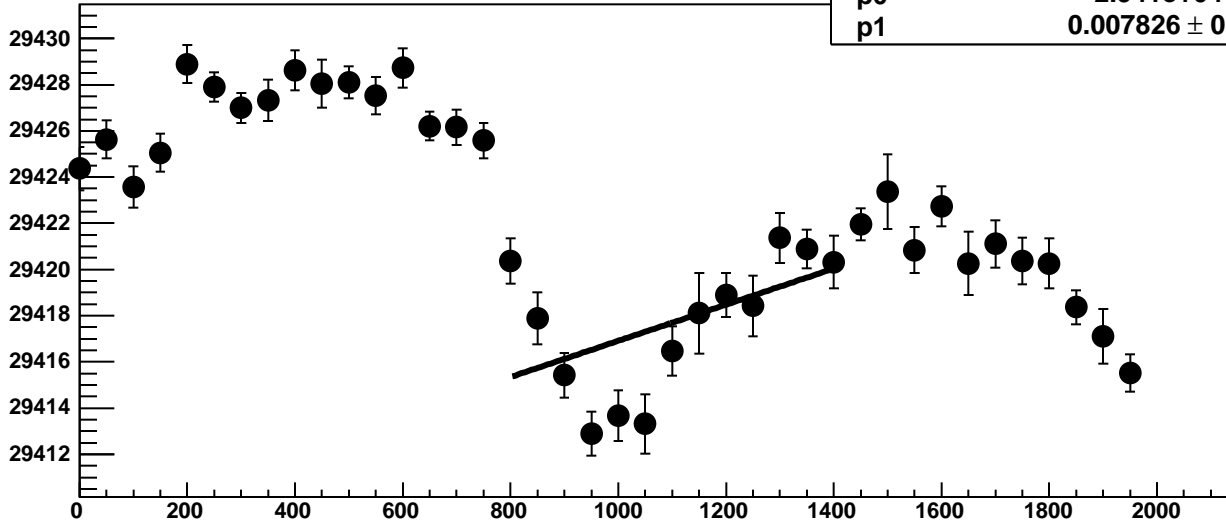
70.73 / 11

p0

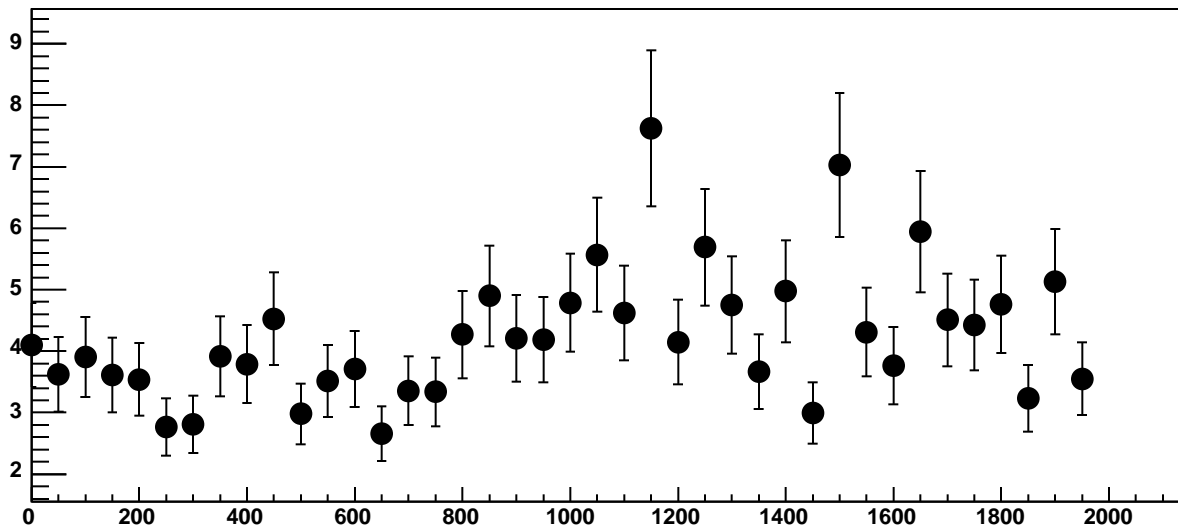
$2.941\text{e}+04 \pm 1.687$

p1

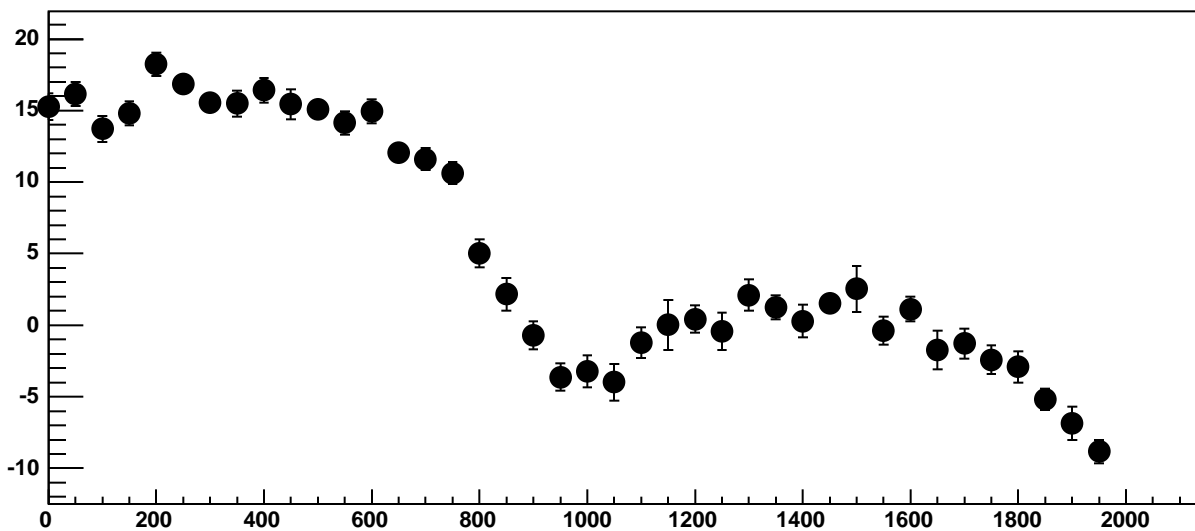
$0.007826 \pm 0.001514$



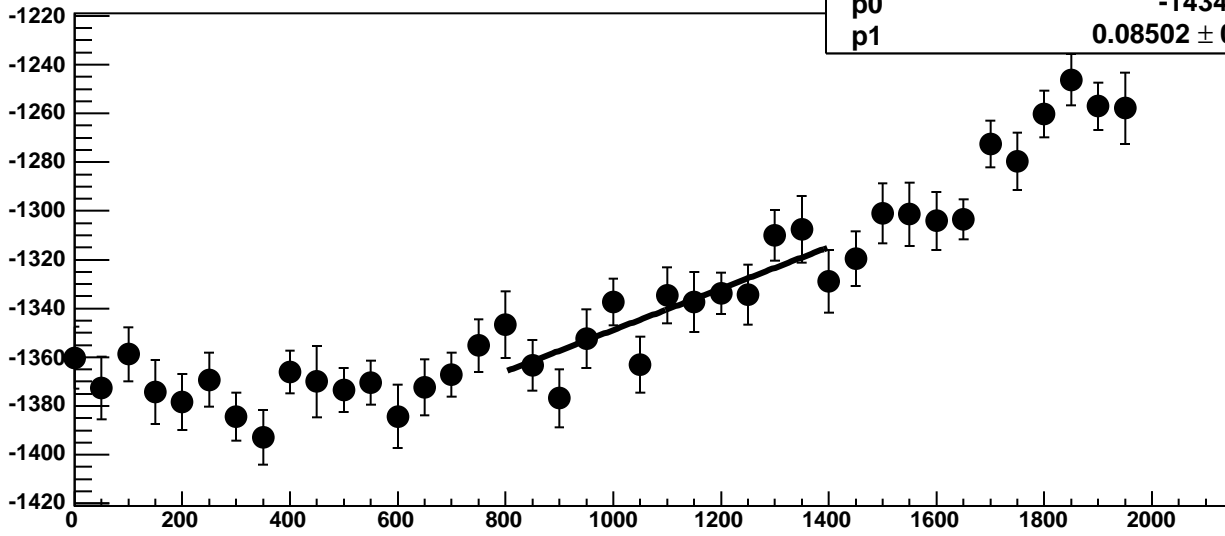
Chip 7, Channel 10, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 7, Channel 10, Enable 0!, Hold=30, ADC Residuals vs DAC

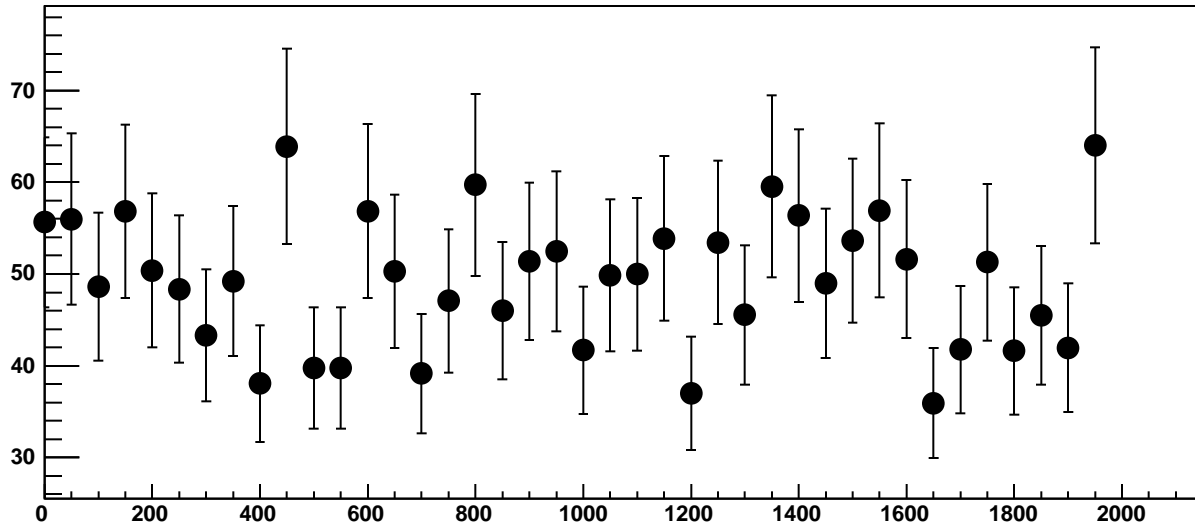


Chip 7, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC

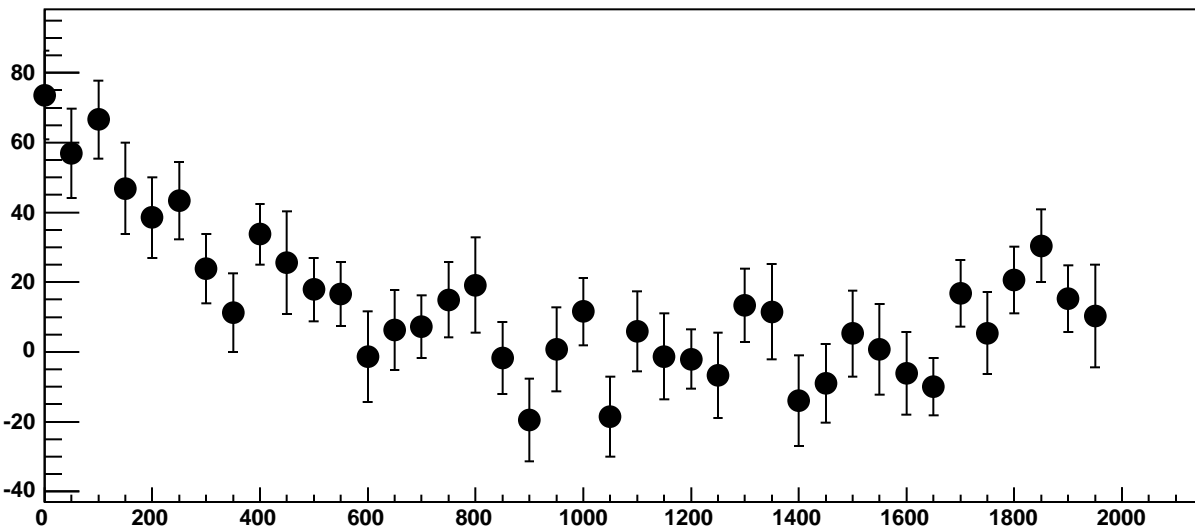


$\chi^2 / \text{ndf}$  12.93 / 11  
p0  $-1434 \pm 19.7$   
p1  $0.08502 \pm 0.01772$

Chip 7, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC

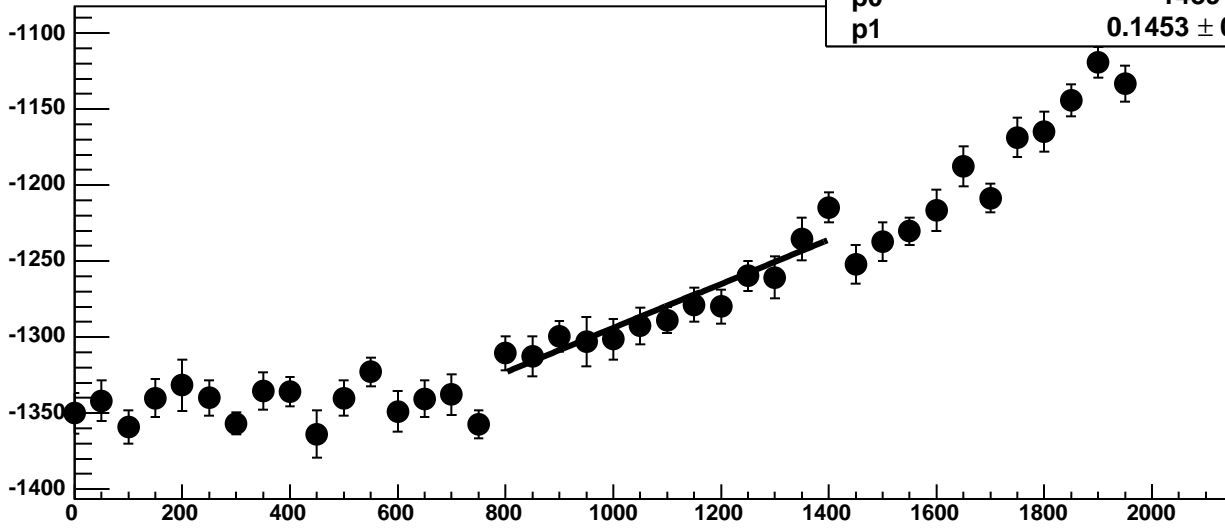


Chip 7, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC

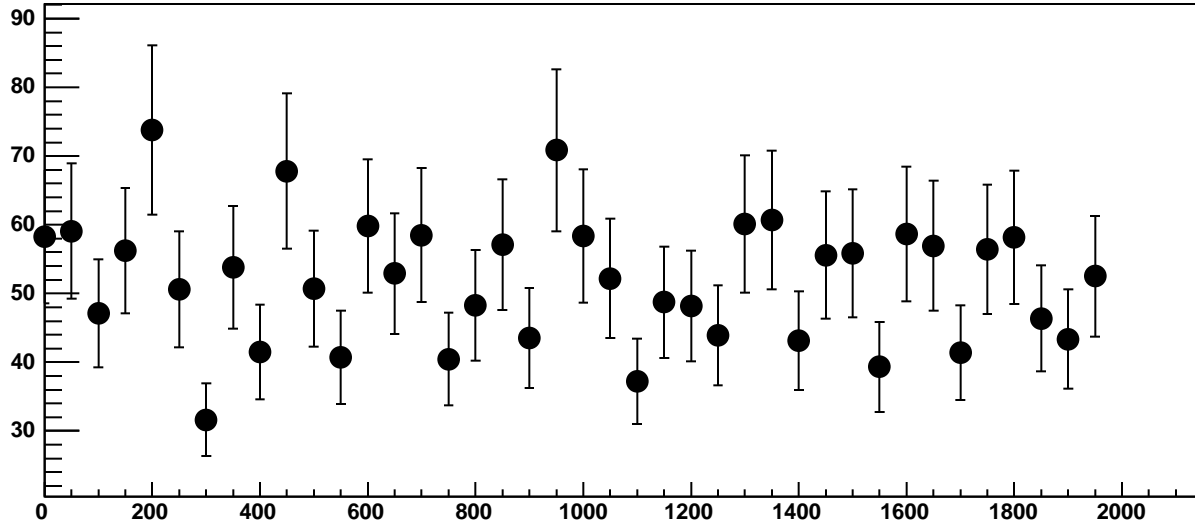




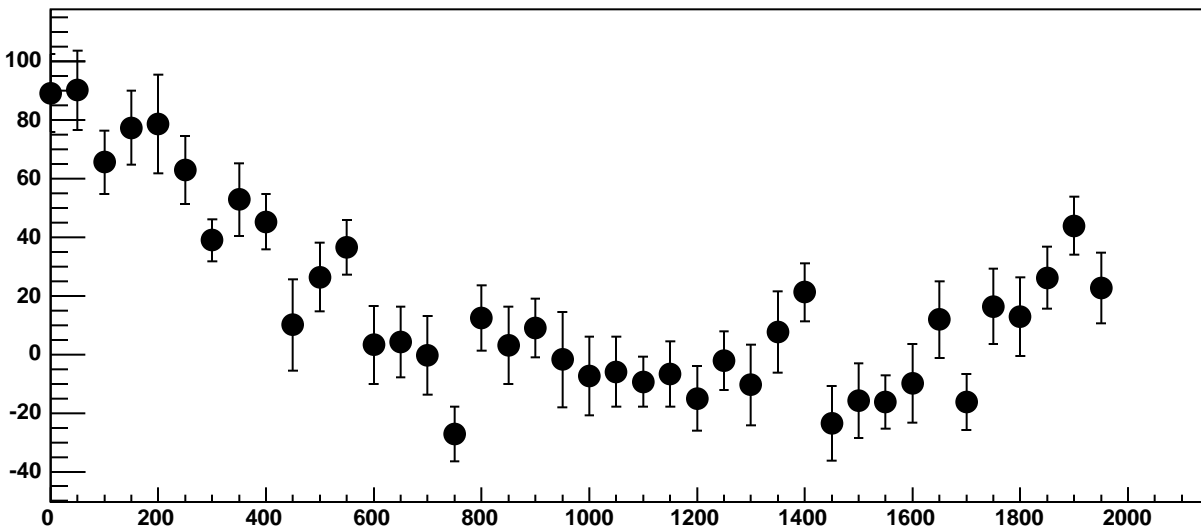
Chip 7, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC



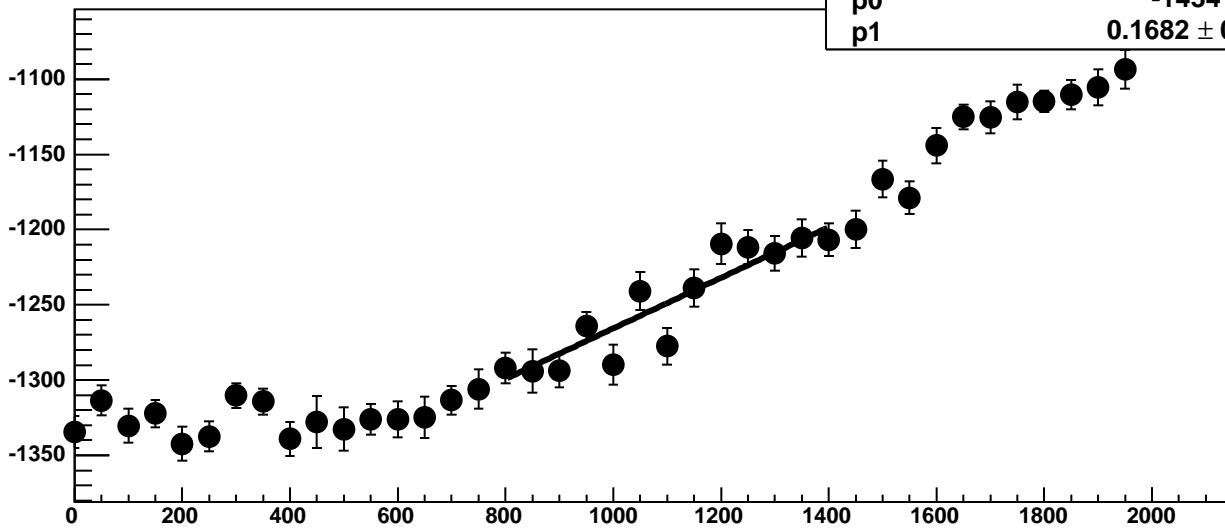
Chip 7, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC



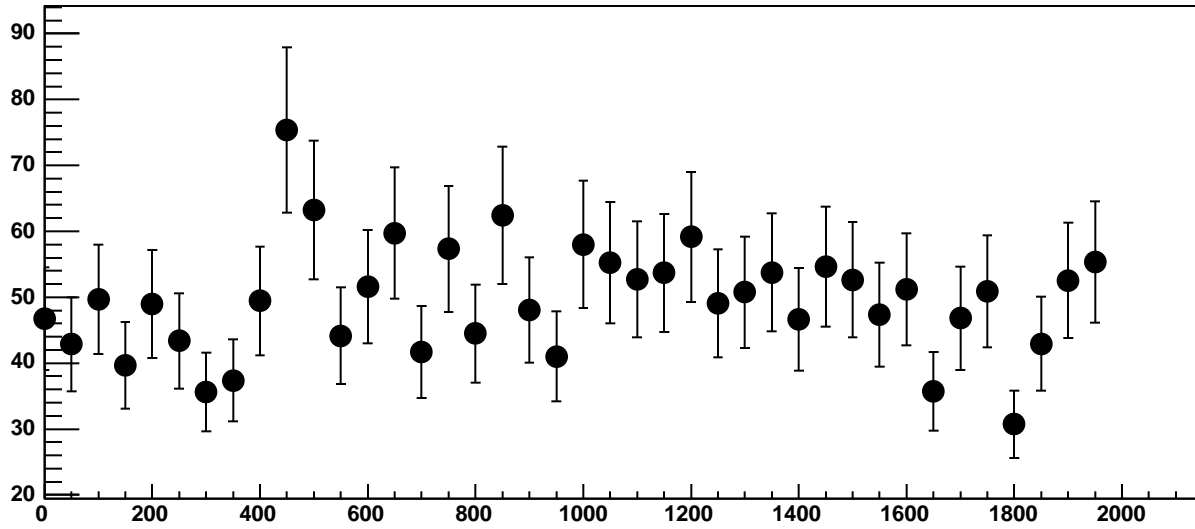
Chip 7, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC



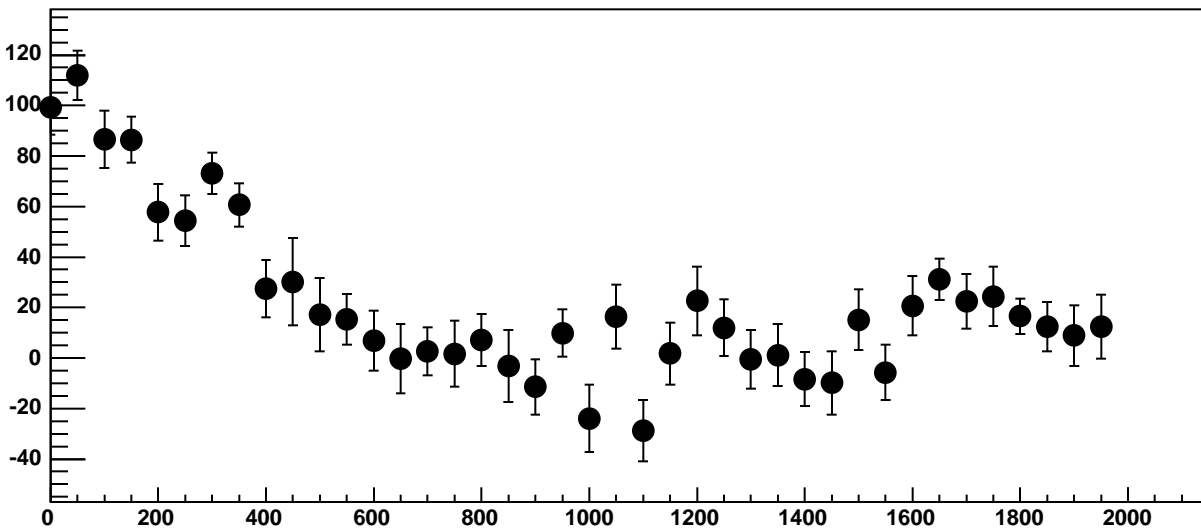
Chip 7, Channel 10, Enable 3, Hold=30, ADC Mean vs DAC



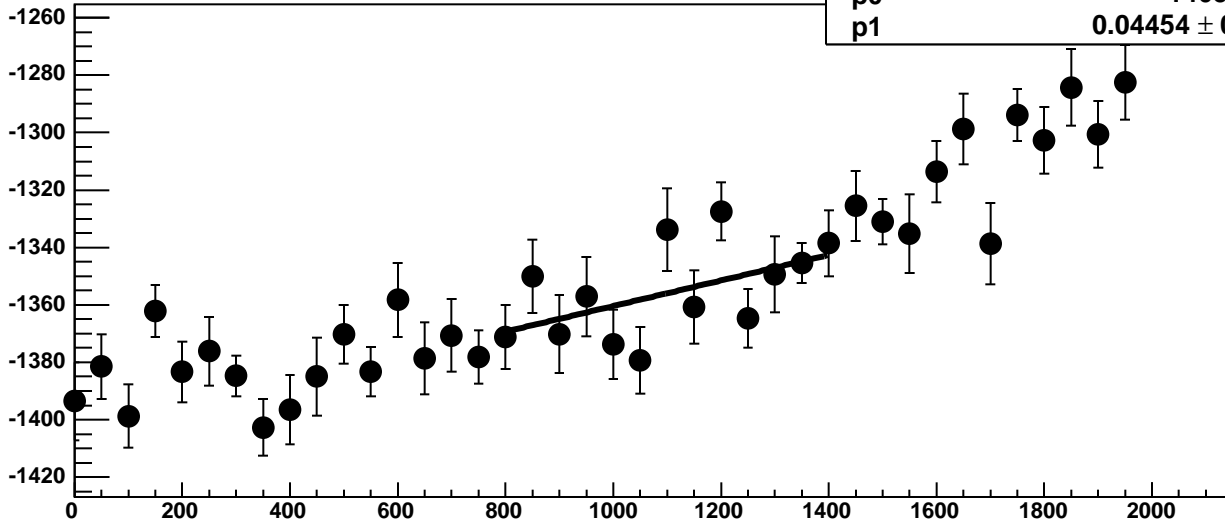
Chip 7, Channel 10, Enable 3, Hold=30, ADC Noise vs DAC



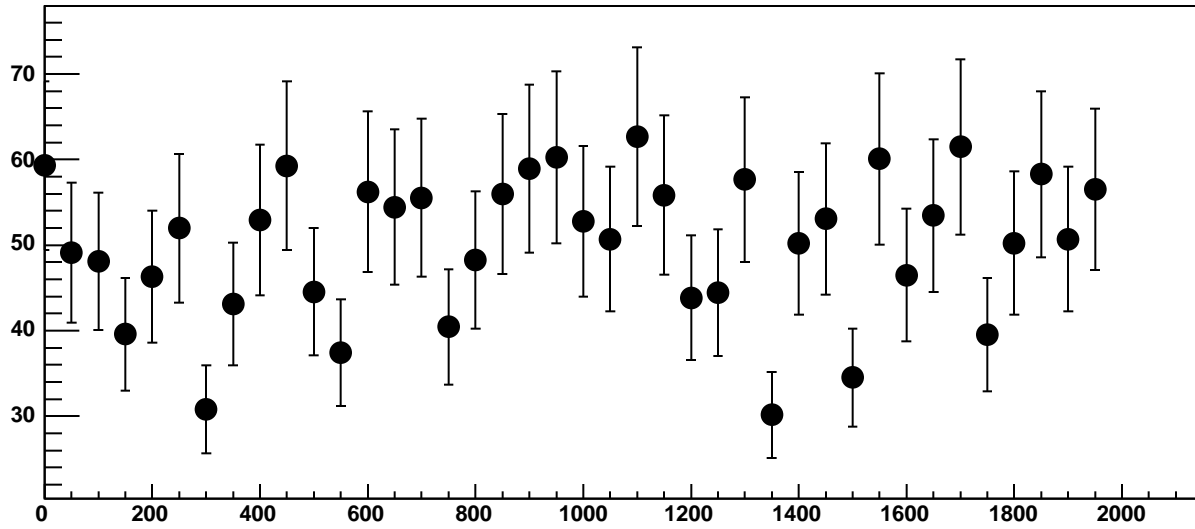
Chip 7, Channel 10, Enable 3, Hold=30, ADC Residuals vs DAC



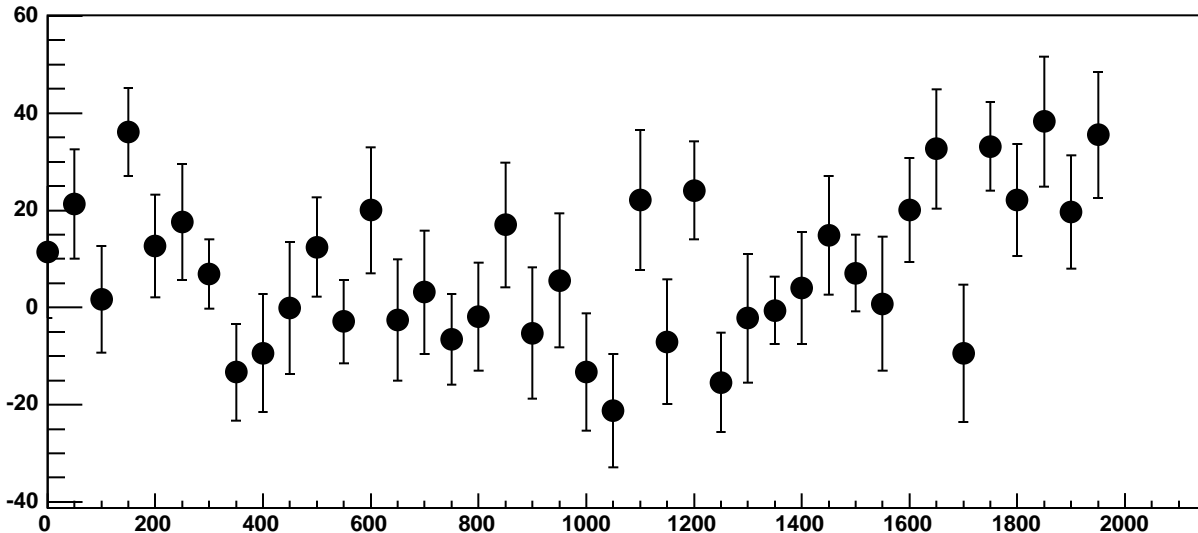
Chip 7, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC



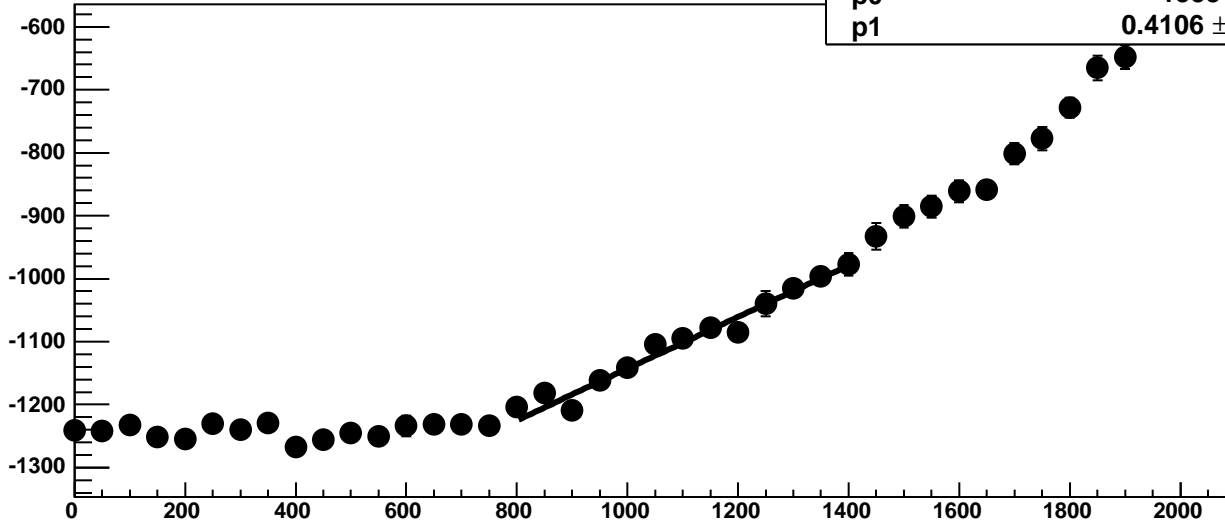
Chip 7, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC



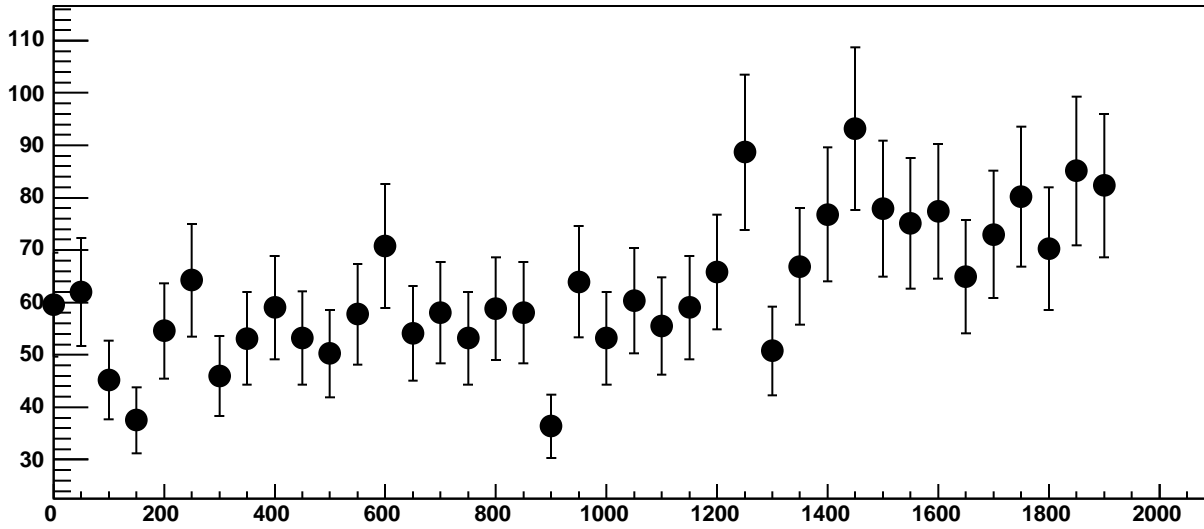
Chip 7, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC



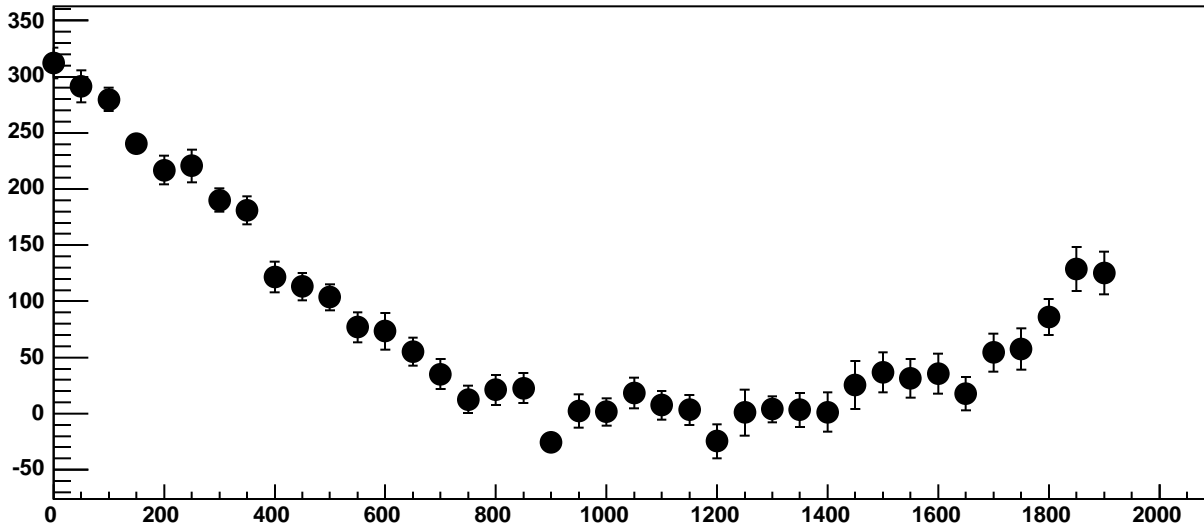
Chip 7, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC



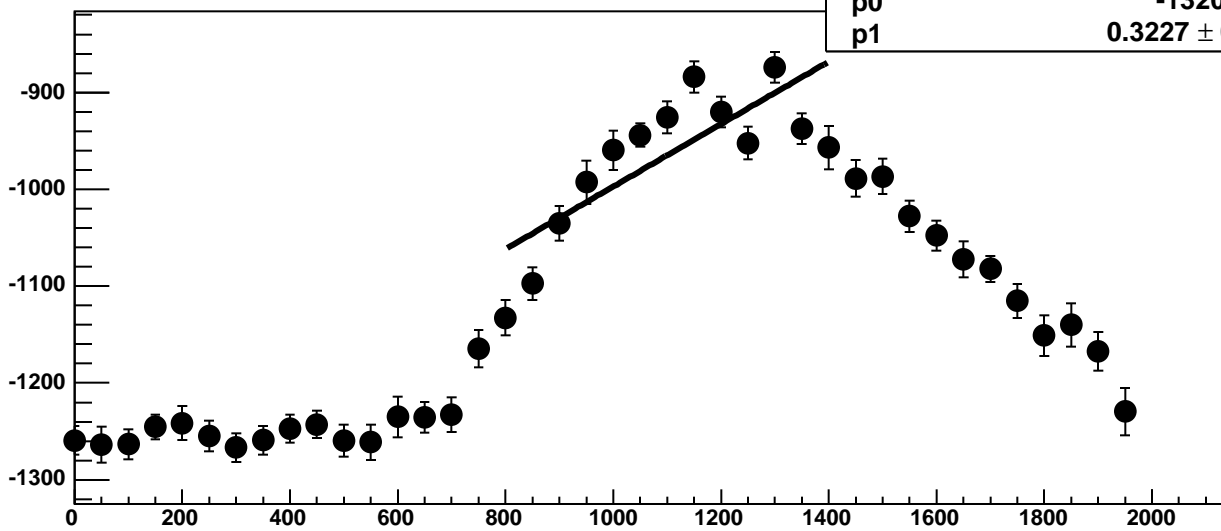
Chip 7, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC



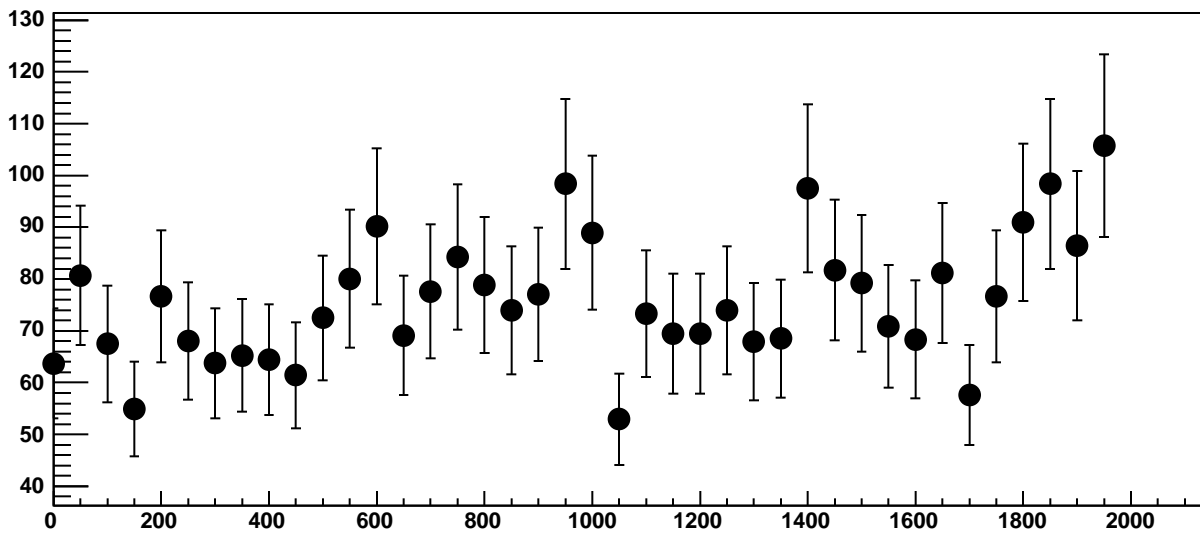
Chip 7, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC



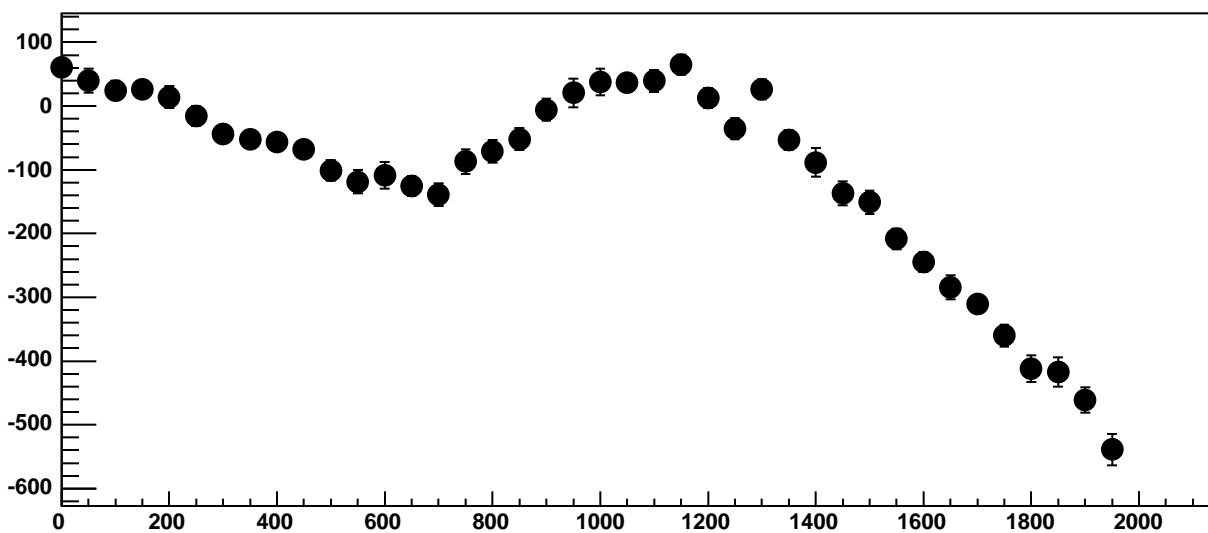
Chip 7, Channel 11, Enable 0, Hold=30, ADC Mean vs DAC



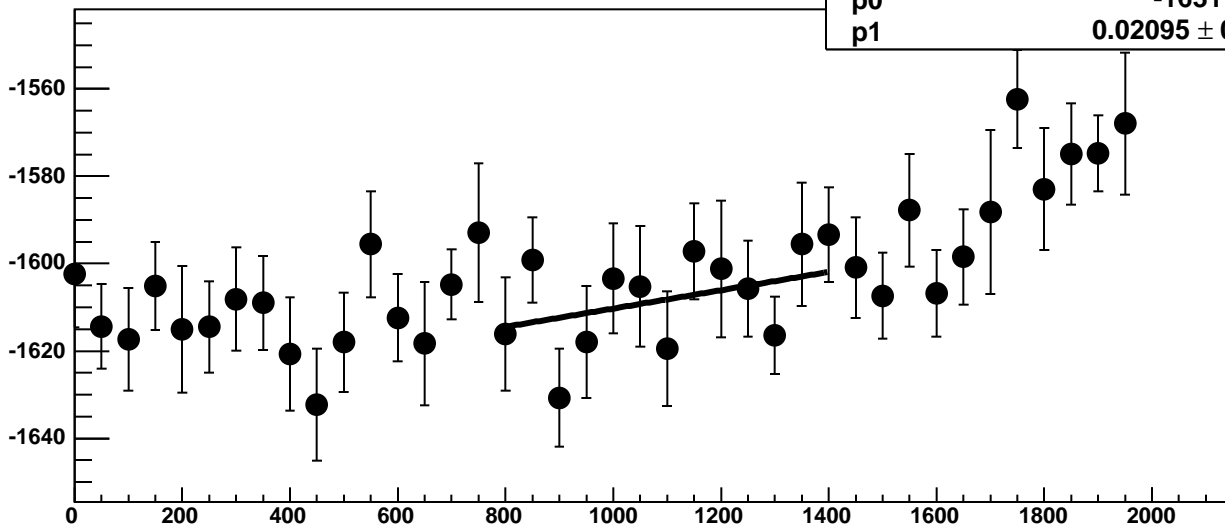
Chip 7, Channel 11, Enable 0, Hold=30, ADC Noise vs DAC



Chip 7, Channel 11, Enable 0, Hold=30, ADC Residuals vs DAC

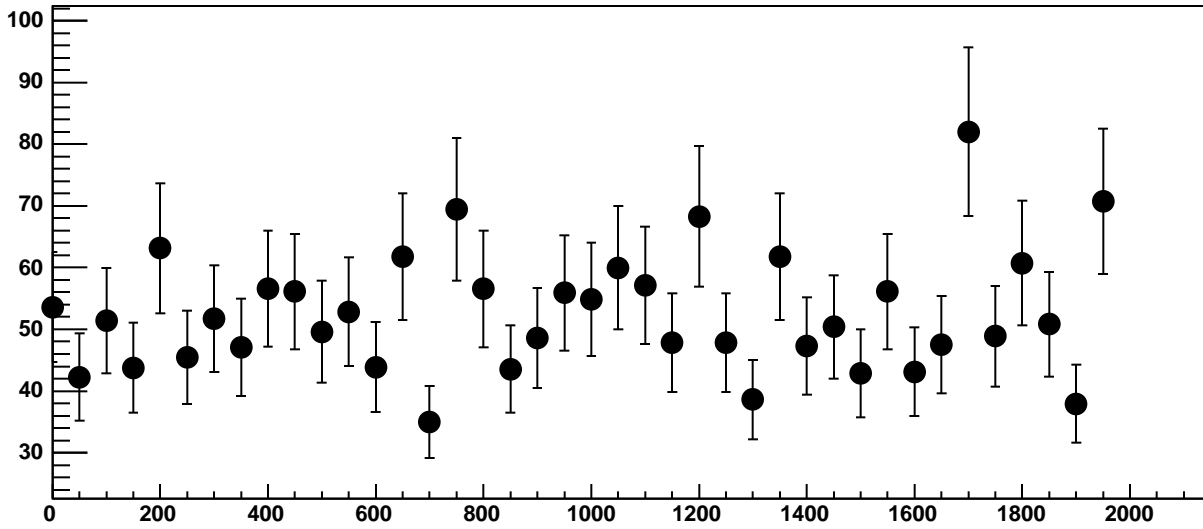


Chip 7, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC

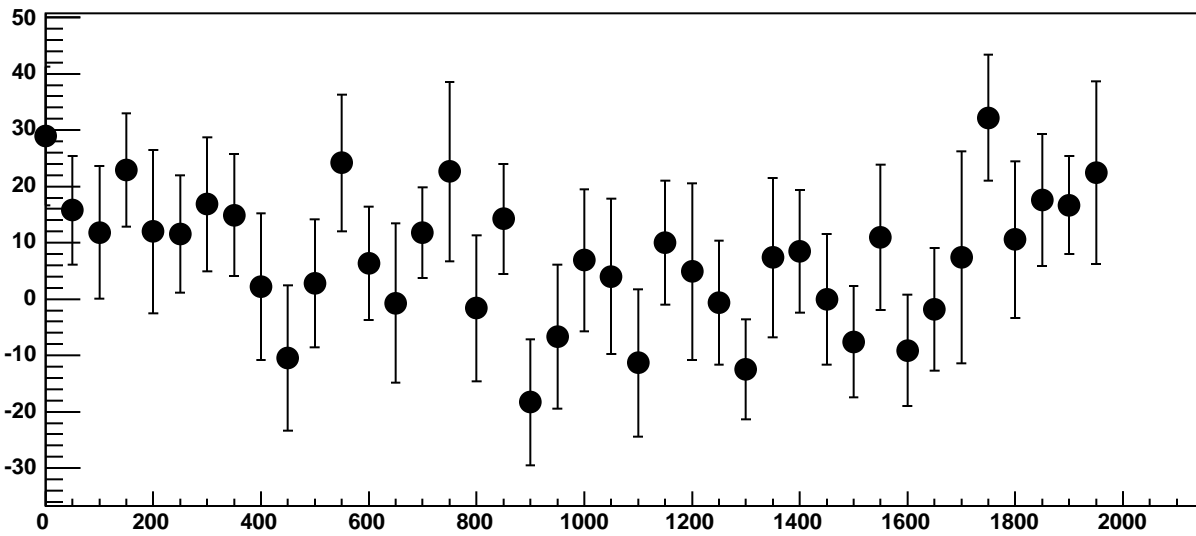


$\chi^2 / \text{ndf}$  10.03 / 11  
p0  $-1631 \pm 18.82$   
p1  $0.02095 \pm 0.01676$

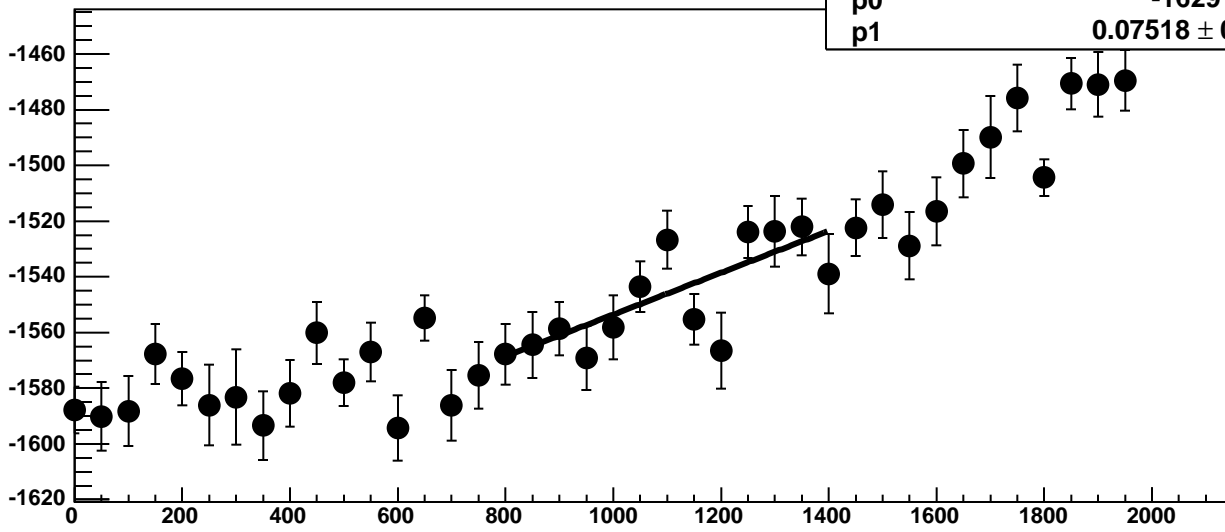
Chip 7, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC



Chip 7, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC

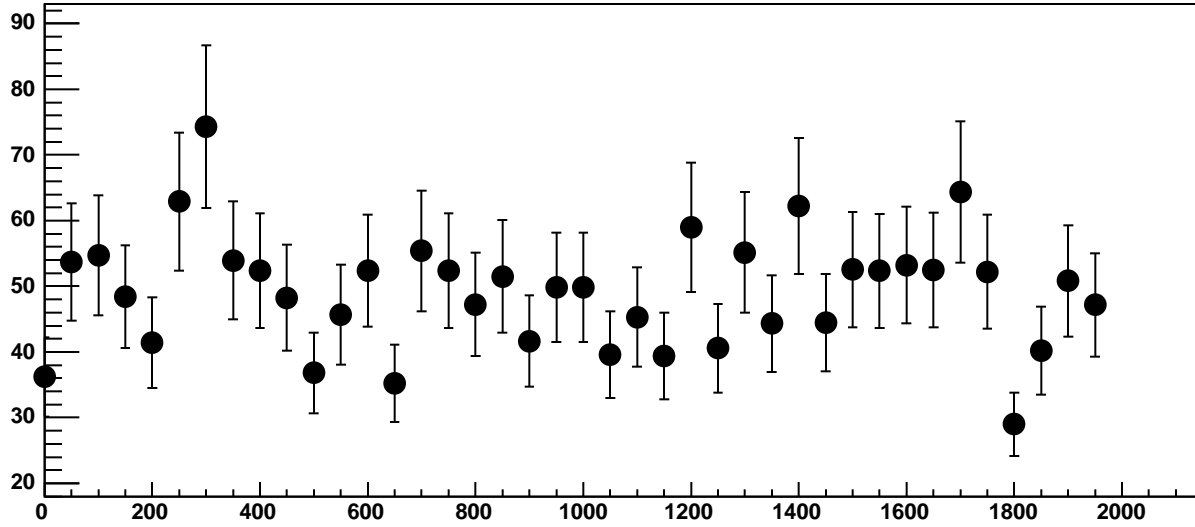


Chip 7, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC

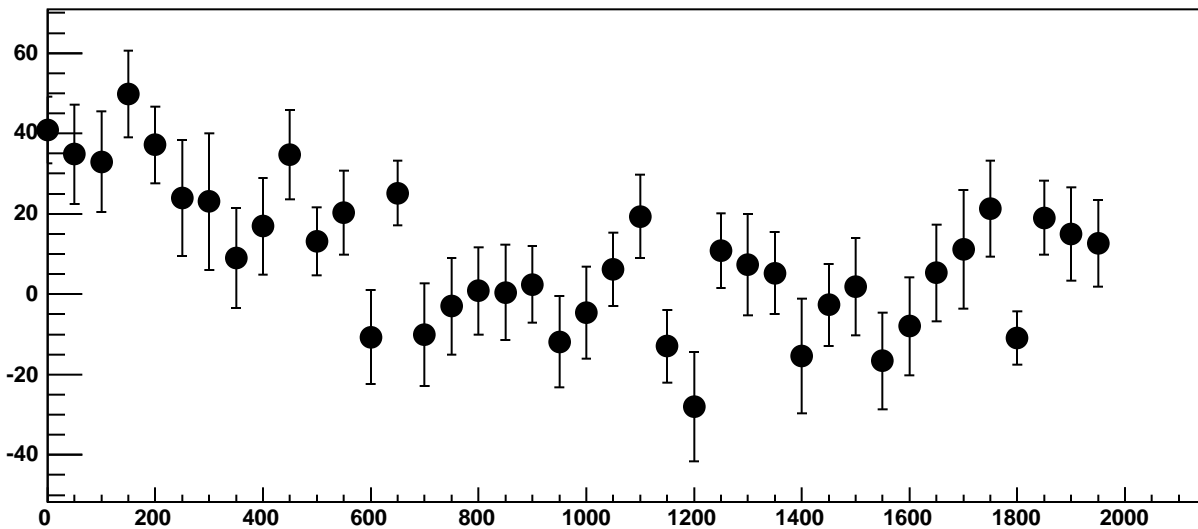


$\chi^2 / \text{ndf}$  14.72 / 11  
p0  $-1629 \pm 18.52$   
p1  $0.07518 \pm 0.01675$

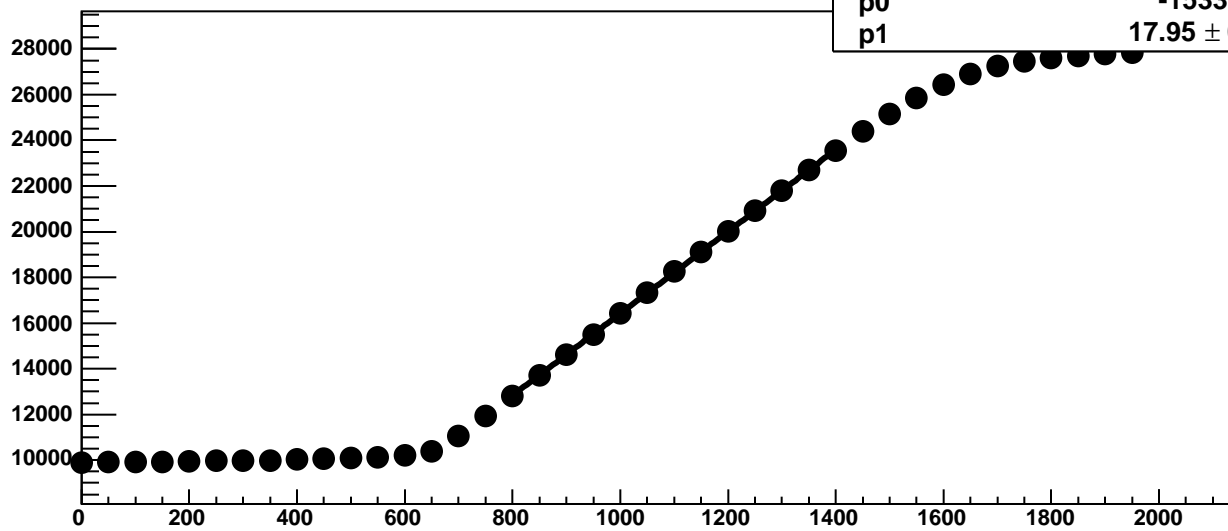
Chip 7, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC



Chip 7, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC

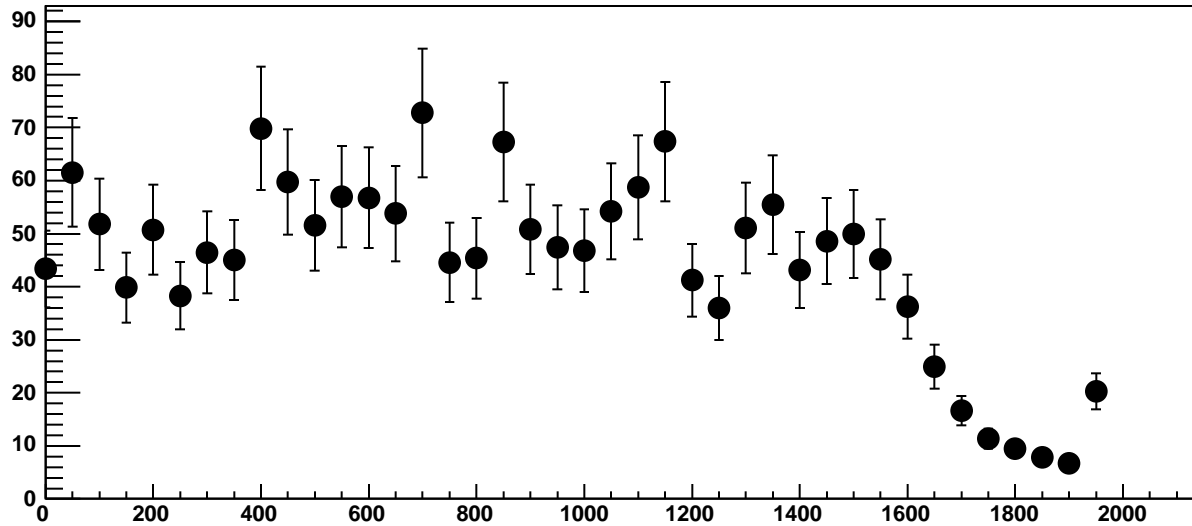


Chip 7, Channel 11, Enable 3!, Hold=30, ADC Mean vs DAC

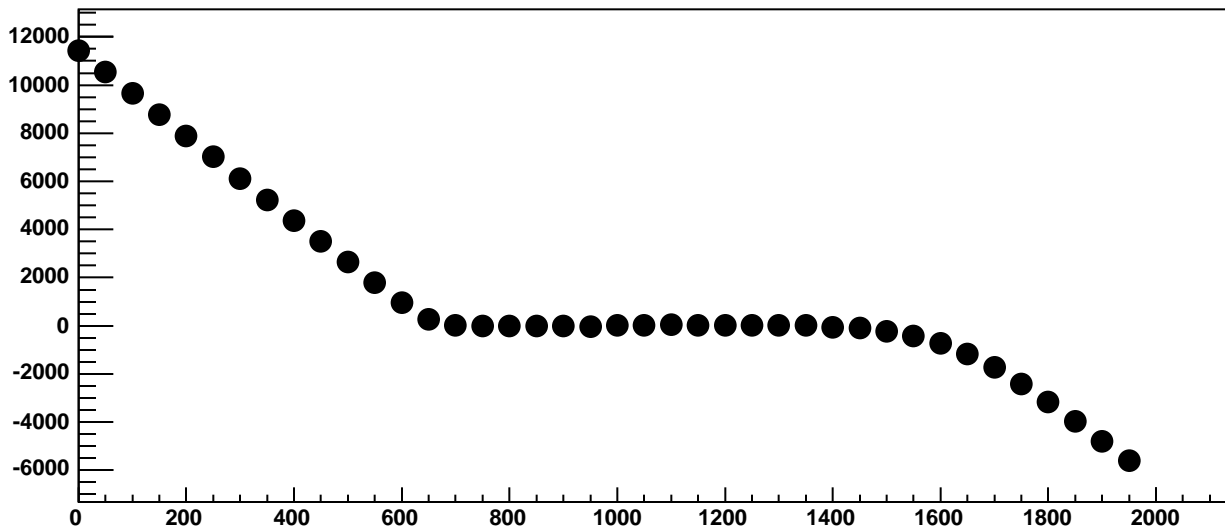


$\chi^2 / \text{ndf}$	69.66 / 11
p0	$-1533 \pm 18.65$
p1	$17.95 \pm 0.01645$

Chip 7, Channel 11, Enable 3!, Hold=30, ADC Noise vs DAC

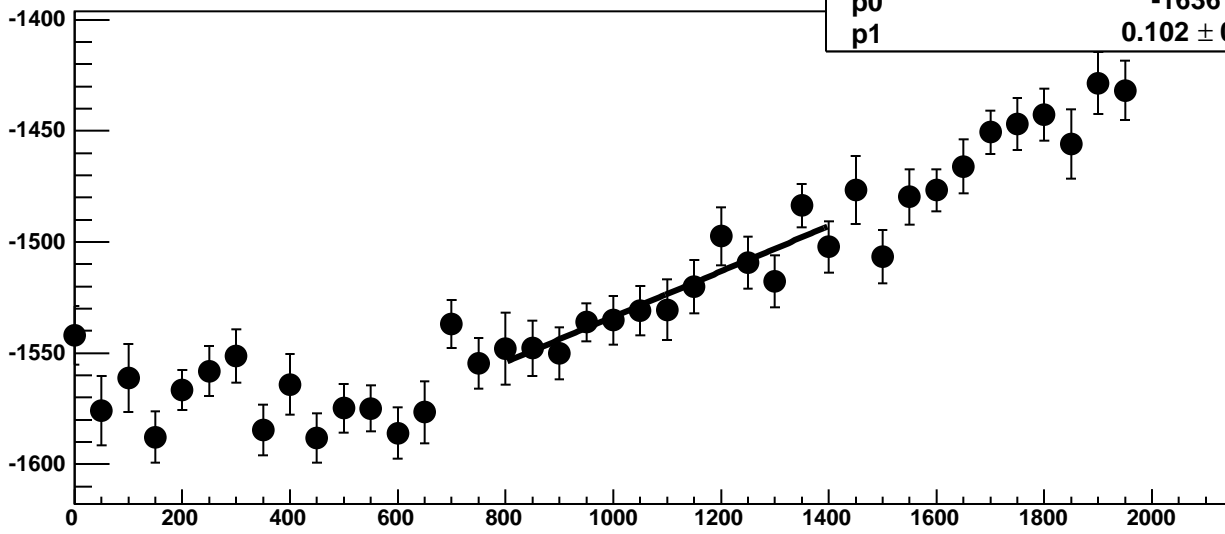


Chip 7, Channel 11, Enable 3!, Hold=30, ADC Residuals vs DAC



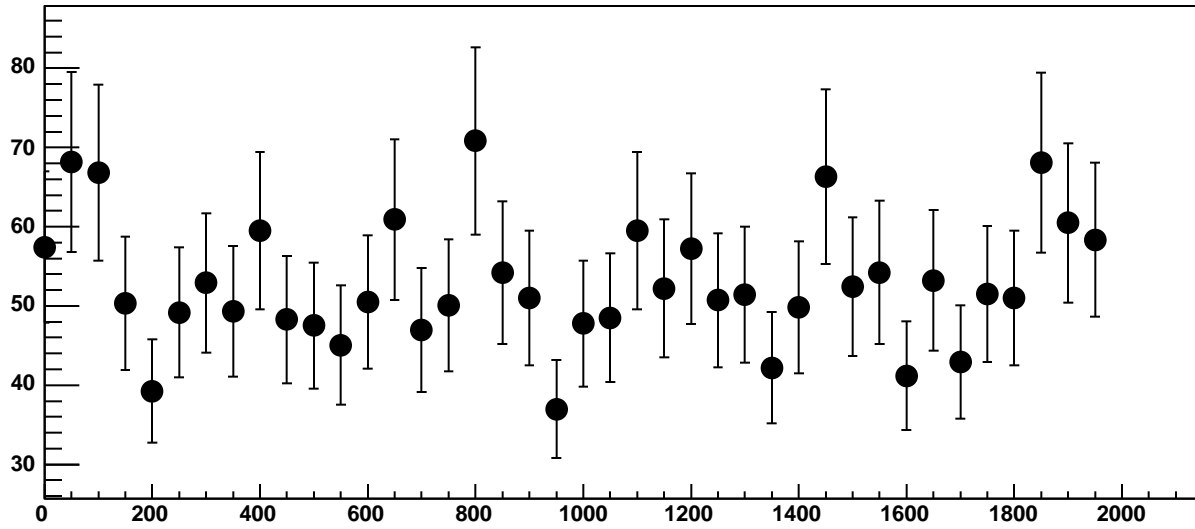


Chip 7, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC

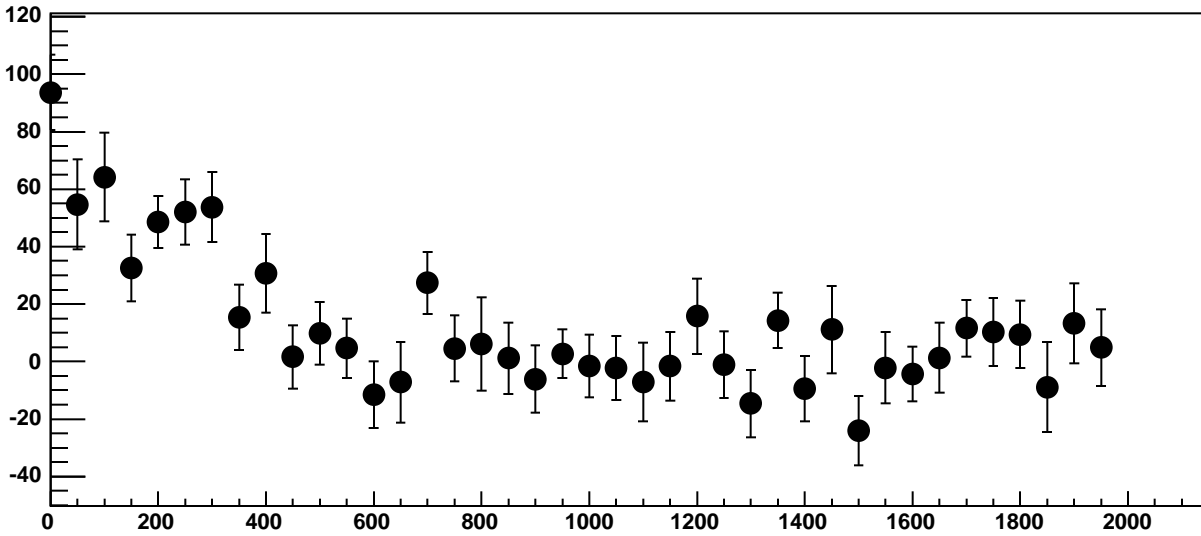


$\chi^2 / \text{ndf}$  6.739 / 11  
p0  $-1636 \pm 19.48$   
p1  $0.102 \pm 0.01733$

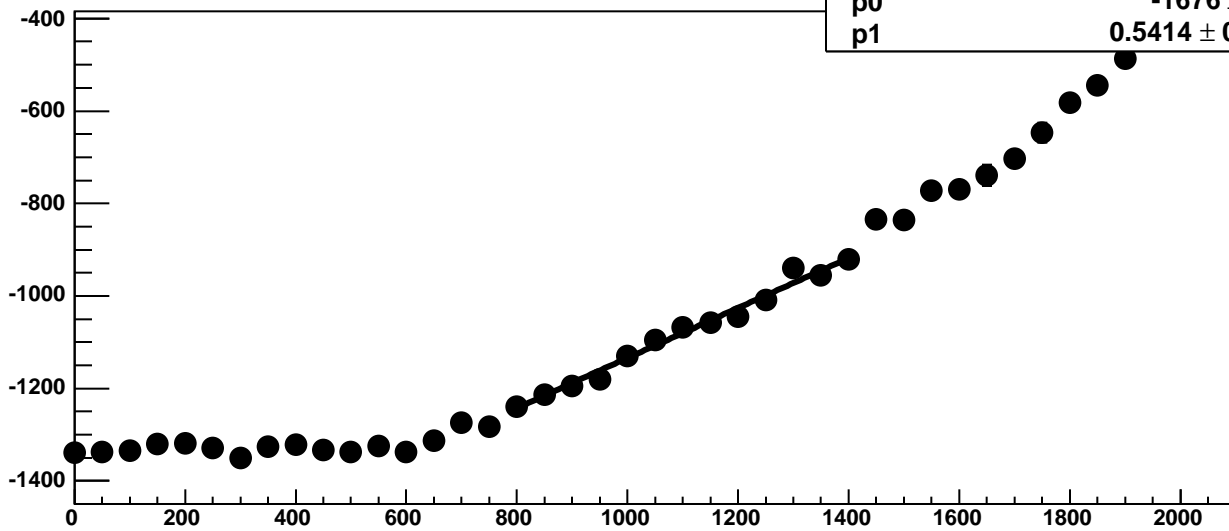
Chip 7, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC



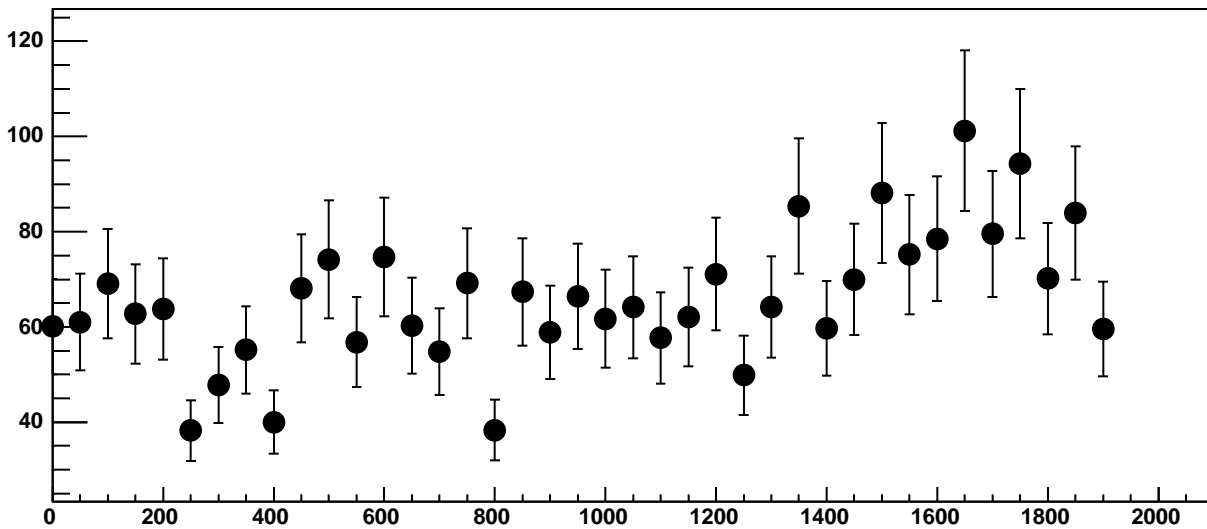
Chip 7, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC



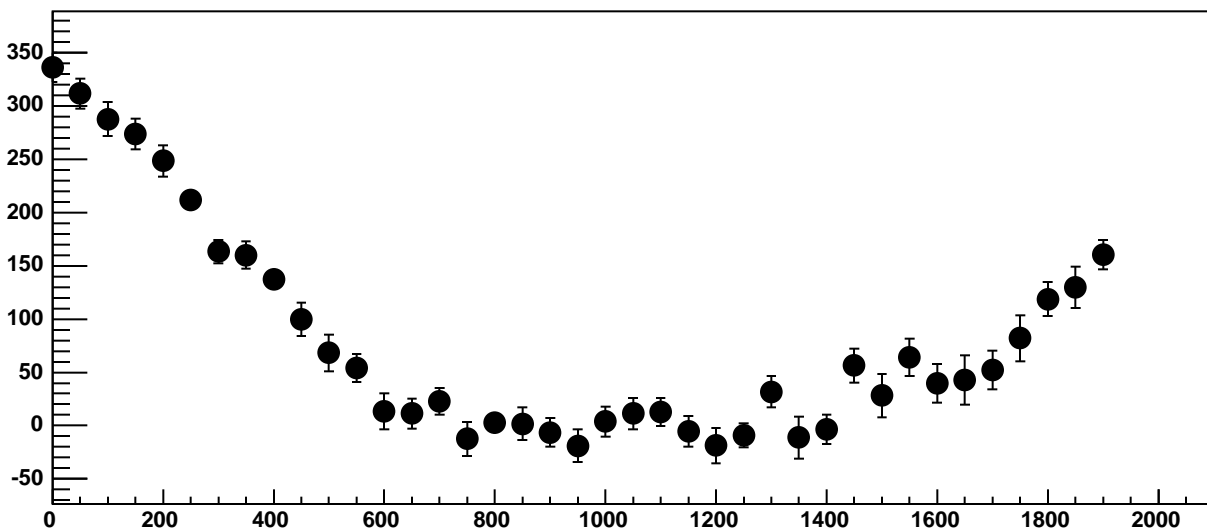
Chip 7, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC



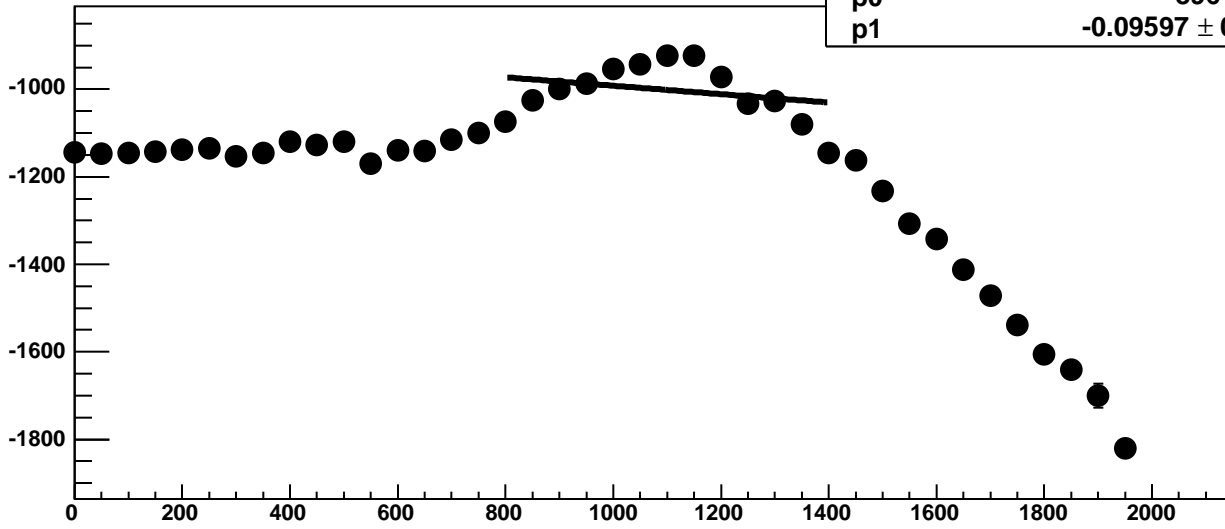
Chip 7, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC



Chip 7, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC

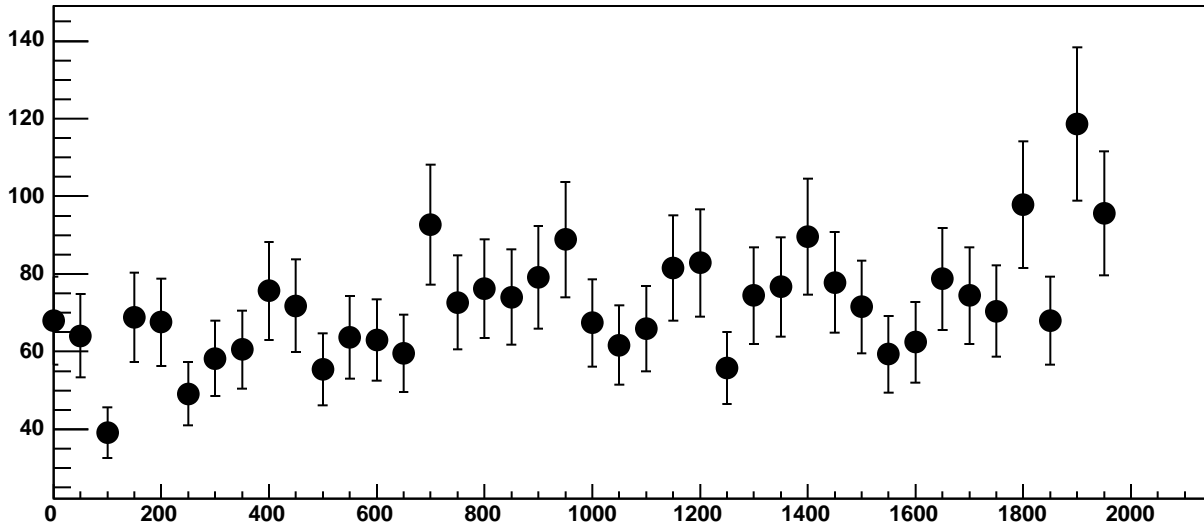


Chip 7, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC

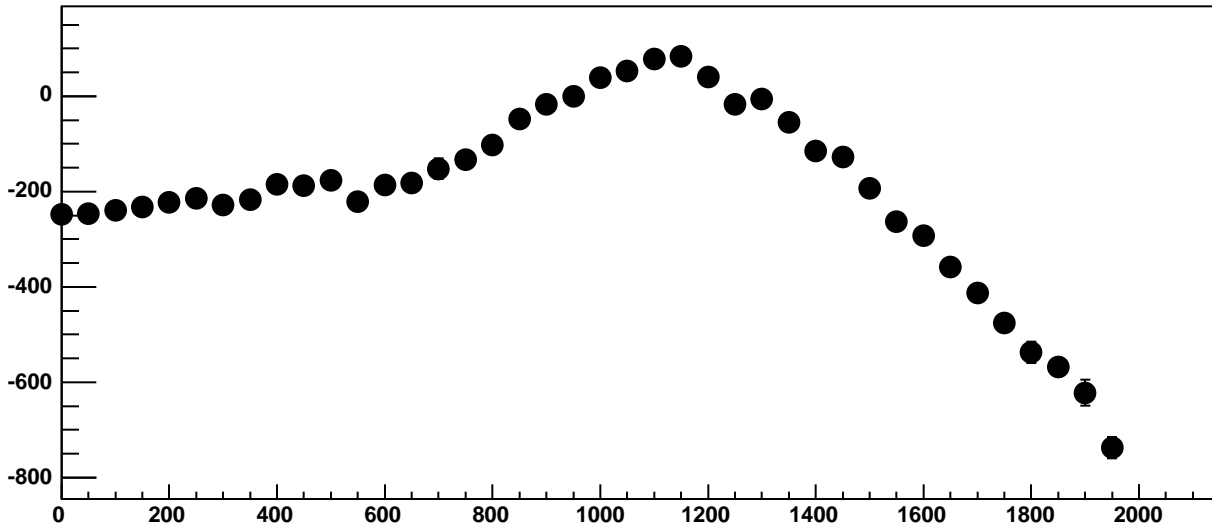


$\chi^2 / \text{ndf}$  156.9 / 11  
p0  $-896 \pm 29.15$   
p1  $-0.09597 \pm 0.02613$

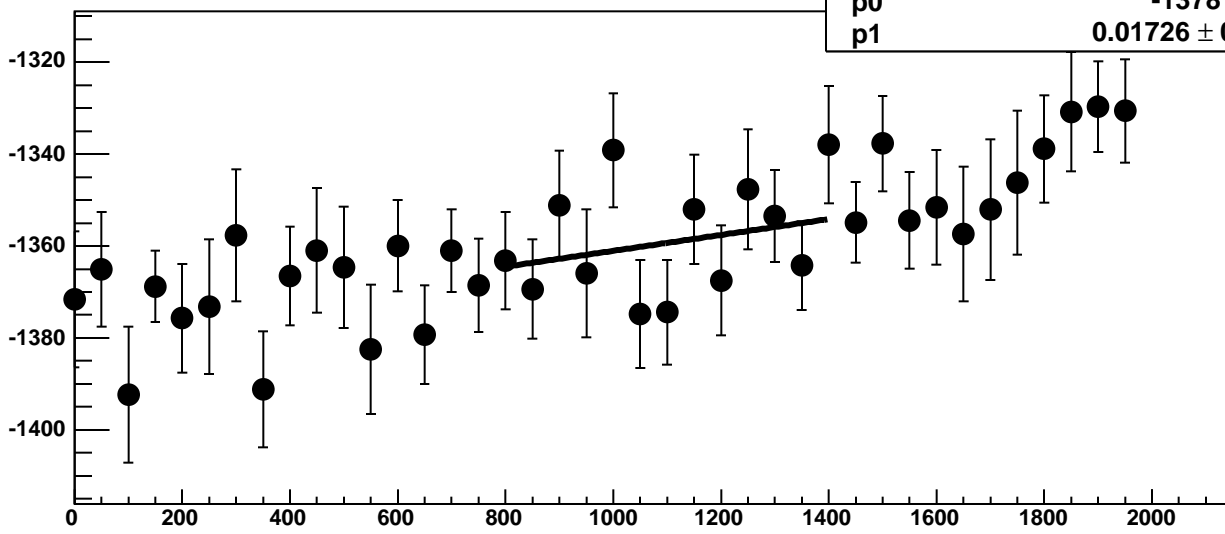
Chip 7, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC



Chip 7, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC

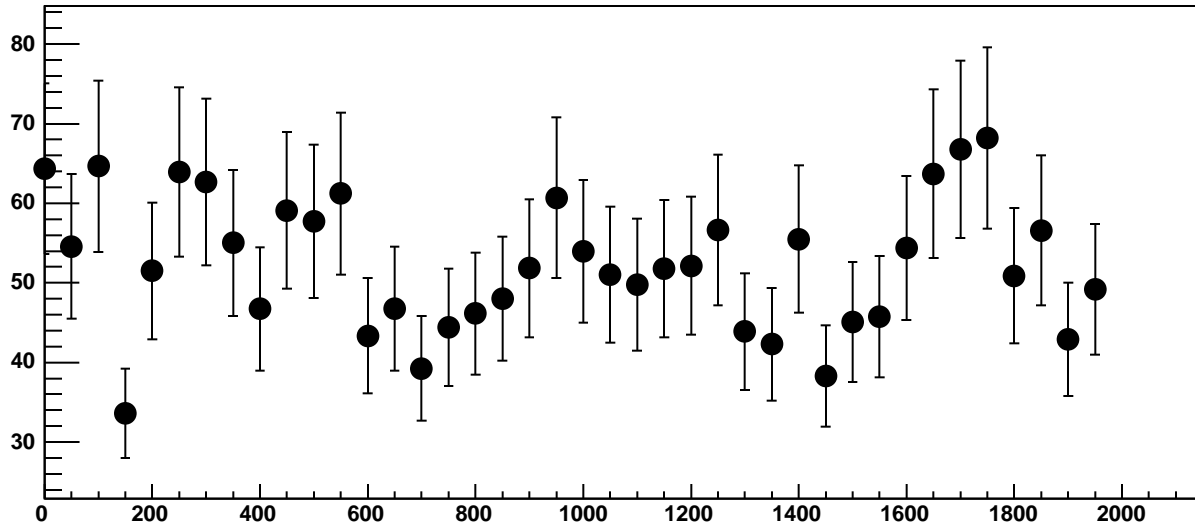


Chip 7, Channel 12, Enable 1, Hold=30, ADC Mean vs DAC

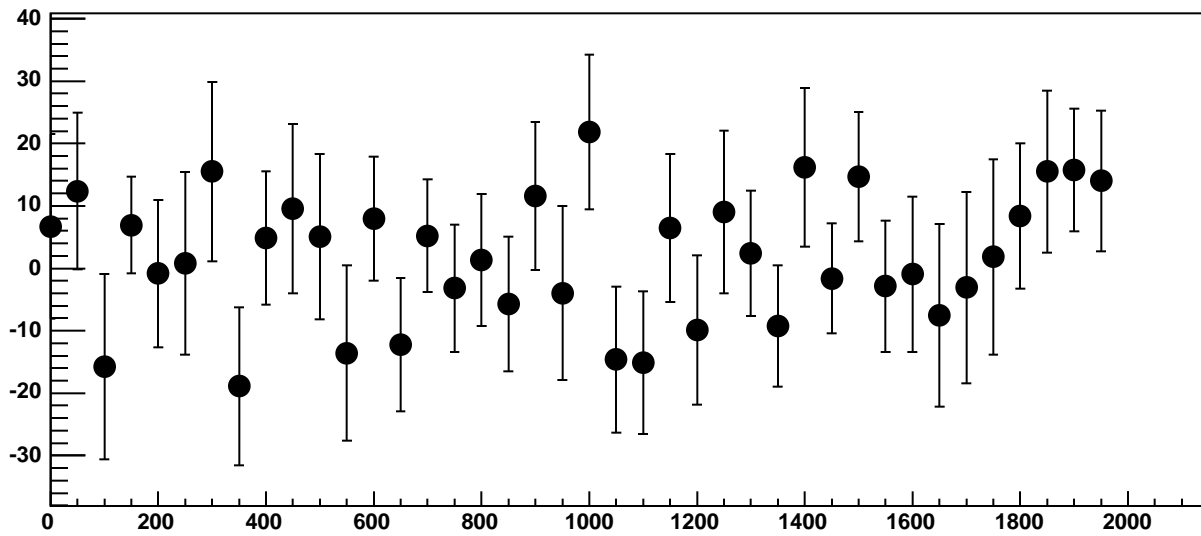


$\chi^2 / \text{ndf}$  11.81 / 11  
p0  $-1378 \pm 18.52$   
p1  $0.01726 \pm 0.01653$

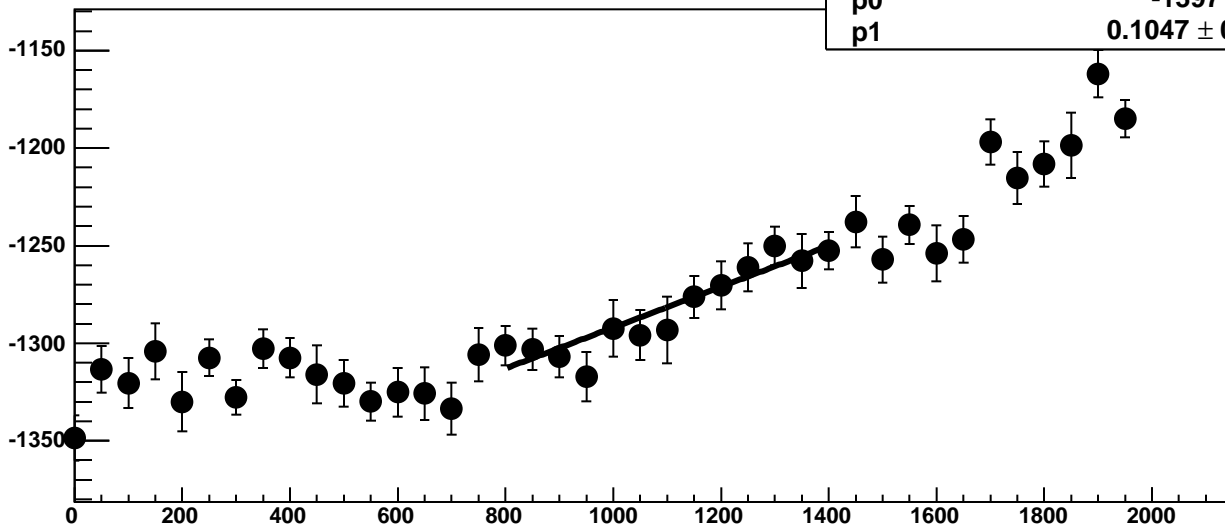
Chip 7, Channel 12, Enable 1, Hold=30, ADC Noise vs DAC



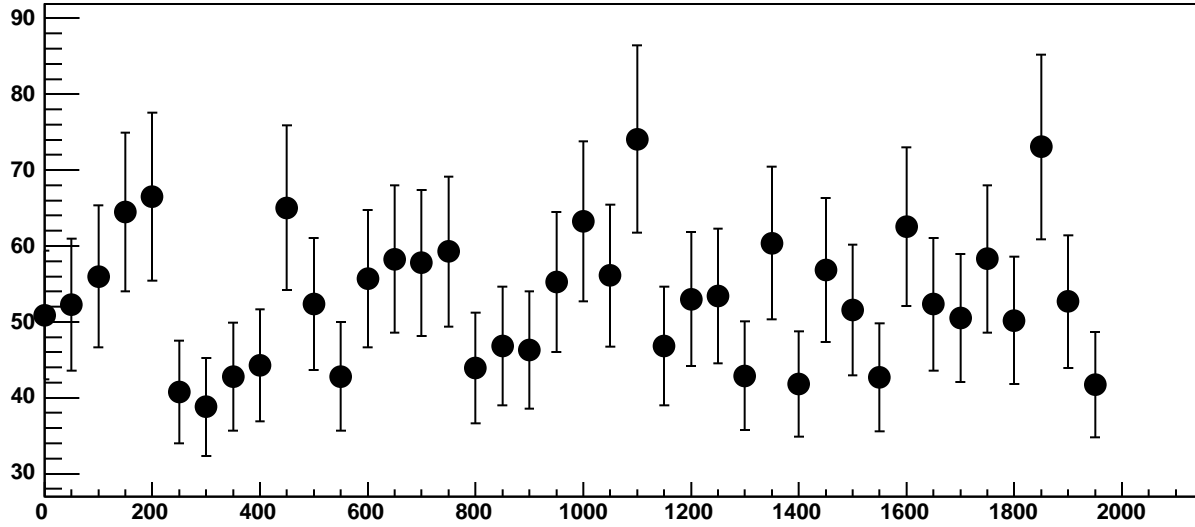
Chip 7, Channel 12, Enable 1, Hold=30, ADC Residuals vs DAC



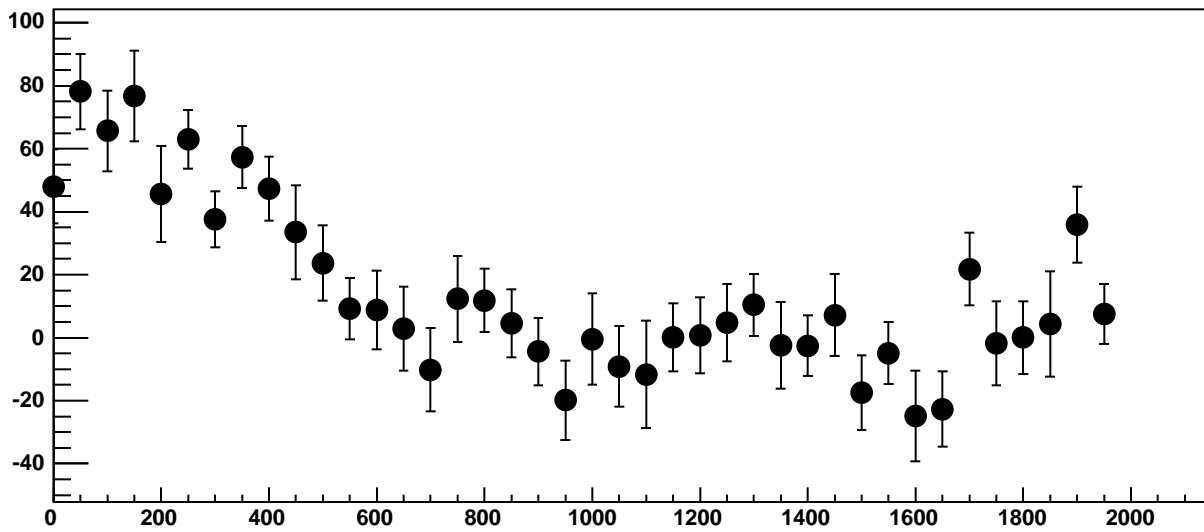
Chip 7, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC



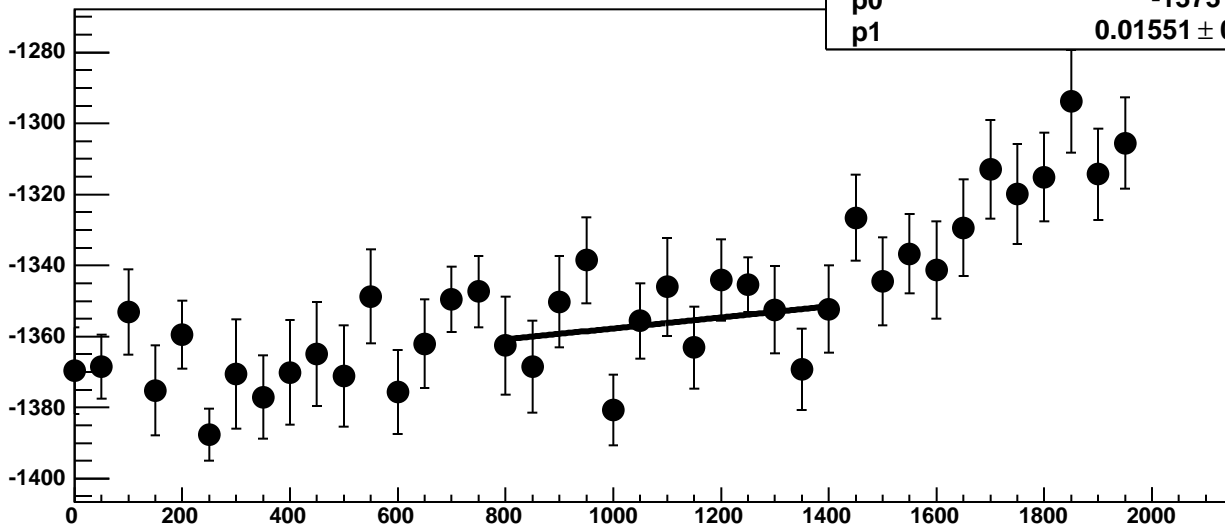
Chip 7, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC



Chip 7, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC

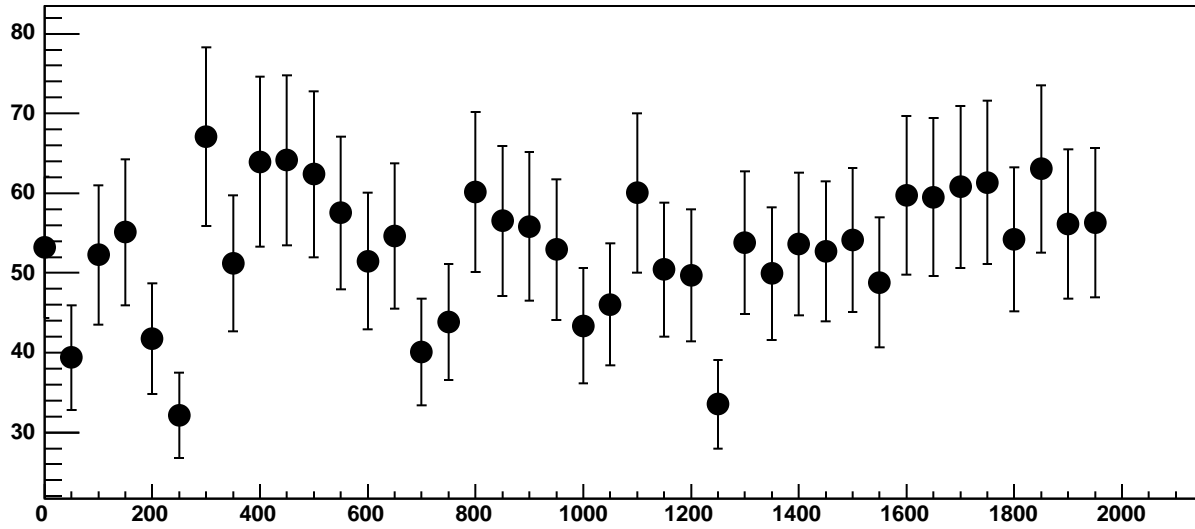


Chip 7, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC

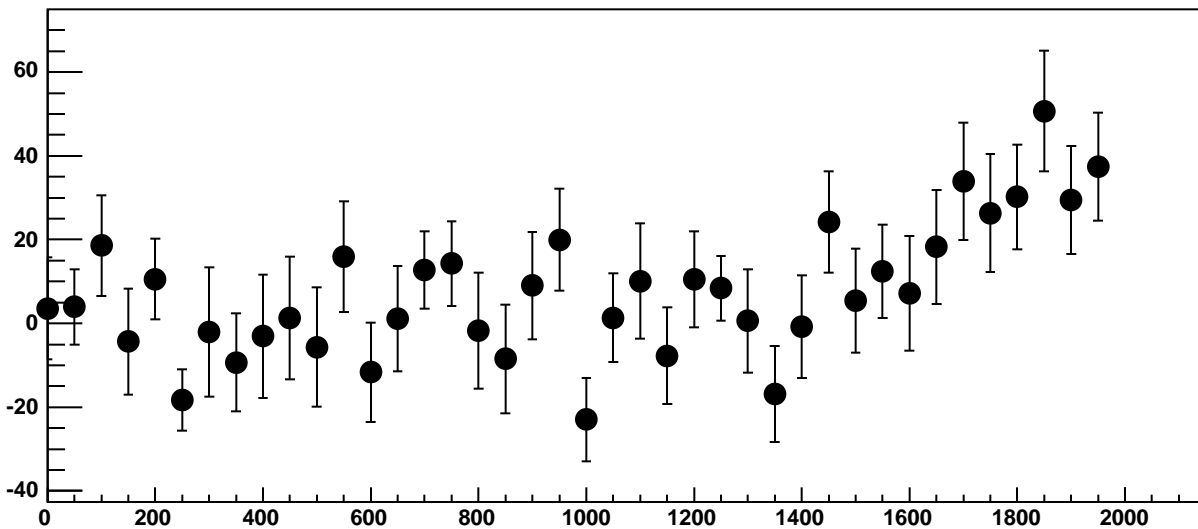


$\chi^2 / \text{ndf}$  14.19 / 11  
p0  $-1373 \pm 20.29$   
p1  $0.01551 \pm 0.01788$

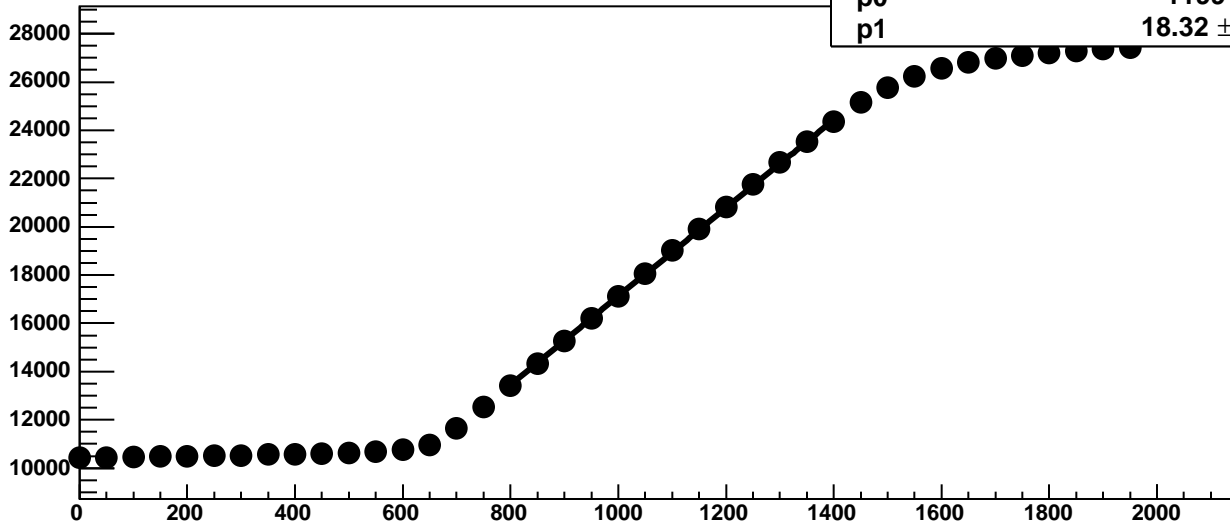
Chip 7, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC



Chip 7, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC

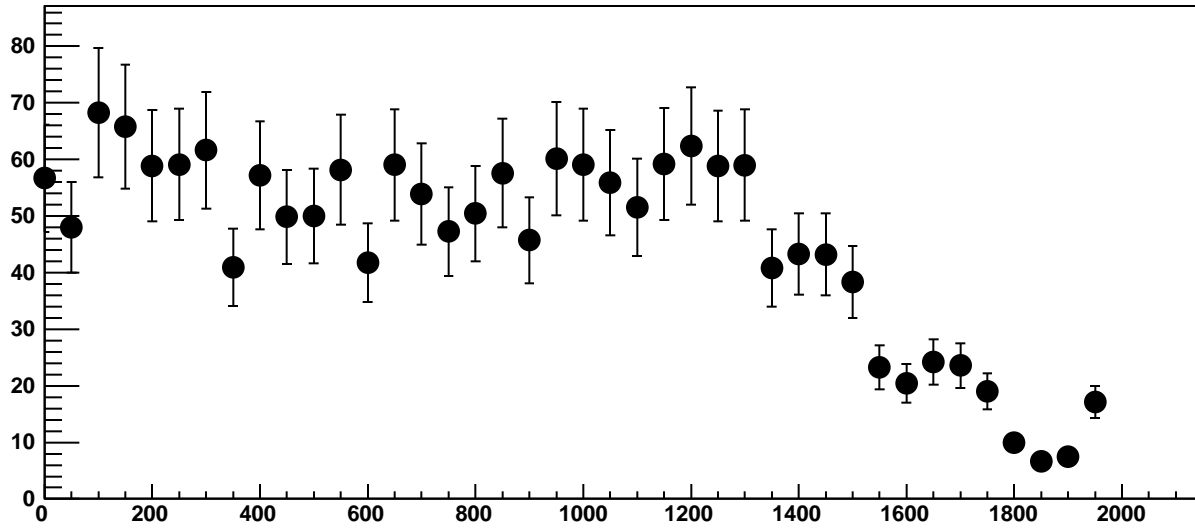


Chip 7, Channel 12, Enable 4!, Hold=30, ADC Mean vs DAC

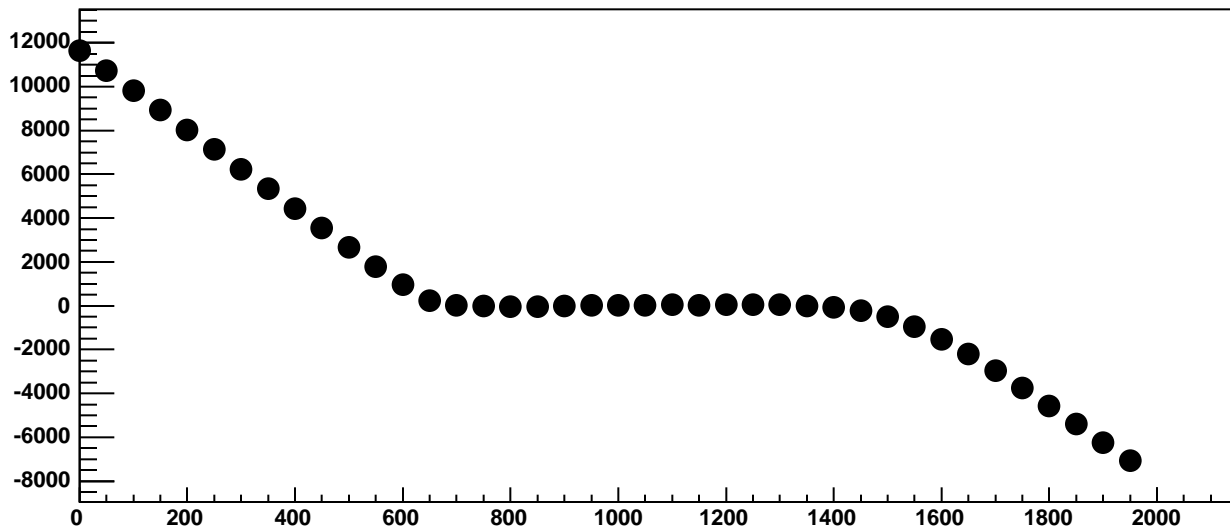


$\chi^2 / \text{ndf}$  157.5 / 11  
p0  $-1199 \pm 18.94$   
p1  $18.32 \pm 0.0167$

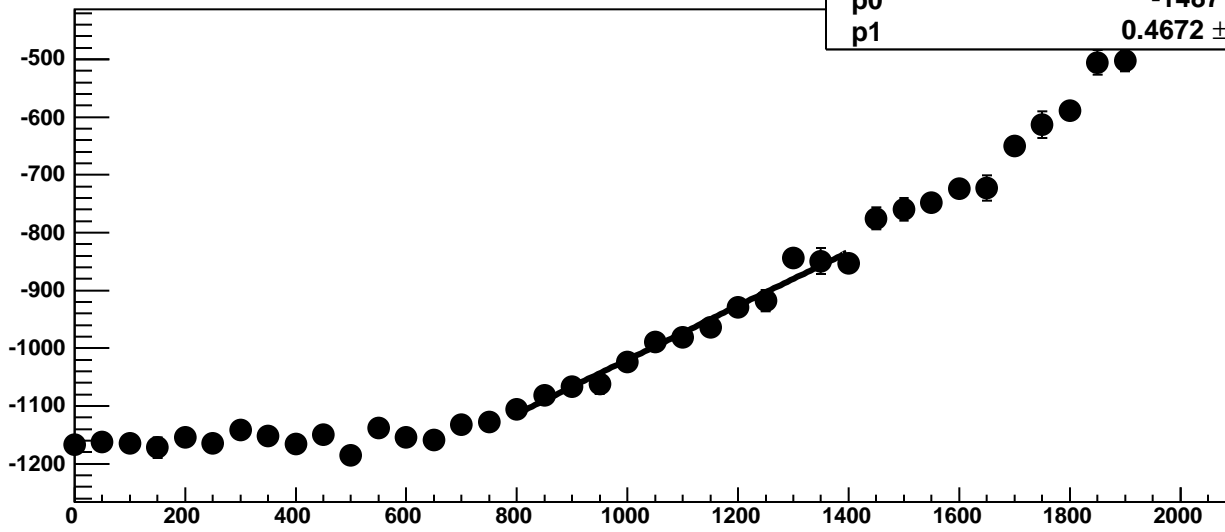
Chip 7, Channel 12, Enable 4!, Hold=30, ADC Noise vs DAC



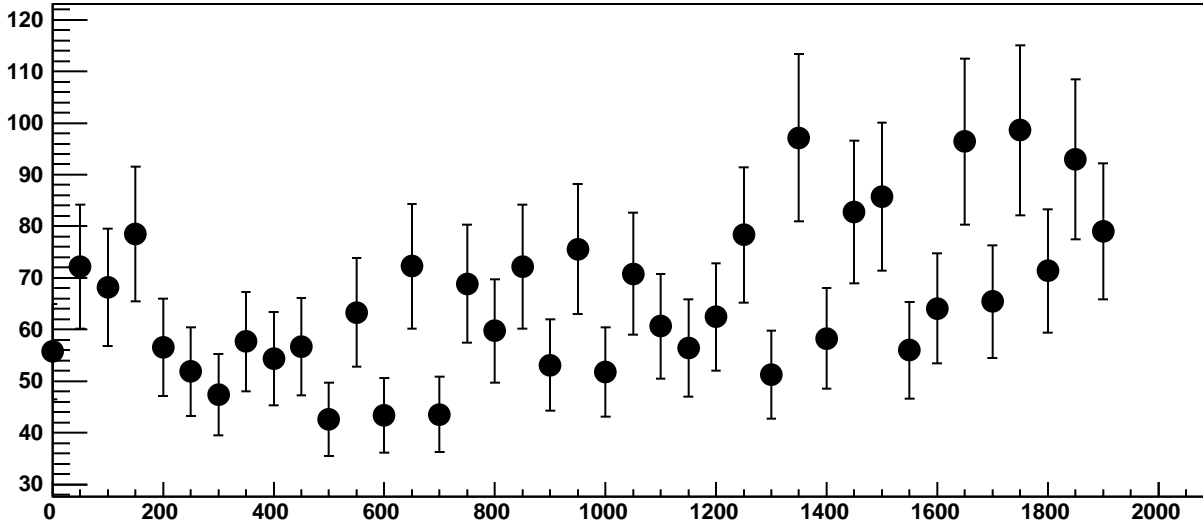
Chip 7, Channel 12, Enable 4!, Hold=30, ADC Residuals vs DAC



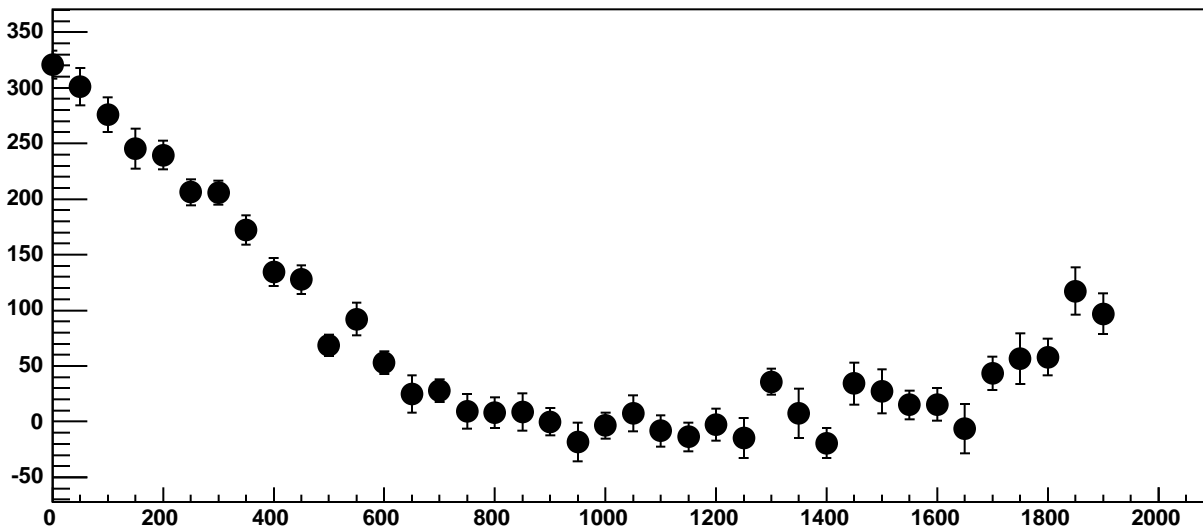
Chip 7, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC



Chip 7, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC

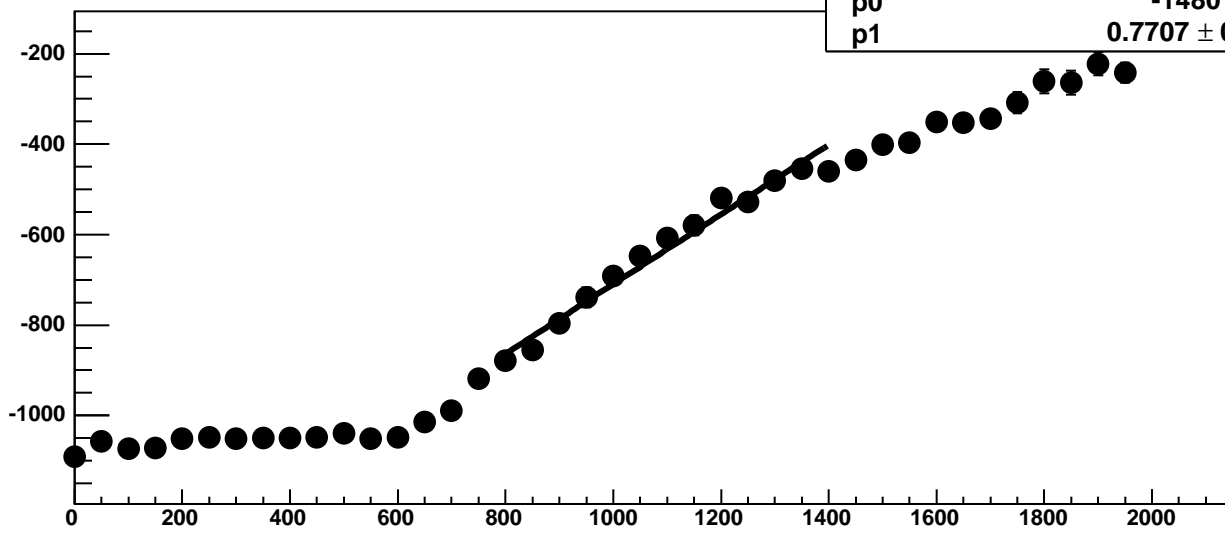


Chip 7, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC

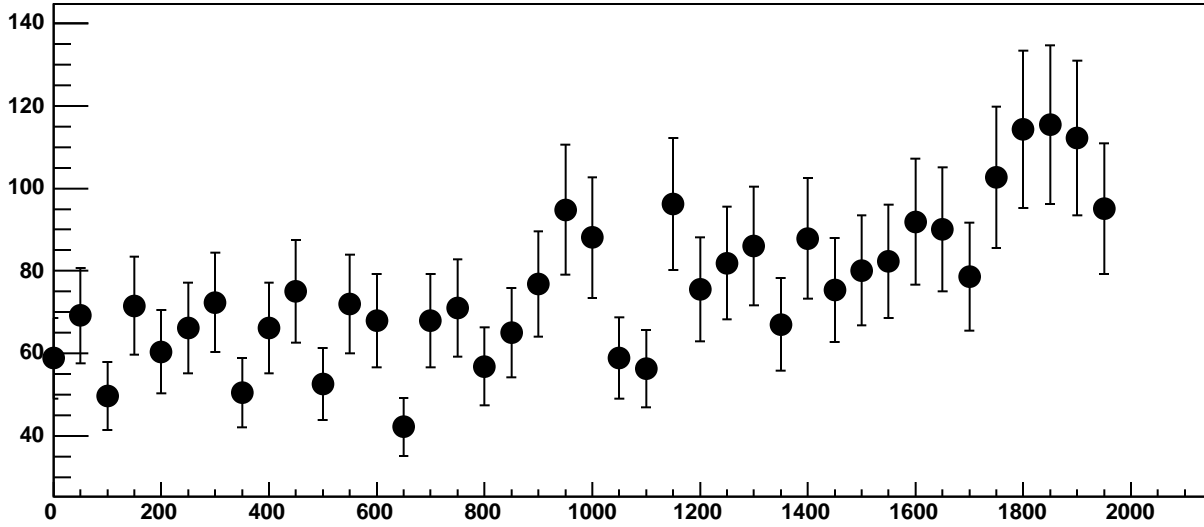




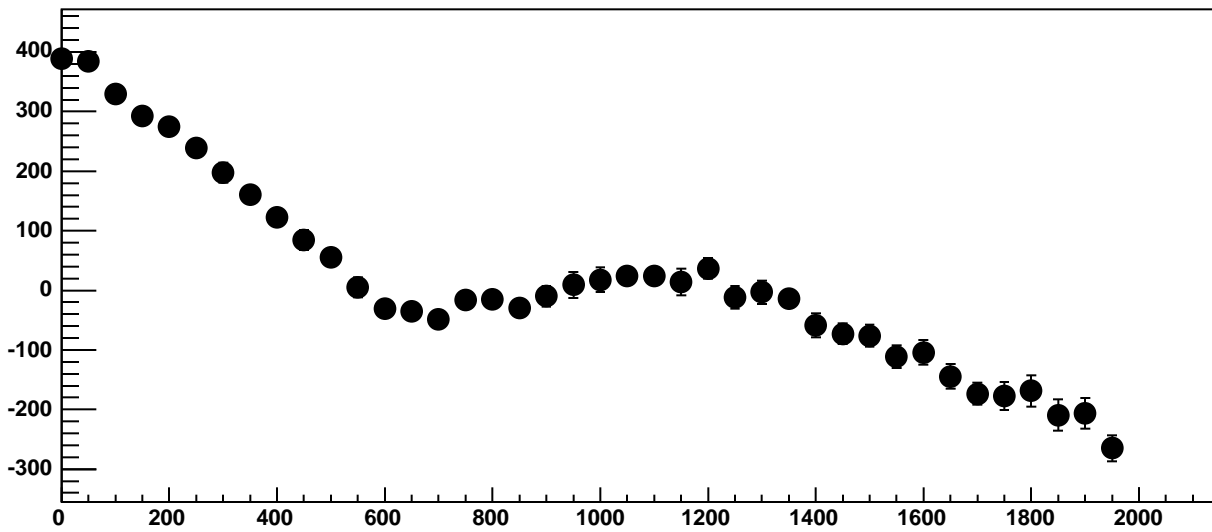
Chip 7, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC



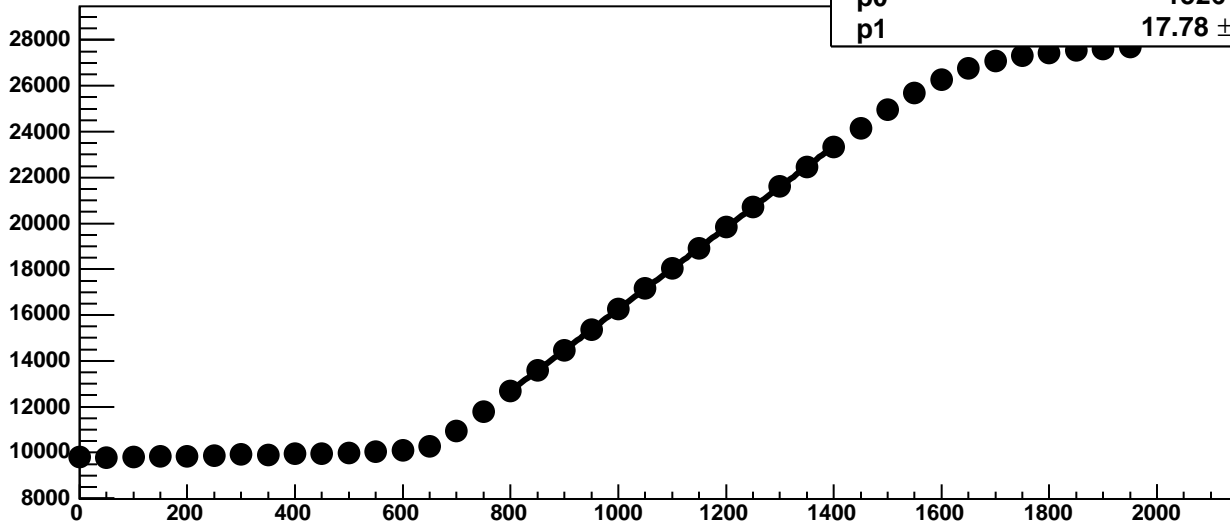
Chip 7, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC



Chip 7, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC

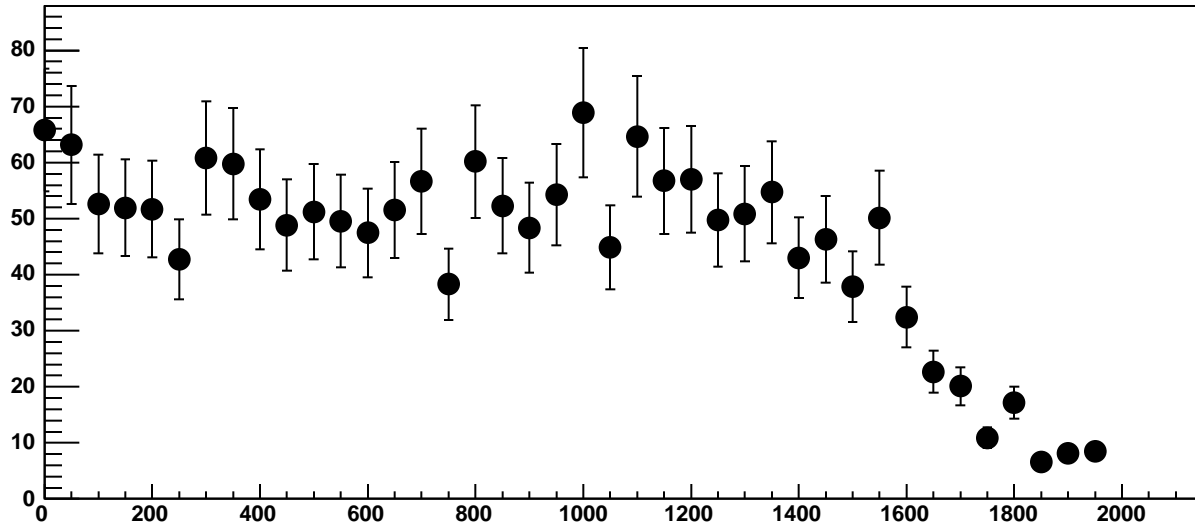


Chip 7, Channel 13, Enable 1!, Hold=30, ADC Mean vs DAC

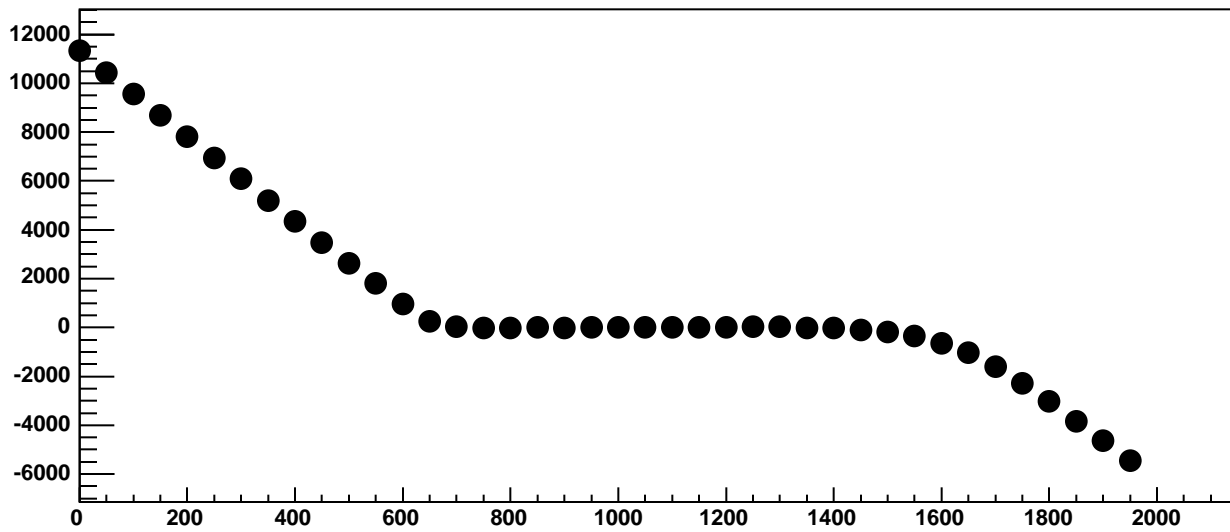


$\chi^2 / \text{ndf}$  34.25 / 11  
p0  $-1526 \pm 19.68$   
p1  $17.78 \pm 0.0174$

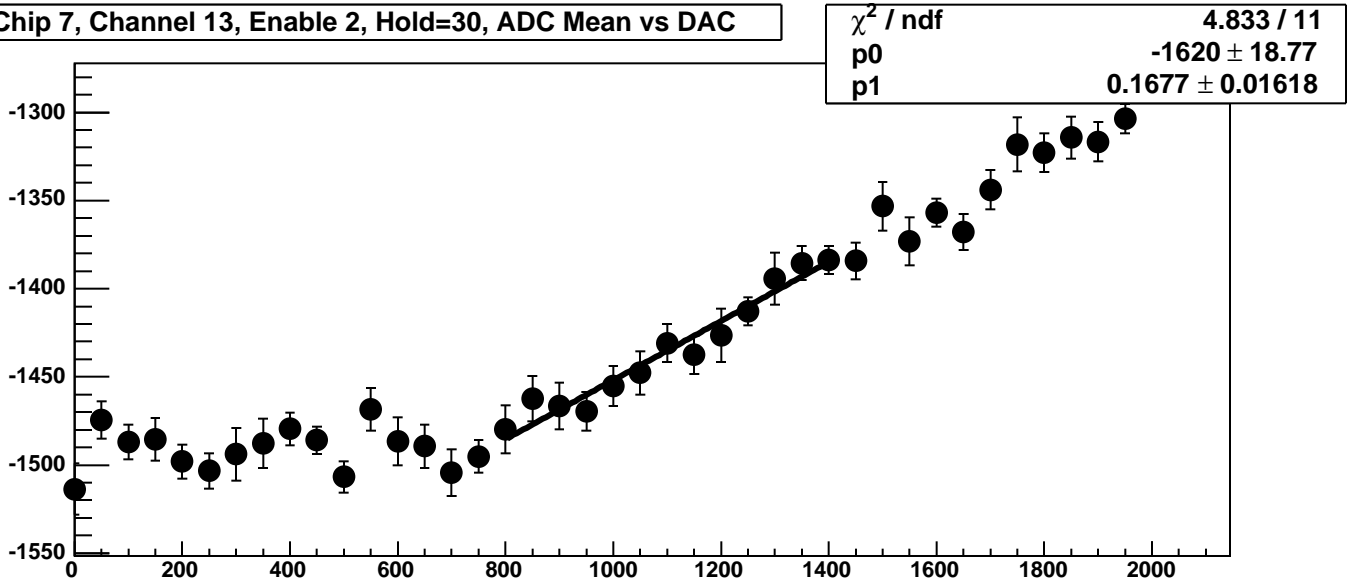
Chip 7, Channel 13, Enable 1!, Hold=30, ADC Noise vs DAC



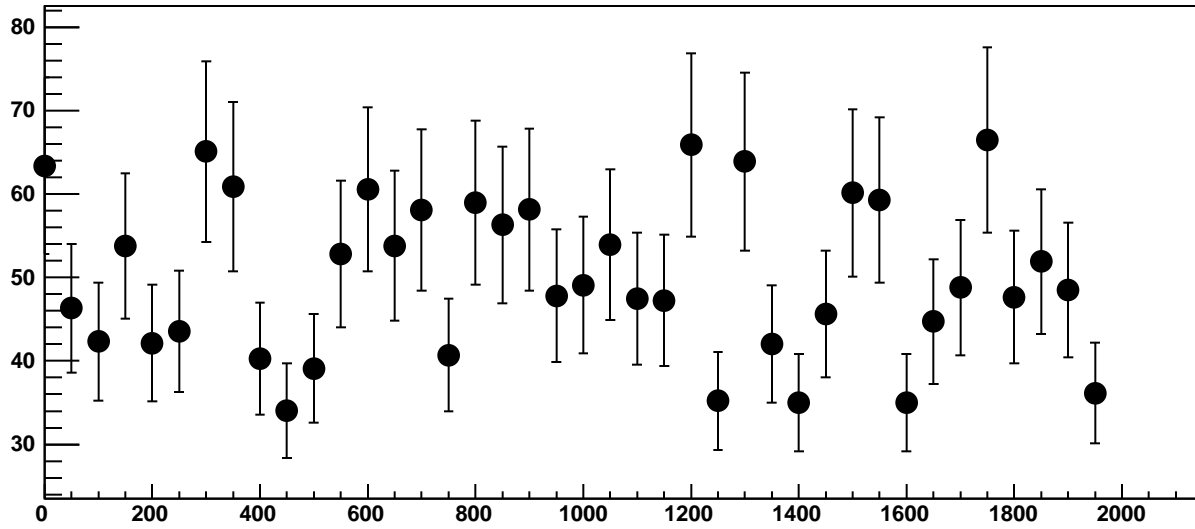
Chip 7, Channel 13, Enable 1!, Hold=30, ADC Residuals vs DAC



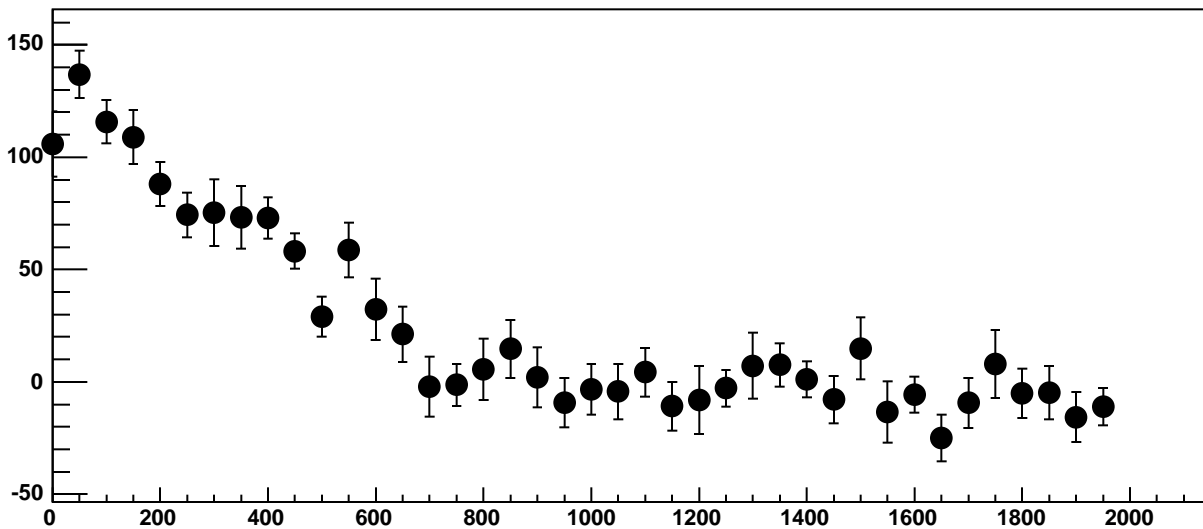
Chip 7, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC



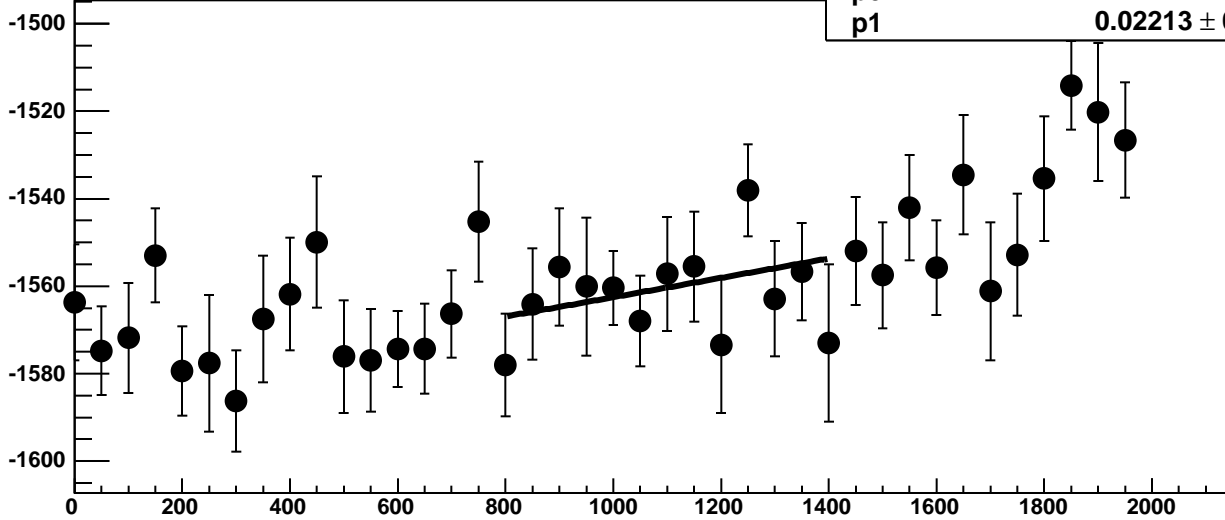
Chip 7, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC



Chip 7, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC

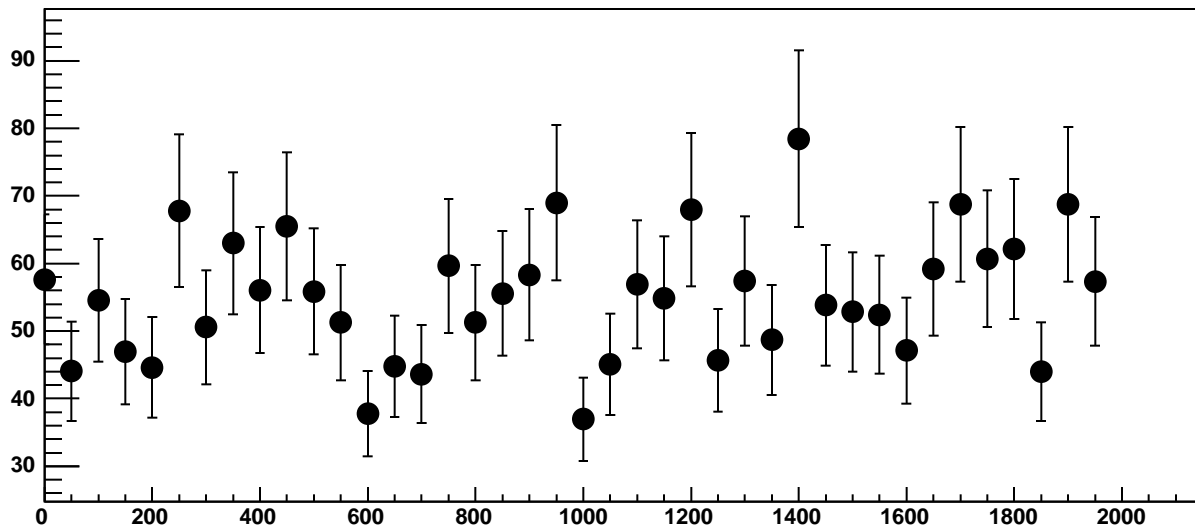


Chip 7, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC

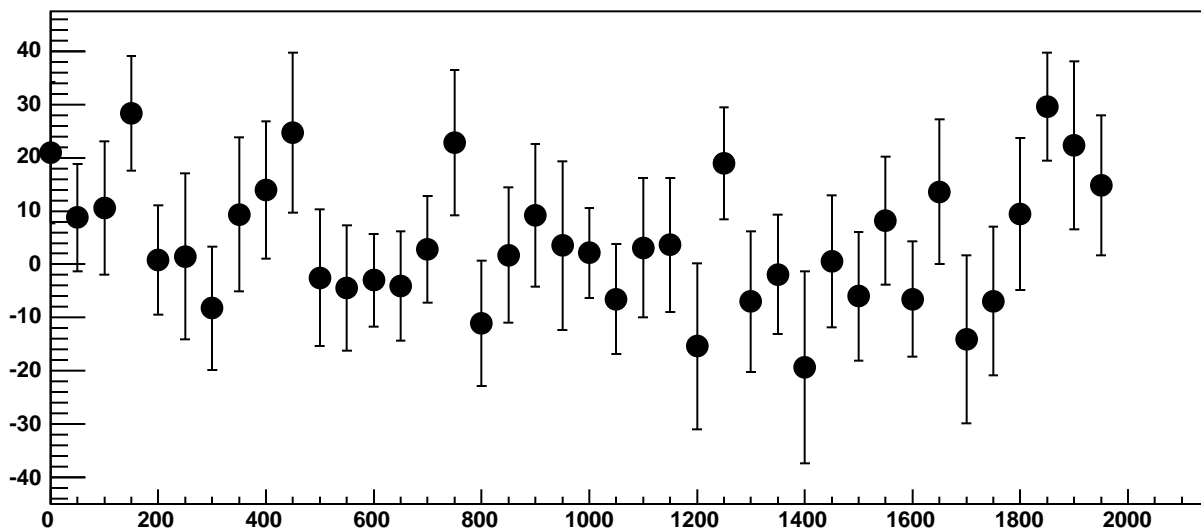


$\chi^2 / \text{ndf}$  7.736 / 11  
p0  $-1585 \pm 20.94$   
p1  $0.02213 \pm 0.01901$

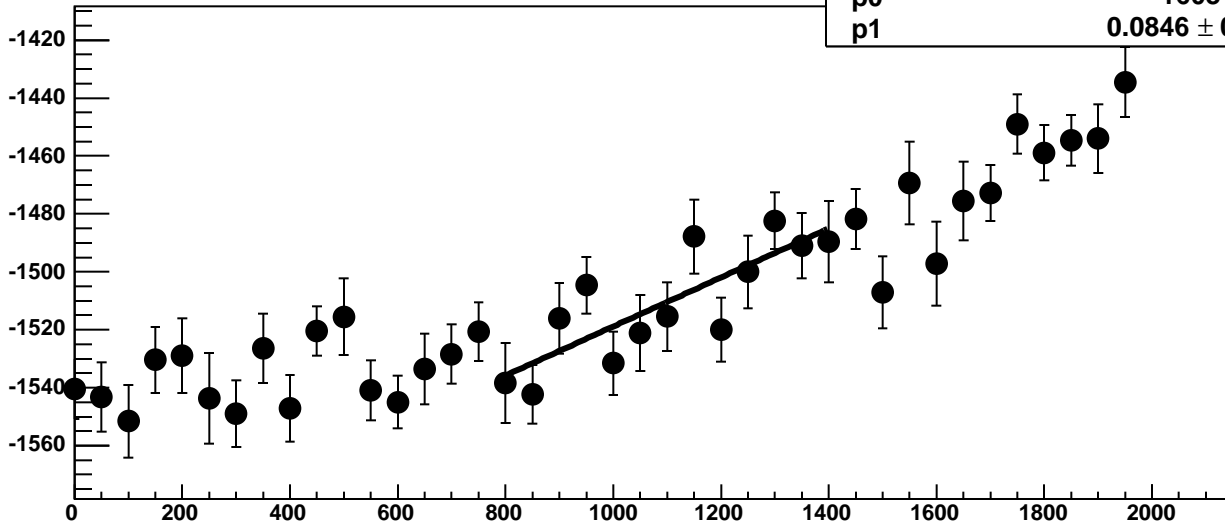
Chip 7, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC



Chip 7, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC

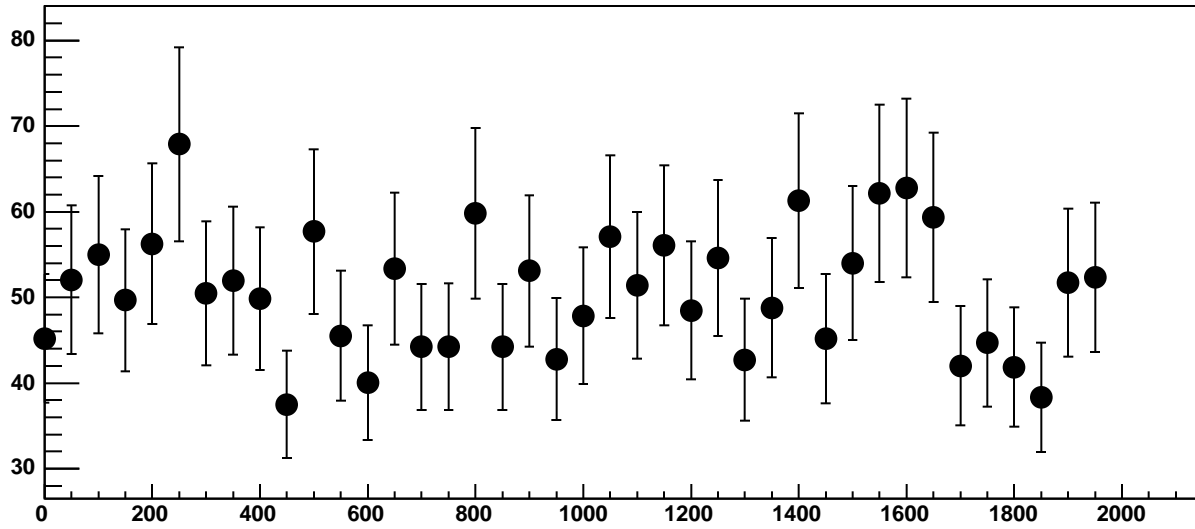


Chip 7, Channel 13, Enable 4, Hold=30, ADC Mean vs DAC

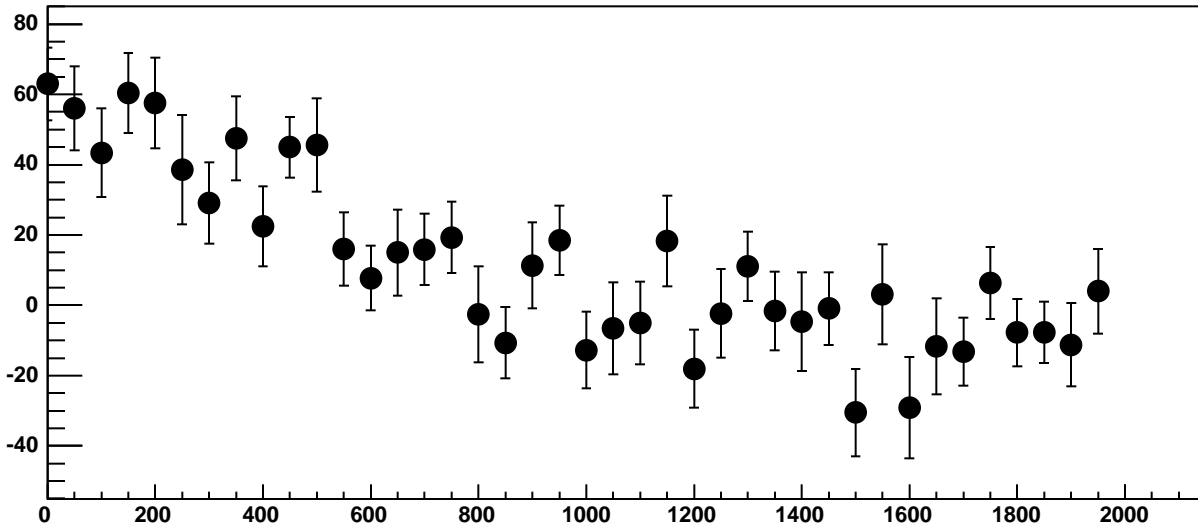


$\chi^2 / \text{ndf}$  13.46 / 11  
p0  $-1603 \pm 19.36$   
p1  $0.0846 \pm 0.01742$

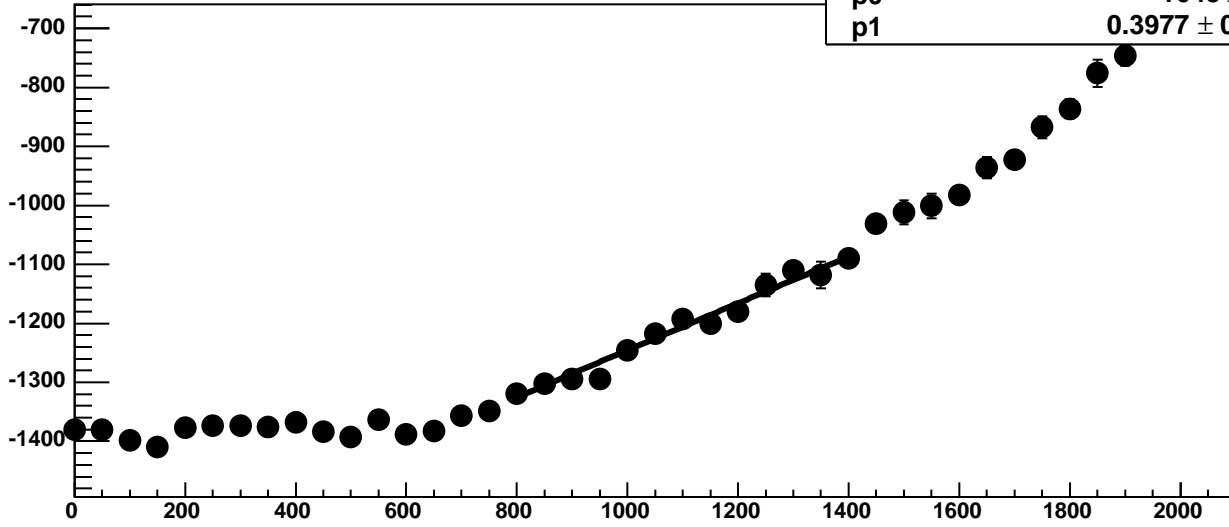
Chip 7, Channel 13, Enable 4, Hold=30, ADC Noise vs DAC



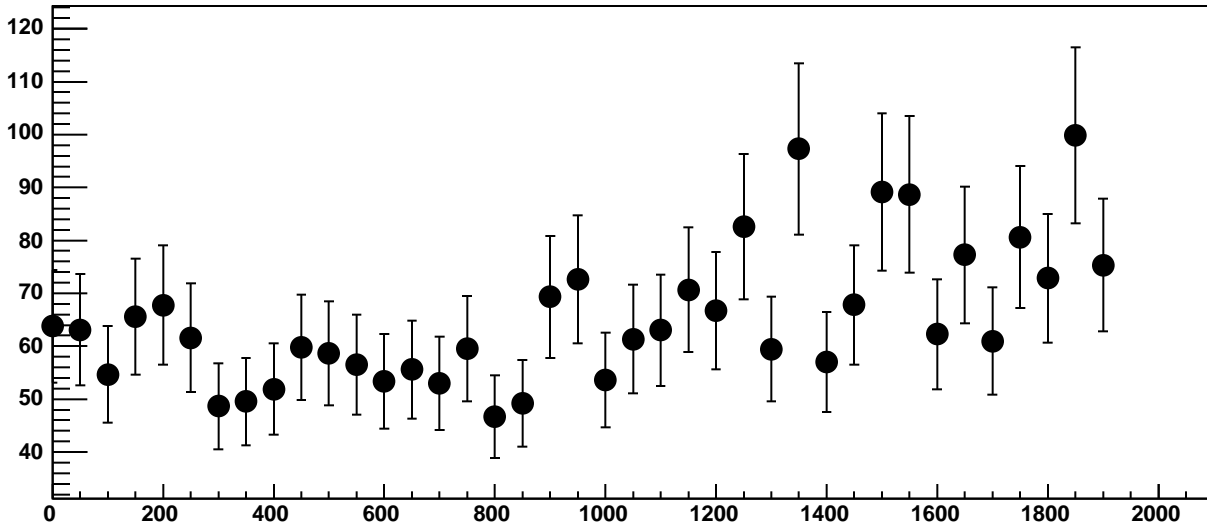
Chip 7, Channel 13, Enable 4, Hold=30, ADC Residuals vs DAC



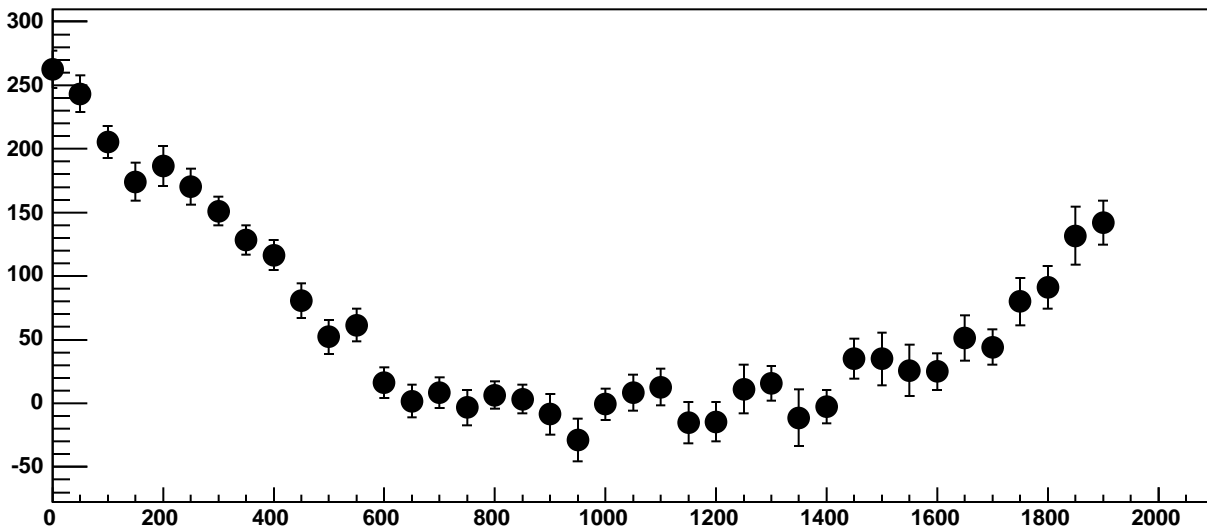
Chip 7, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC



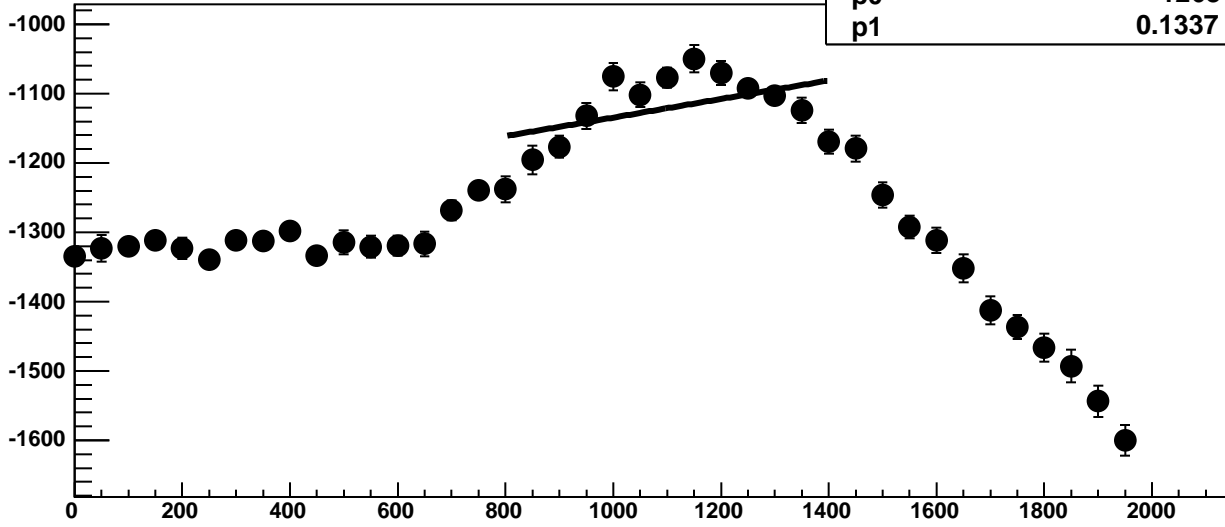
Chip 7, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC



Chip 7, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC

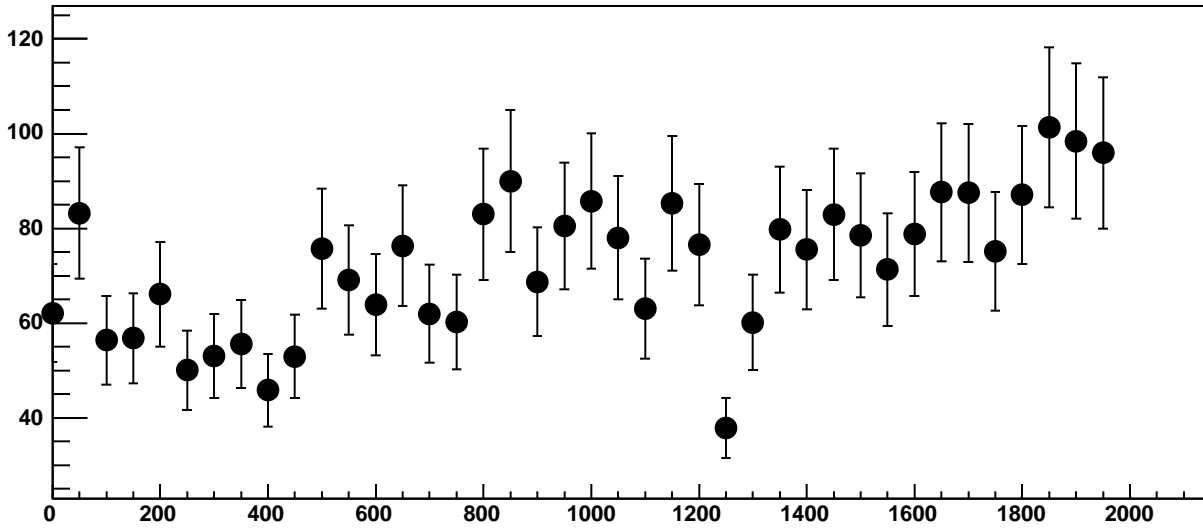


Chip 7, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC

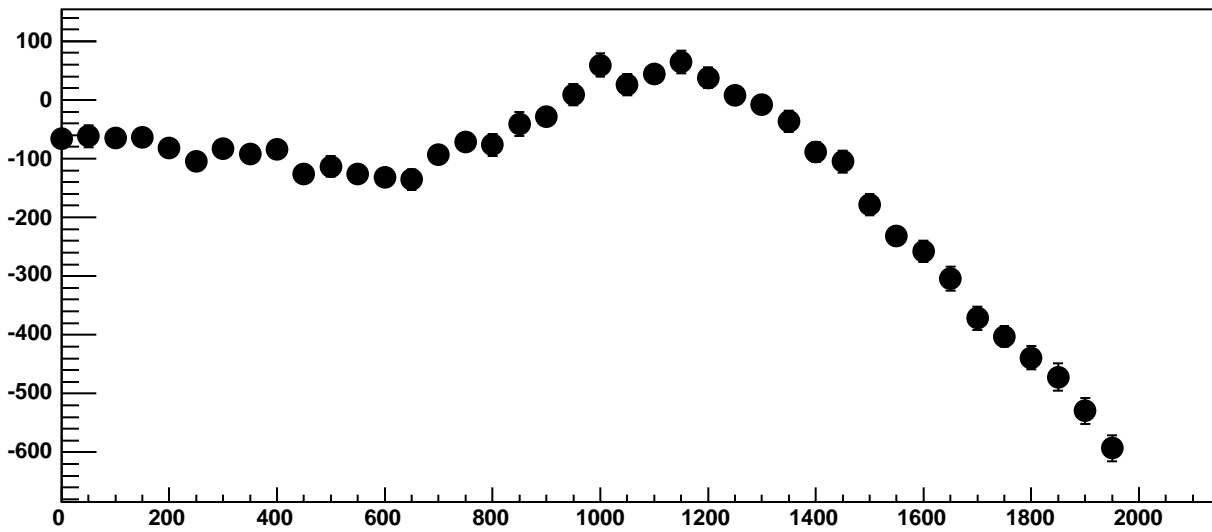


$\chi^2 / \text{ndf}$	91.11 / 11
p0	$-1268 \pm 28.84$
p1	$0.1337 \pm 0.025$

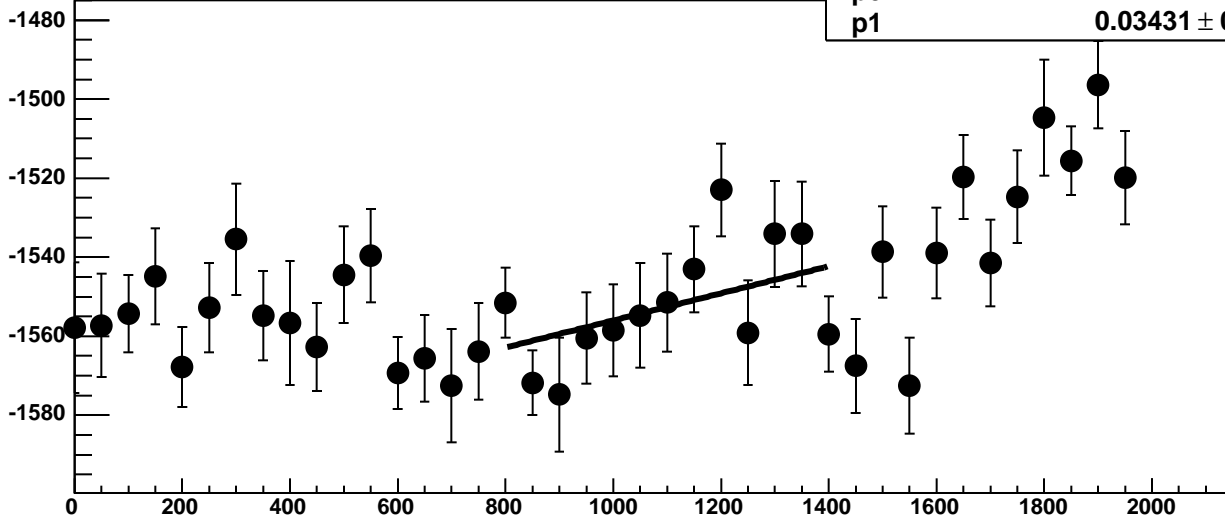
Chip 7, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC



Chip 7, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC

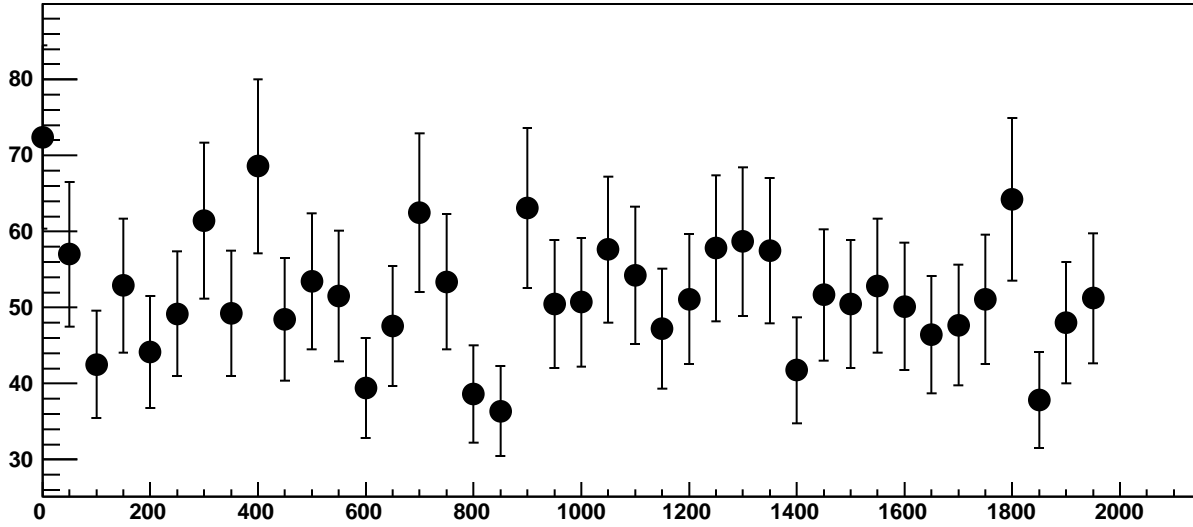


Chip 7, Channel 14, Enable 1, Hold=30, ADC Mean vs DAC

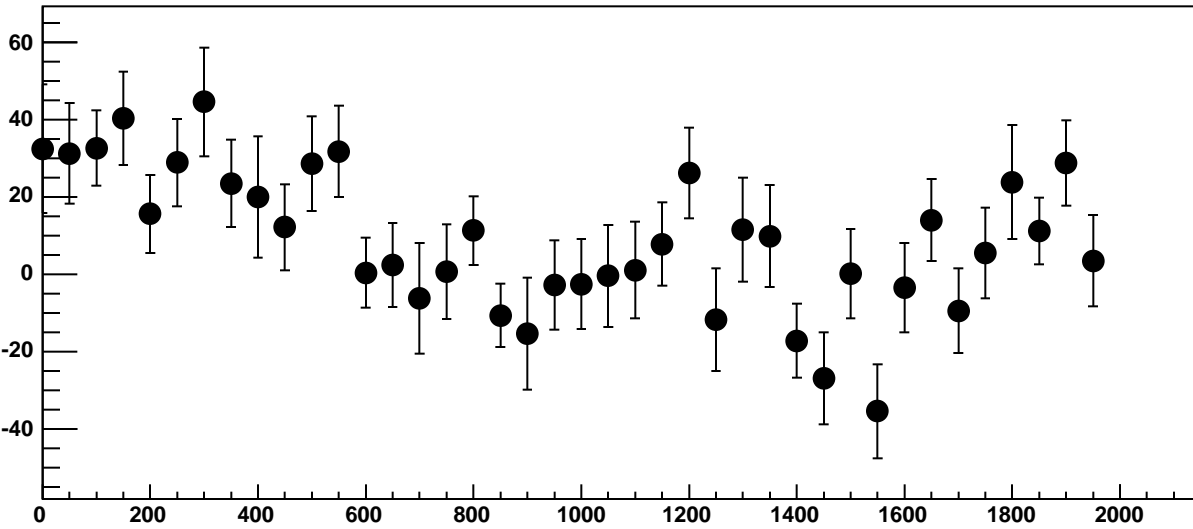


$\chi^2 / \text{ndf}$  15.39 / 11  
p0  $-1590 \pm 16.76$   
p1  $0.03431 \pm 0.01536$

Chip 7, Channel 14, Enable 1, Hold=30, ADC Noise vs DAC

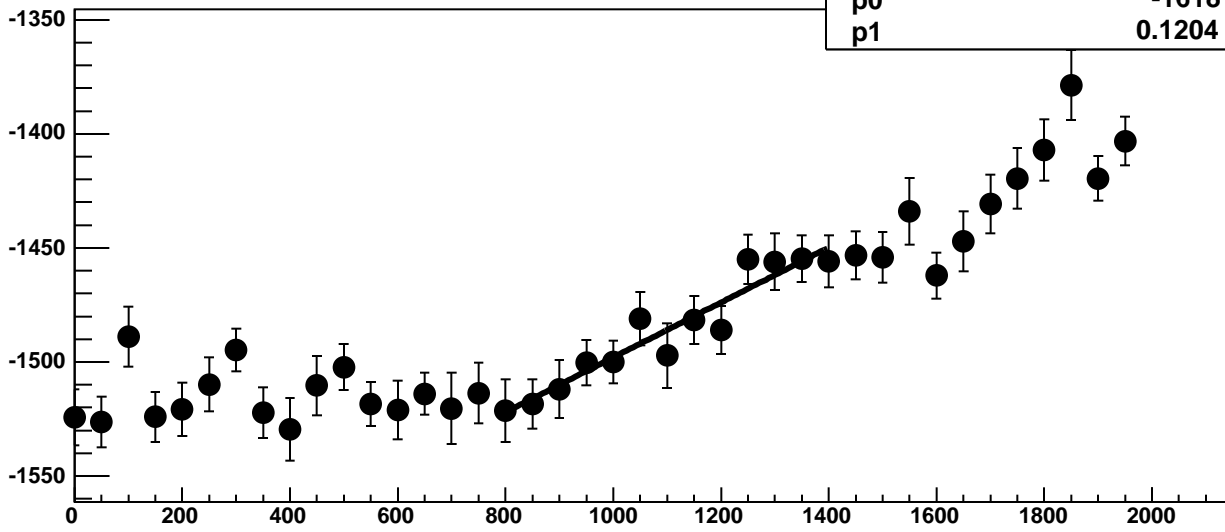


Chip 7, Channel 14, Enable 1, Hold=30, ADC Residuals vs DAC

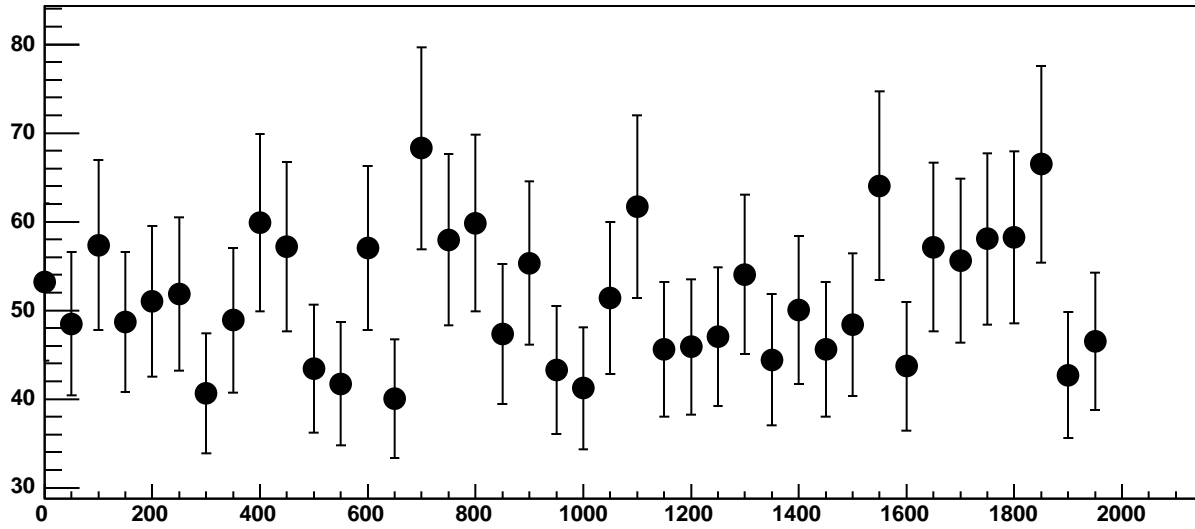




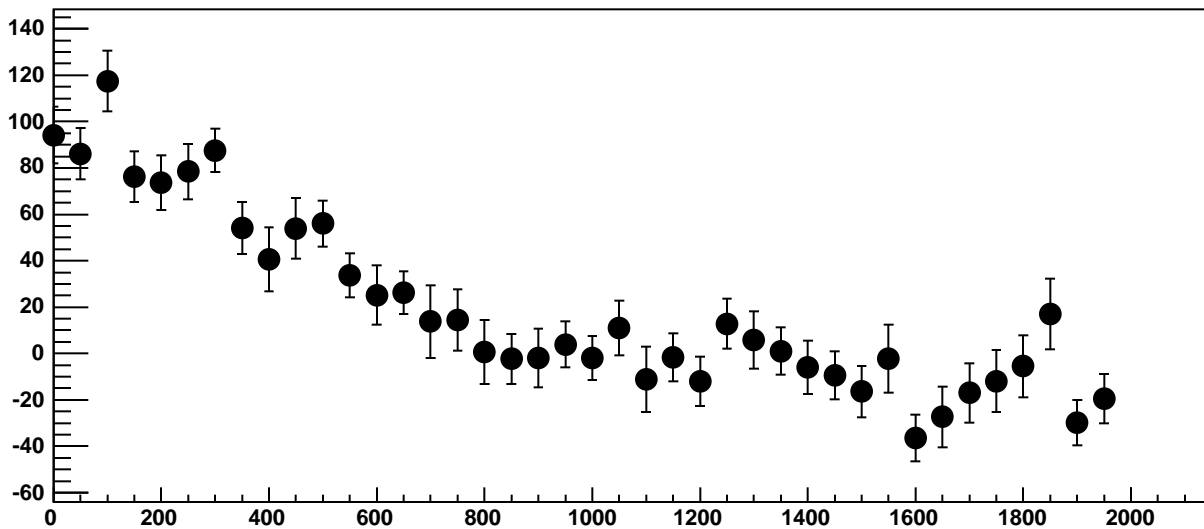
Chip 7, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC



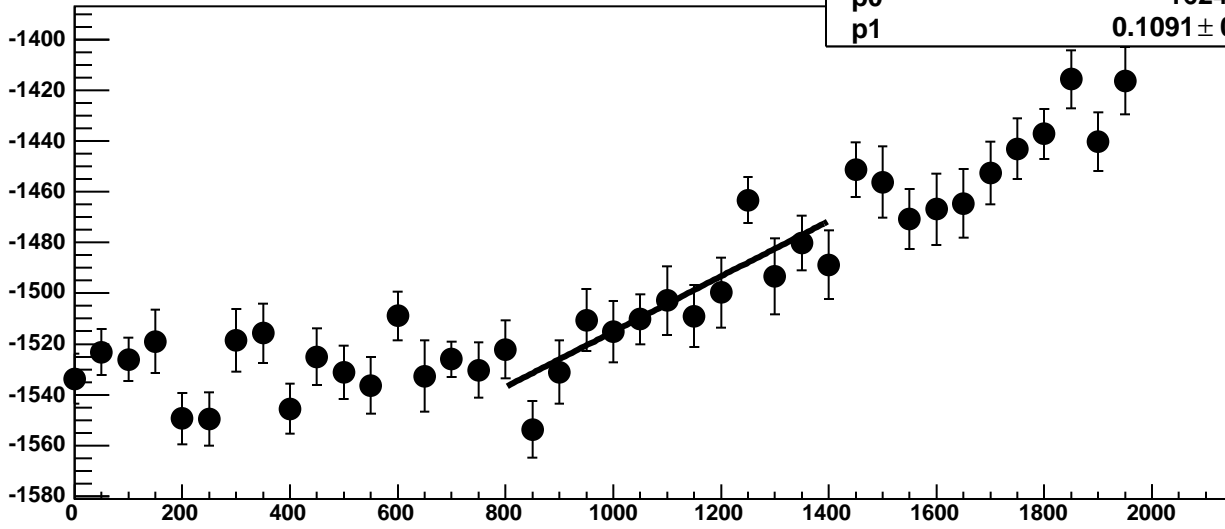
Chip 7, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC



Chip 7, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC

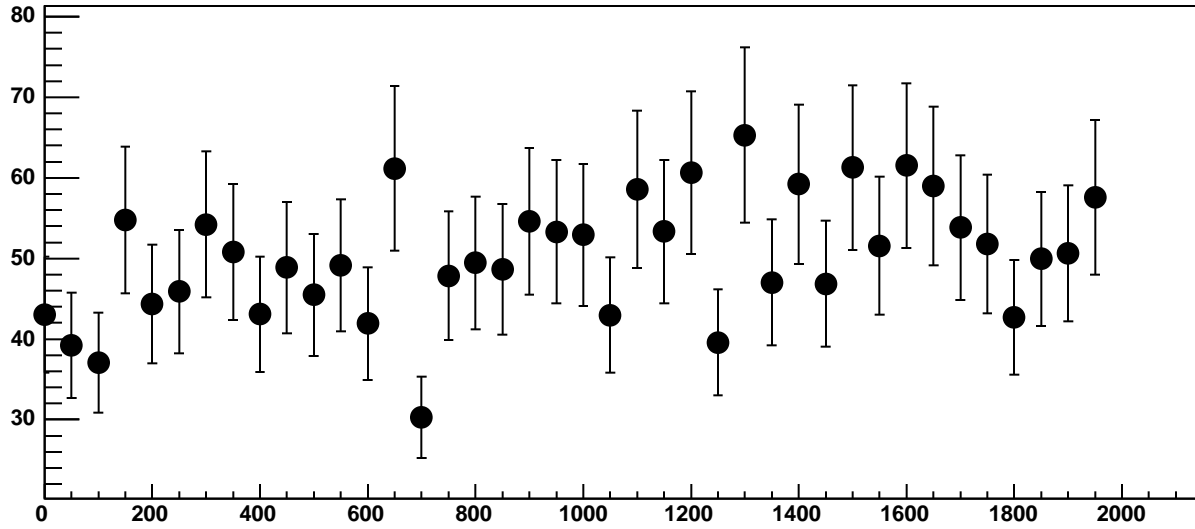


Chip 7, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC

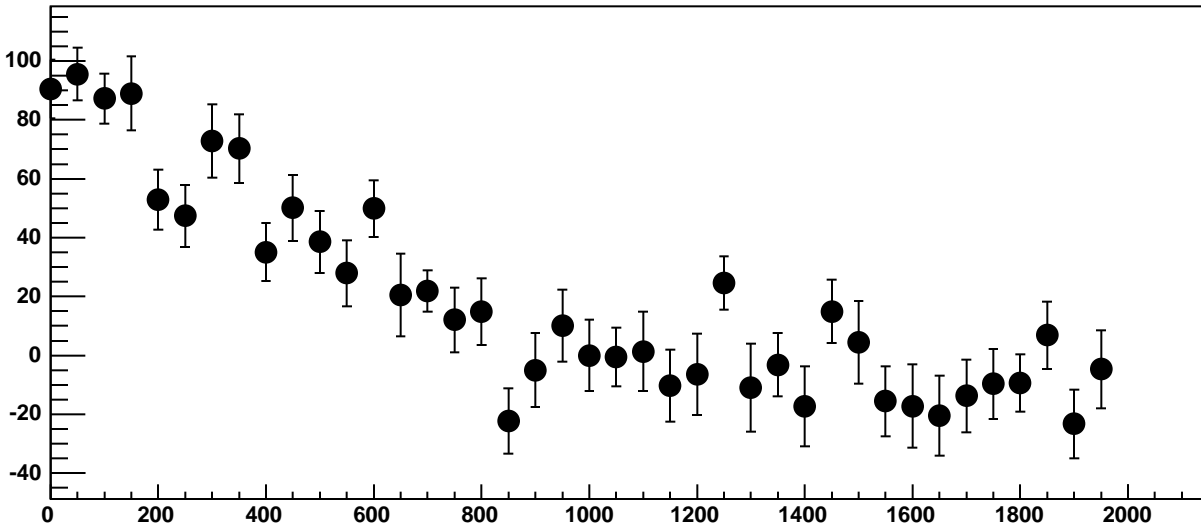


$\chi^2 / \text{ndf}$  16.99 / 11  
p0  $-1624 \pm 19.5$   
p1  $0.1091 \pm 0.01756$

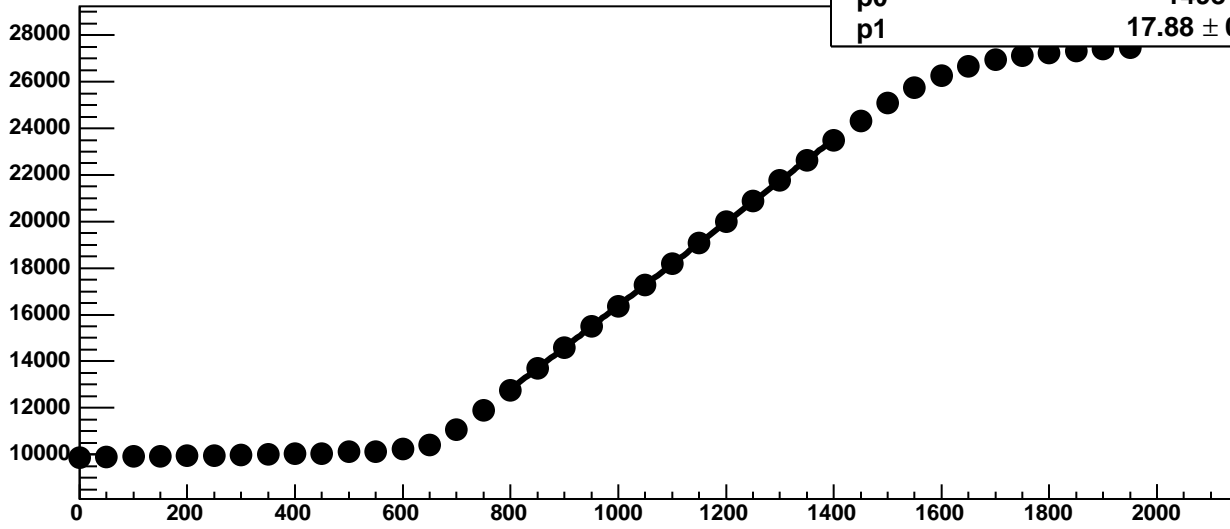
Chip 7, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC



Chip 7, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC

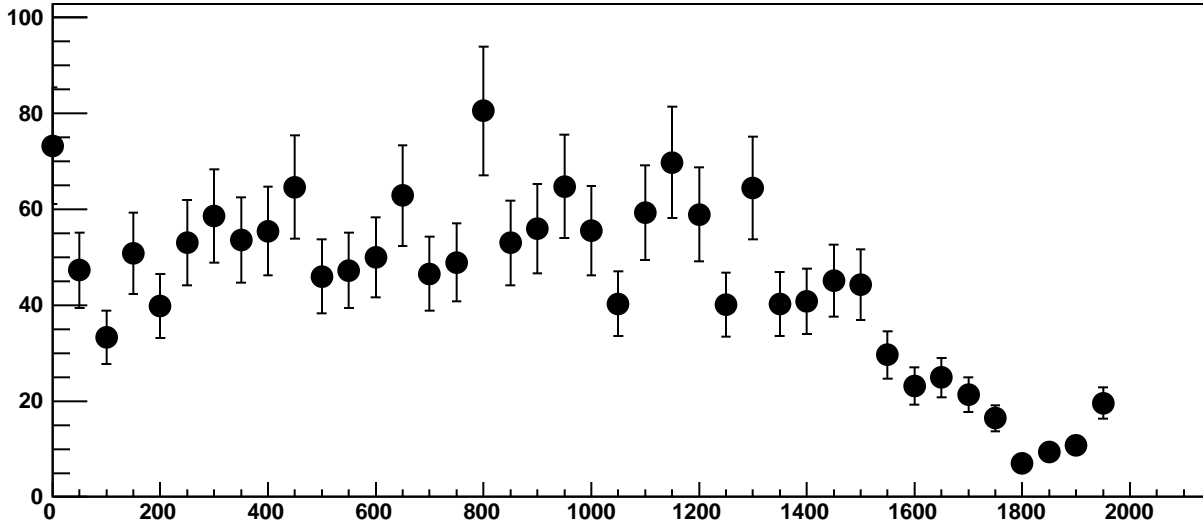


Chip 7, Channel 14, Enable 4!, Hold=30, ADC Mean vs DAC

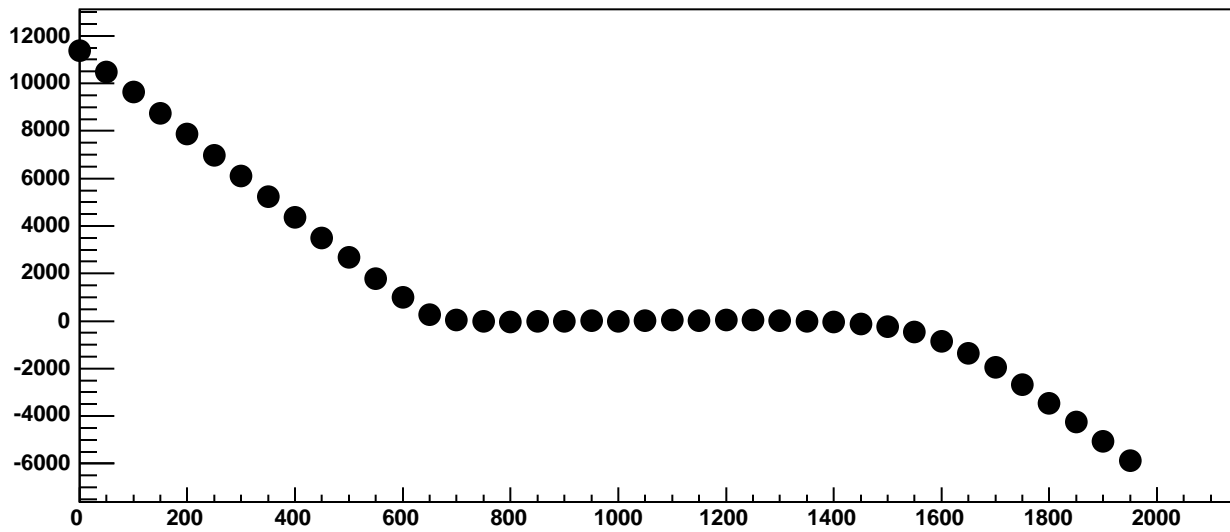


$\chi^2 / \text{ndf}$  66.26 / 11  
p0  $-1498 \pm 20.46$   
p1  $17.88 \pm 0.01764$

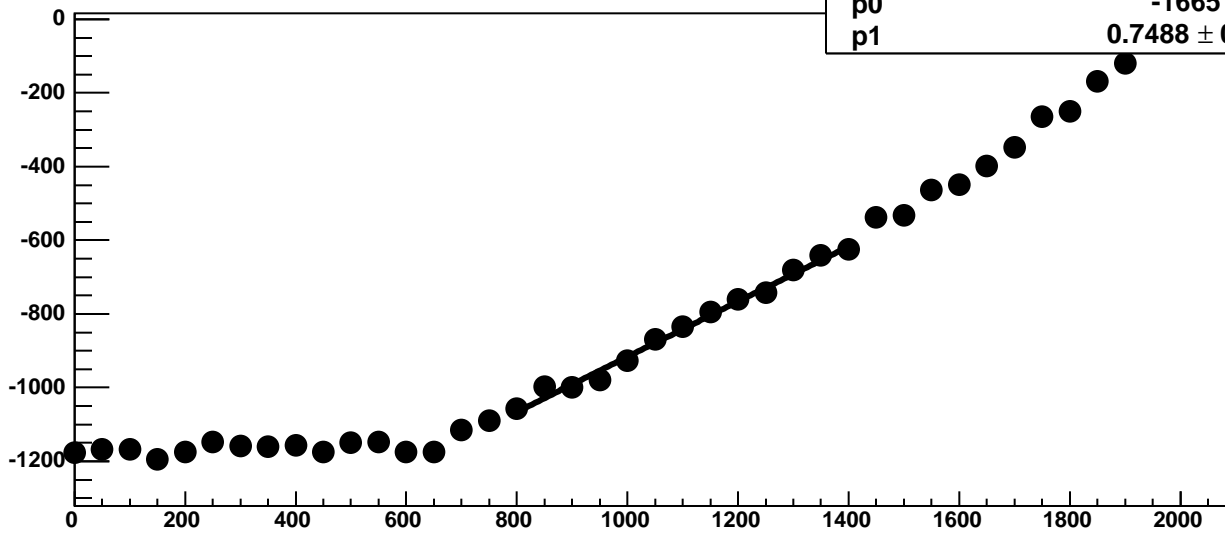
Chip 7, Channel 14, Enable 4!, Hold=30, ADC Noise vs DAC



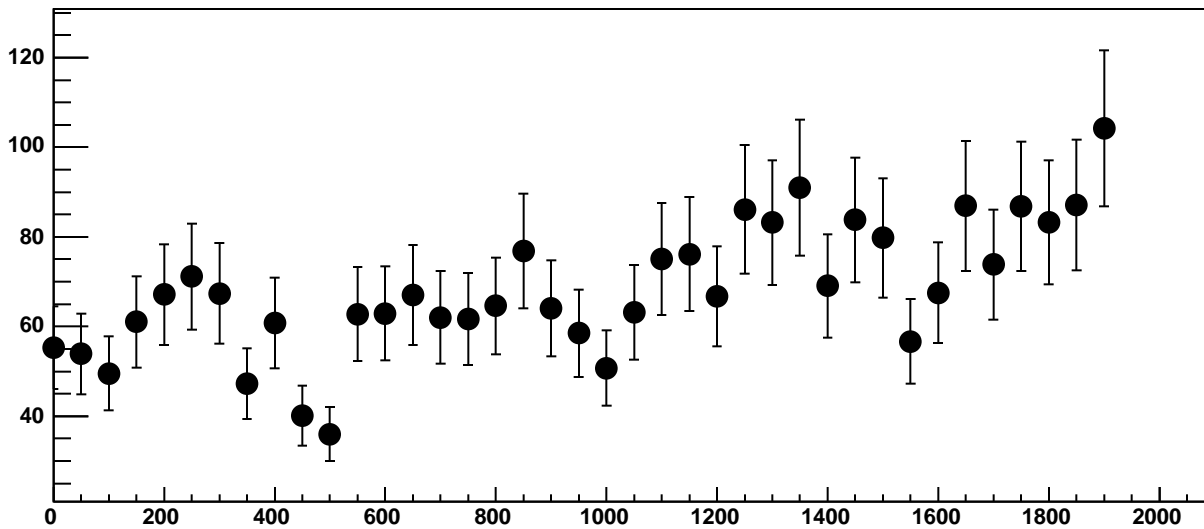
Chip 7, Channel 14, Enable 4!, Hold=30, ADC Residuals vs DAC



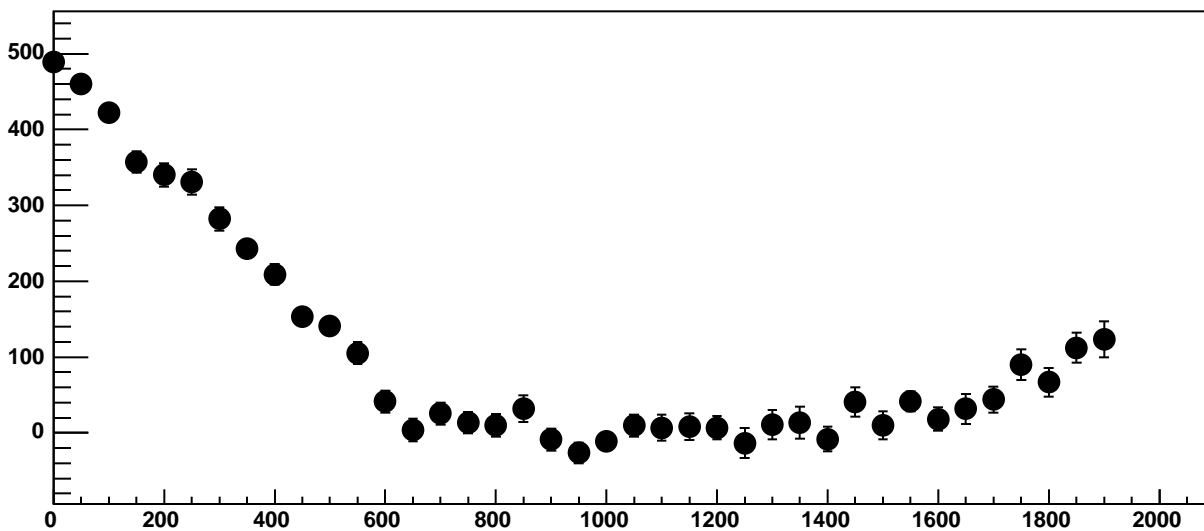
Chip 7, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC



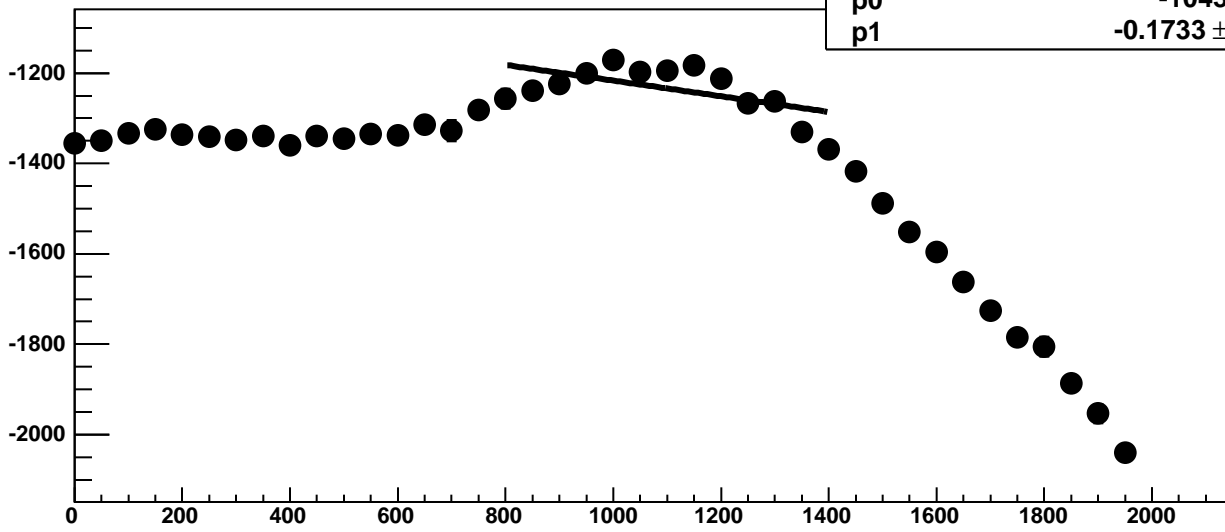
Chip 7, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC



Chip 7, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC

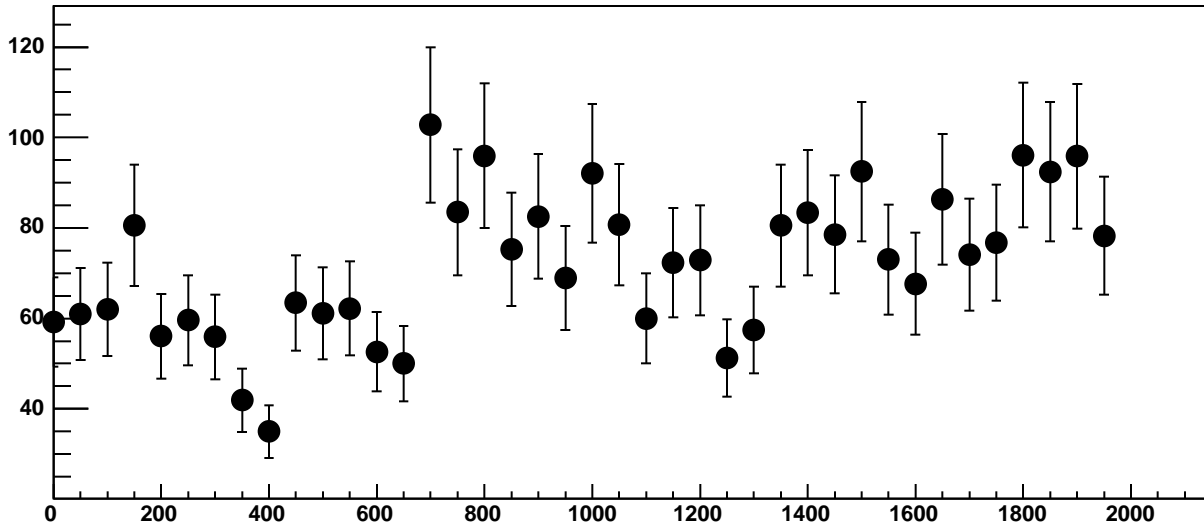


Chip 7, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC

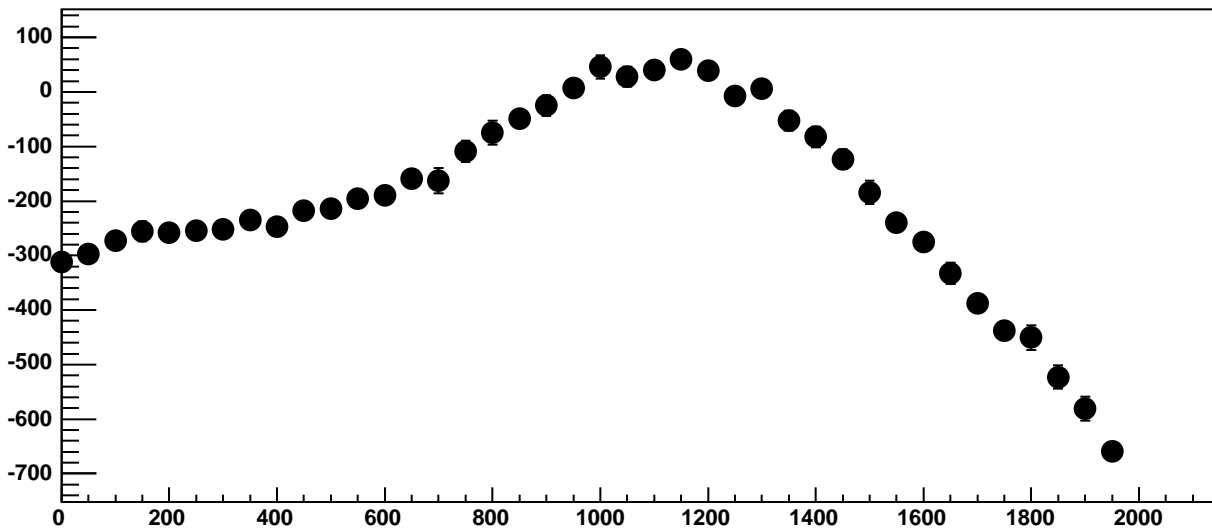


$\chi^2 / \text{ndf}$	82.54 / 11
p0	$-1043 \pm 29.9$
p1	$-0.1733 \pm 0.0262$

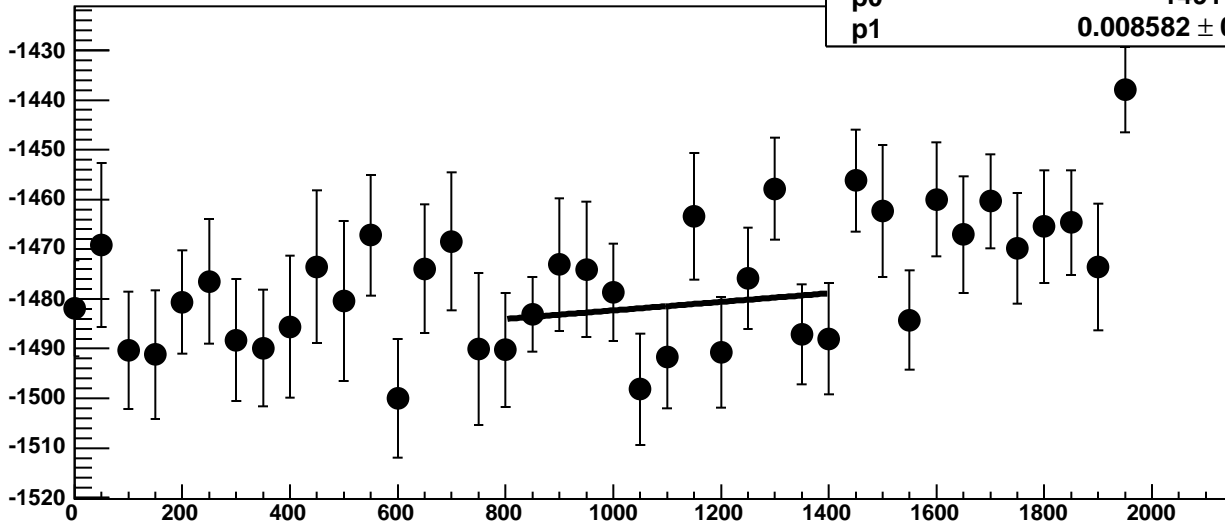
Chip 7, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC



Chip 7, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 7, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

13.3 / 11

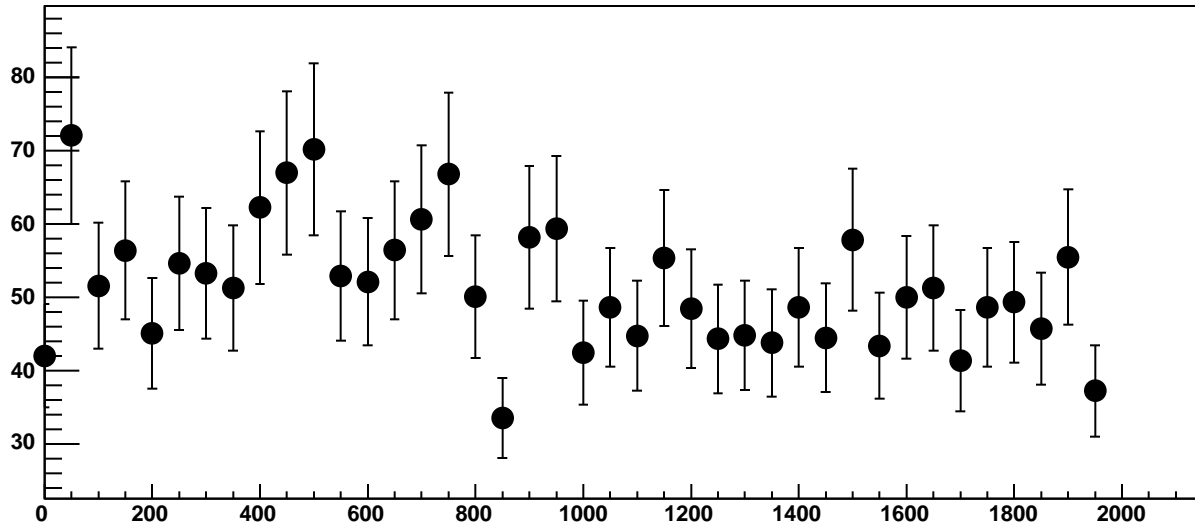
p0

$-1491 \pm 16.95$

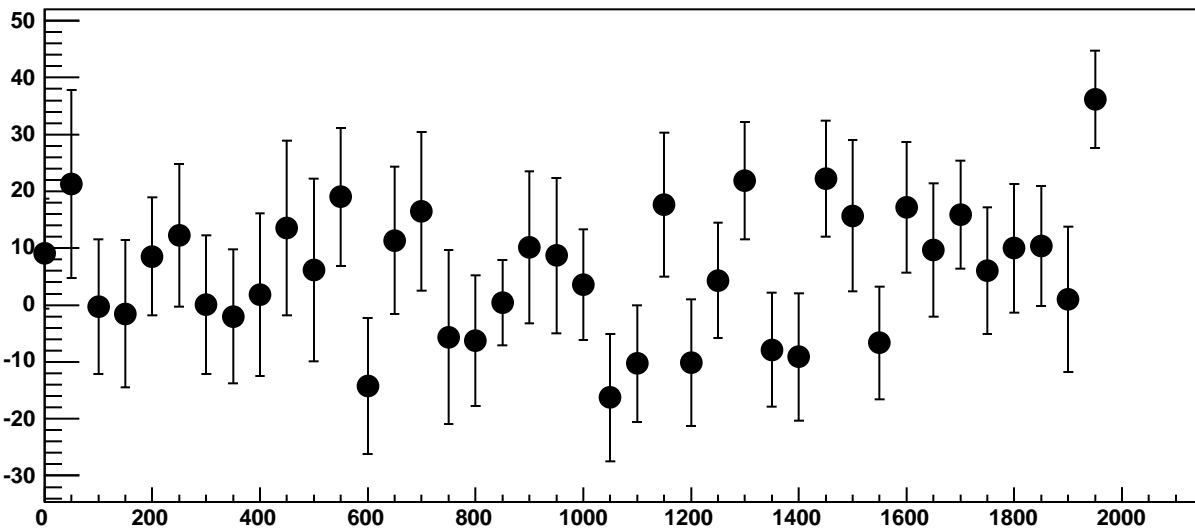
p1

$0.008582 \pm 0.01527$

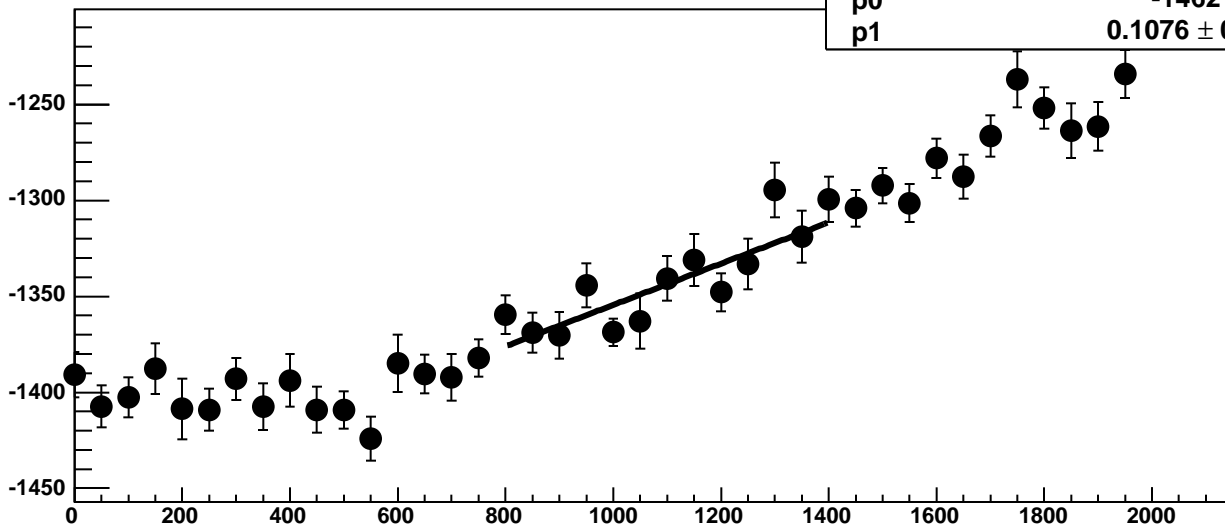
Chip 7, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC



Chip 7, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC

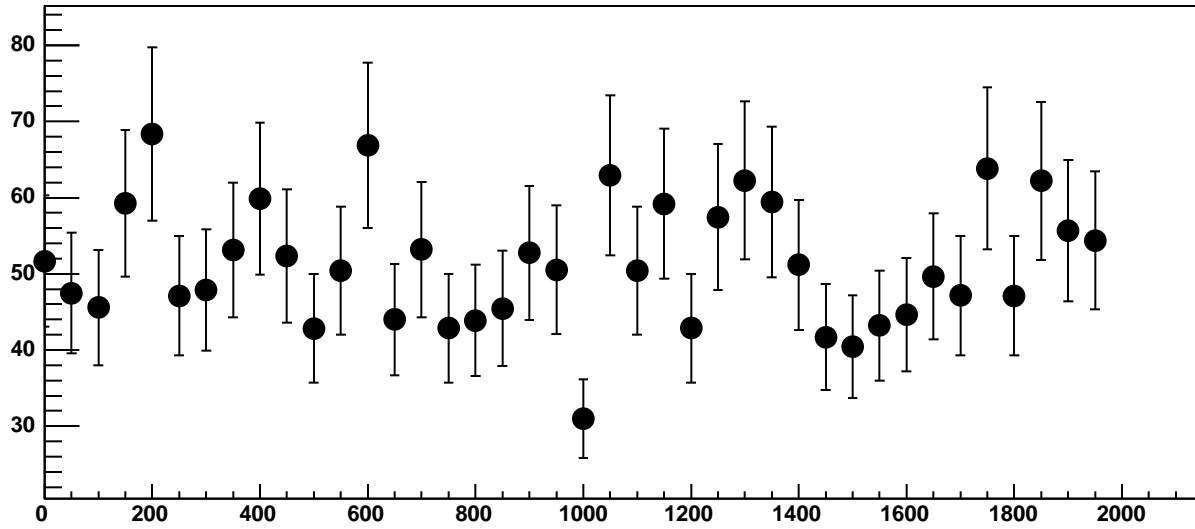


Chip 7, Channel 15, Enable 2, Hold=30, ADC Mean vs DAC

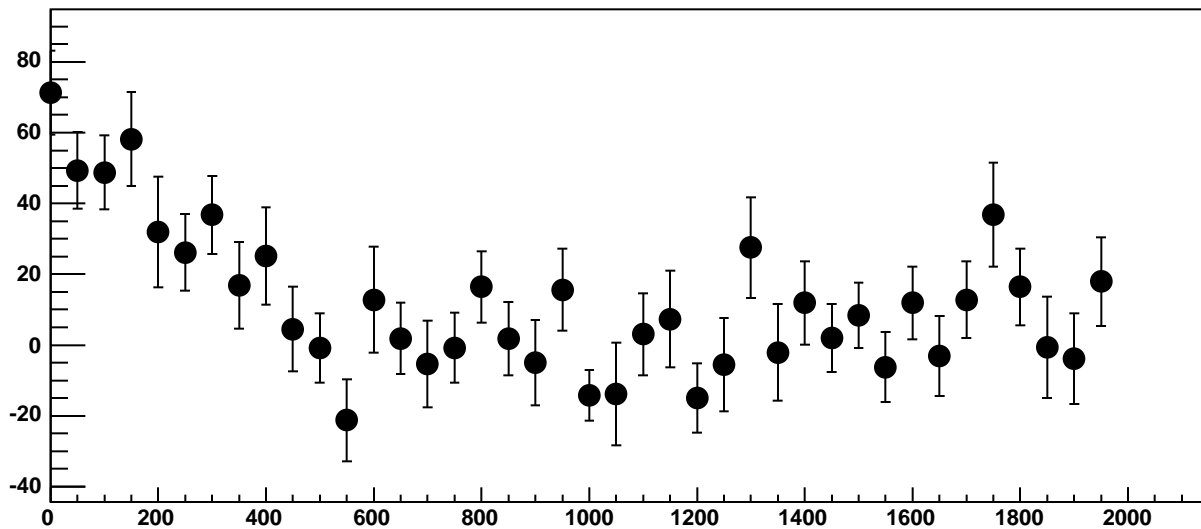


$\chi^2 / \text{ndf}$  17.17 / 11  
p0  $-1462 \pm 18.48$   
p1  $0.1076 \pm 0.01705$

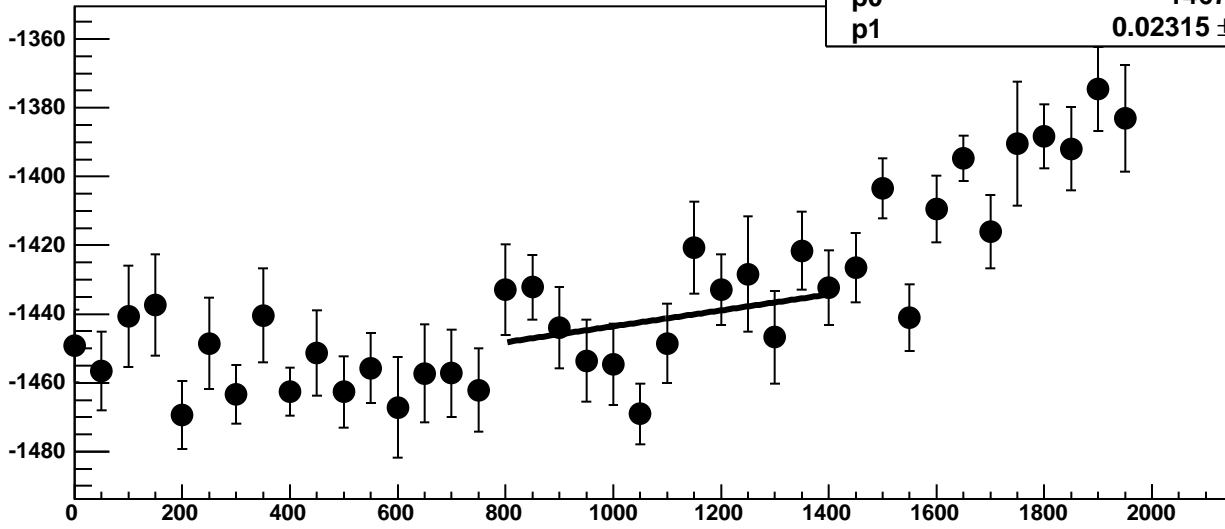
Chip 7, Channel 15, Enable 2, Hold=30, ADC Noise vs DAC



Chip 7, Channel 15, Enable 2, Hold=30, ADC Residuals vs DAC

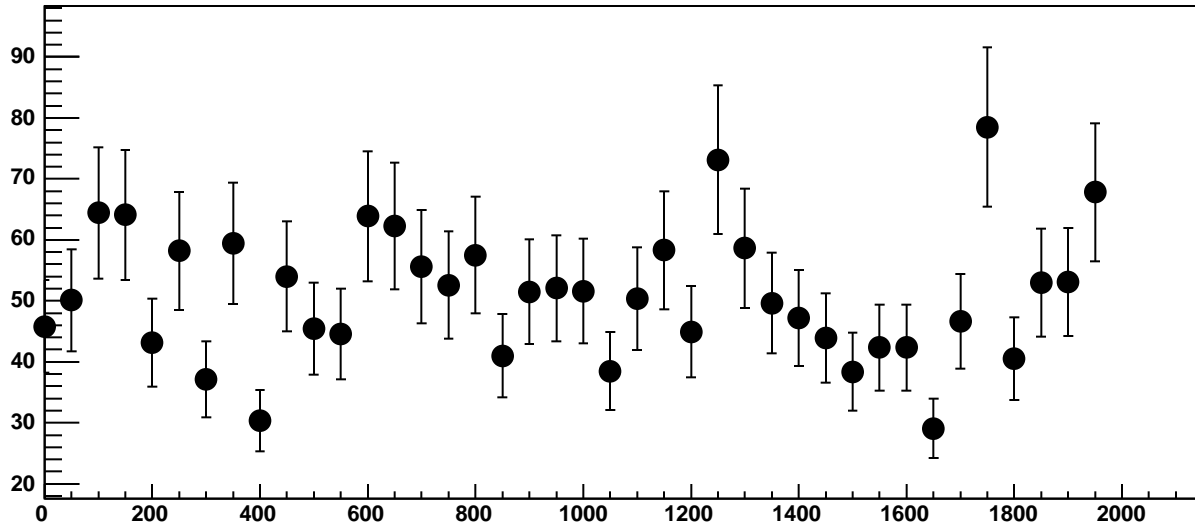


Chip 7, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC

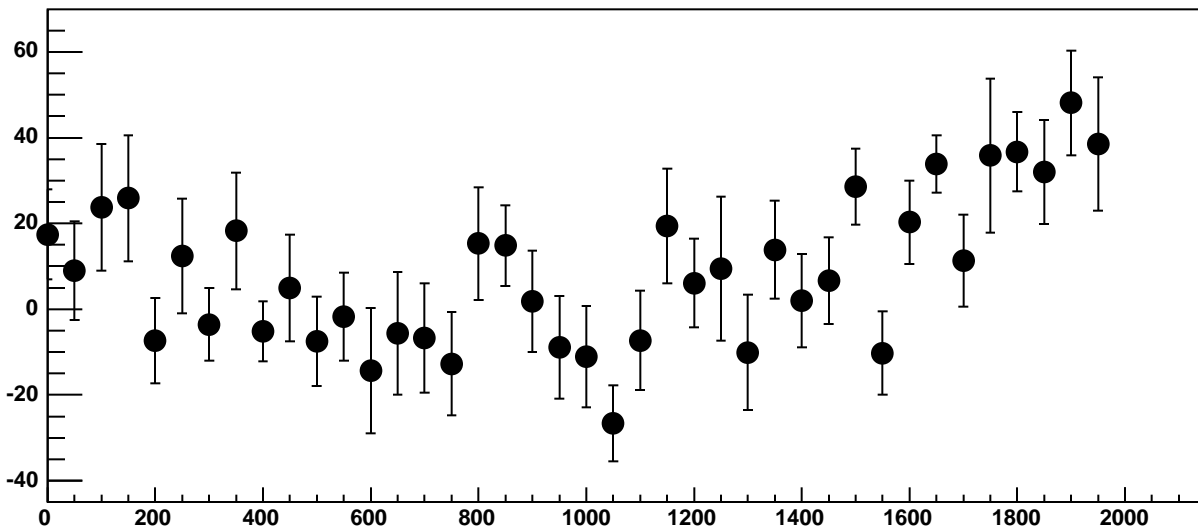


$\chi^2 / \text{ndf}$  19.63 / 11  
p0  $-1467 \pm 18.9$   
p1  $0.02315 \pm 0.0171$

Chip 7, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC

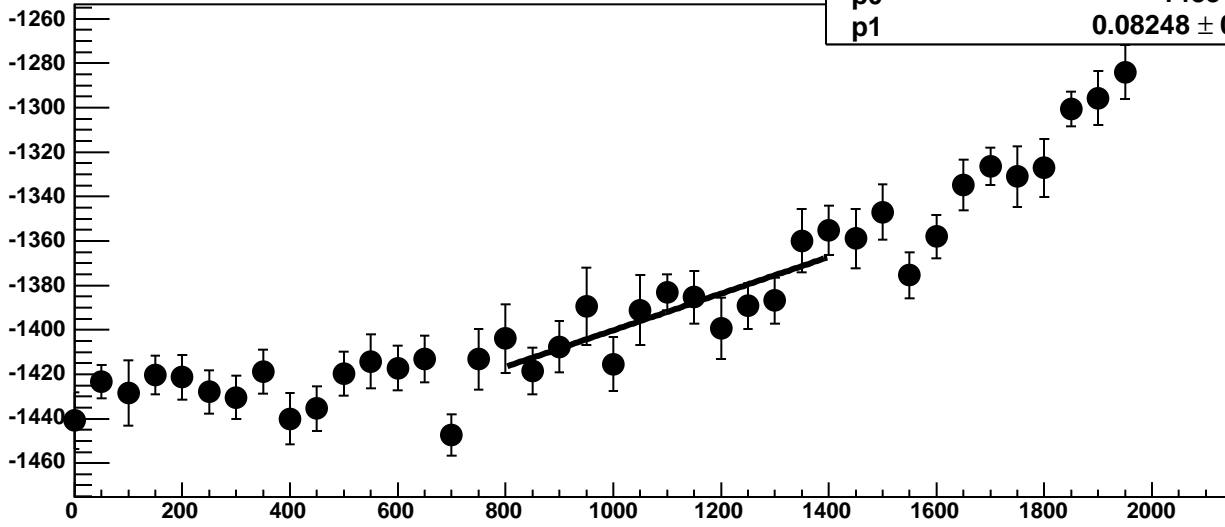


Chip 7, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC



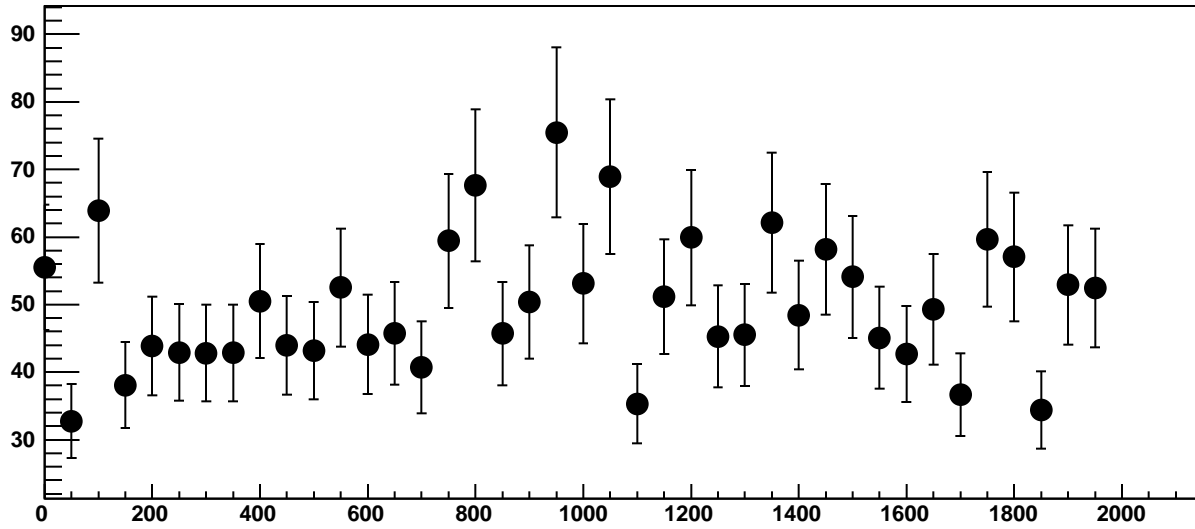


Chip 7, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC

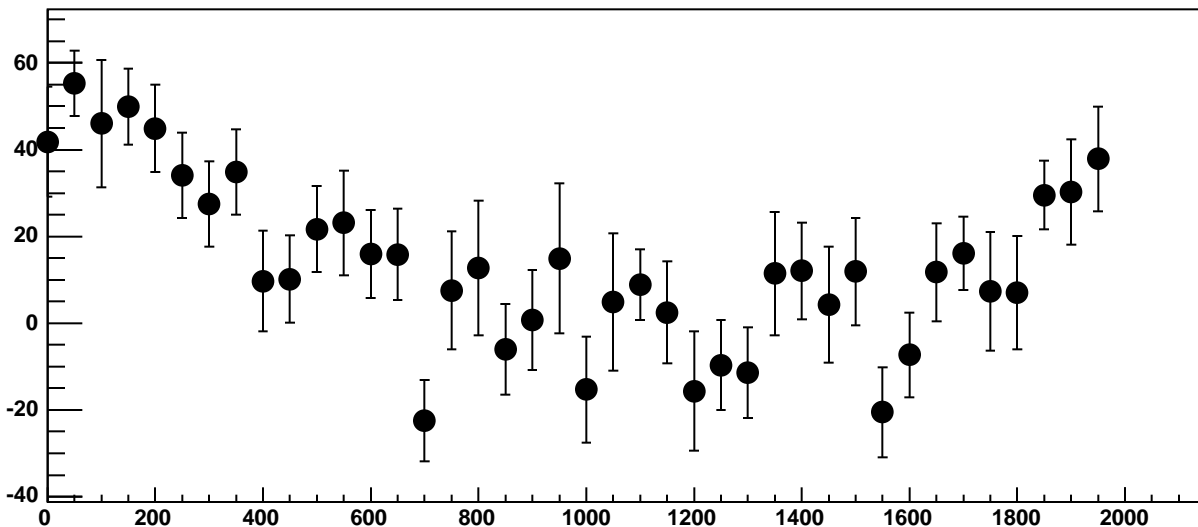


$\chi^2 / \text{ndf}$  9.849 / 11  
p0  $-1483 \pm 20.42$   
p1  $0.08248 \pm 0.01809$

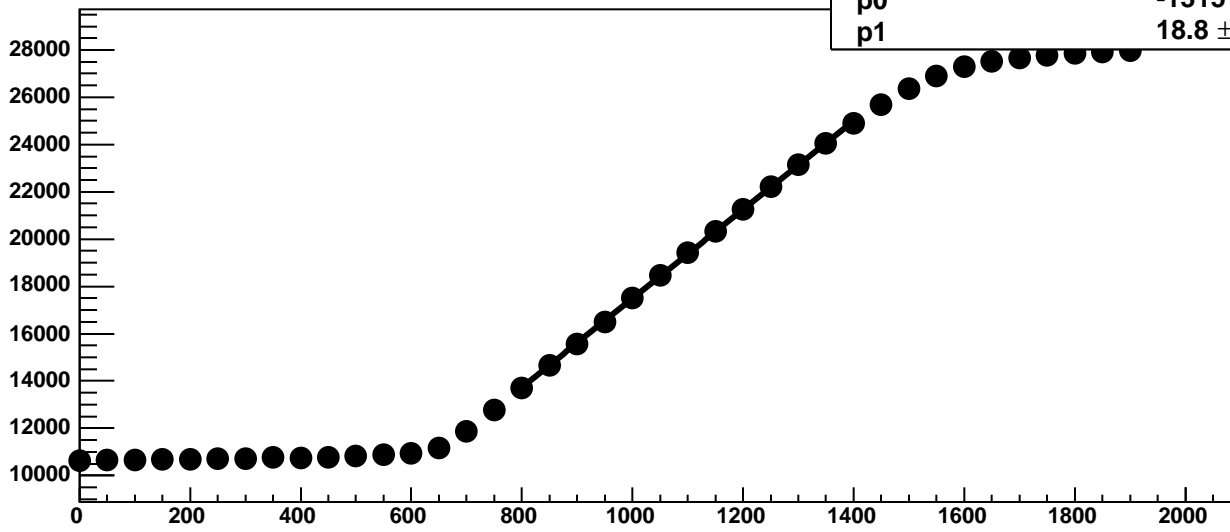
Chip 7, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC



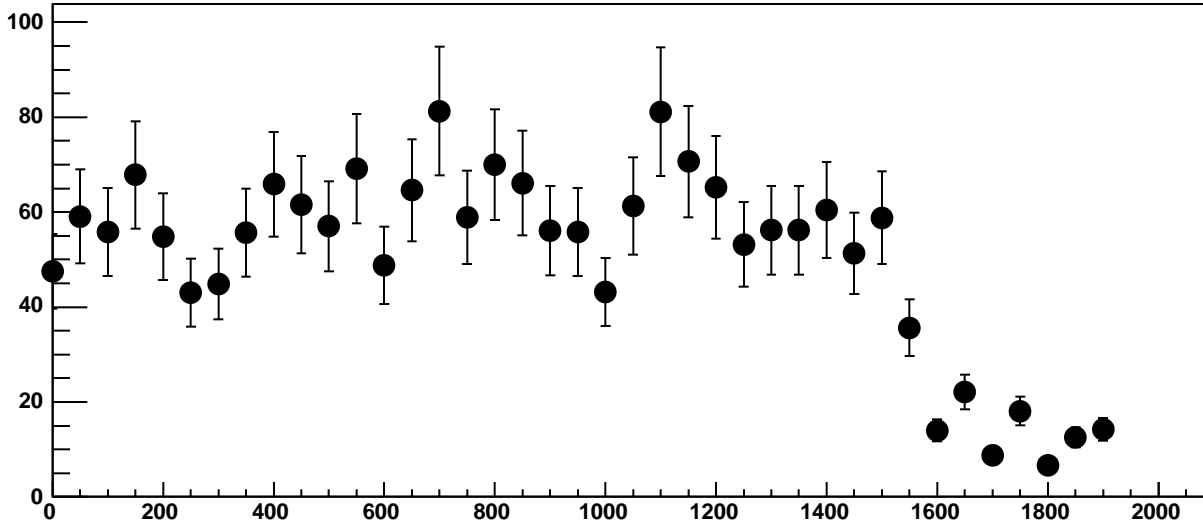
Chip 7, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC



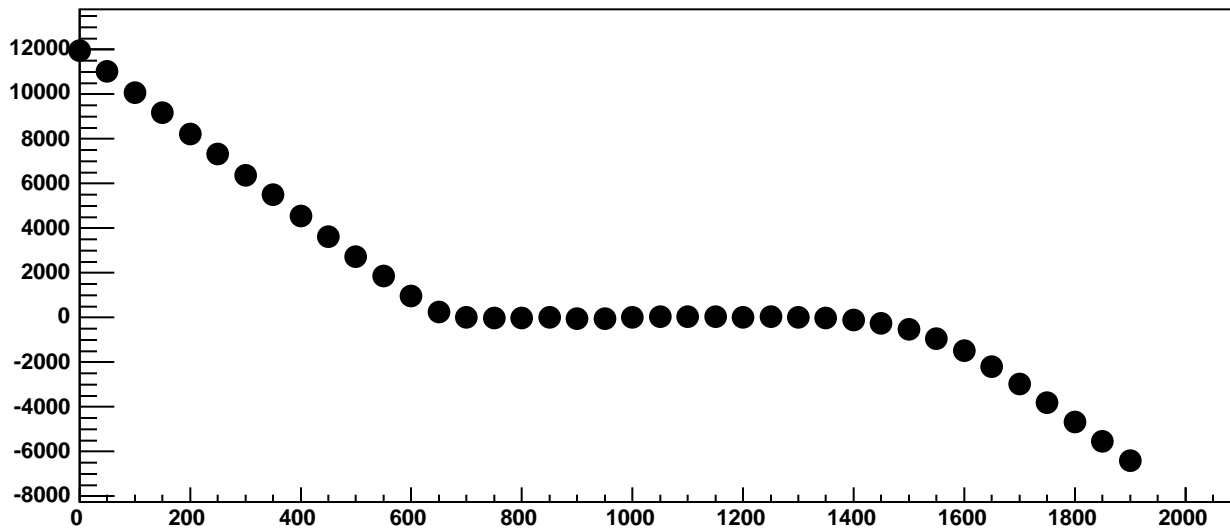
Chip 7, Channel 15, Enable 5!, Hold=30, ADC Mean vs DAC



Chip 7, Channel 15, Enable 5!, Hold=30, ADC Noise vs DAC



Chip 7, Channel 15, Enable 5!, Hold=30, ADC Residuals vs DAC



Chip 7, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

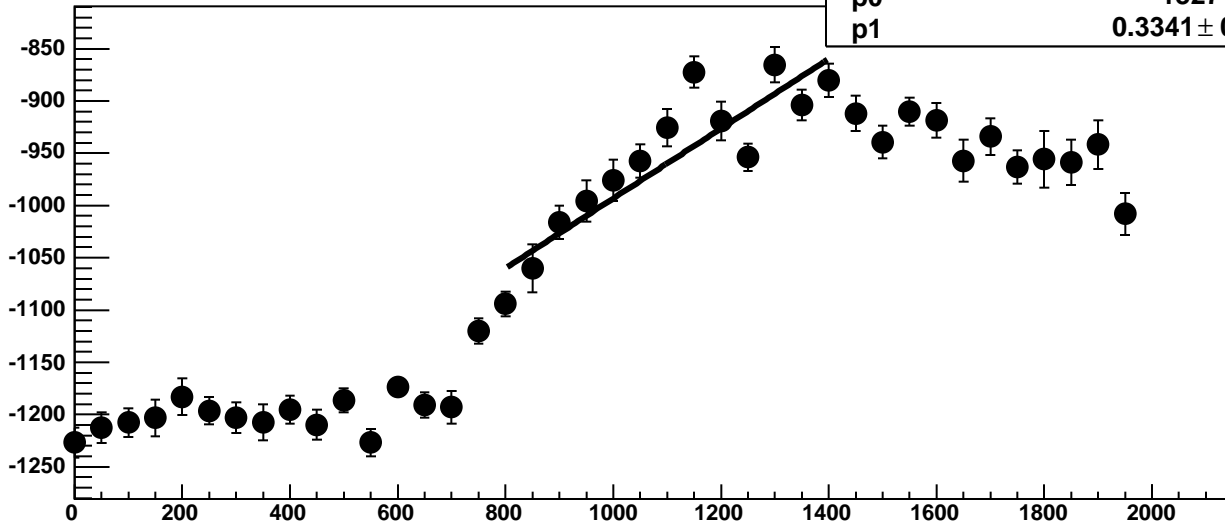
57.23 / 11

p0

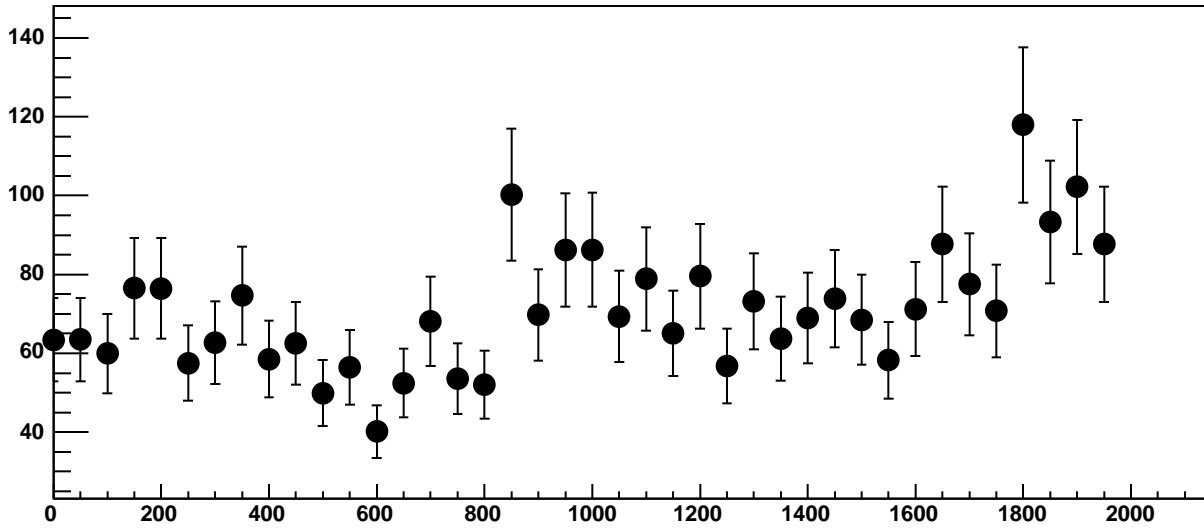
$-1327 \pm 25.35$

p1

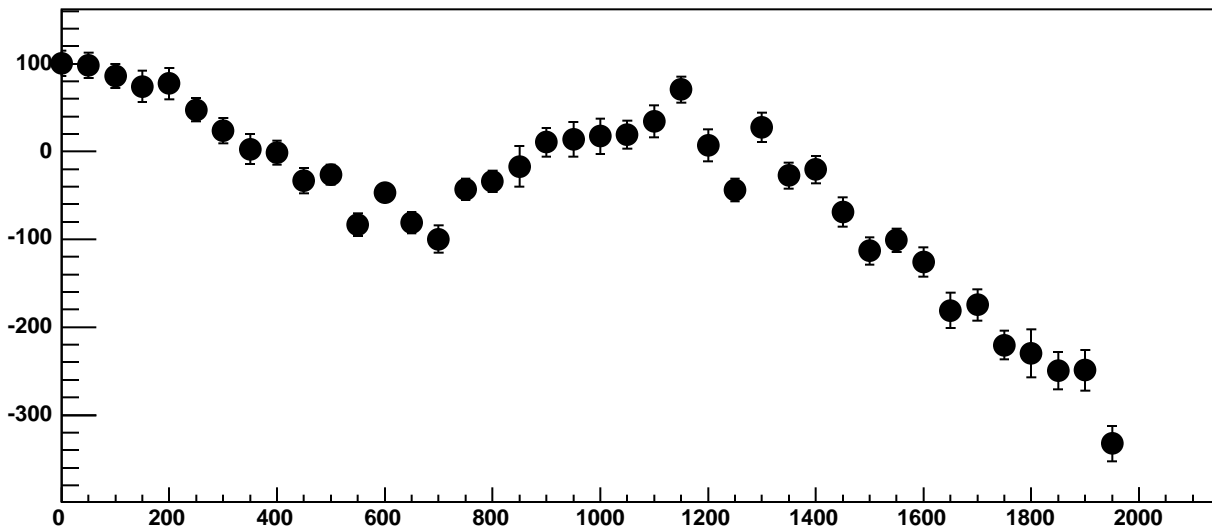
$0.3341 \pm 0.02257$



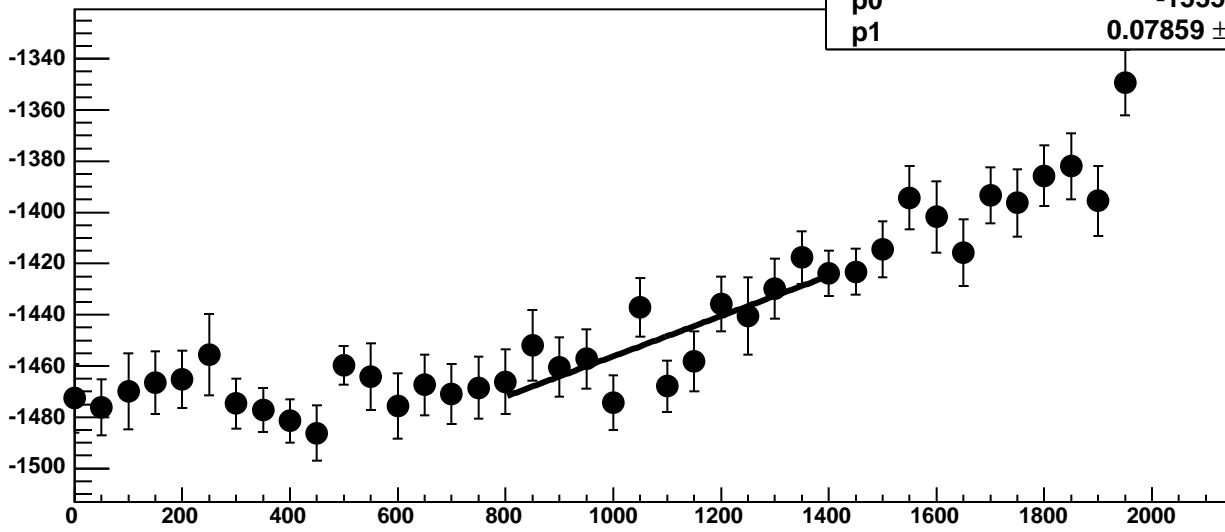
Chip 7, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC



Chip 7, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC

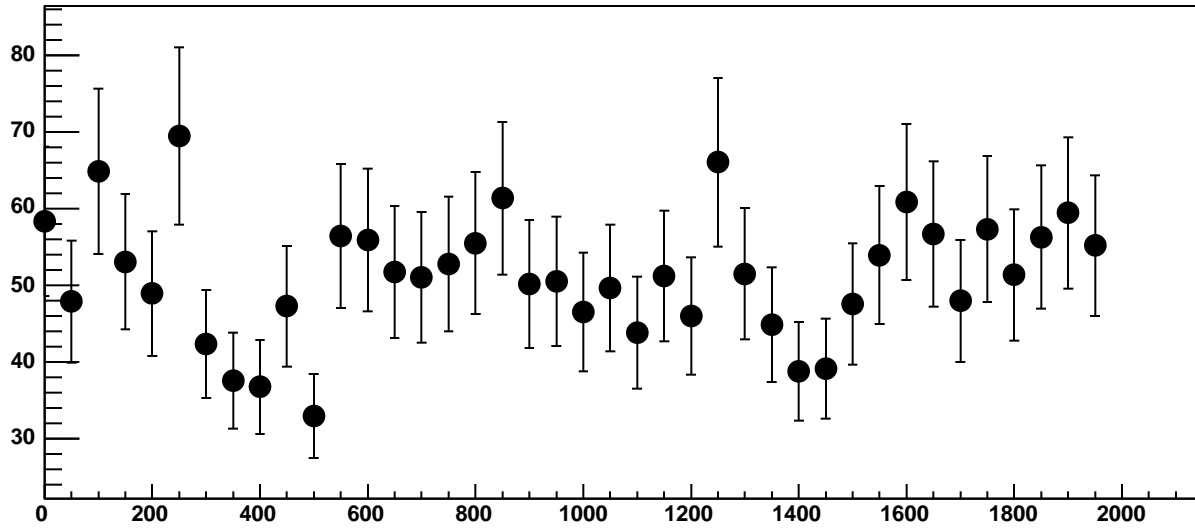


Chip 7, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC

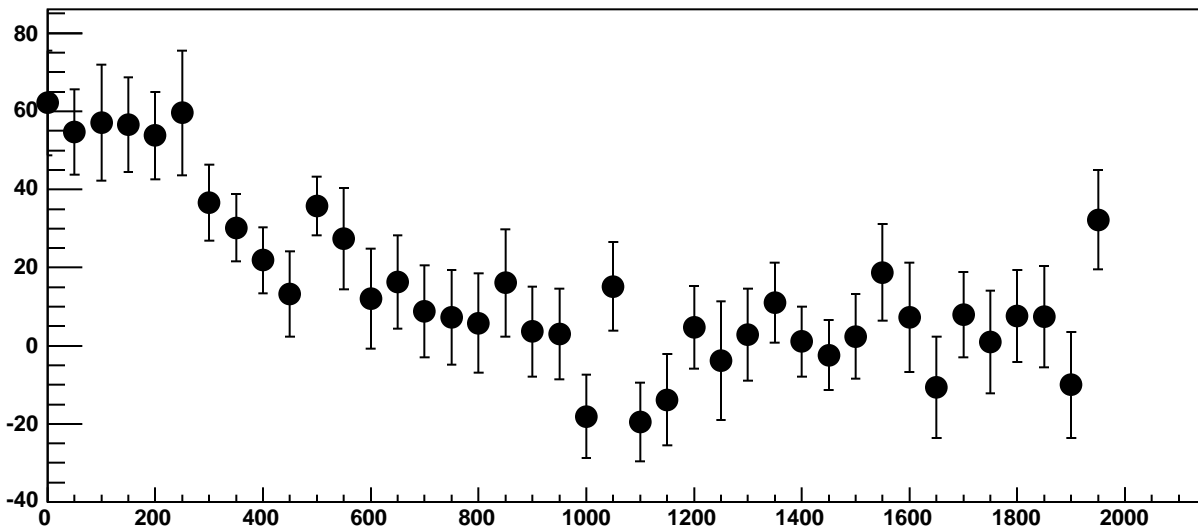


$\chi^2 / \text{ndf}$  13.05 / 11  
p0  $-1535 \pm 18.91$   
p1  $0.07859 \pm 0.0166$

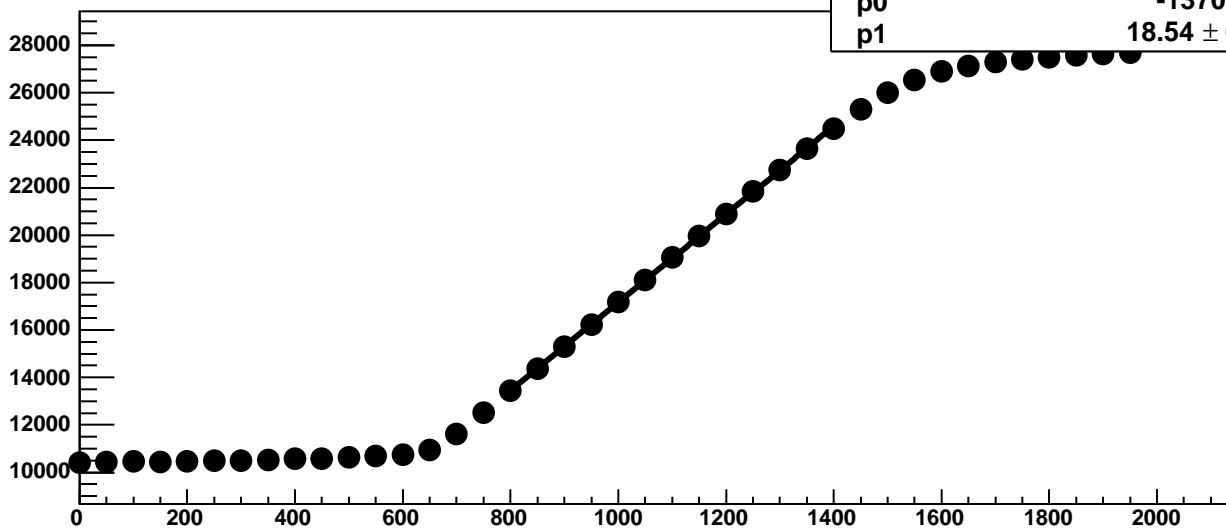
Chip 7, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC



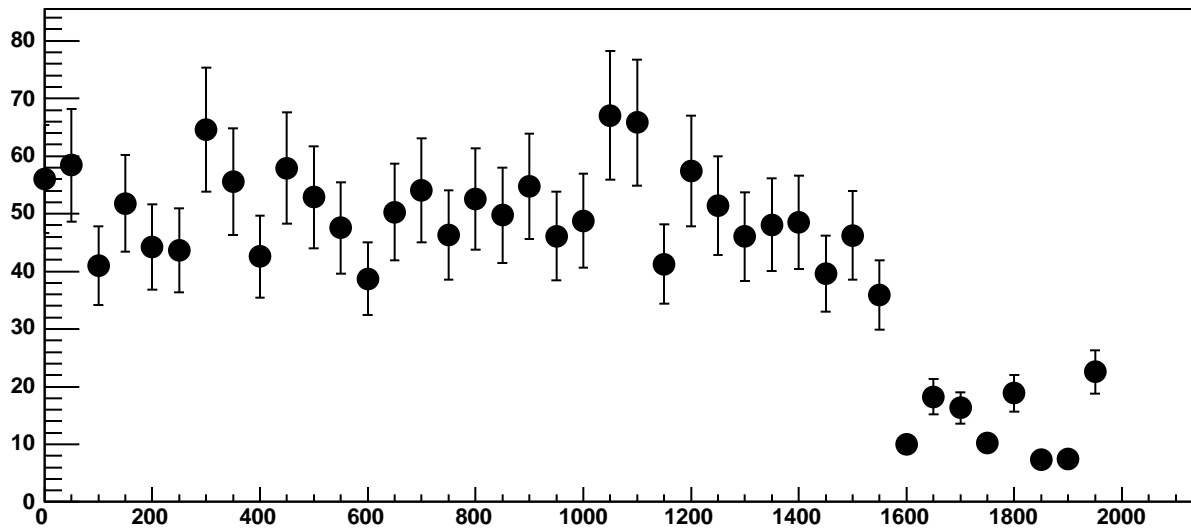
Chip 7, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC



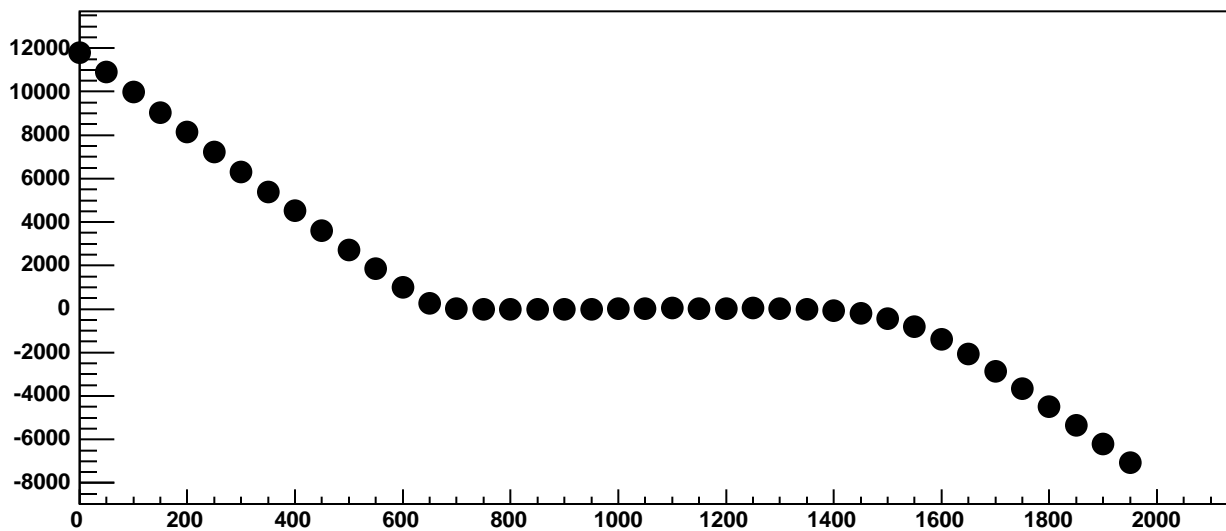
Chip 7, Channel 16, Enable 2!, Hold=30, ADC Mean vs DAC



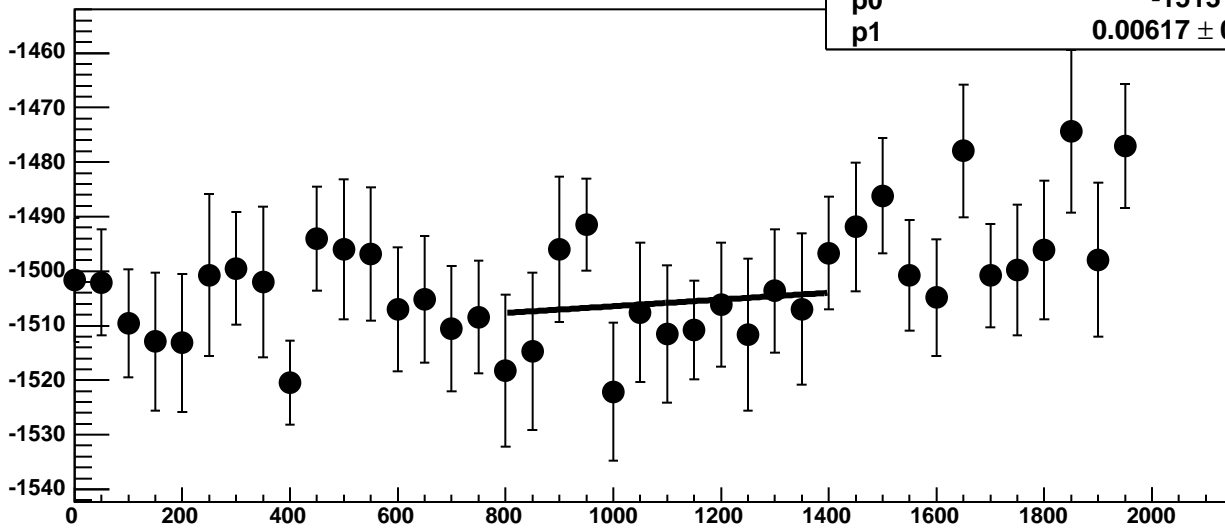
Chip 7, Channel 16, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 7, Channel 16, Enable 2!, Hold=30, ADC Residuals vs DAC

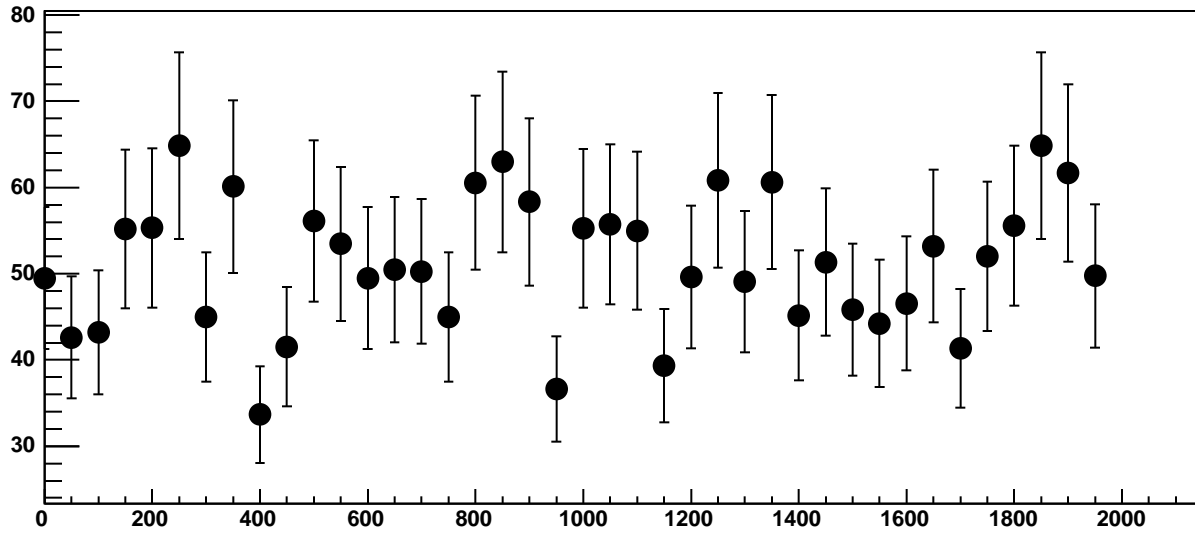


Chip 7, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC

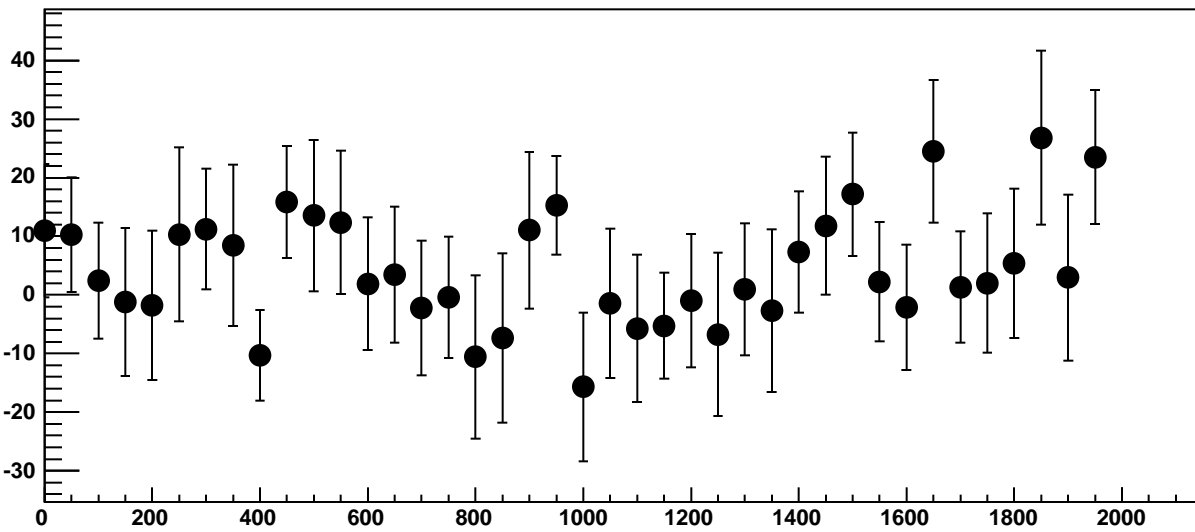


$\chi^2 / \text{ndf}$  7.686 / 11  
p0  $-1513 \pm 20.26$   
p1  $0.00617 \pm 0.01803$

Chip 7, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC

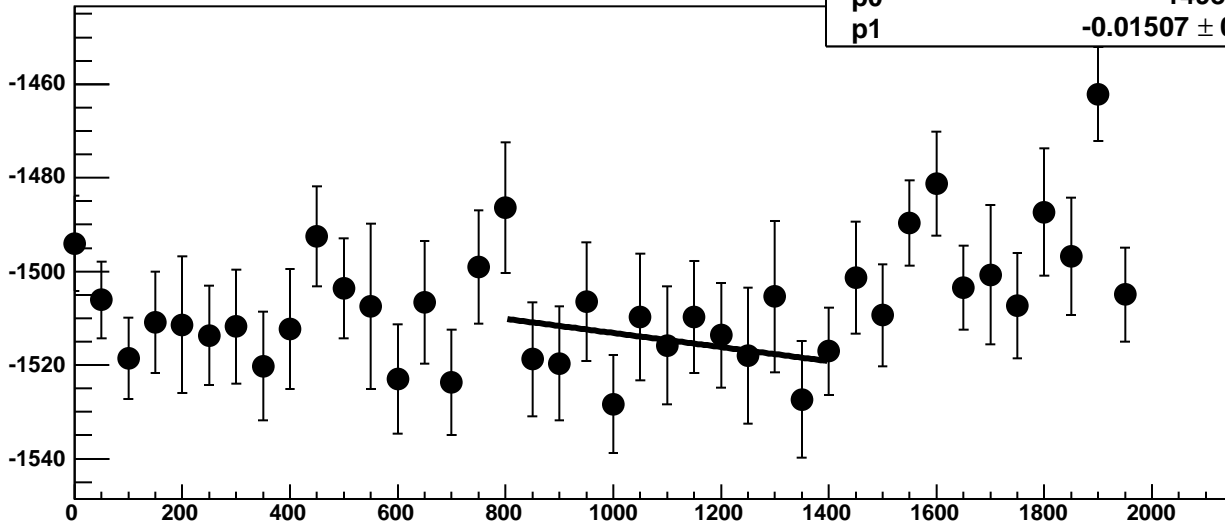


Chip 7, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC

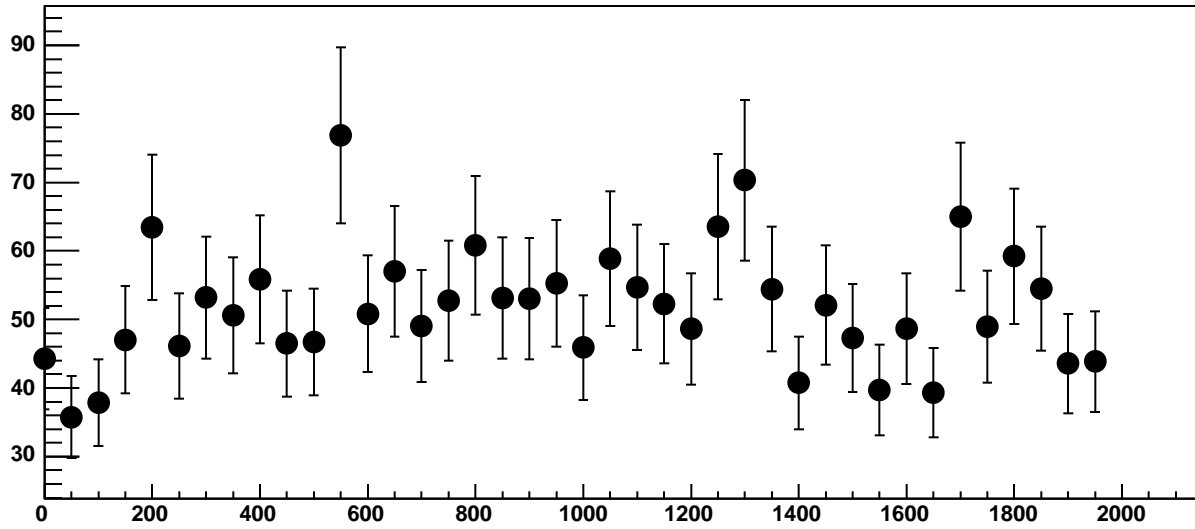


Chip 7, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC

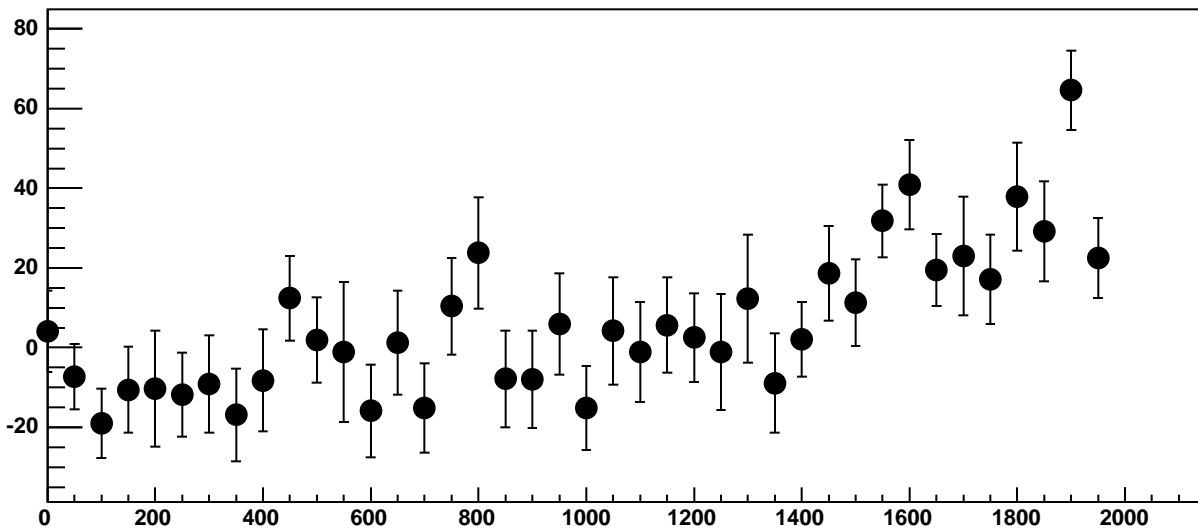
$\chi^2 / \text{ndf}$  7.581 / 11  
p0  $-1498 \pm 20.01$   
p1  $-0.01507 \pm 0.01775$



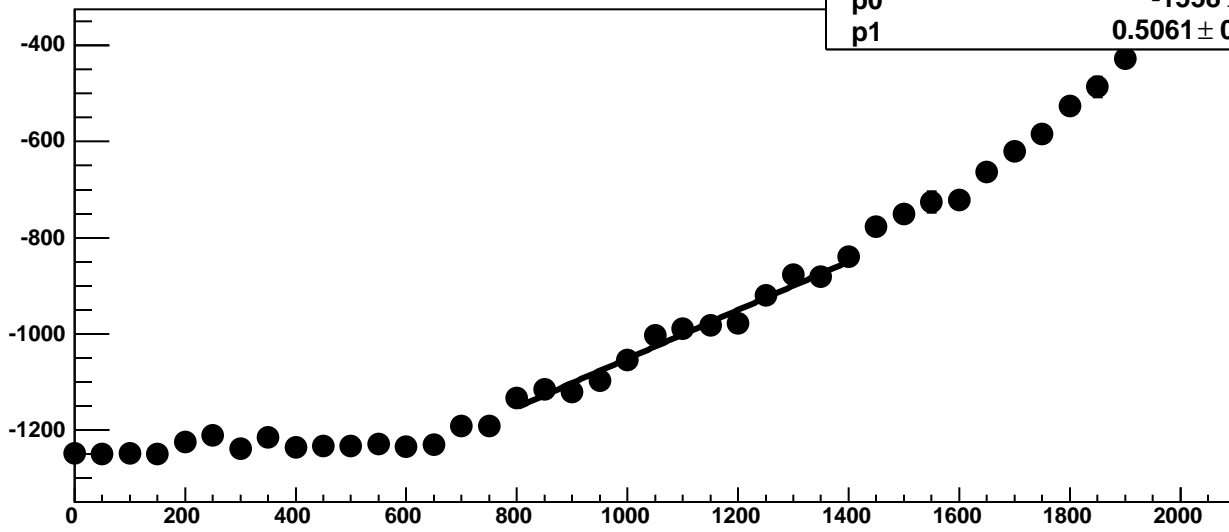
Chip 7, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC



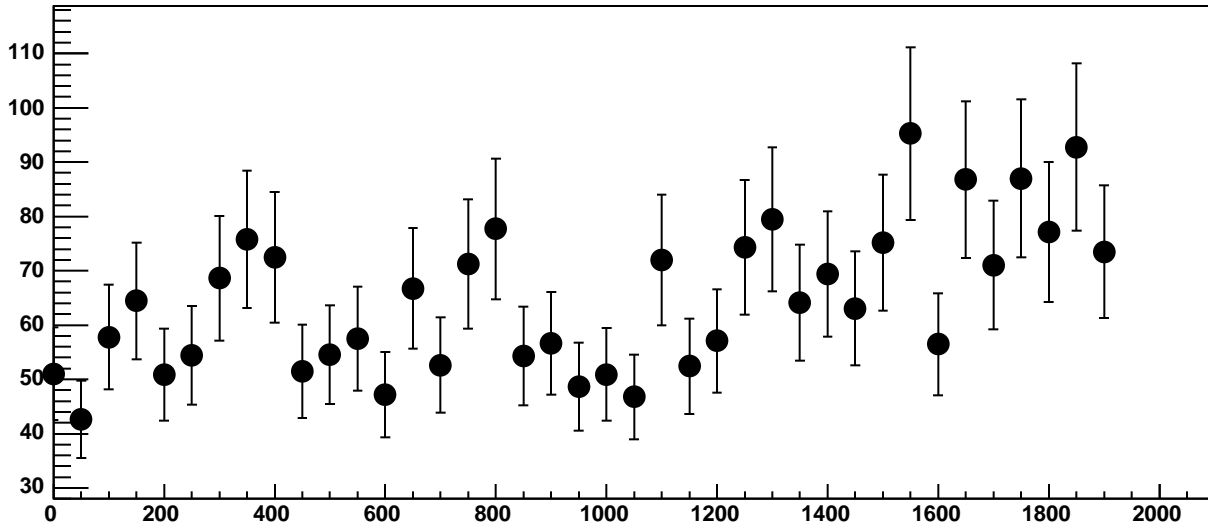
Chip 7, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC



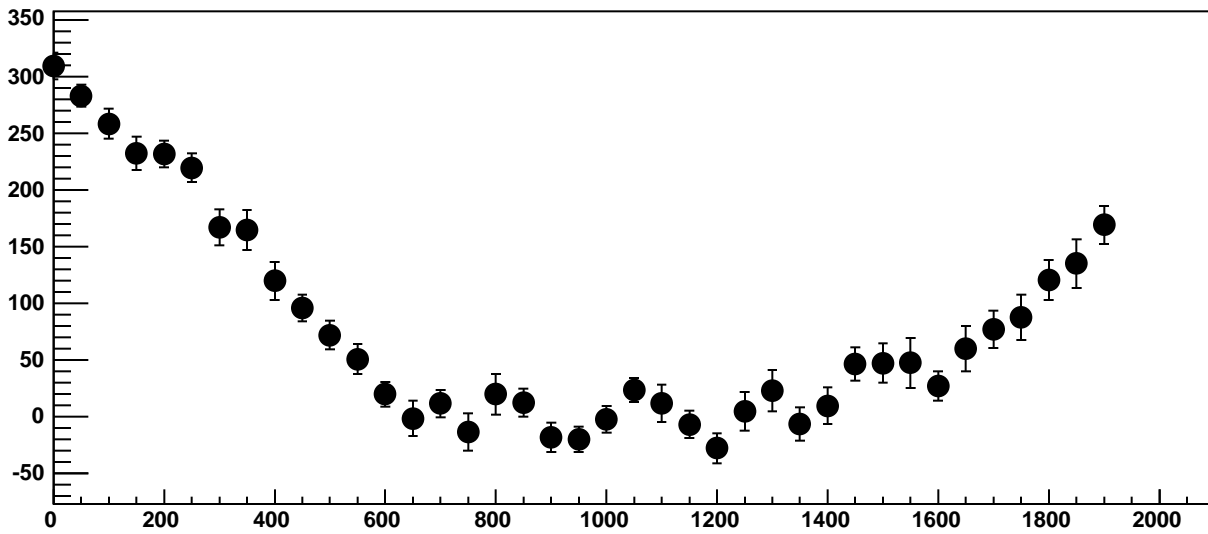
Chip 7, Channel 16, Enable 5, Hold=30, ADC Mean vs DAC



Chip 7, Channel 16, Enable 5, Hold=30, ADC Noise vs DAC

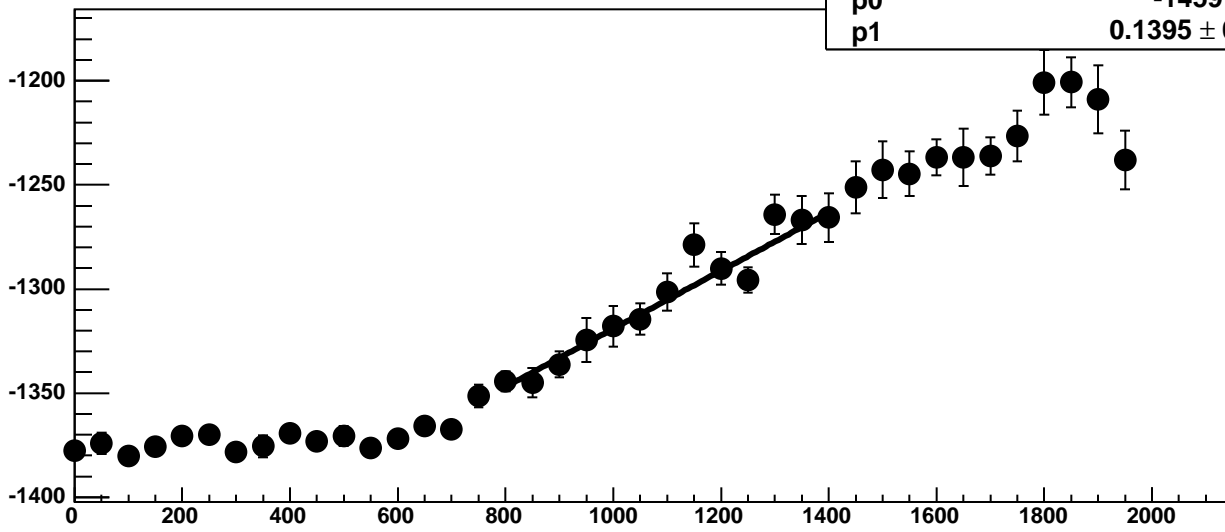


Chip 7, Channel 16, Enable 5, Hold=30, ADC Residuals vs DAC

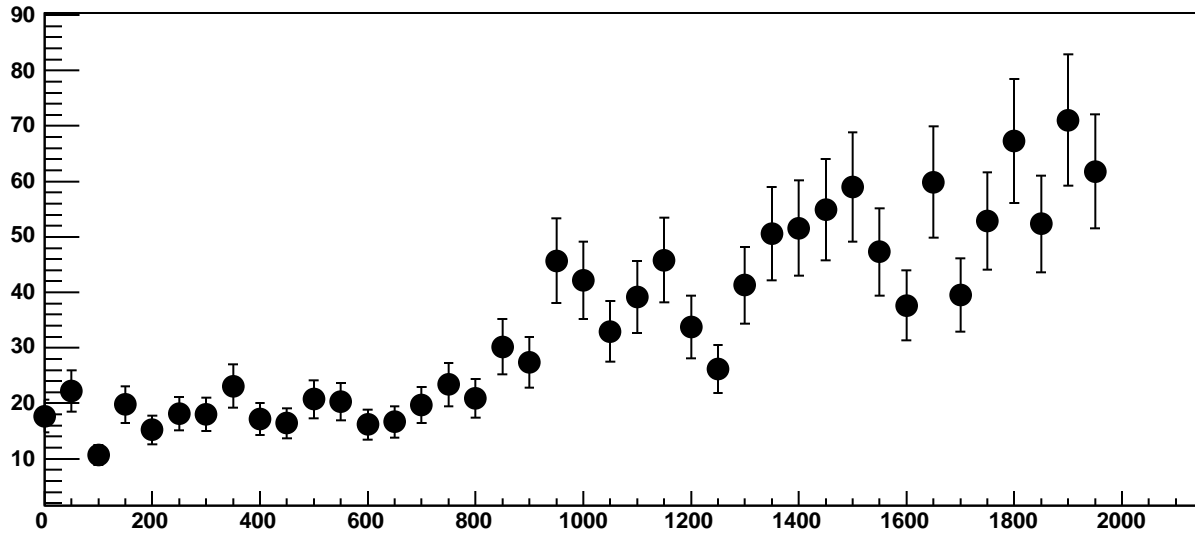




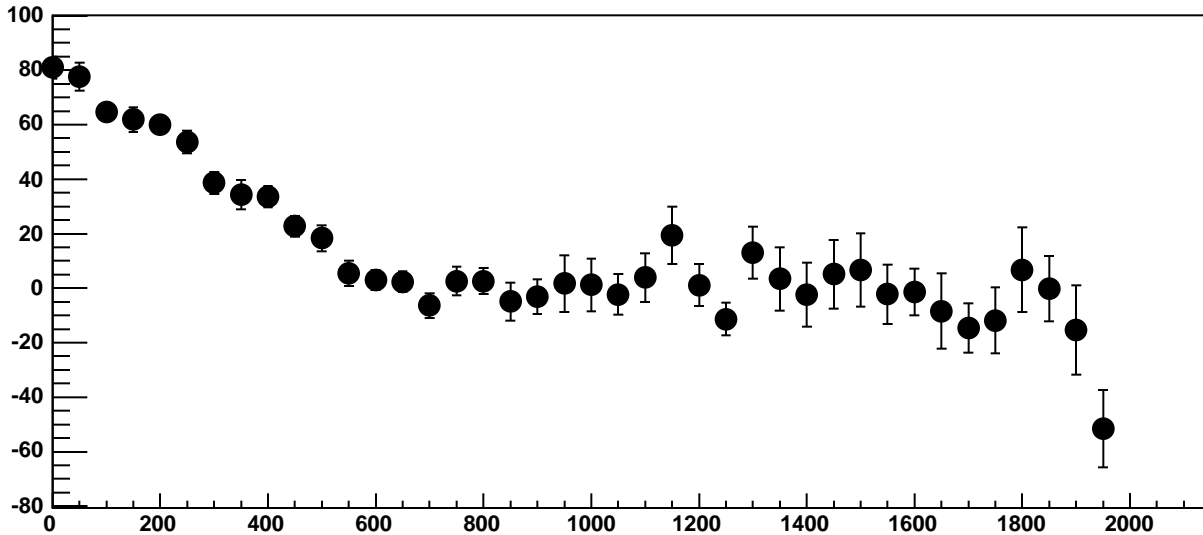
Chip 7, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC



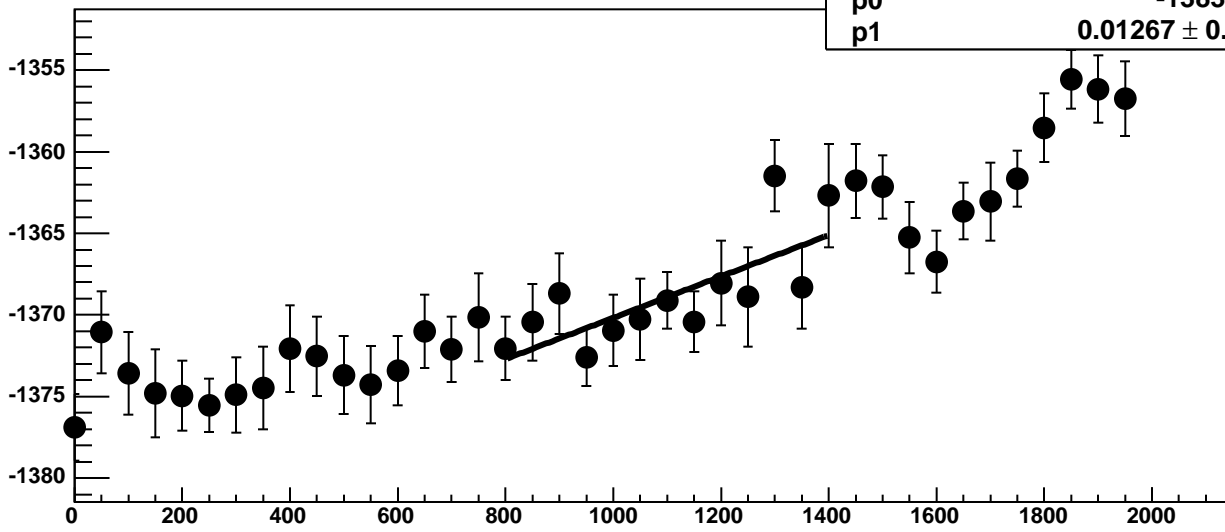
Chip 7, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC



Chip 7, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC

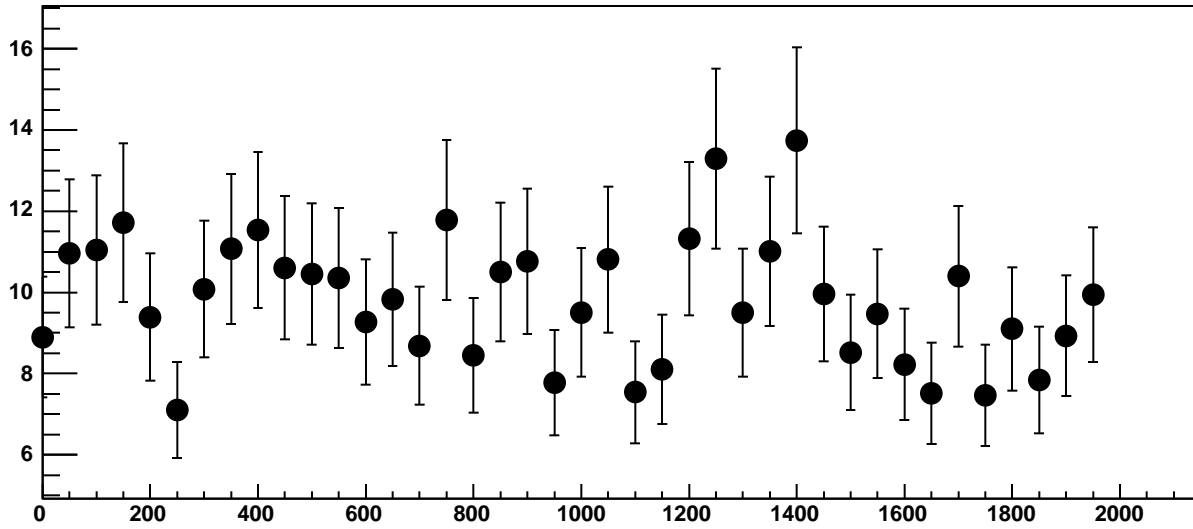


Chip 7, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC

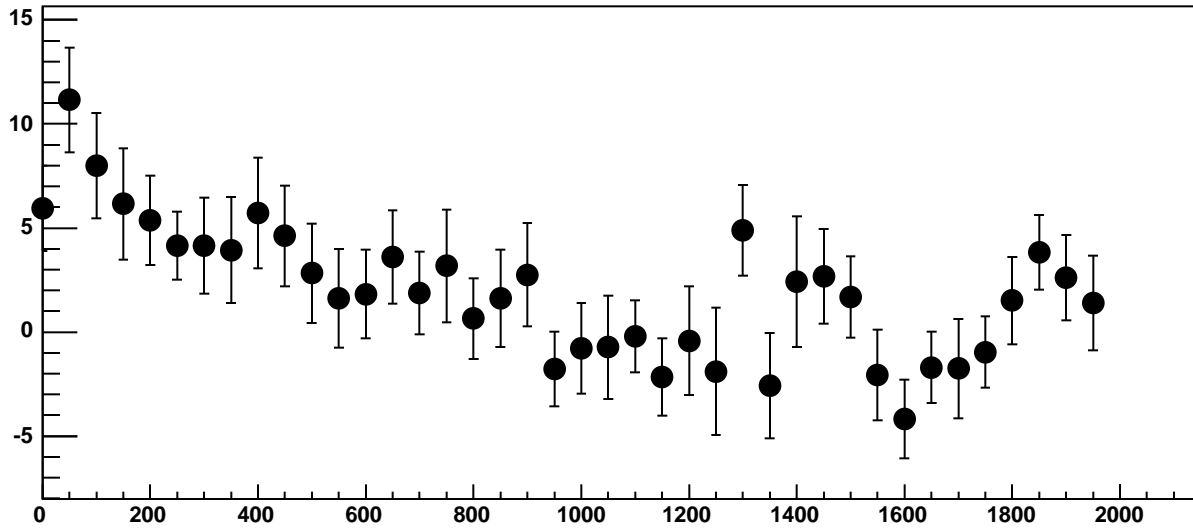


$\chi^2 / \text{ndf}$  11.48 / 11  
p0  $-1383 \pm 3.811$   
p1  $0.01267 \pm 0.003512$

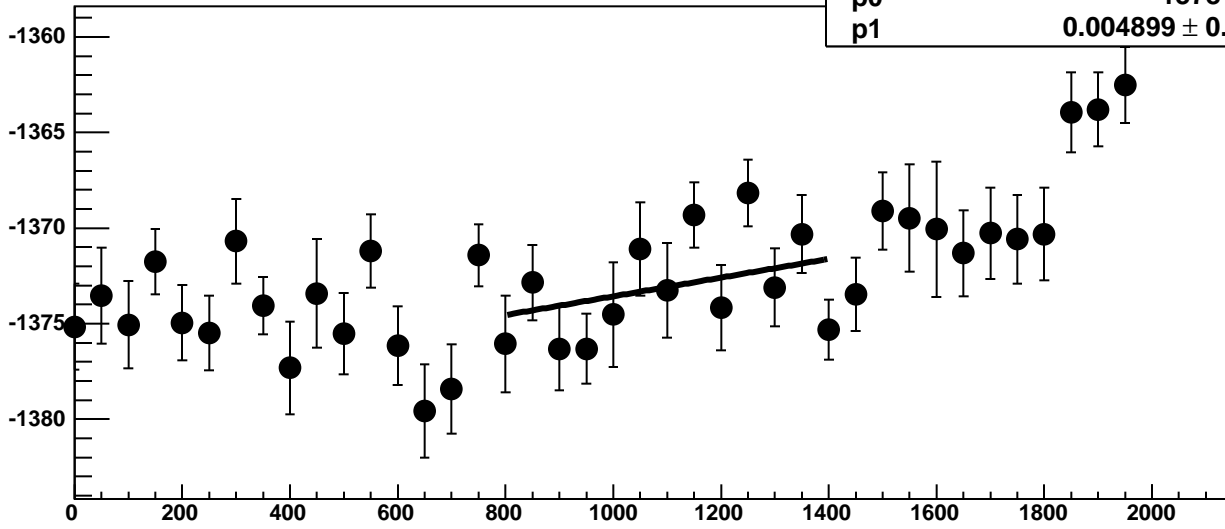
Chip 7, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC



Chip 7, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC

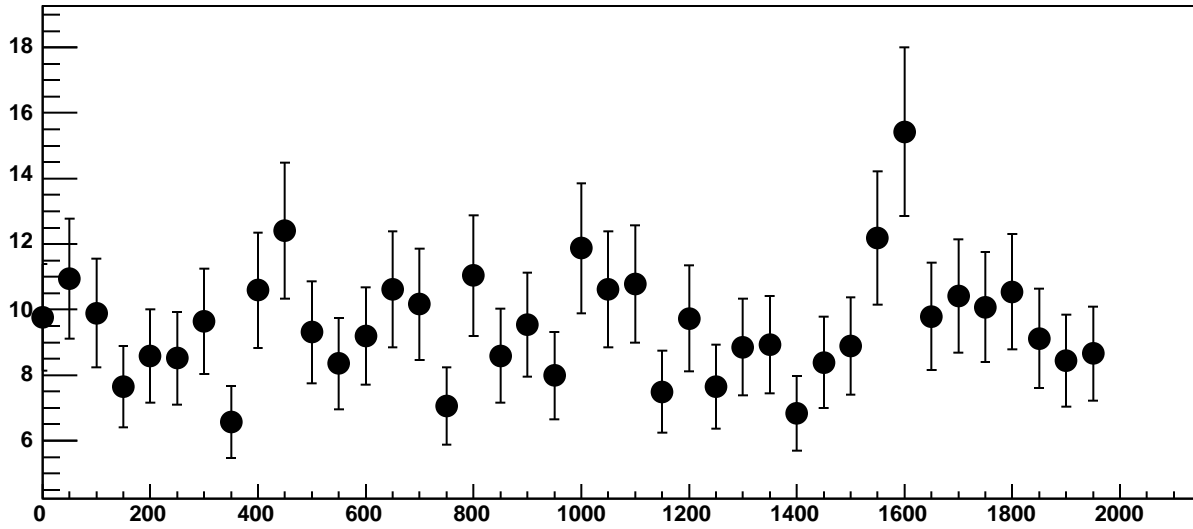


Chip 7, Channel 17, Enable 2, Hold=30, ADC Mean vs DAC

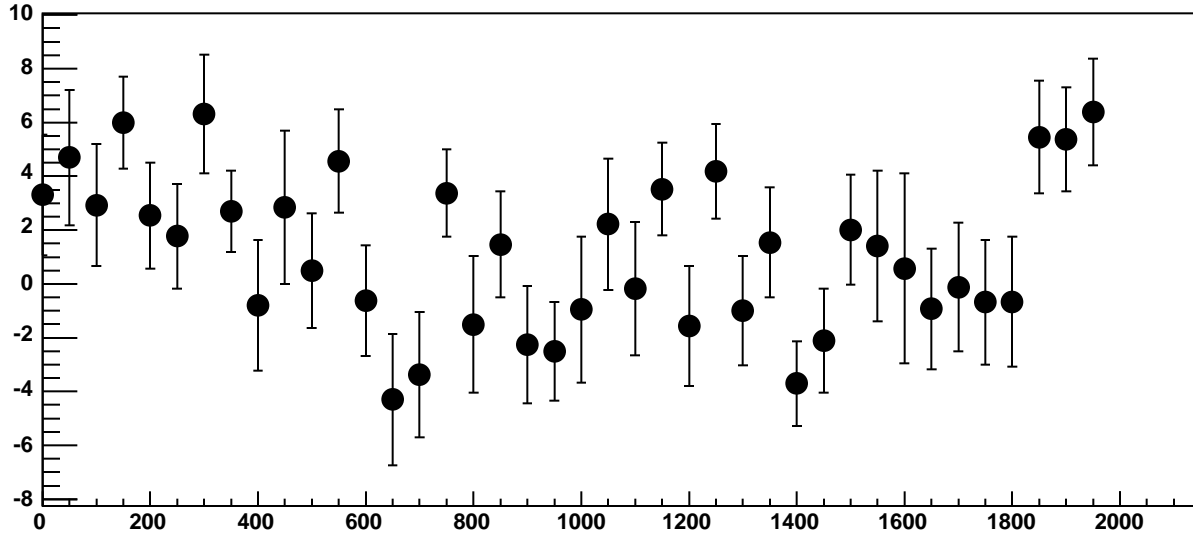


$\chi^2 / \text{ndf}$  21.55 / 11  
p0  $-1378 \pm 3.378$   
p1  $0.004899 \pm 0.002945$

Chip 7, Channel 17, Enable 2, Hold=30, ADC Noise vs DAC

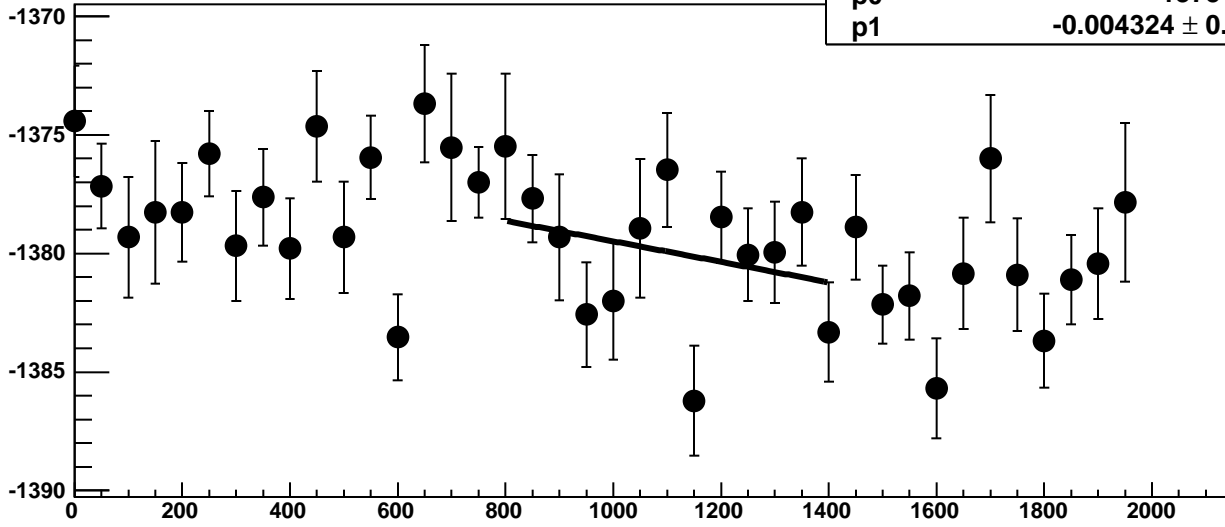


Chip 7, Channel 17, Enable 2, Hold=30, ADC Residuals vs DAC

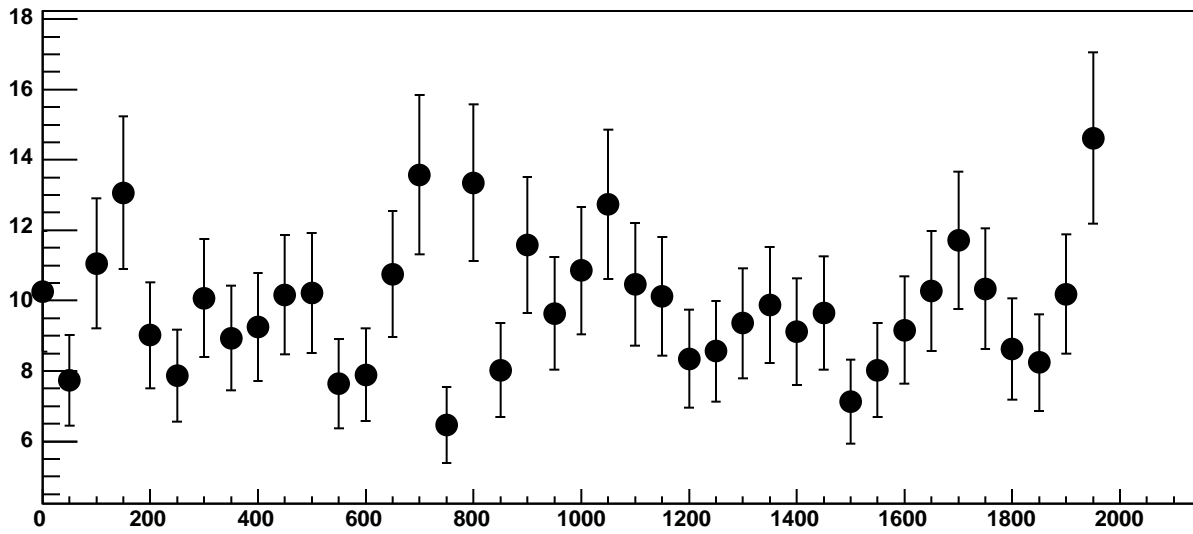


Chip 7, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC

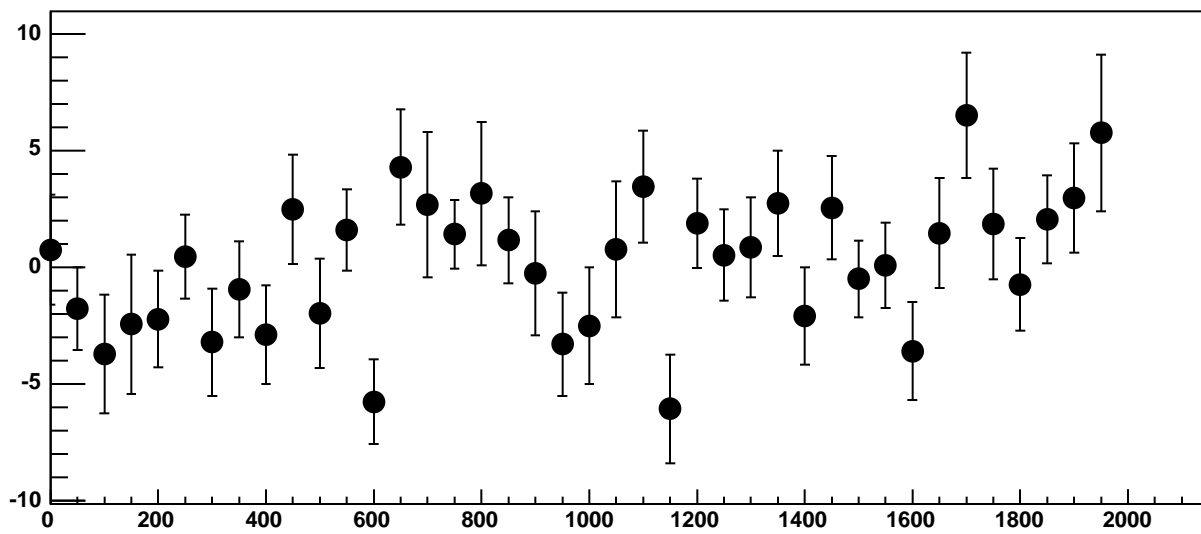
$\chi^2 / \text{ndf}$  17.34 / 11  
p0  $-1375 \pm 3.788$   
p1  $-0.004324 \pm 0.003338$



Chip 7, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC

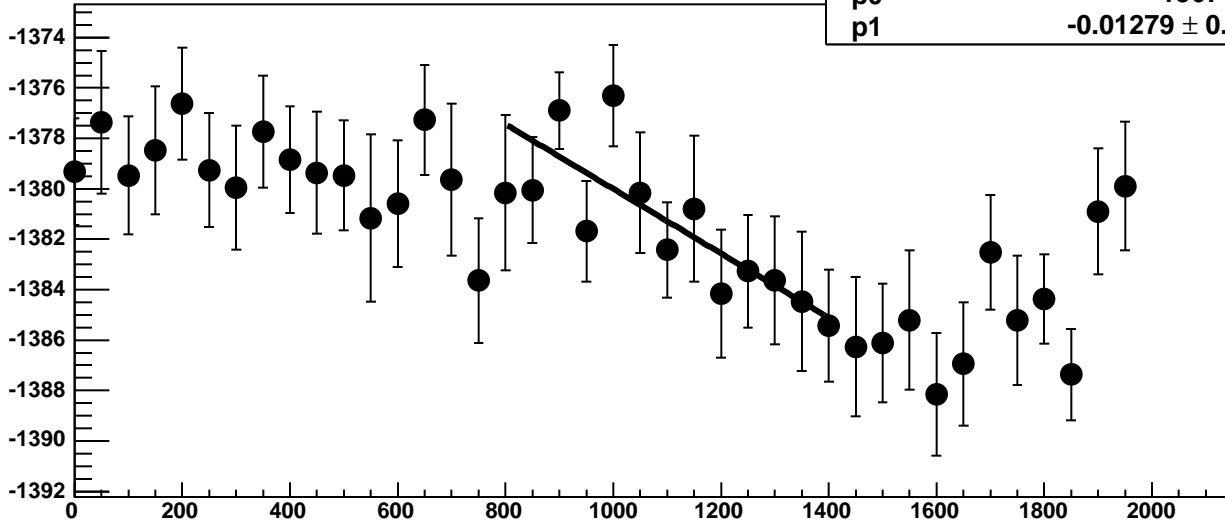


Chip 7, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC

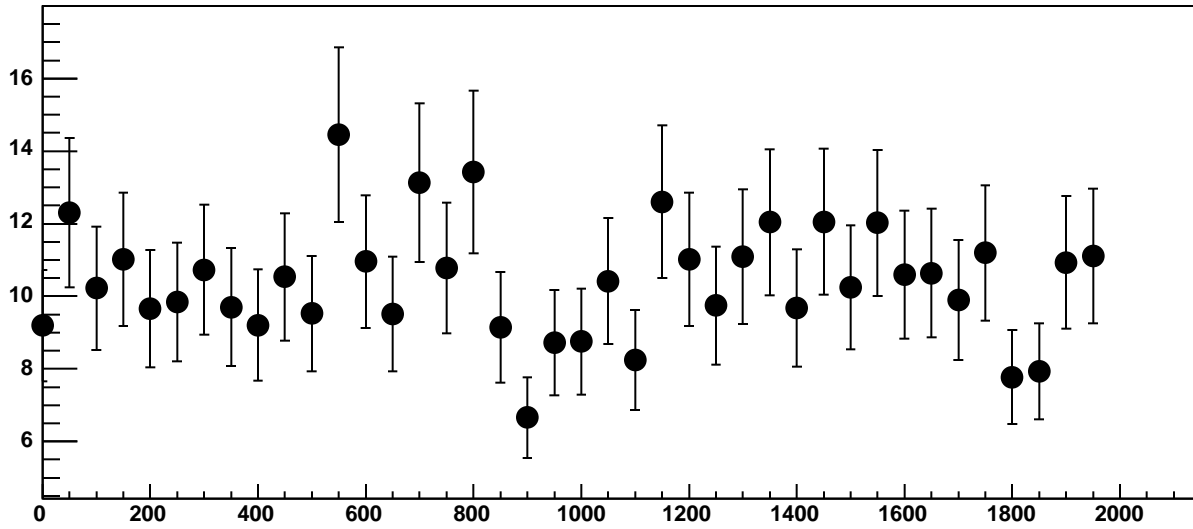


Chip 7, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC

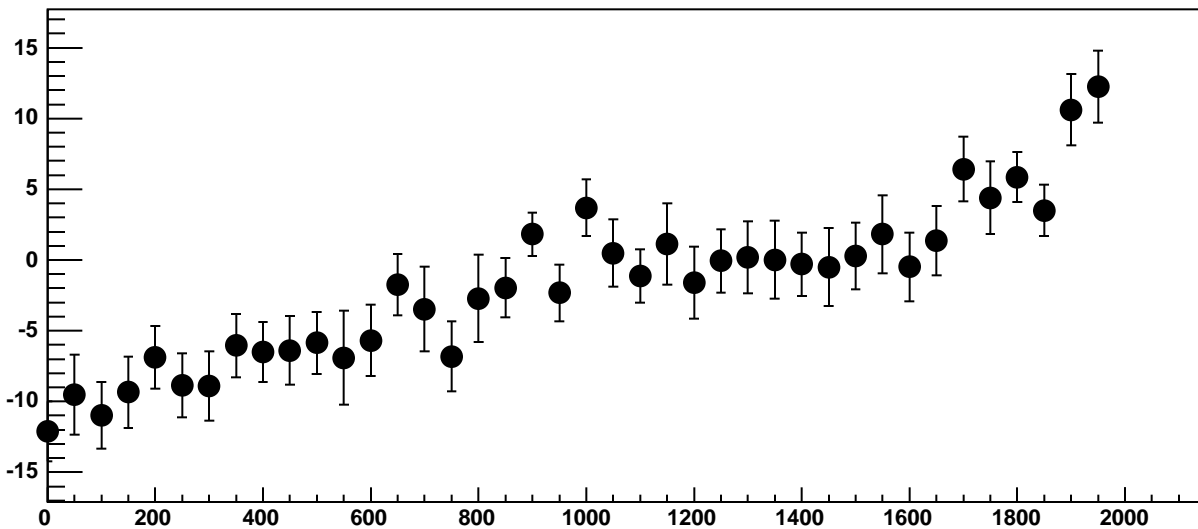
$\chi^2 / \text{ndf}$  8.784 / 11  
p0  $-1367 \pm 3.702$   
p1  $-0.01279 \pm 0.003399$



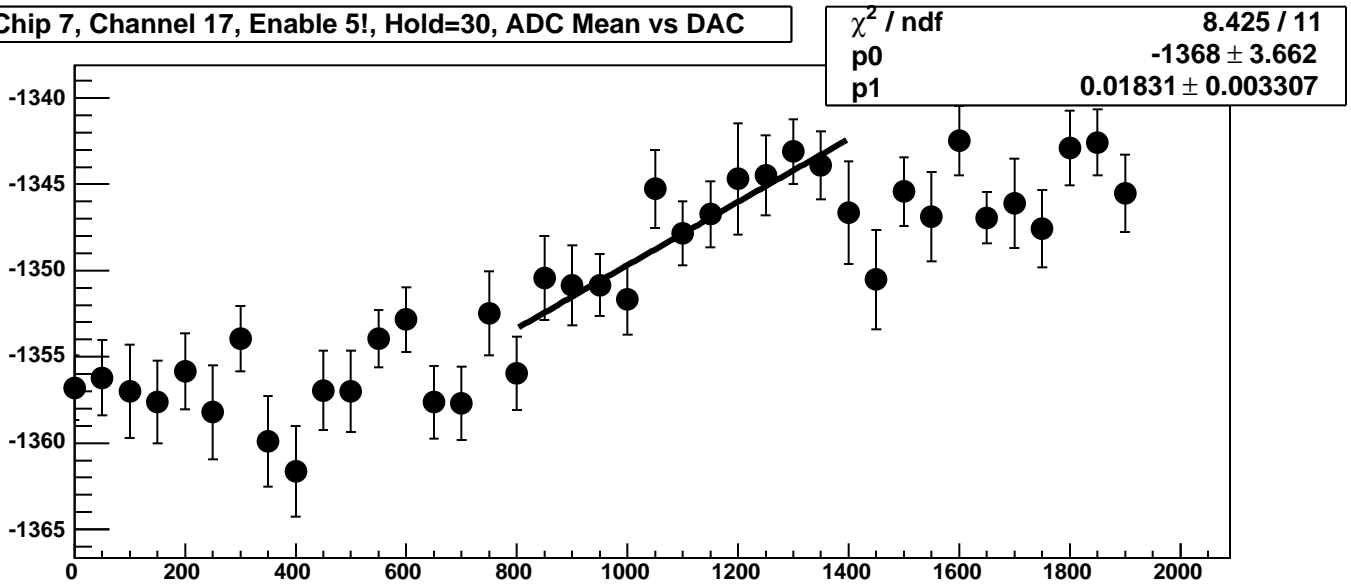
Chip 7, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC



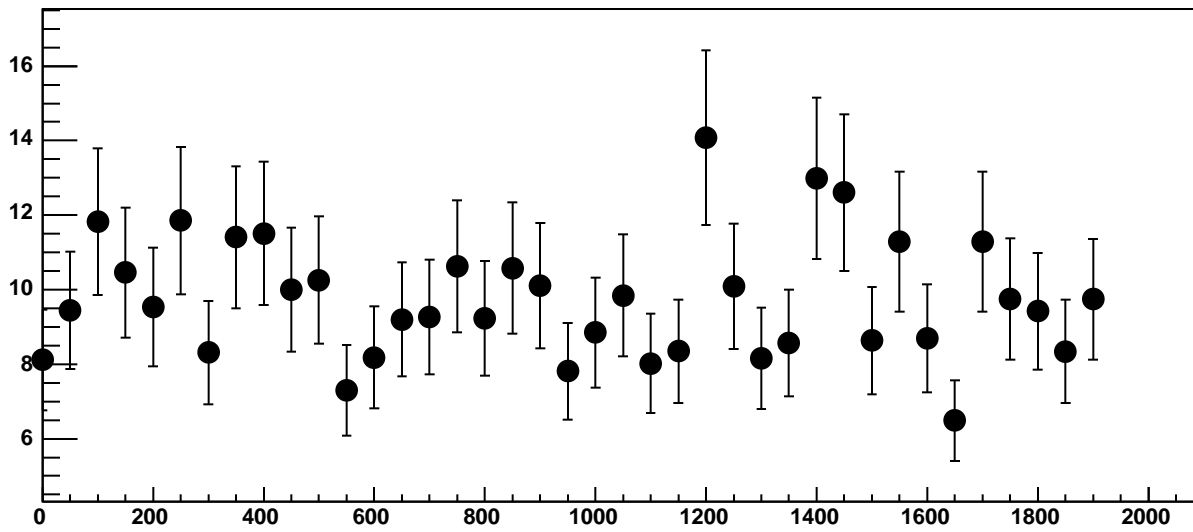
Chip 7, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC



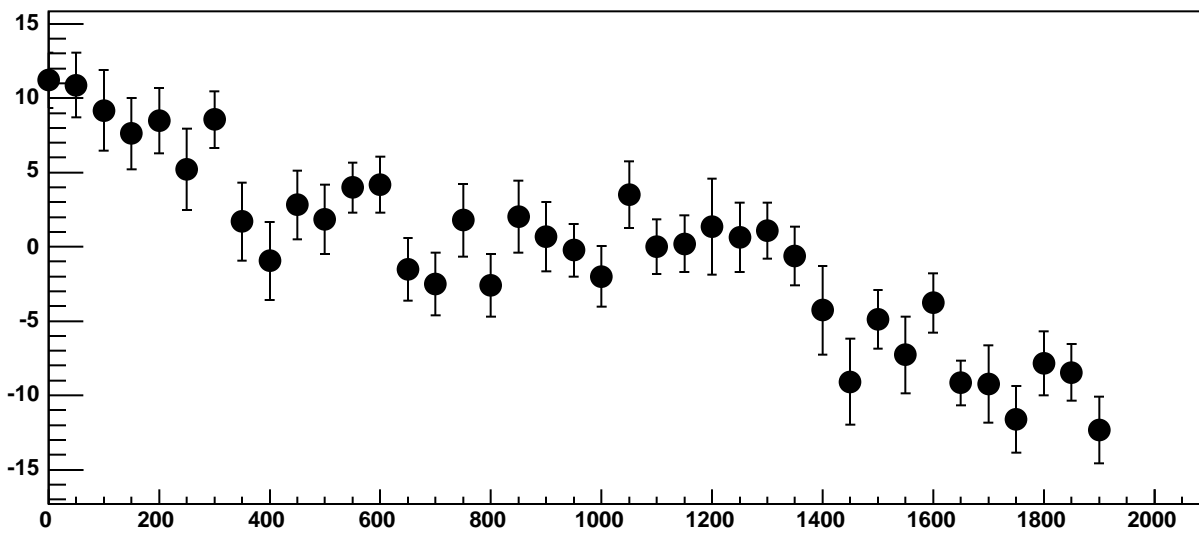
Chip 7, Channel 17, Enable 5!, Hold=30, ADC Mean vs DAC



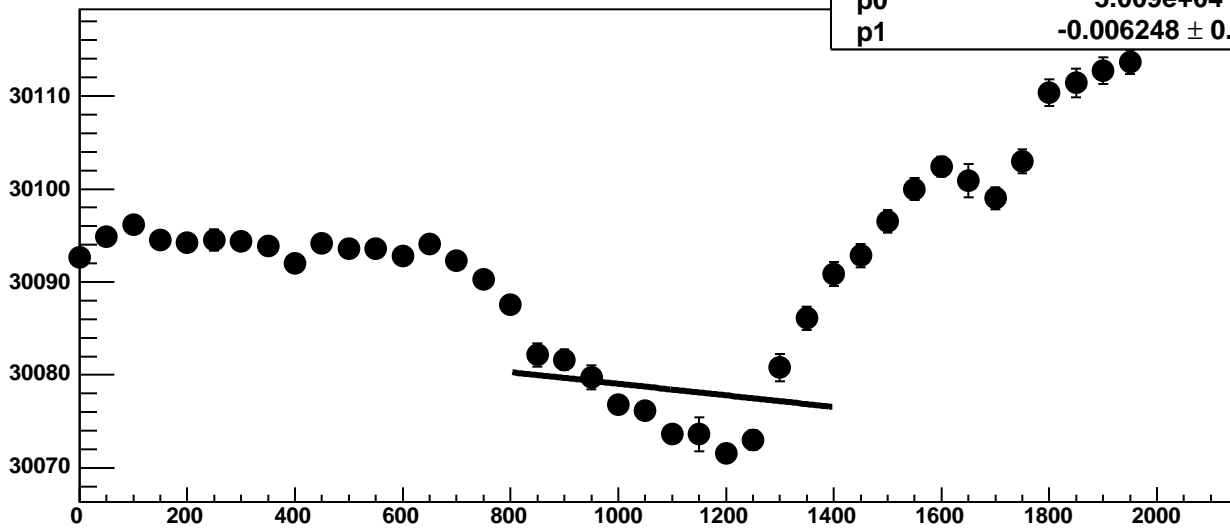
Chip 7, Channel 17, Enable 5!, Hold=30, ADC Noise vs DAC



Chip 7, Channel 17, Enable 5!, Hold=30, ADC Residuals vs DAC

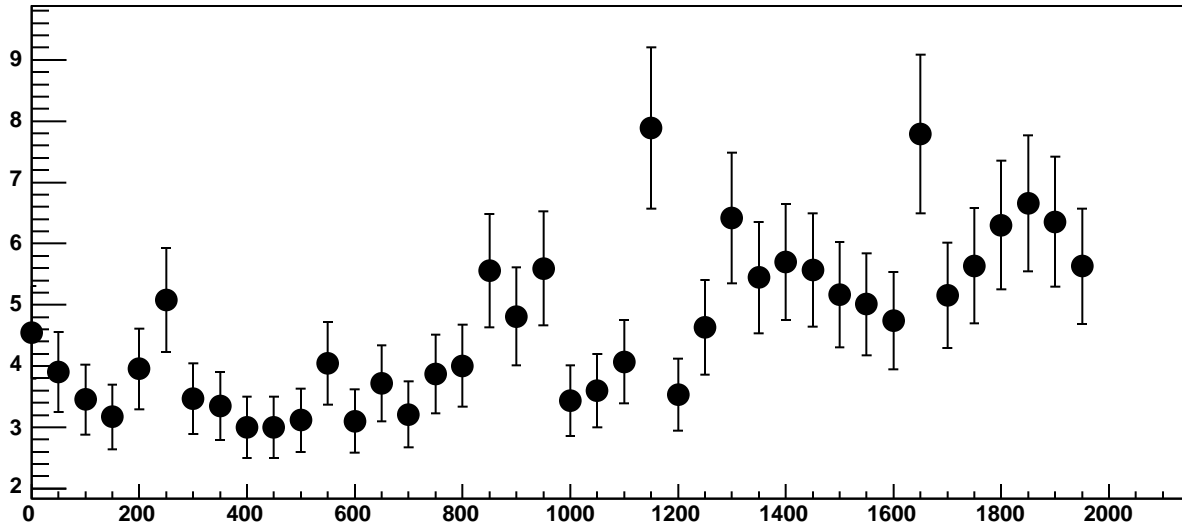


Chip 8, Channel 0, Enable 0!, Hold=30, ADC Mean vs DAC

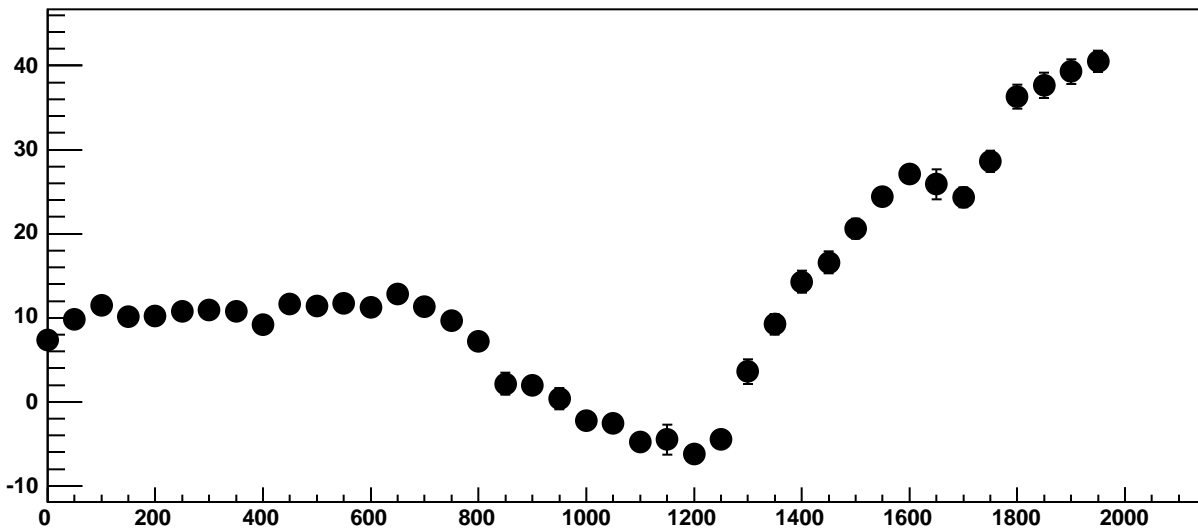


$\chi^2 / \text{ndf}$  374.6 / 11  
p0  $3.009\text{e}+04 \pm 1.838$   
p1  $-0.006248 \pm 0.001685$

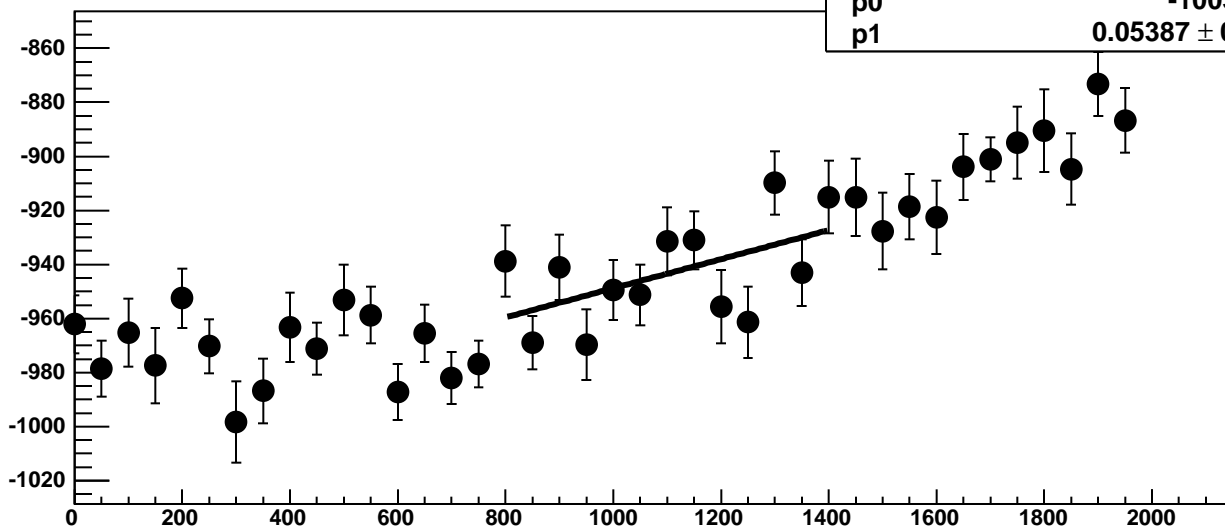
Chip 8, Channel 0, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 8, Channel 0, Enable 0!, Hold=30, ADC Residuals vs DAC

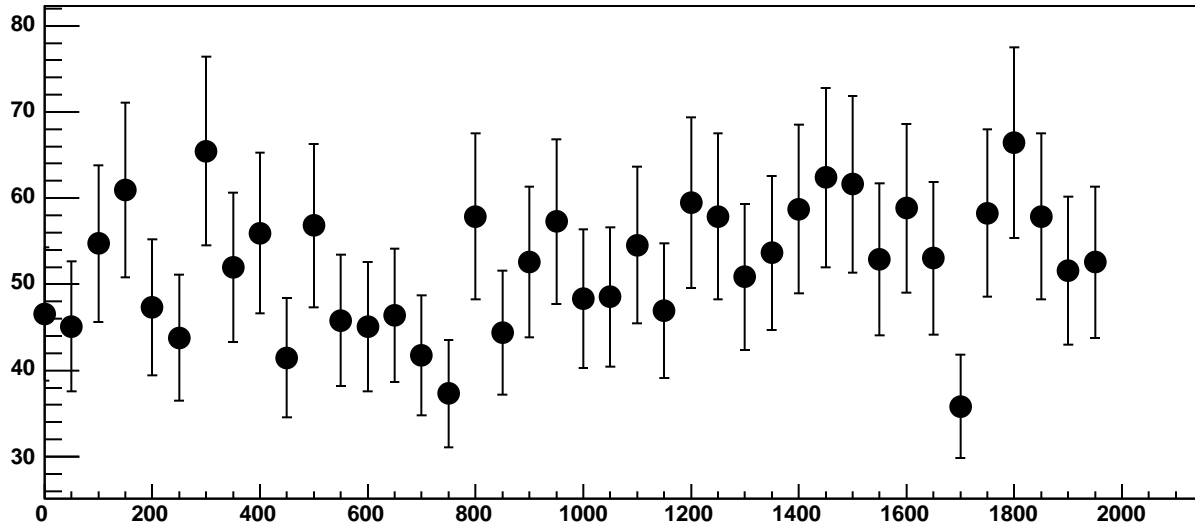


Chip 8, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC

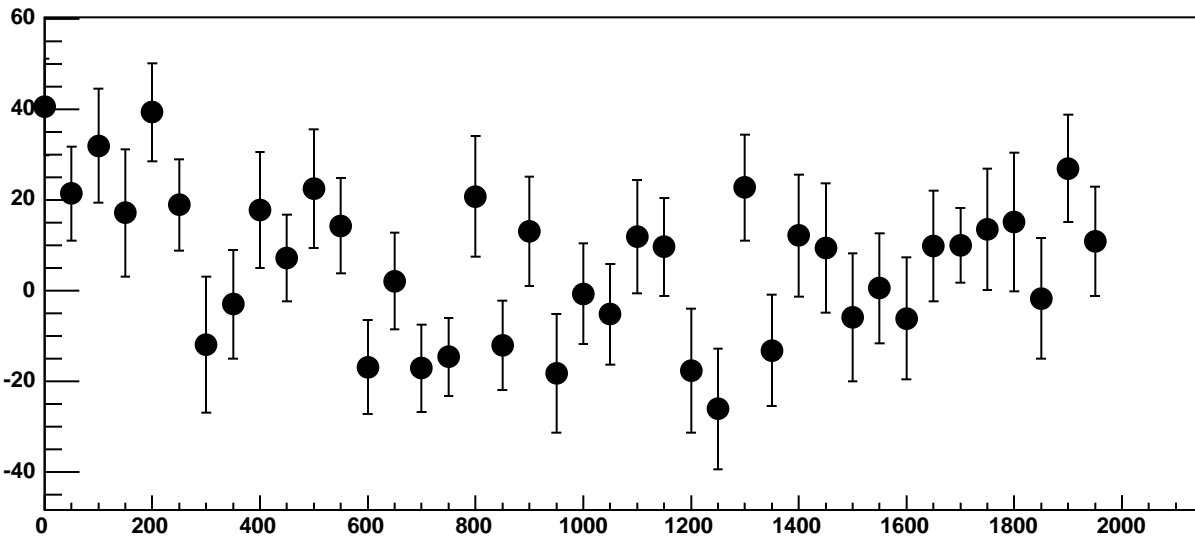


$\chi^2 / \text{ndf}$  20.25 / 11  
p0  $-1003 \pm 20$   
p1  $0.05387 \pm 0.01813$

Chip 8, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC

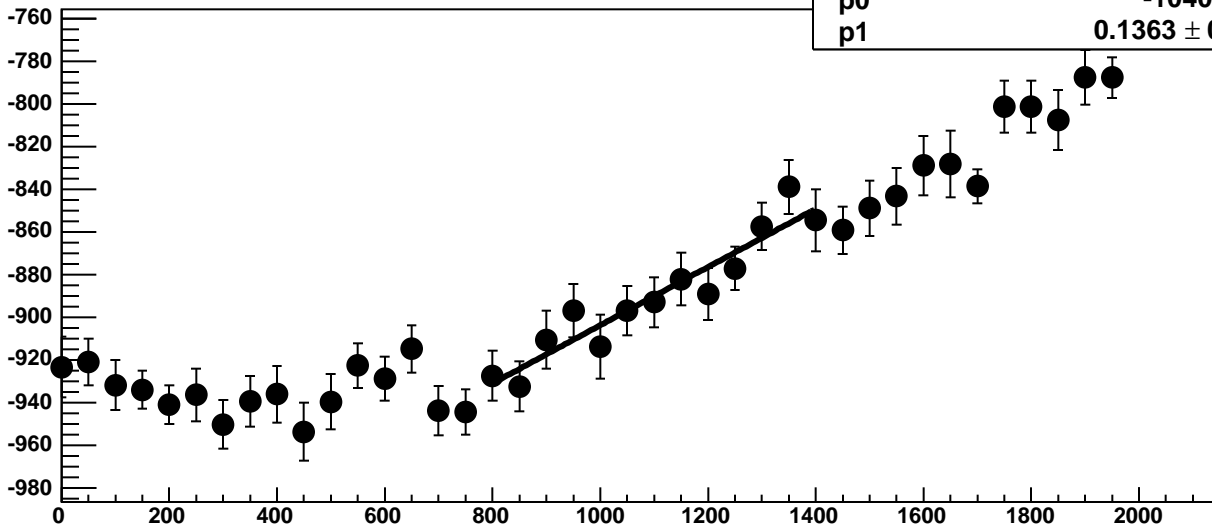


Chip 8, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC





Chip 8, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

6.428 / 11

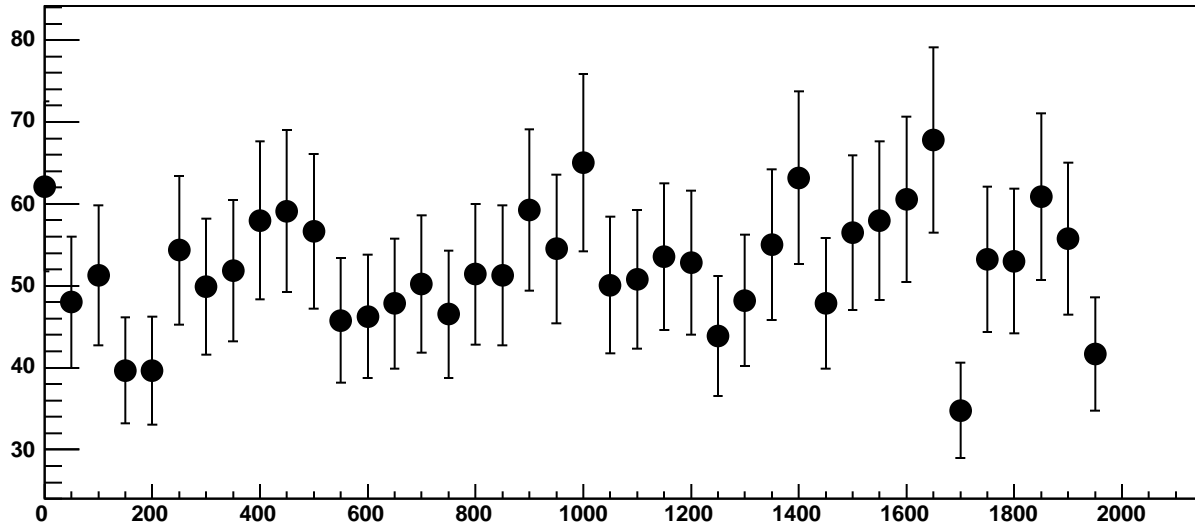
p0

-1040 ± 20.51

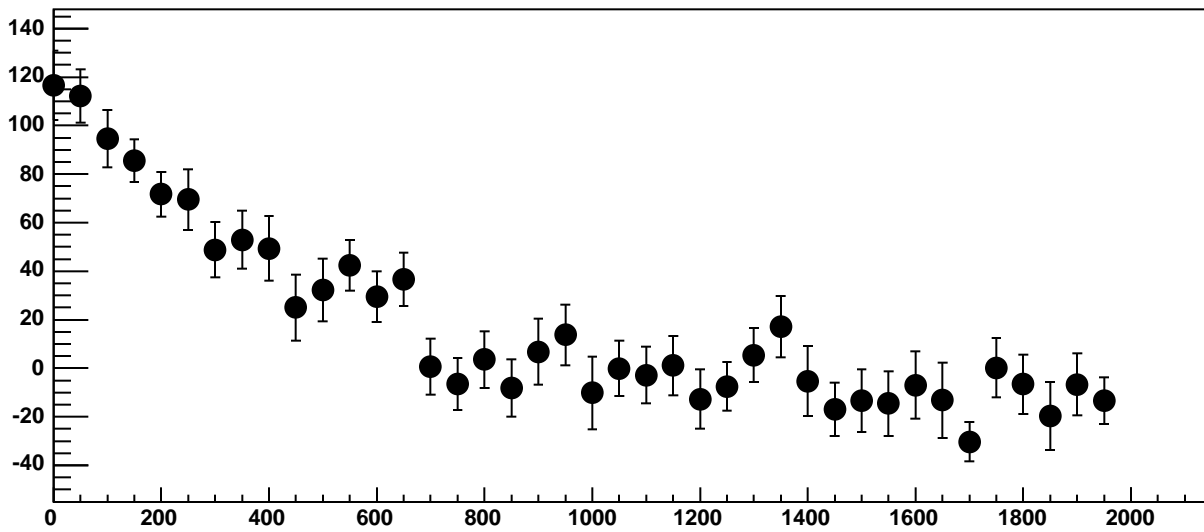
p1

0.1363 ± 0.01833

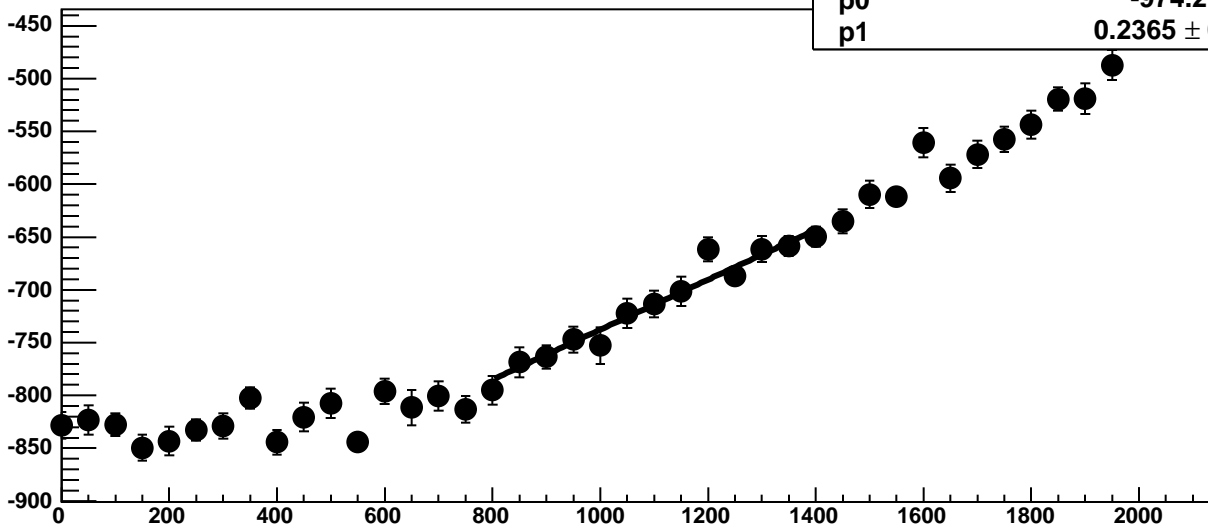
Chip 8, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC



Chip 8, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC

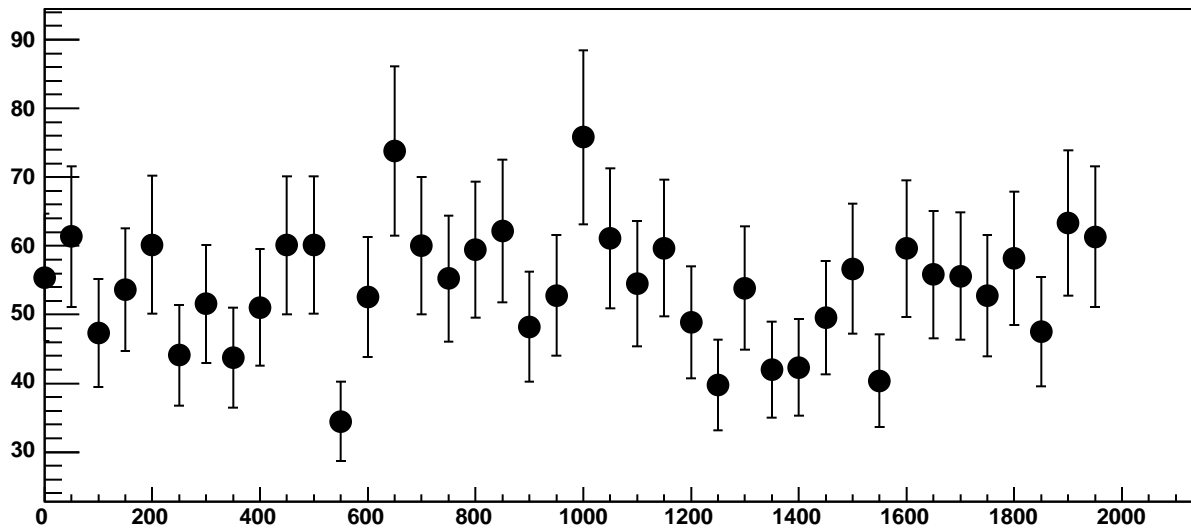


Chip 8, Channel 0, Enable 3, Hold=30, ADC Mean vs DAC

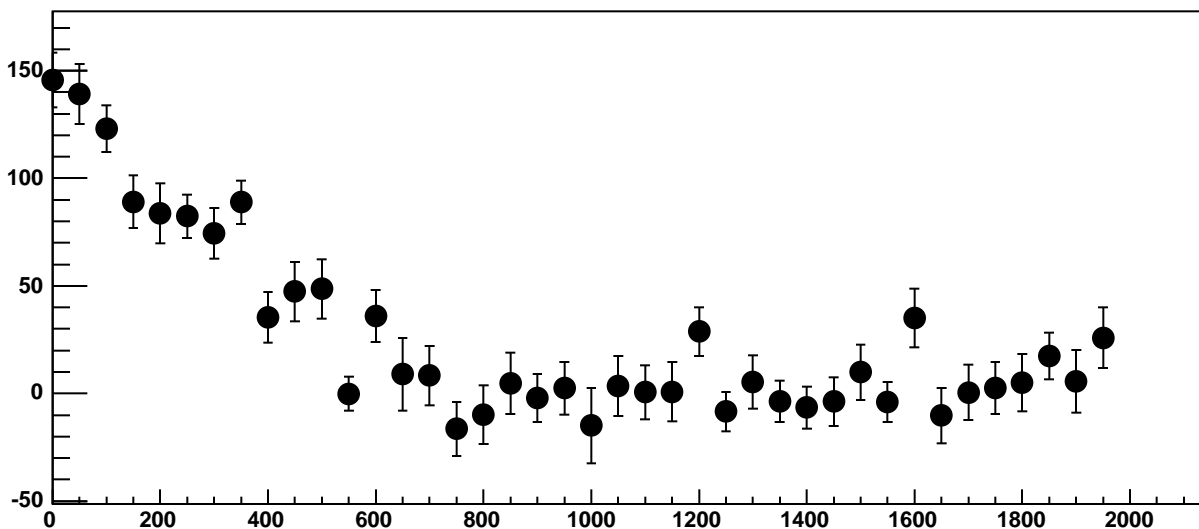


$\chi^2 / \text{ndf}$  9.704 / 11  
p0  $-974.2 \pm 19.86$   
p1  $0.2365 \pm 0.01714$

Chip 8, Channel 0, Enable 3, Hold=30, ADC Noise vs DAC

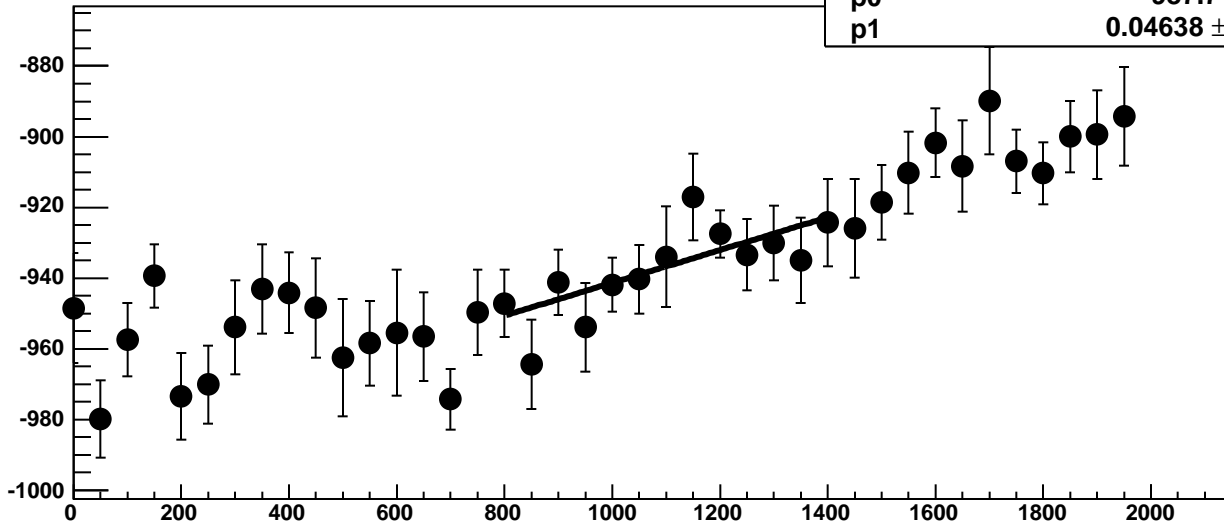


Chip 8, Channel 0, Enable 3, Hold=30, ADC Residuals vs DAC

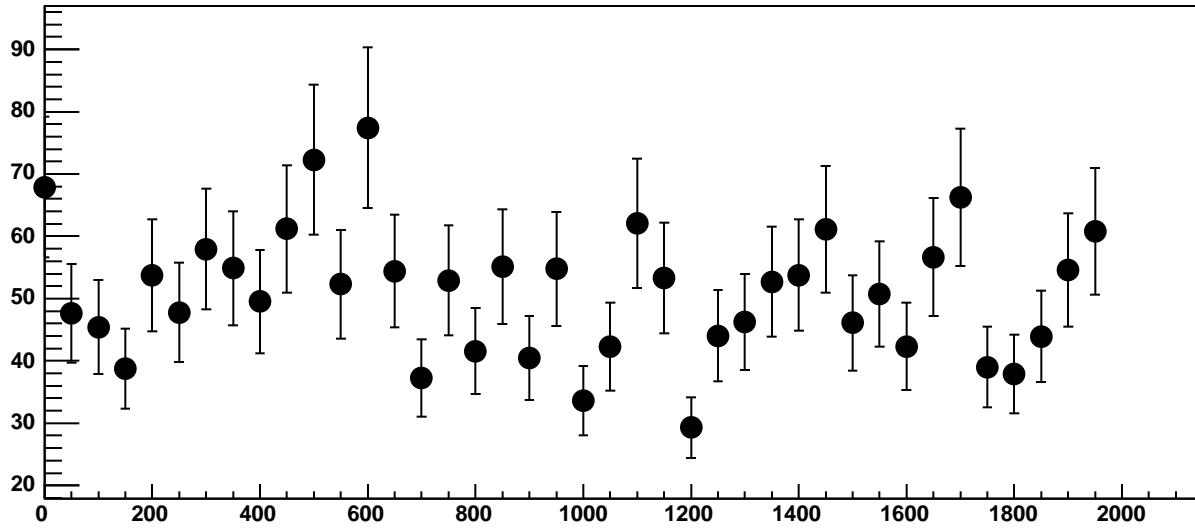


Chip 8, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC

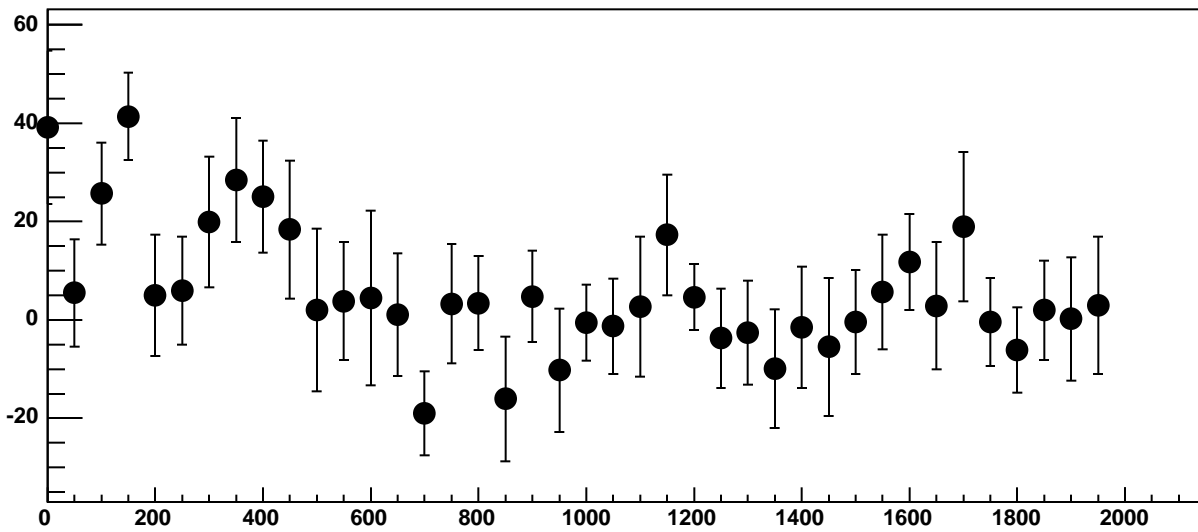
$\chi^2 / \text{ndf}$  6.076 / 11  
p0  $-987.7 \pm 17.49$   
p1  $0.04638 \pm 0.0158$



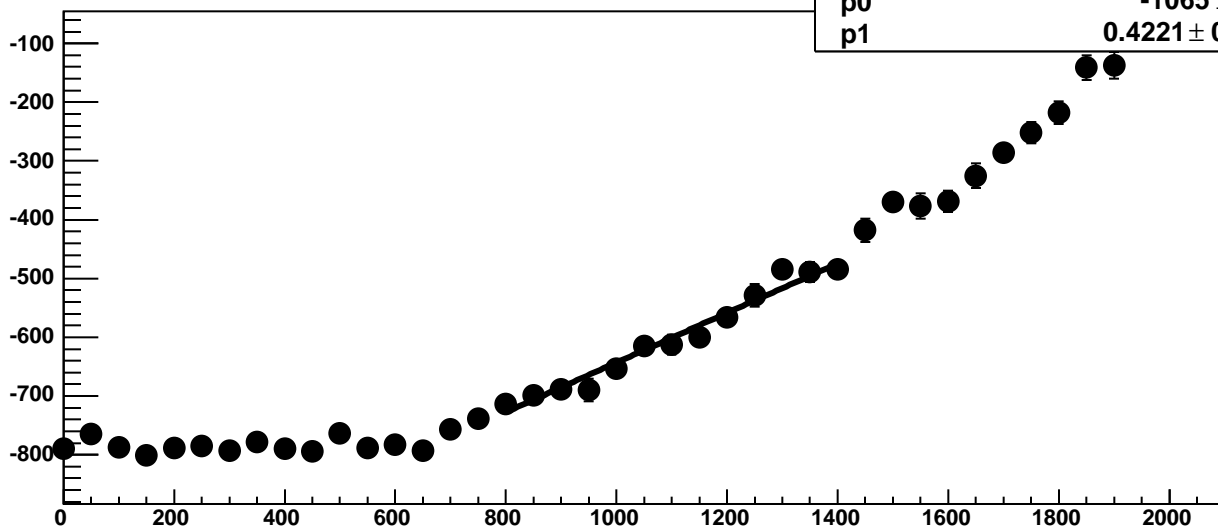
Chip 8, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC



Chip 8, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC

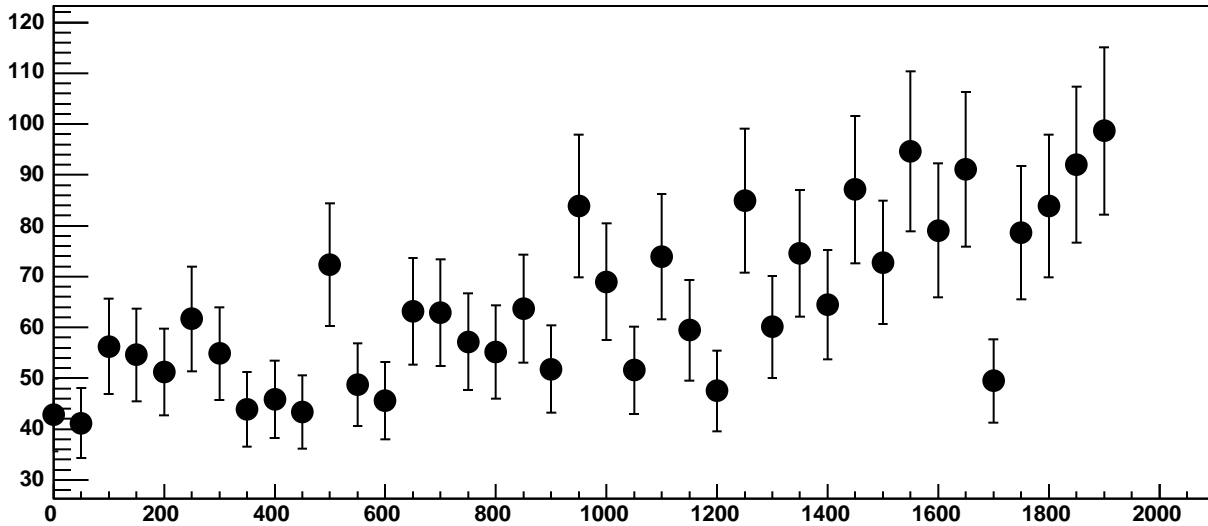


Chip 8, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC

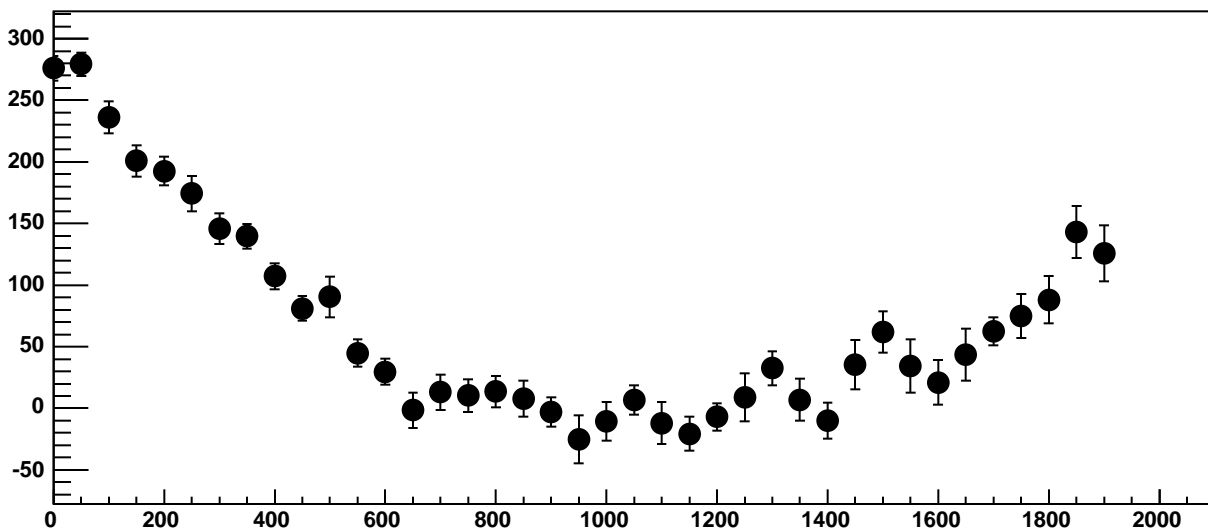


$\chi^2 / \text{ndf}$  13.63 / 11  
p0  $-1065 \pm 23.33$   
p1  $0.4221 \pm 0.02115$

Chip 8, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC

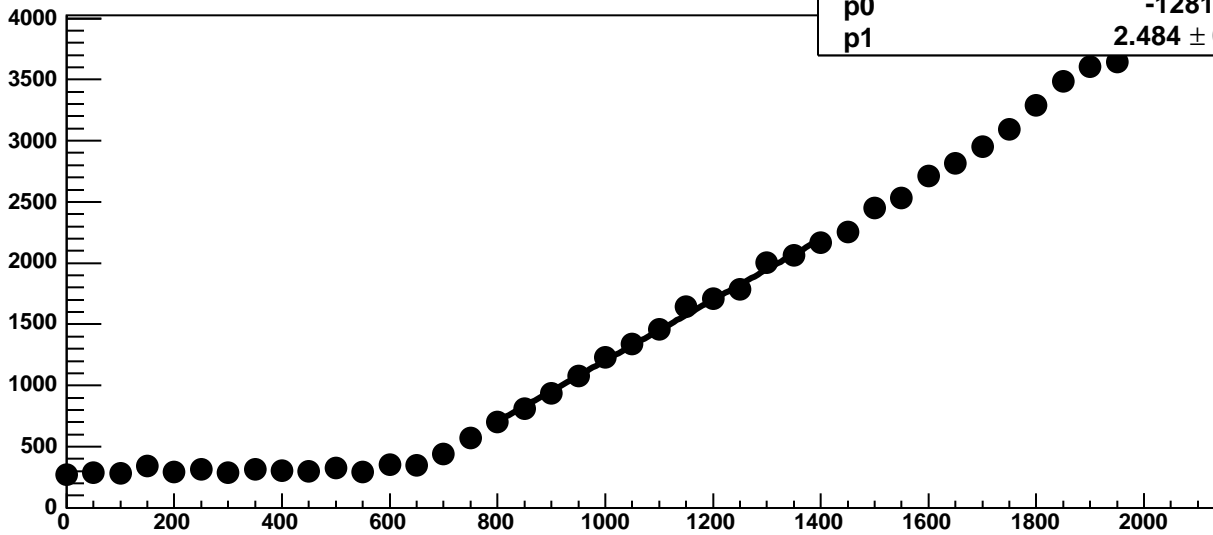


Chip 8, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC

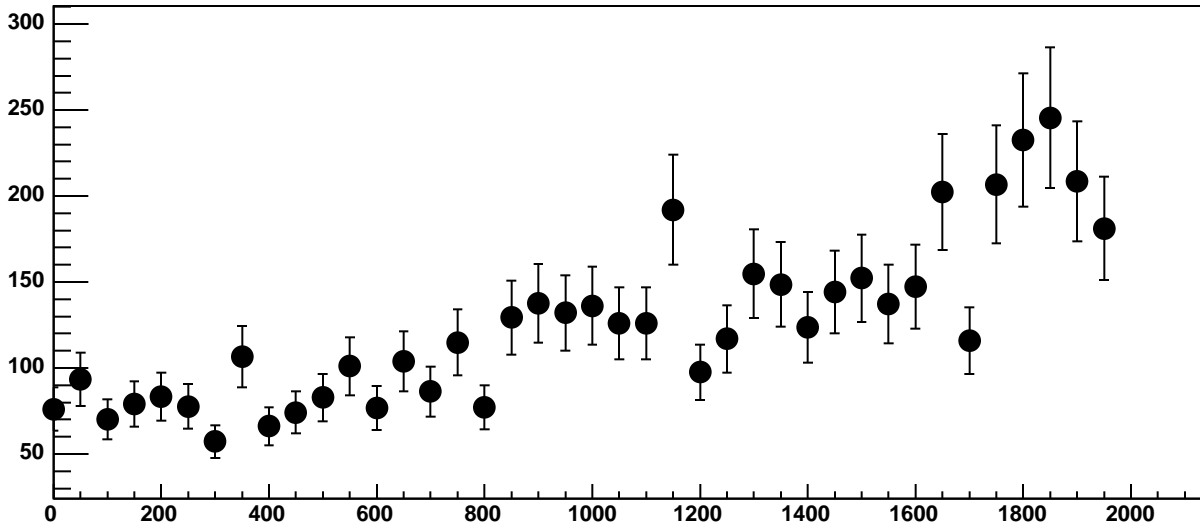


Chip 8, Channel 1, Enable 0, Hold=30, ADC Mean vs DAC

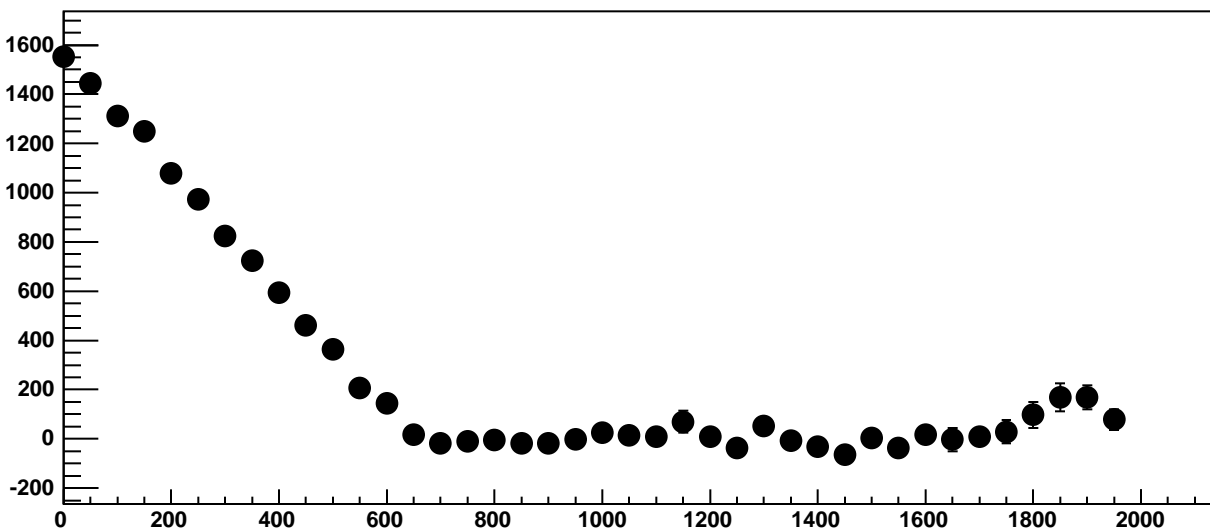
$\chi^2 / \text{ndf}$  10.05 / 11  
p0  $-1281 \pm 41.98$   
p1  $2.484 \pm 0.03875$



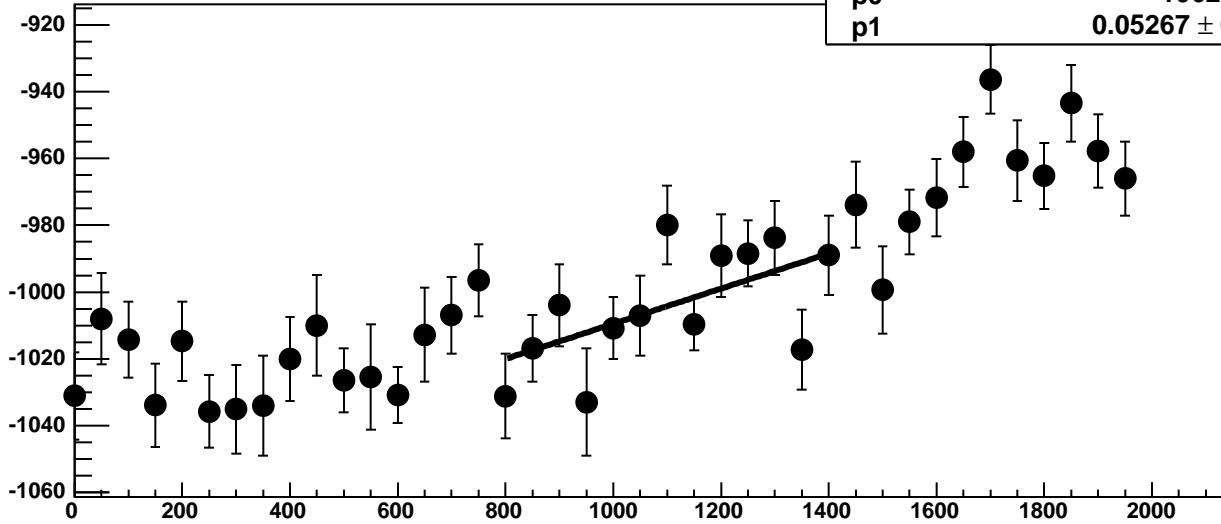
Chip 8, Channel 1, Enable 0, Hold=30, ADC Noise vs DAC



Chip 8, Channel 1, Enable 0, Hold=30, ADC Residuals vs DAC

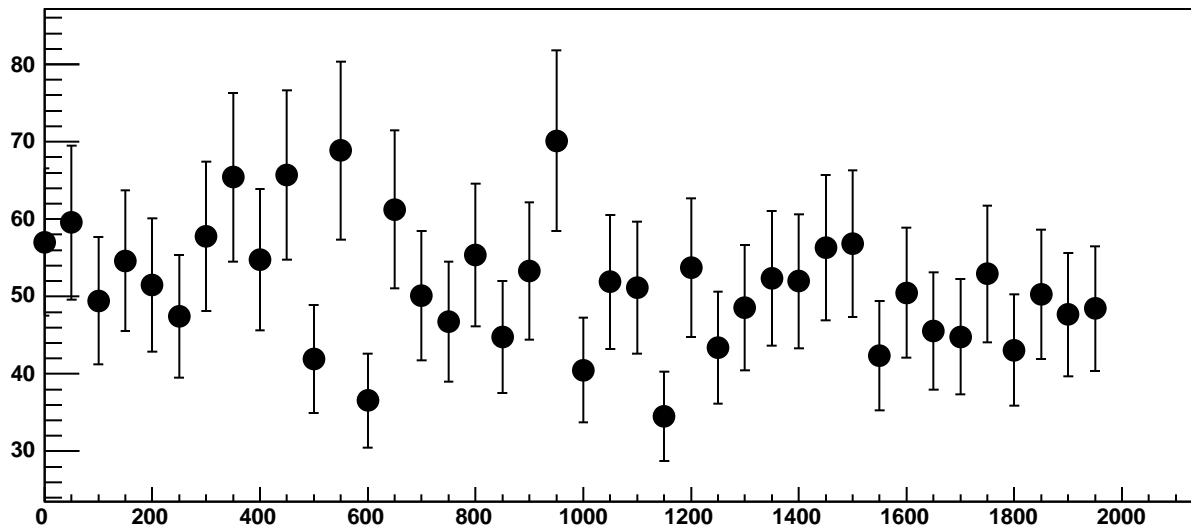


Chip 8, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC

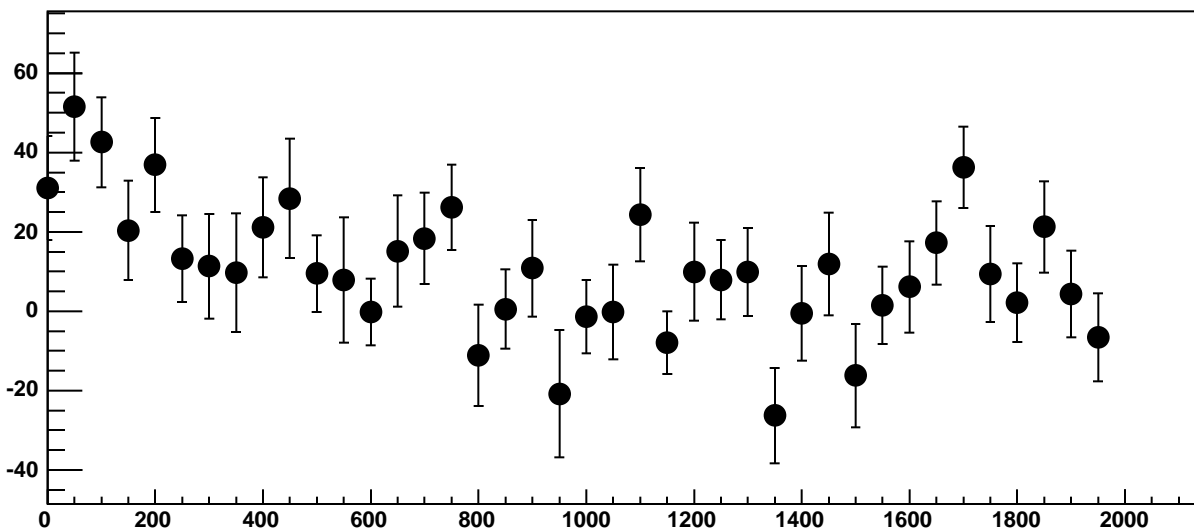


$\chi^2 / \text{ndf}$  15.4 / 11  
p0  $-1062 \pm 19.24$   
p1  $0.05267 \pm 0.01718$

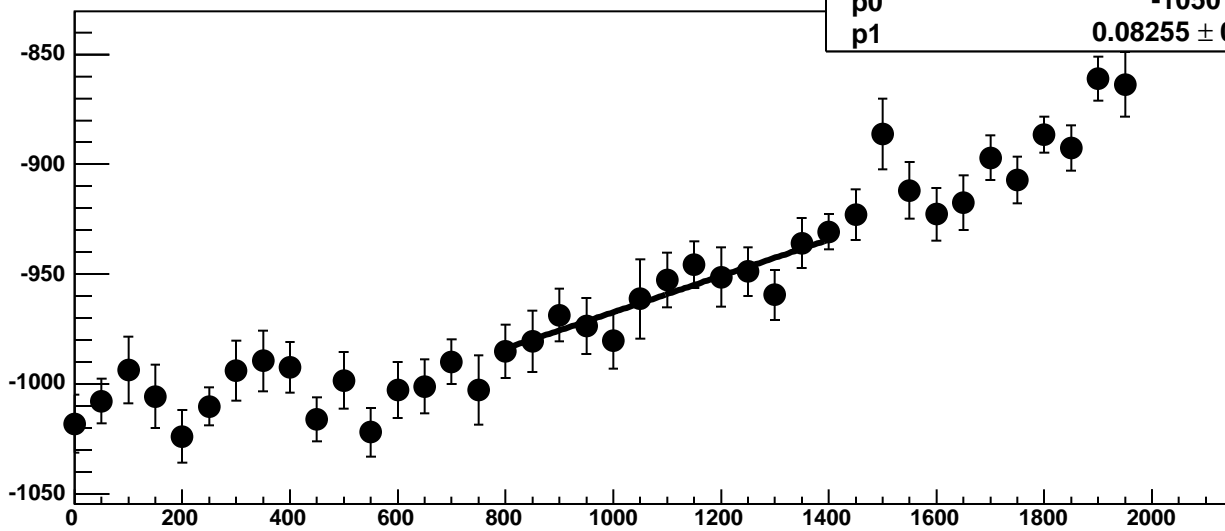
Chip 8, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC



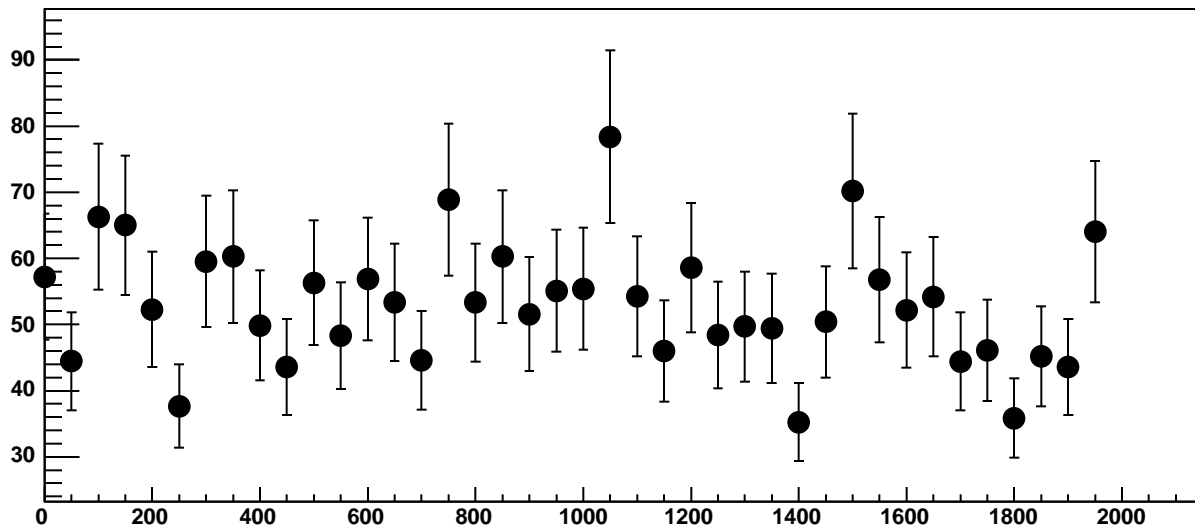
Chip 8, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC



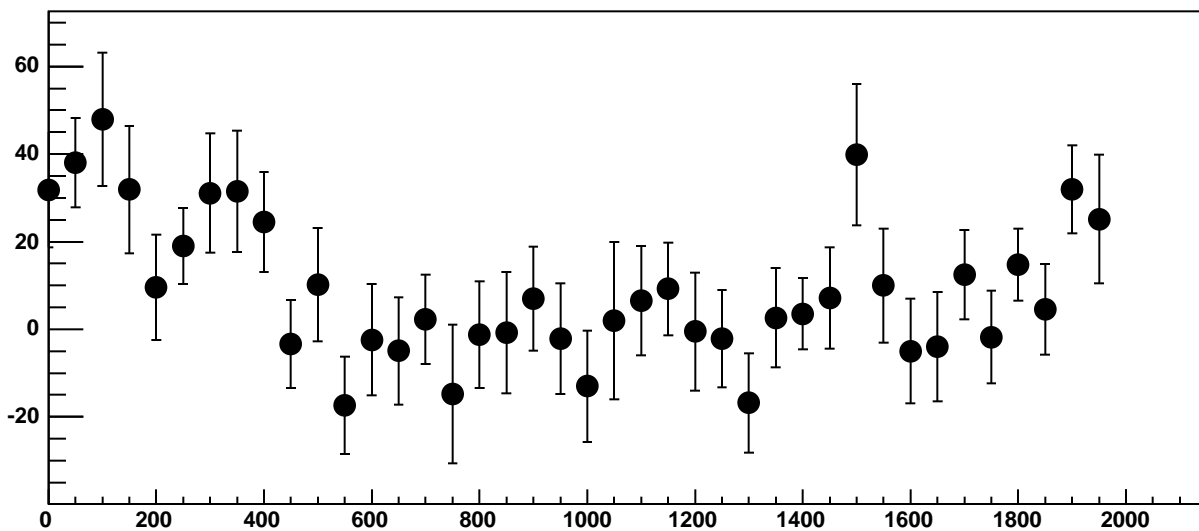
Chip 8, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC



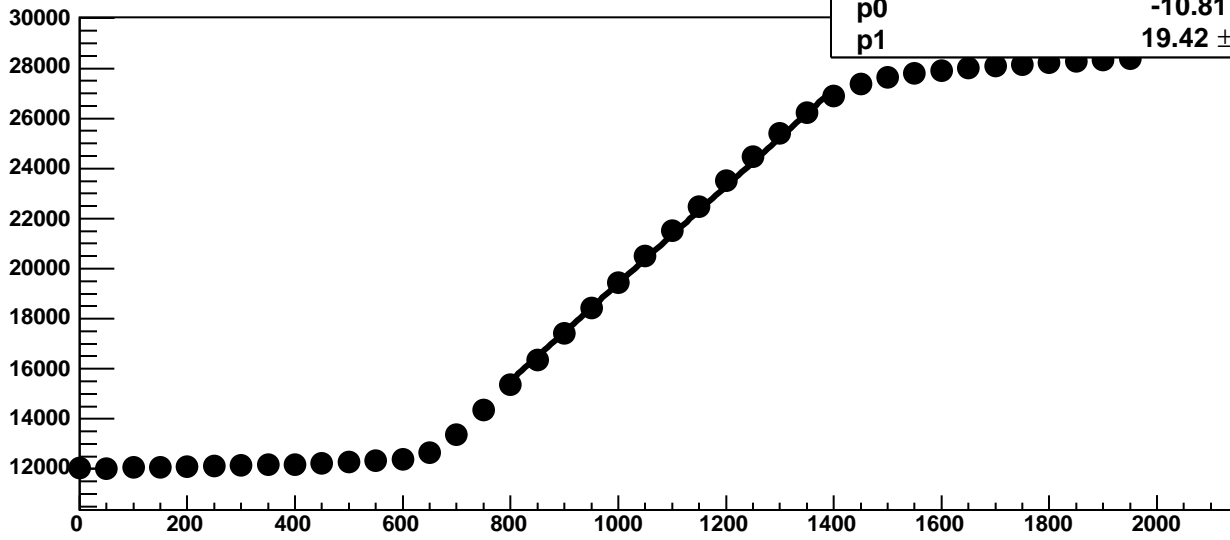
Chip 8, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC



Chip 8, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC

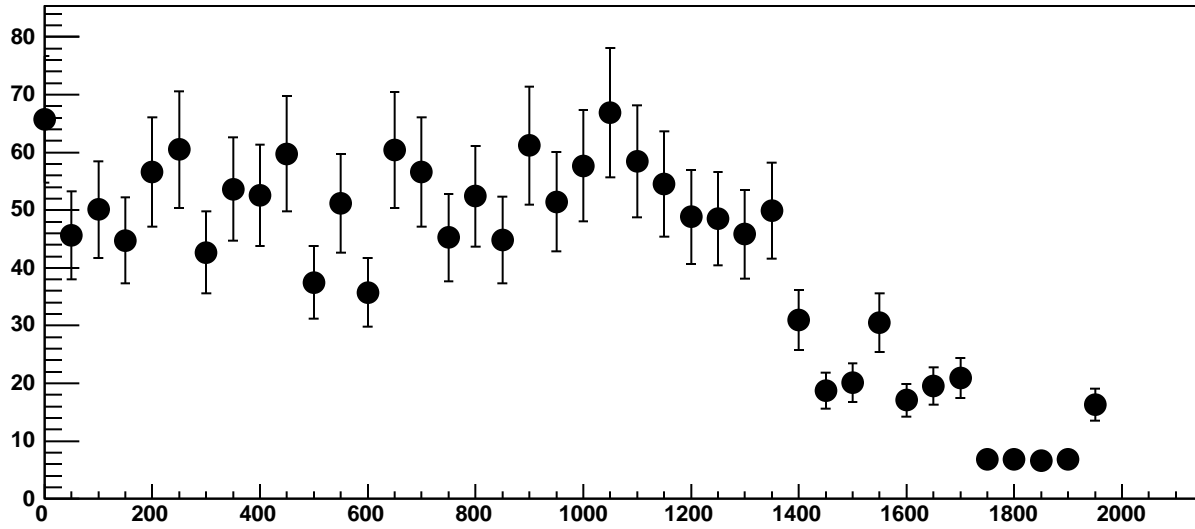


Chip 8, Channel 1, Enable 3!, Hold=30, ADC Mean vs DAC

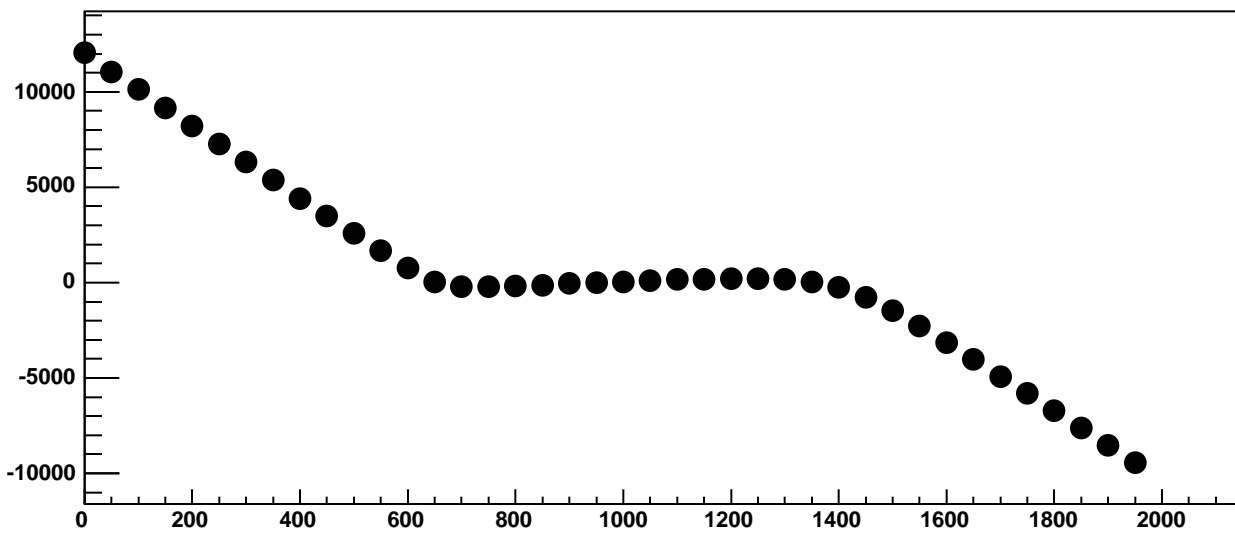


$\chi^2 / \text{ndf}$  3224 / 11  
p0  $-10.81 \pm 17.68$   
p1  $19.42 \pm 0.0152$

Chip 8, Channel 1, Enable 3!, Hold=30, ADC Noise vs DAC

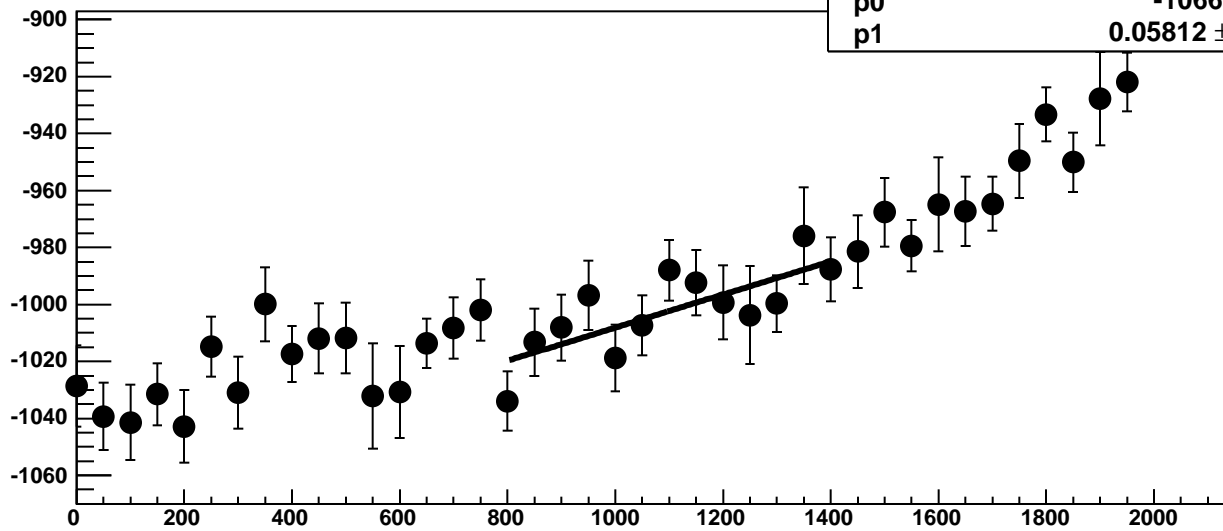


Chip 8, Channel 1, Enable 3!, Hold=30, ADC Residuals vs DAC





Chip 8, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

8.388 / 11

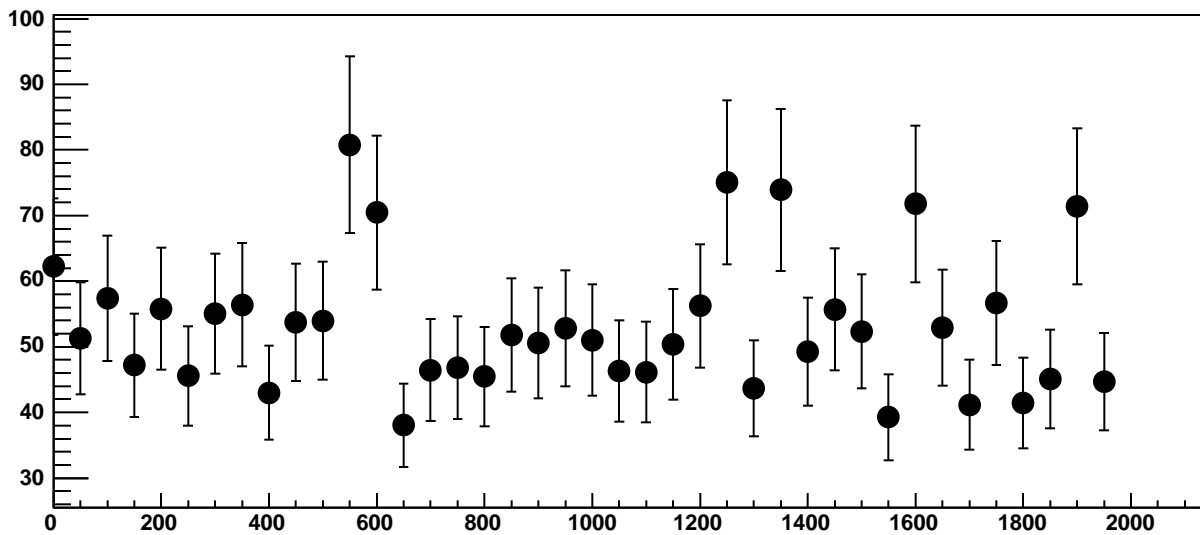
p0

$-1066 \pm 19.25$

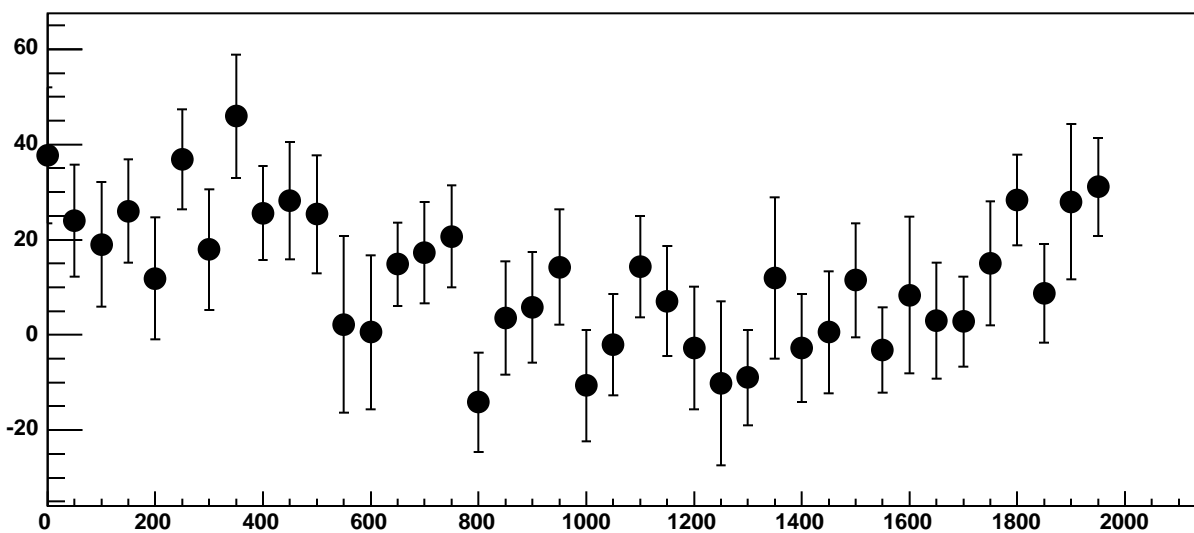
p1

$0.05812 \pm 0.0175$

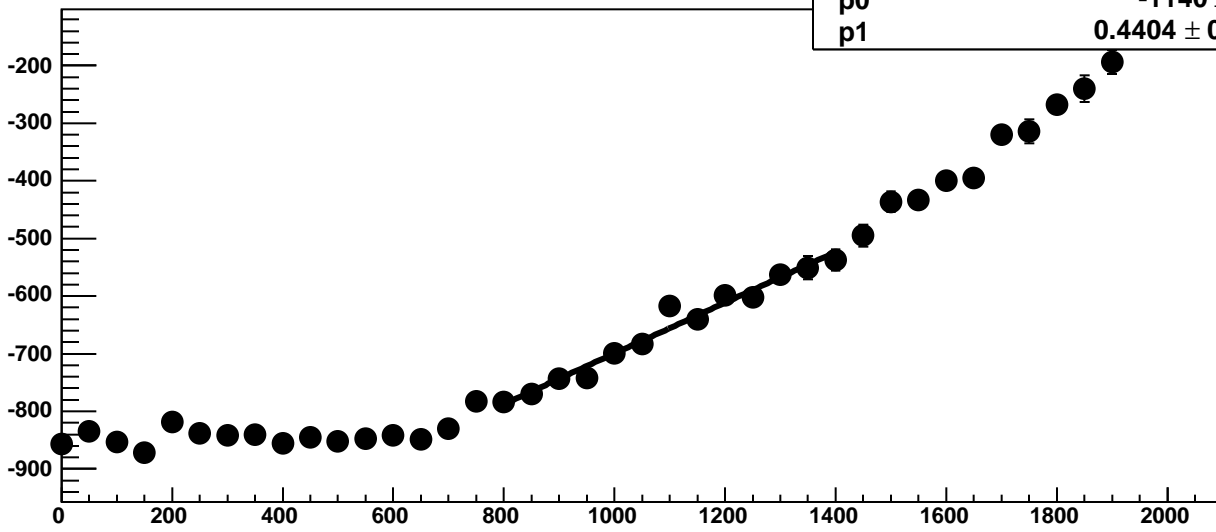
Chip 8, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC



Chip 8, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC

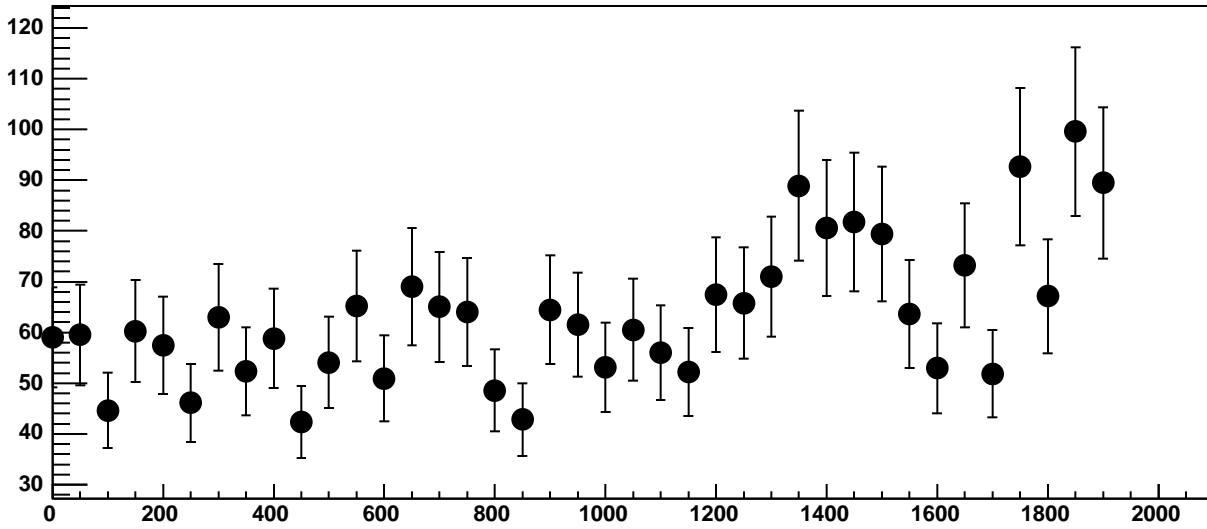


Chip 8, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC

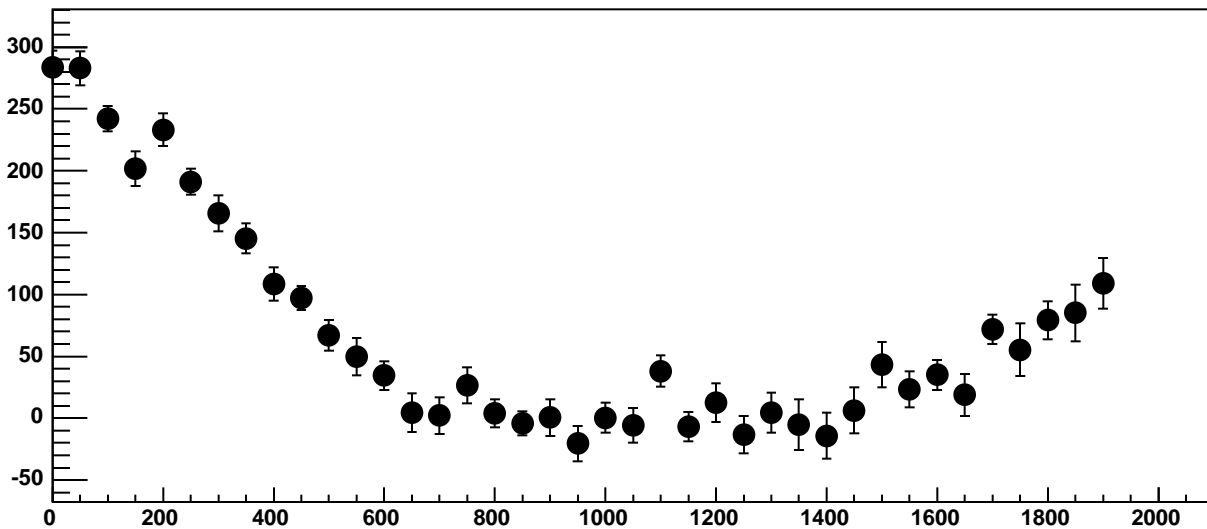


$\chi^2 / \text{ndf}$  13.83 / 11  
p0  $-1140 \pm 22.33$   
p1  $0.4404 \pm 0.02109$

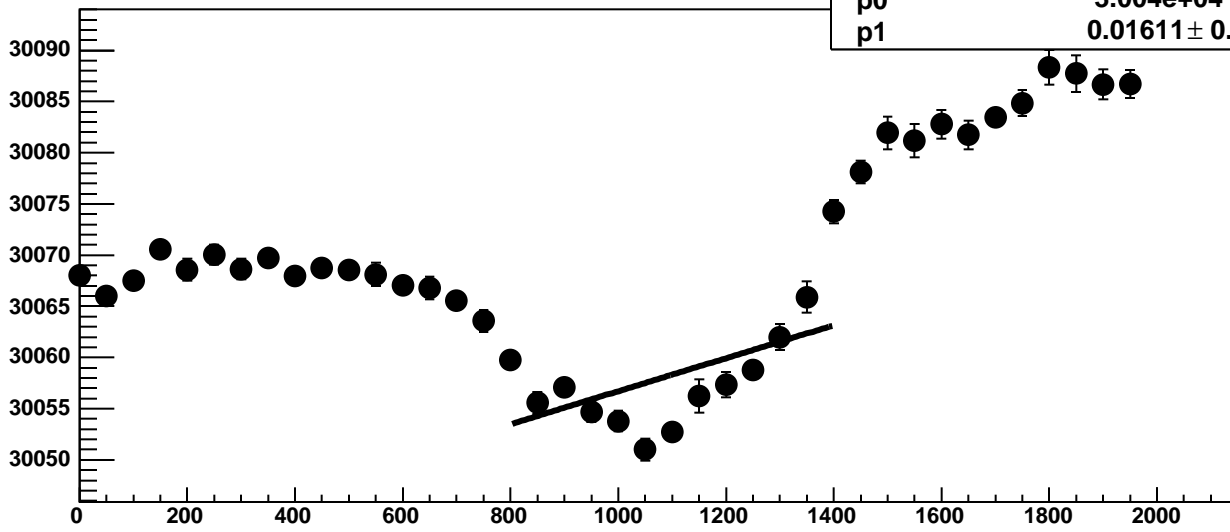
Chip 8, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC



Chip 8, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC

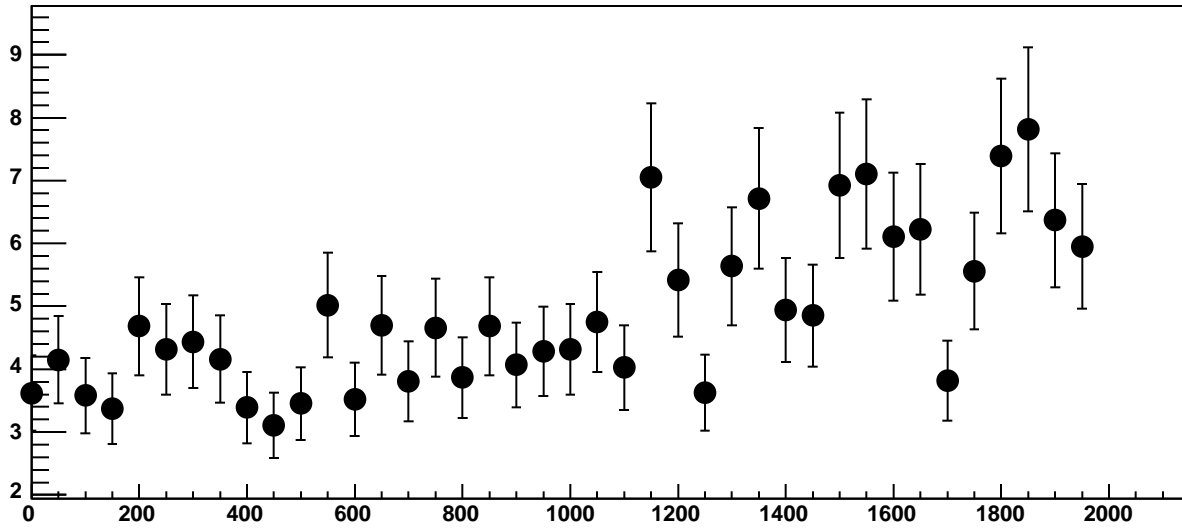


Chip 8, Channel 2, Enable 0!, Hold=30, ADC Mean vs DAC

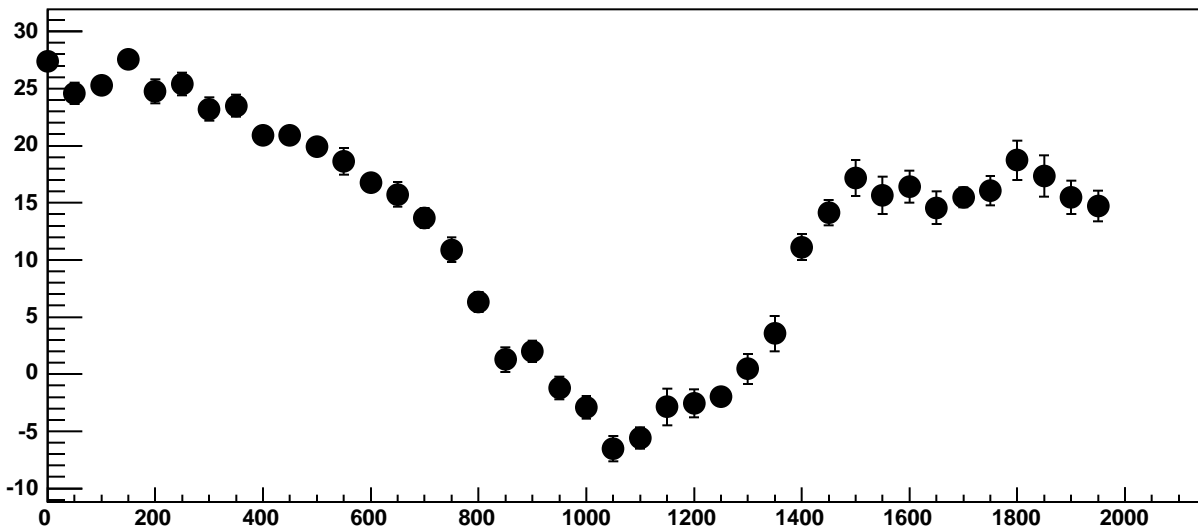


$\chi^2 / \text{ndf}$  253.4 / 11  
p0  $3.004\text{e}+04 \pm 1.717$   
p1  $0.01611 \pm 0.001584$

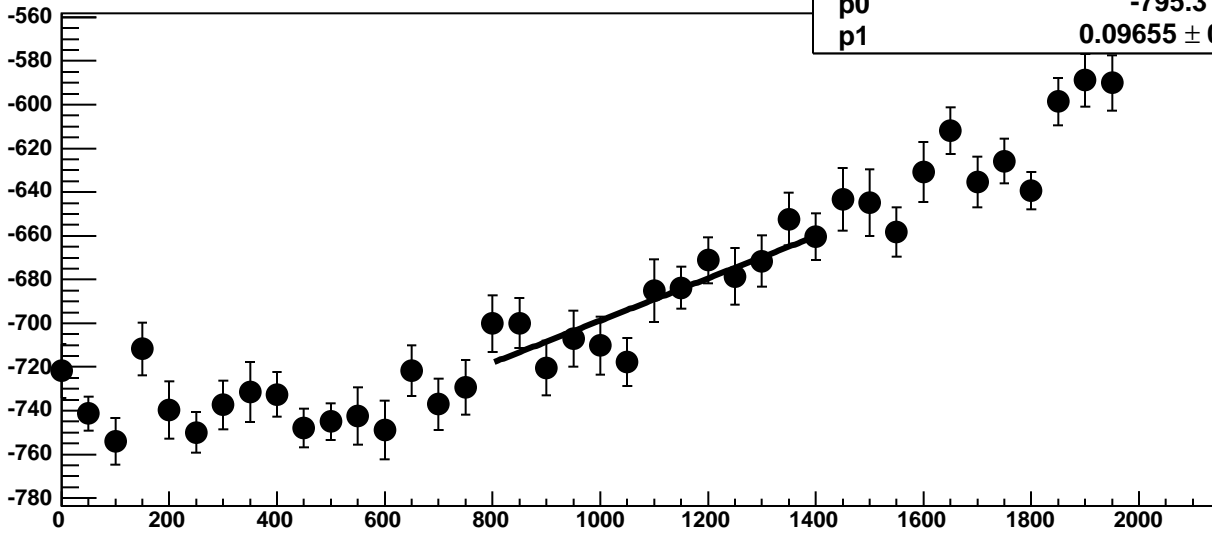
Chip 8, Channel 2, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 8, Channel 2, Enable 0!, Hold=30, ADC Residuals vs DAC

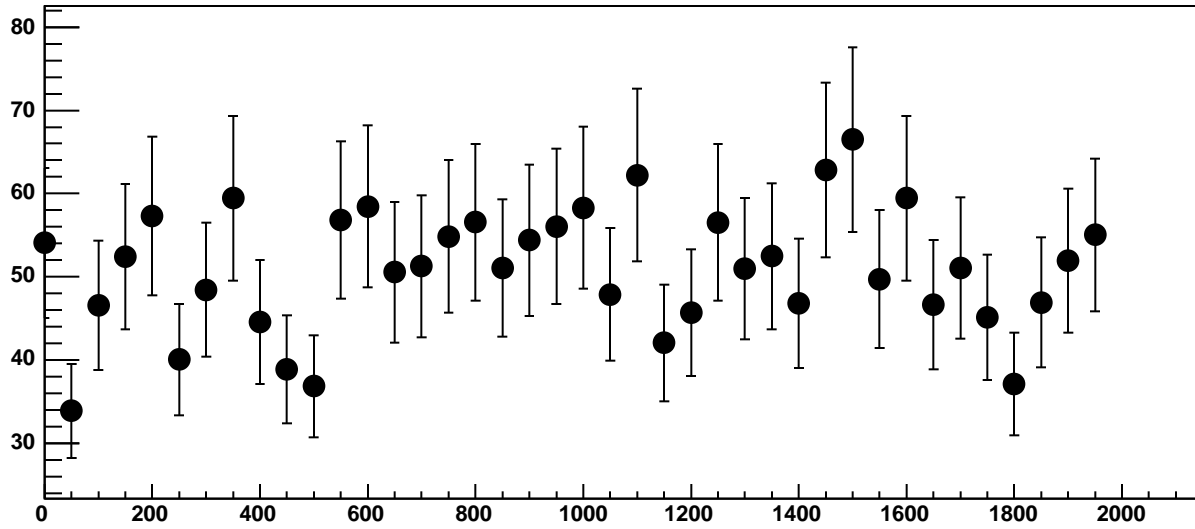


Chip 8, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC

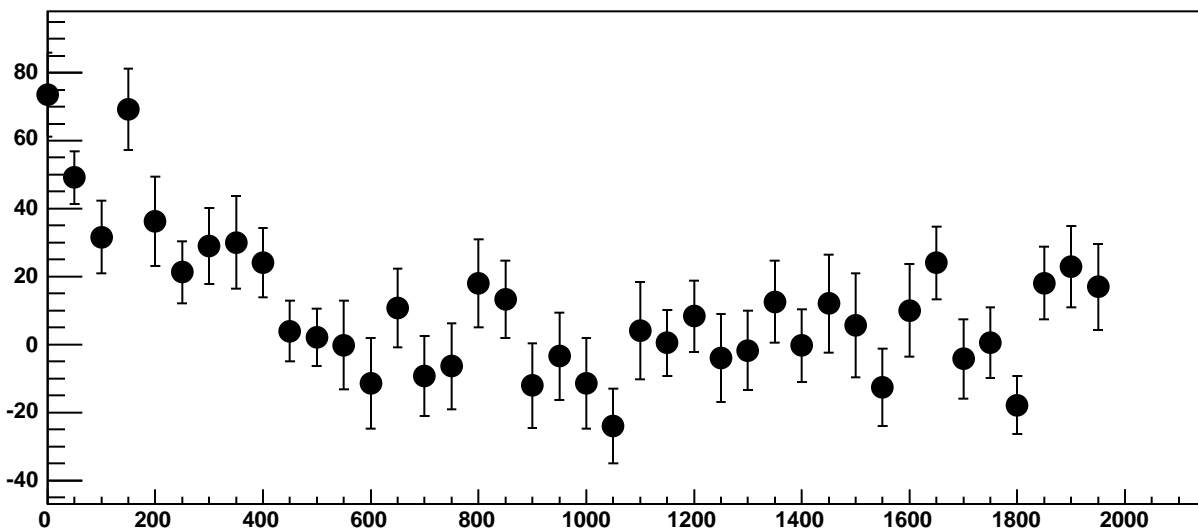


$\chi^2 / \text{ndf}$  11.68 / 11  
p0  $-795.3 \pm 19.89$   
p1  $0.09655 \pm 0.01762$

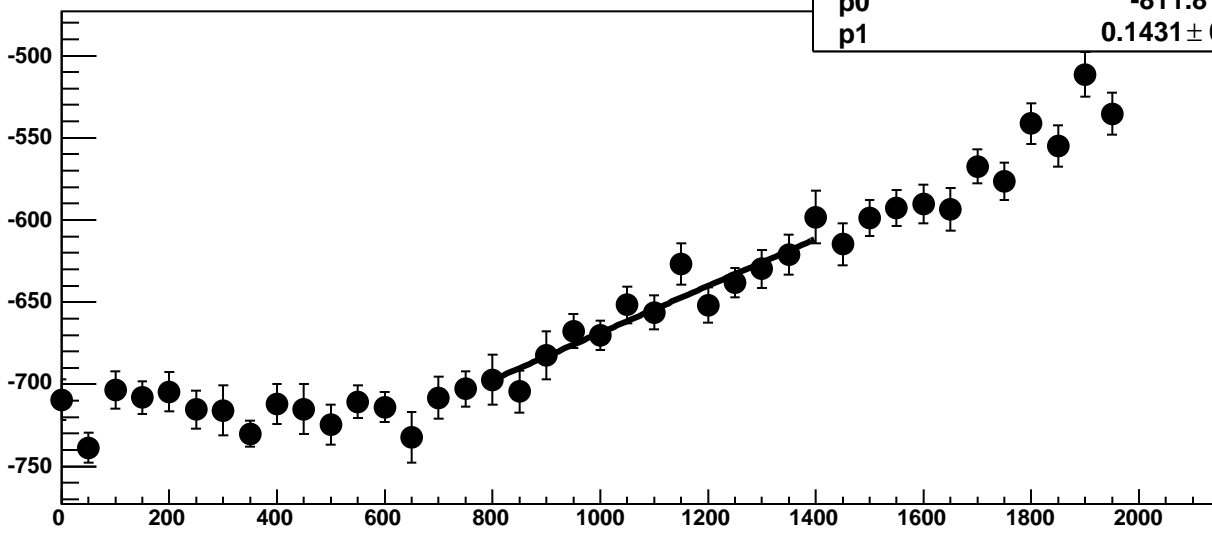
Chip 8, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC



Chip 8, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC

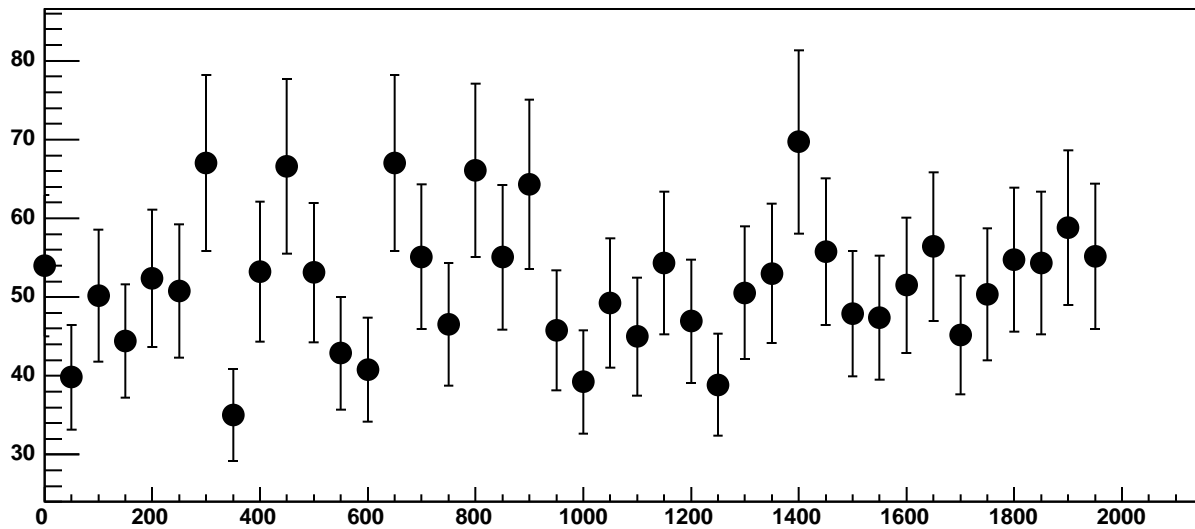


Chip 8, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC

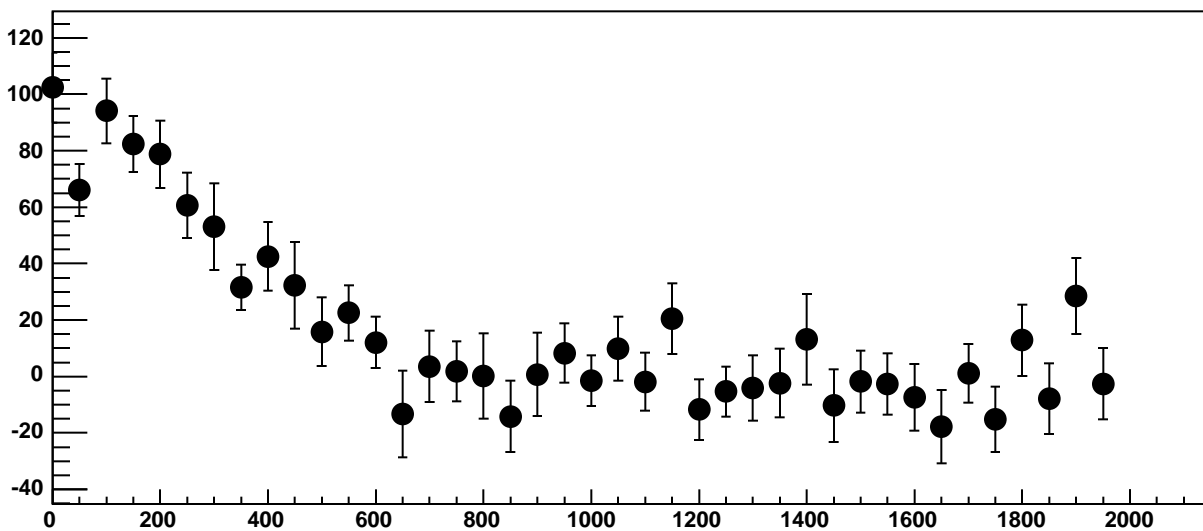


$\chi^2 / \text{ndf}$  7.783 / 11  
p0  $-811.8 \pm 21.27$   
p1  $0.1431 \pm 0.01901$

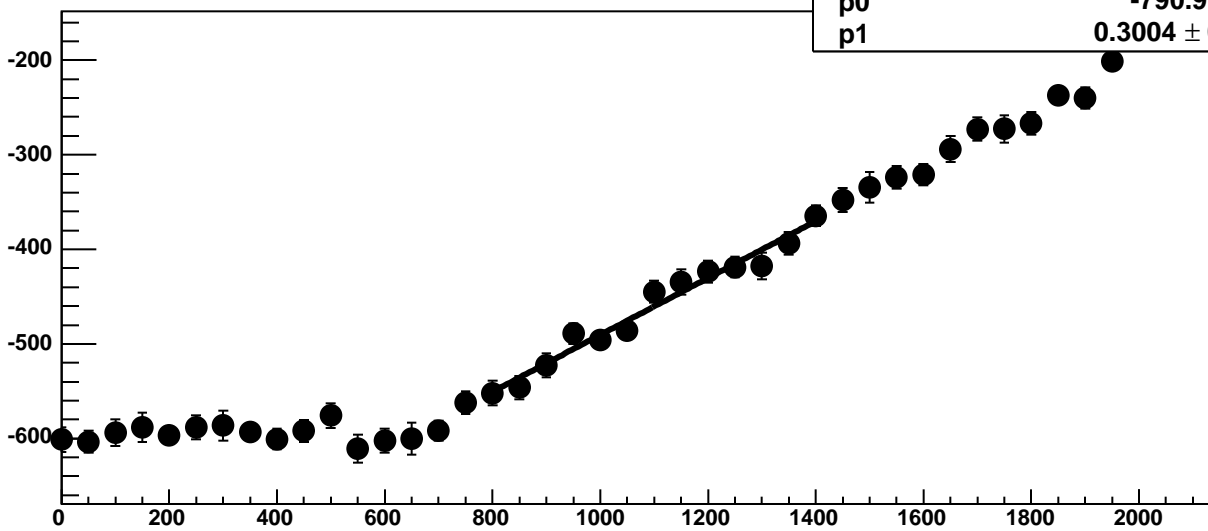
Chip 8, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC



Chip 8, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC

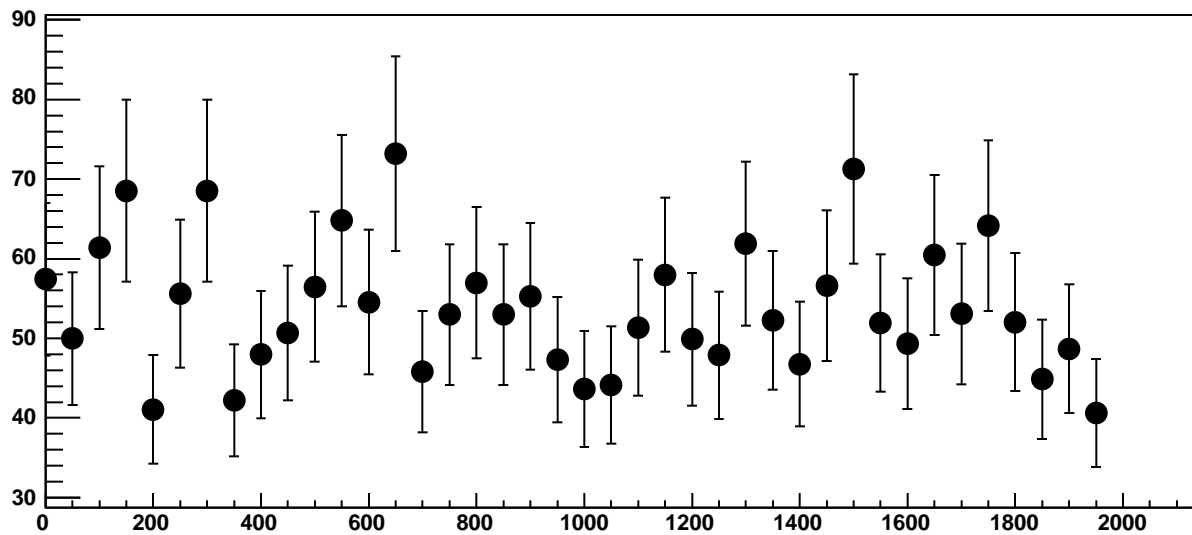


Chip 8, Channel 2, Enable 3, Hold=30, ADC Mean vs DAC

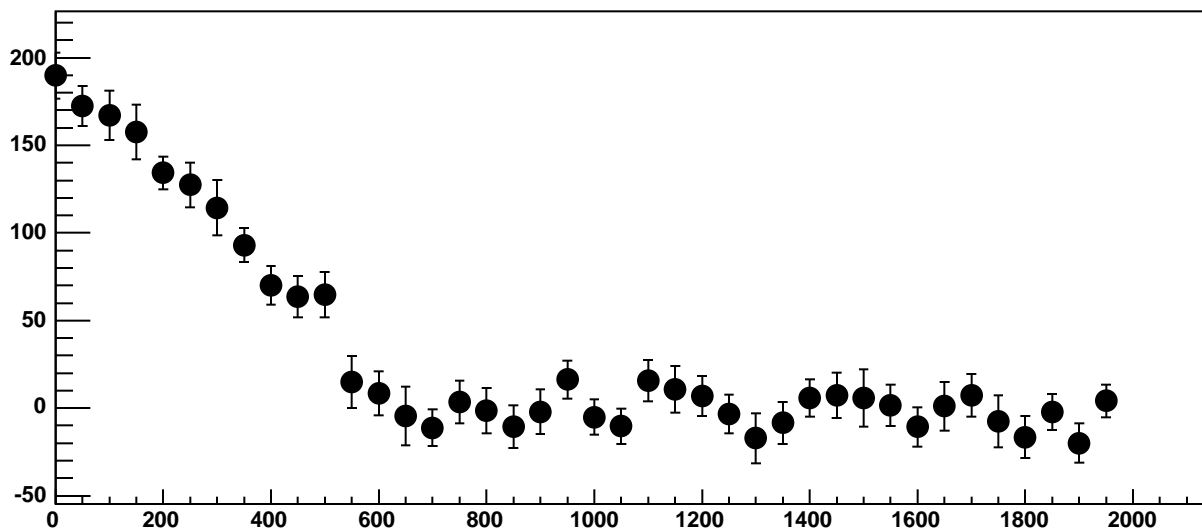


$\chi^2 / \text{ndf}$  9.457 / 11  
p0  $-790.9 \pm 19.73$   
p1  $0.3004 \pm 0.01766$

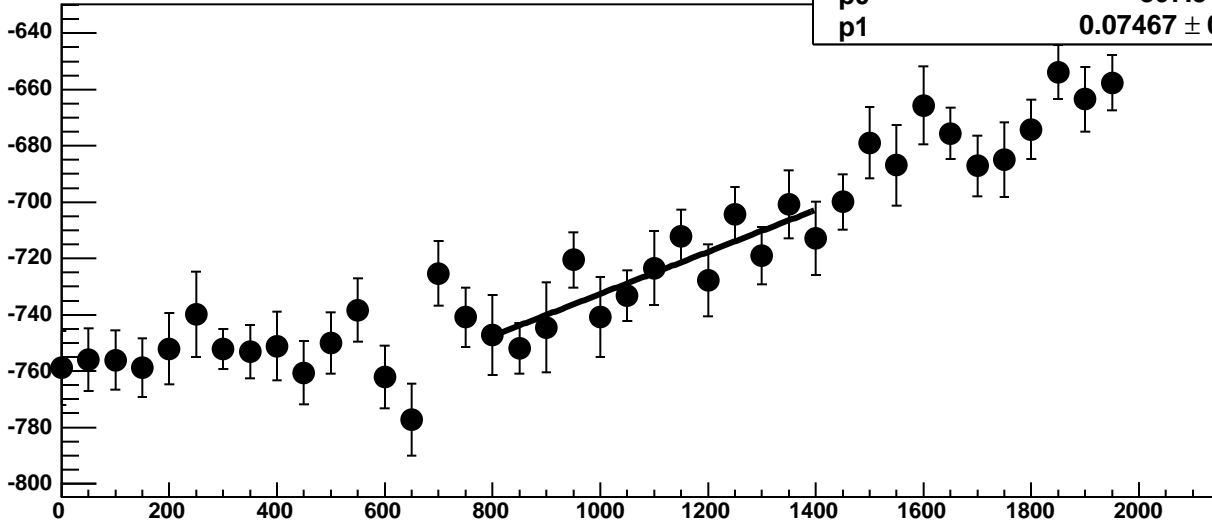
Chip 8, Channel 2, Enable 3, Hold=30, ADC Noise vs DAC



Chip 8, Channel 2, Enable 3, Hold=30, ADC Residuals vs DAC

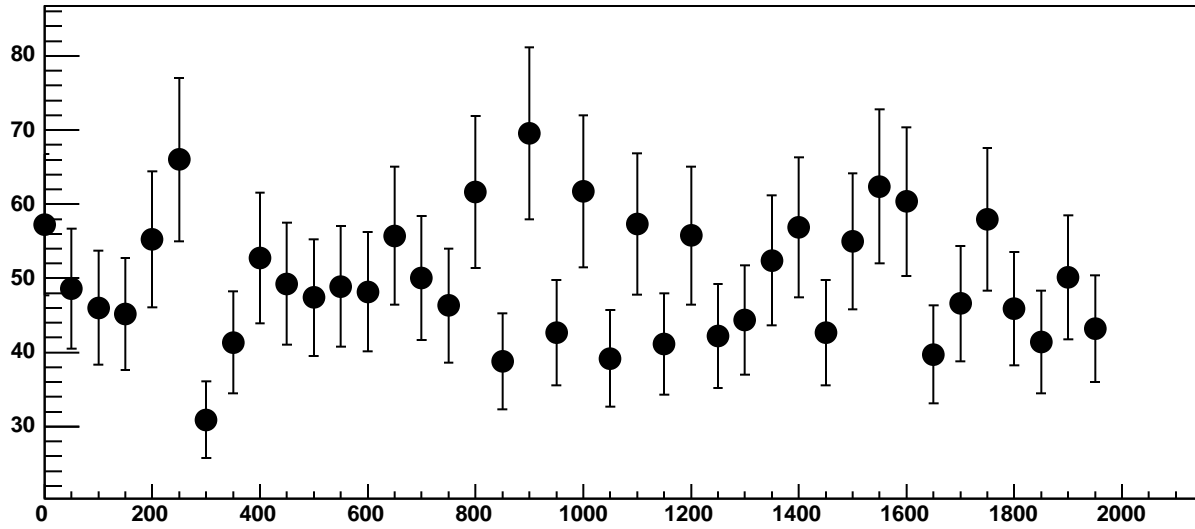


Chip 8, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC

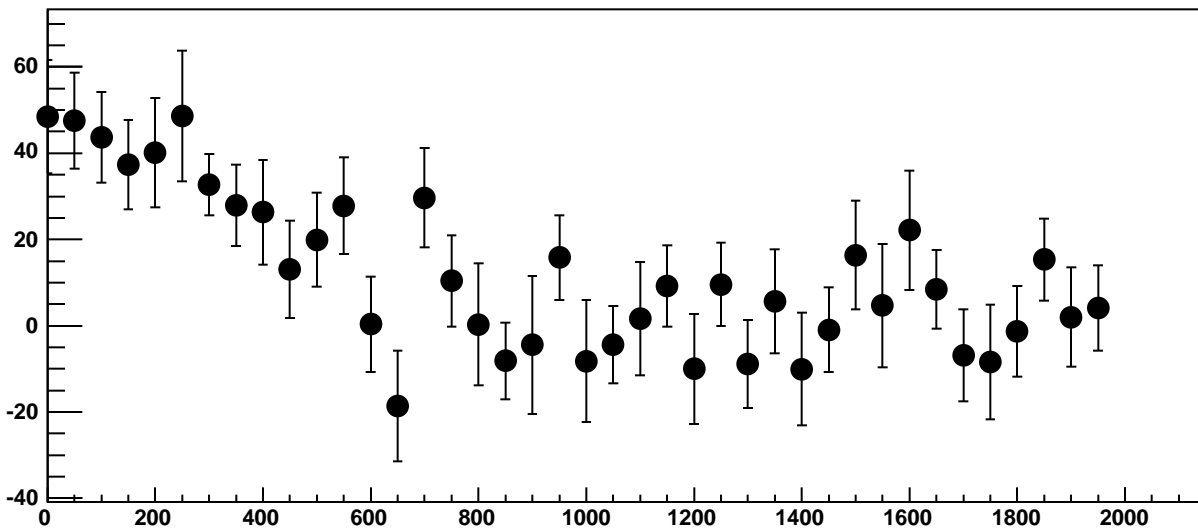


$\chi^2 / \text{ndf}$  8.226 / 11  
p0  $-807.3 \pm 19.16$   
p1  $0.07467 \pm 0.01718$

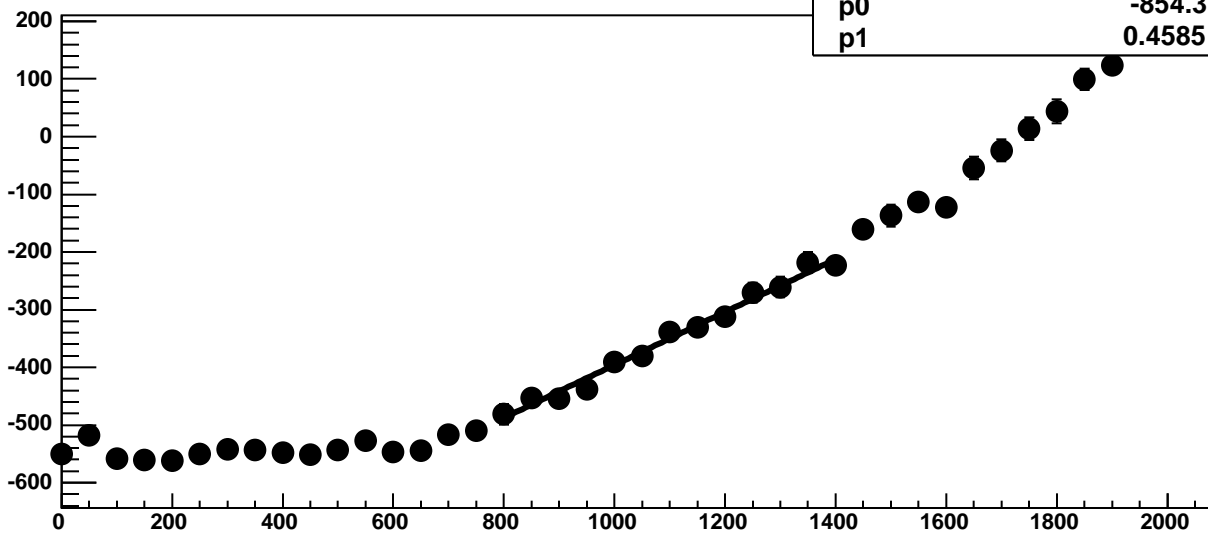
Chip 8, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC



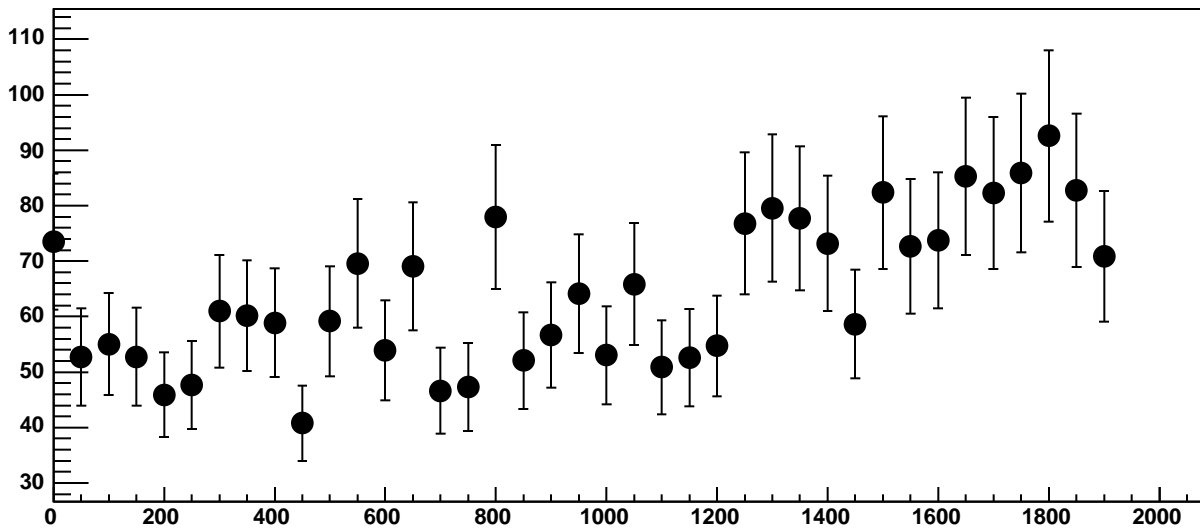
Chip 8, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC



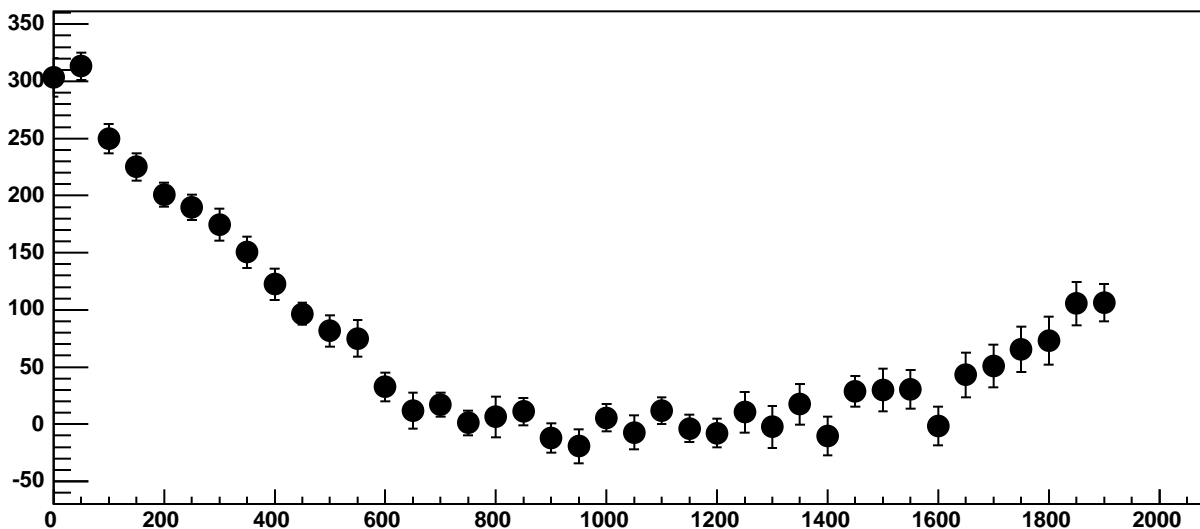
Chip 8, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC



Chip 8, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC

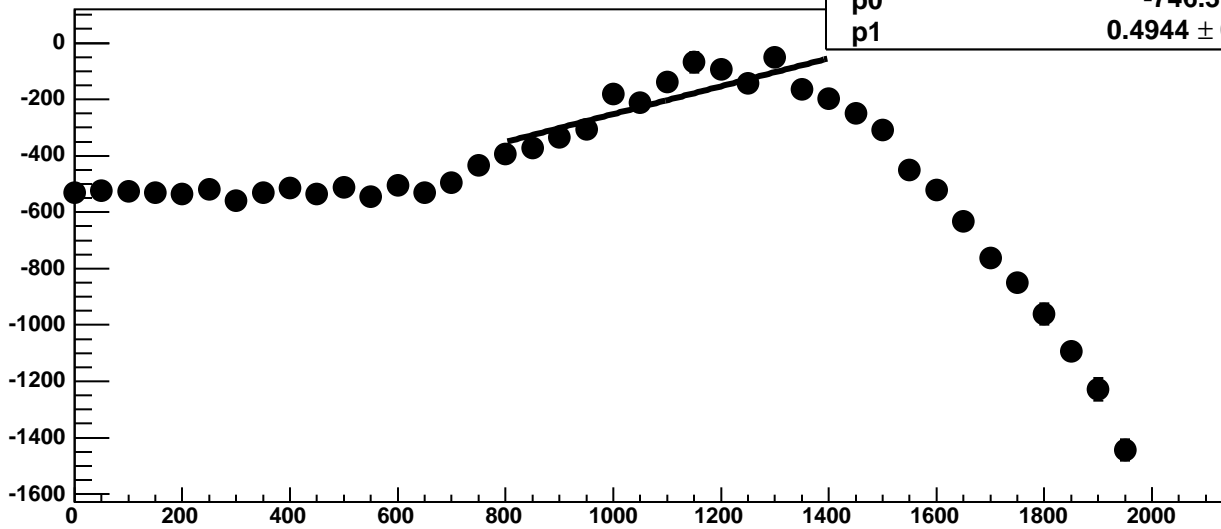


Chip 8, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC





Chip 8, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

75.53 / 11

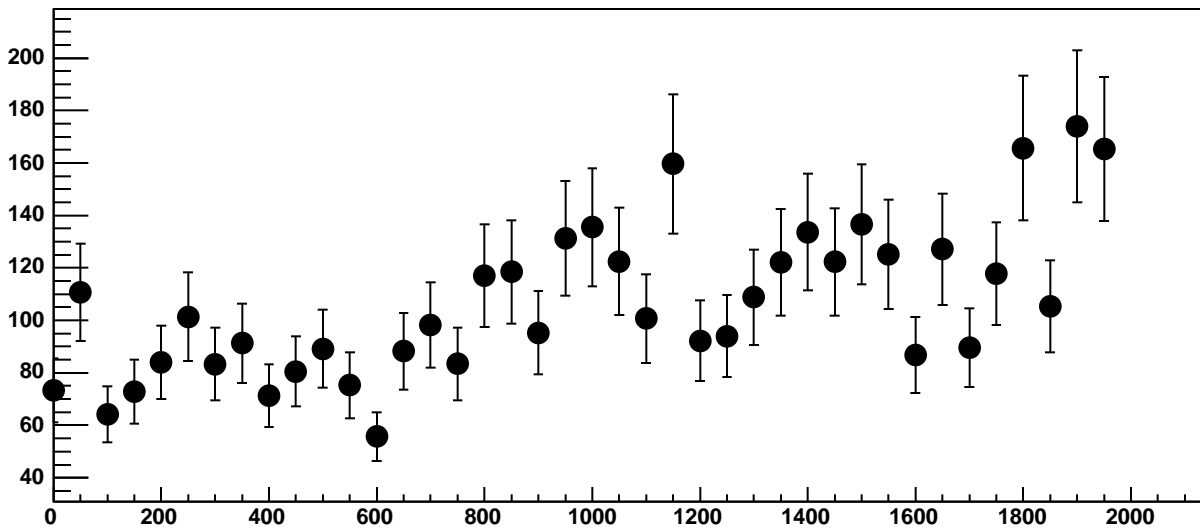
p0

$-746.3 \pm 44.04$

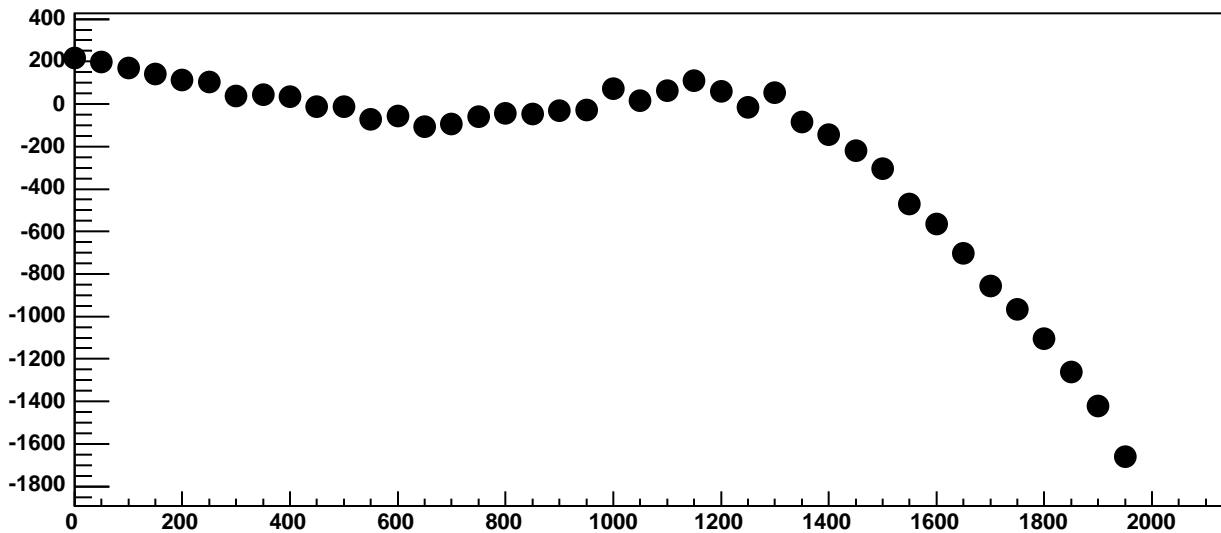
p1

$0.4944 \pm 0.03942$

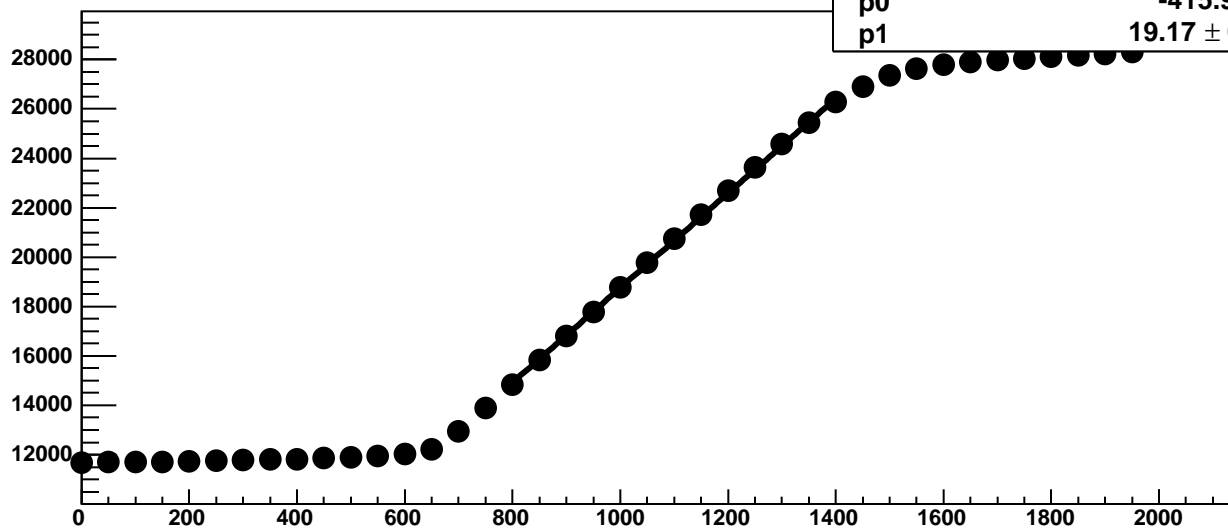
Chip 8, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC



Chip 8, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC

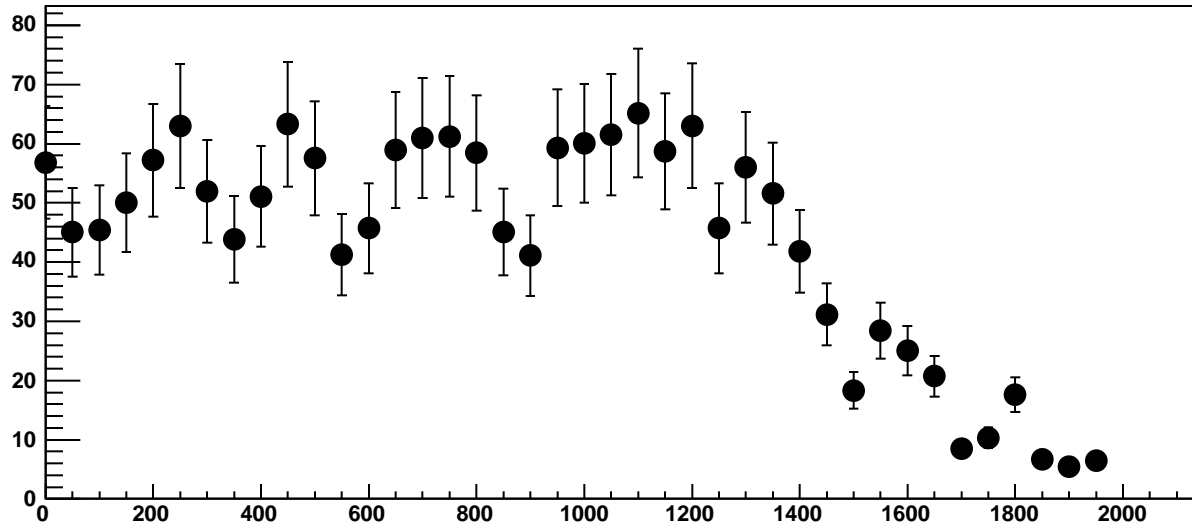


Chip 8, Channel 3, Enable 1!, Hold=30, ADC Mean vs DAC

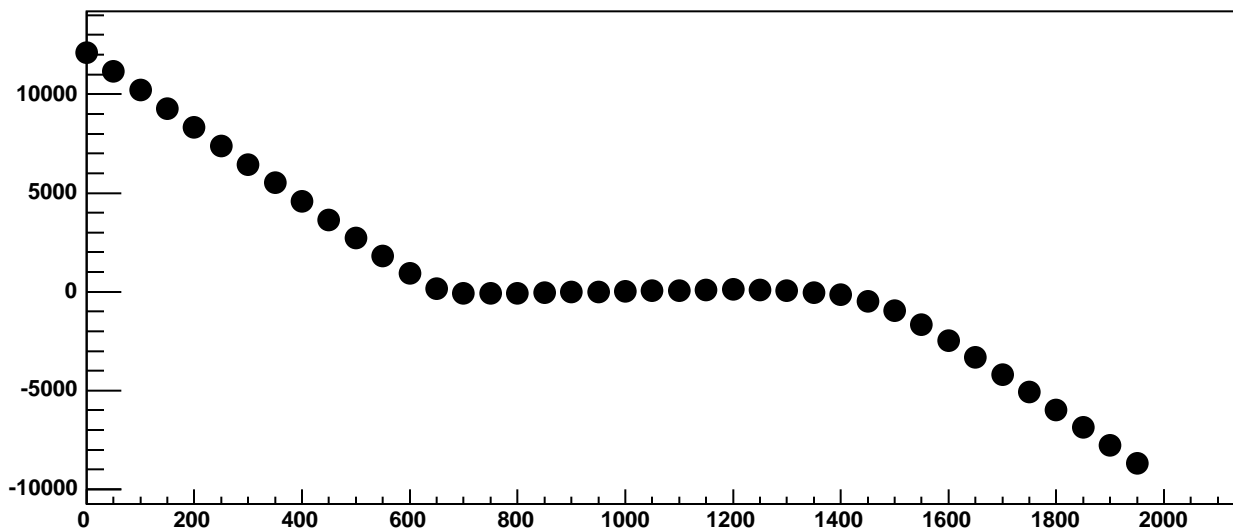


$\chi^2 / \text{ndf}$  550.2 / 11  
p0  $-415.9 \pm 18.5$   
p1  $19.17 \pm 0.01648$

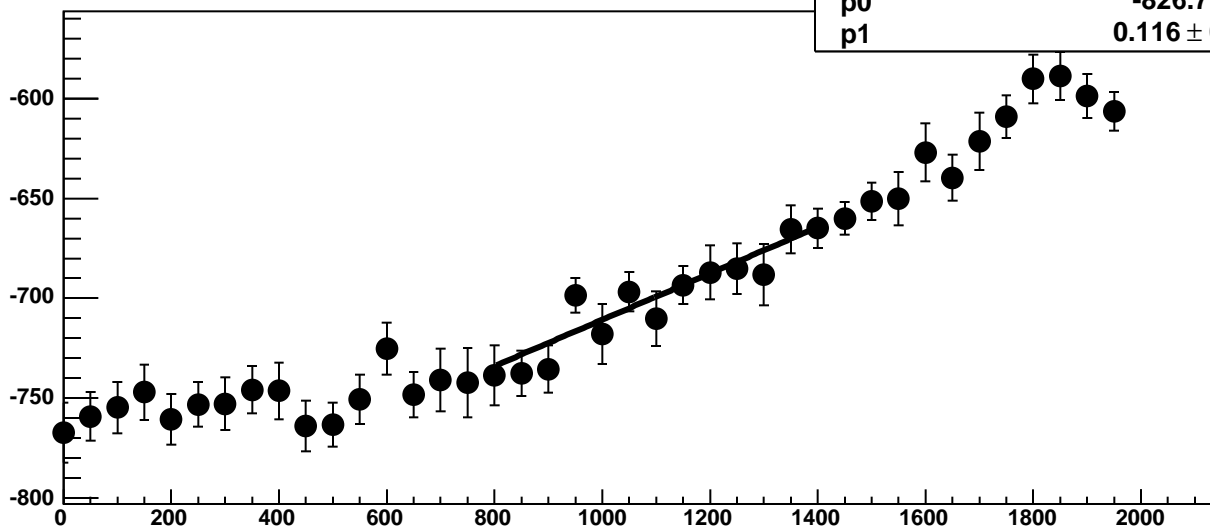
Chip 8, Channel 3, Enable 1!, Hold=30, ADC Noise vs DAC



Chip 8, Channel 3, Enable 1!, Hold=30, ADC Residuals vs DAC



Chip 8, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

8.78 / 11

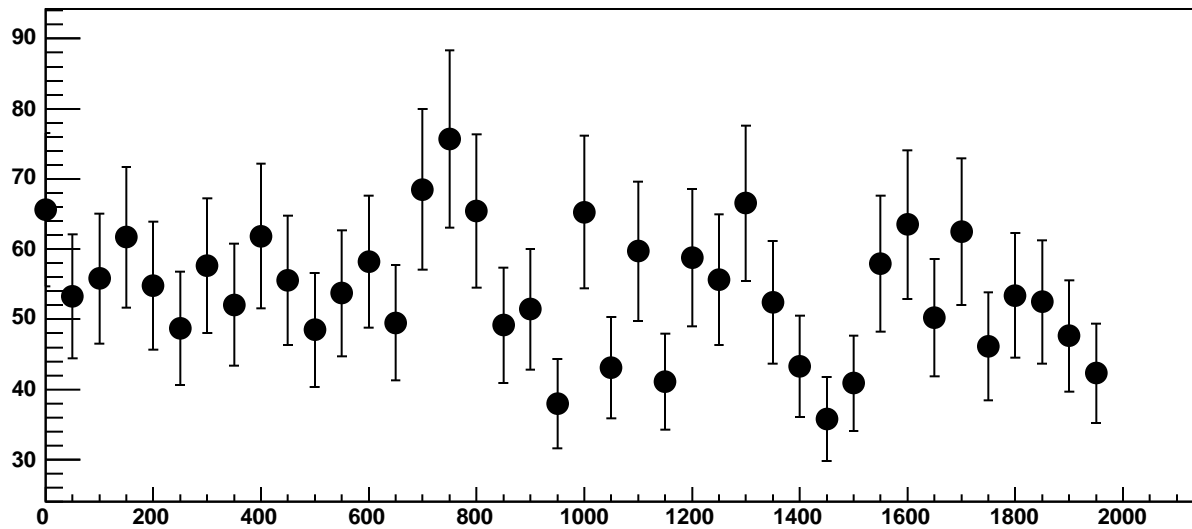
p0

$-826.7 \pm 19.47$

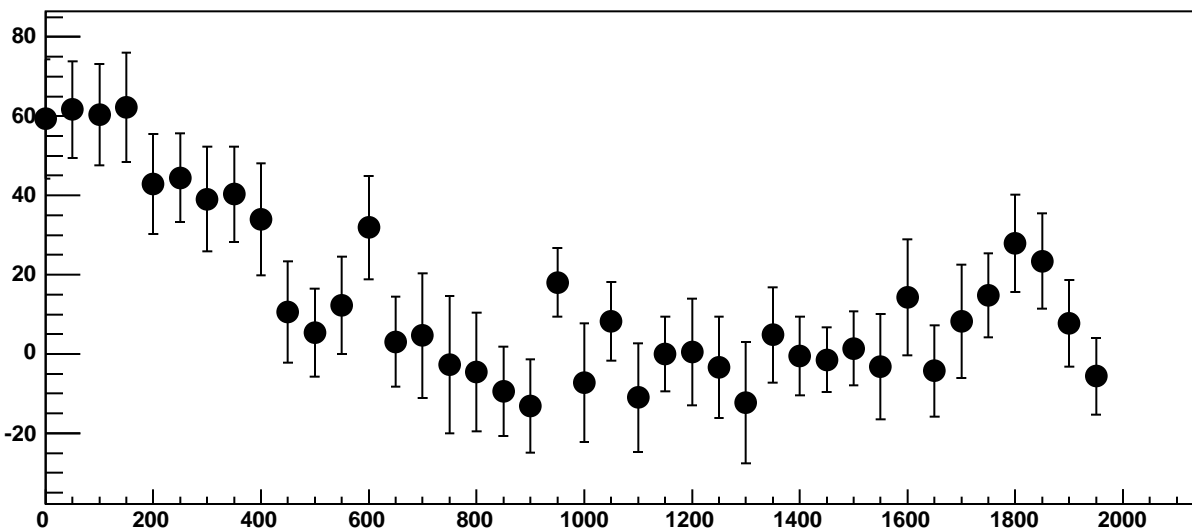
p1

$0.116 \pm 0.01745$

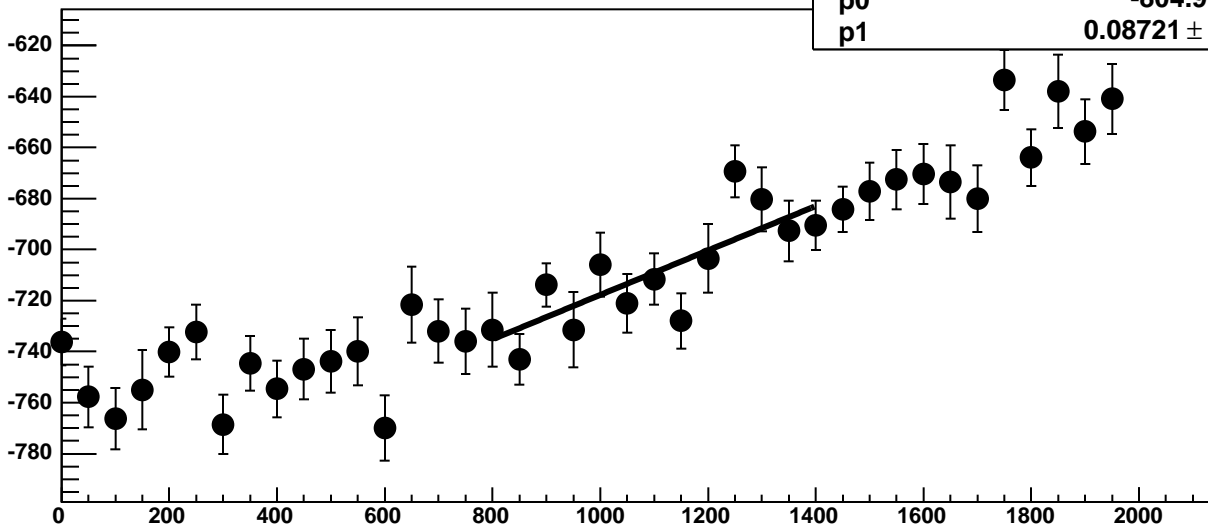
Chip 8, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC



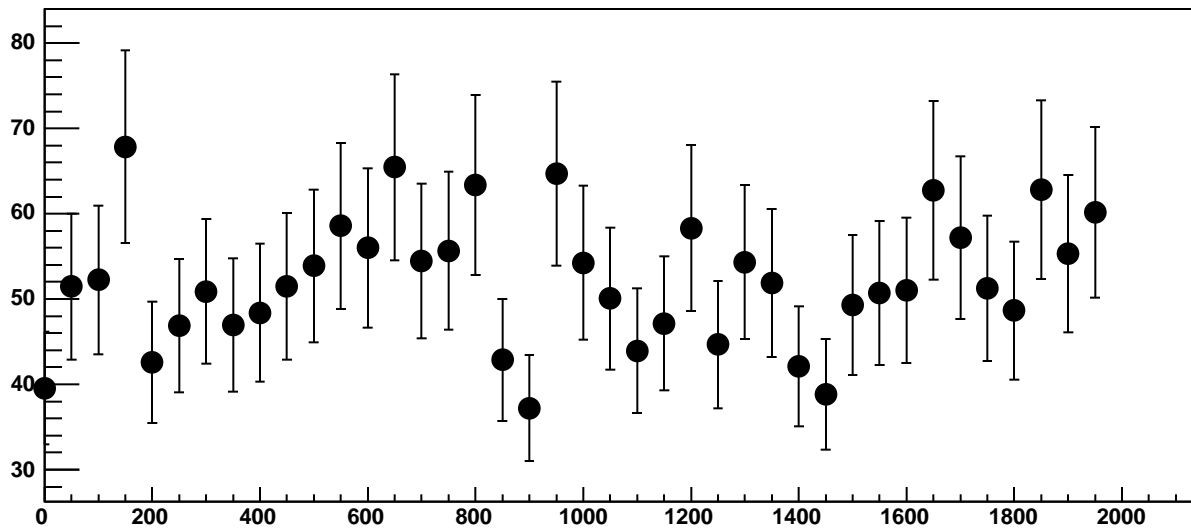
Chip 8, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC



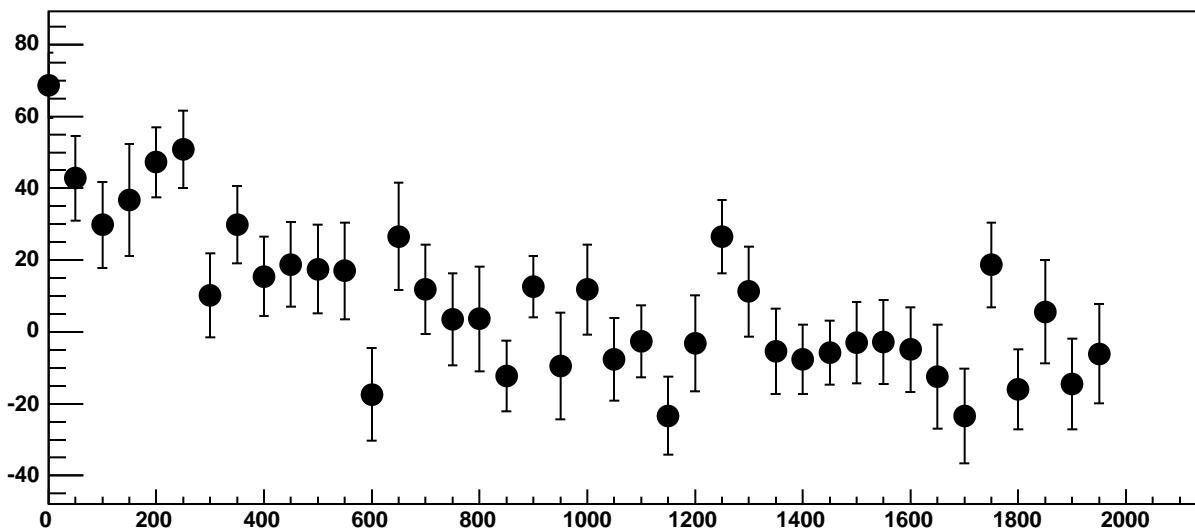
Chip 8, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC



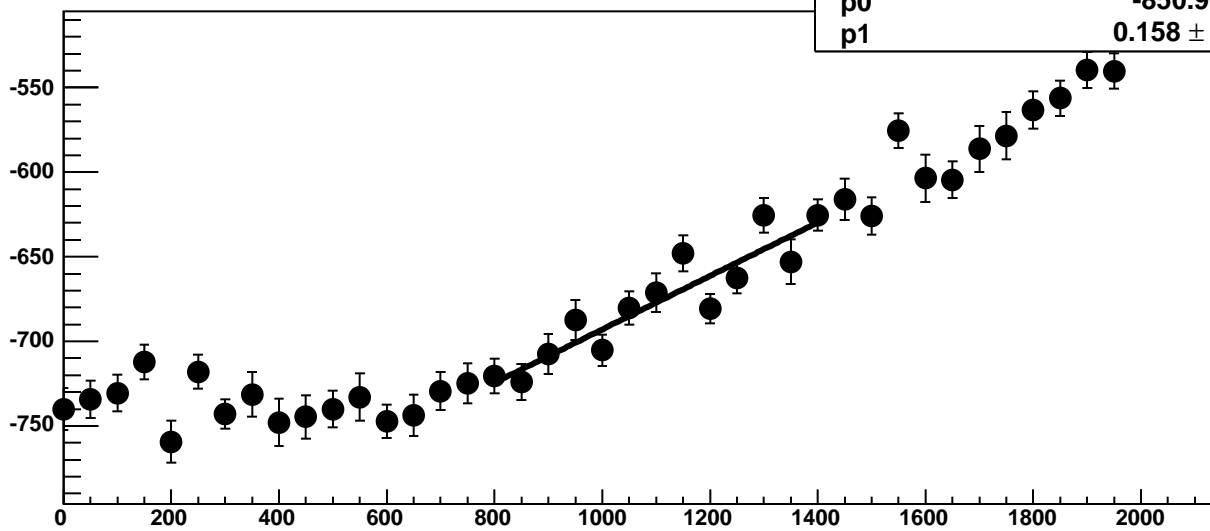
Chip 8, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC



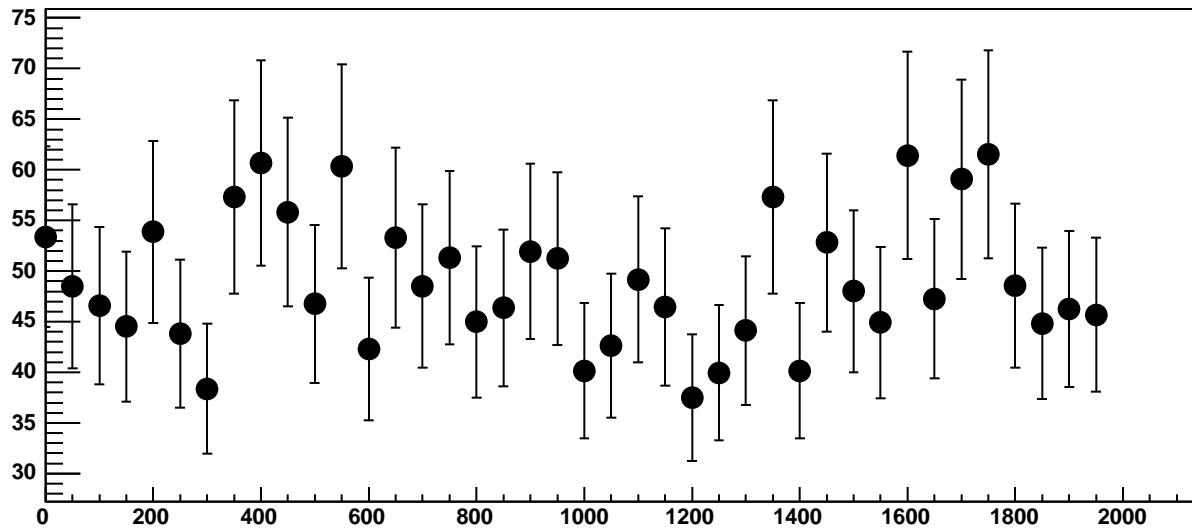
Chip 8, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC



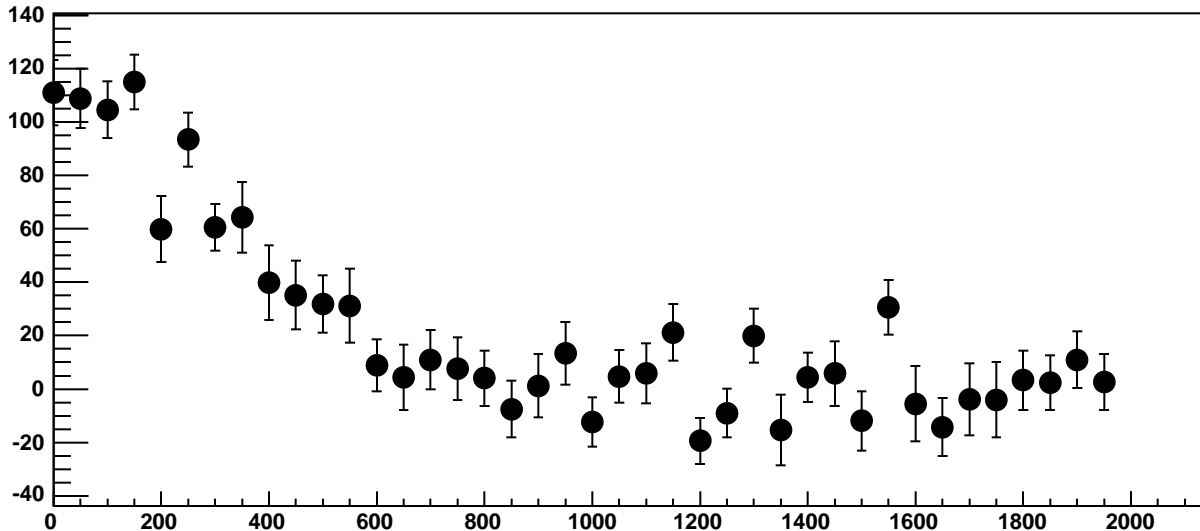
Chip 8, Channel 3, Enable 4, Hold=30, ADC Mean vs DAC



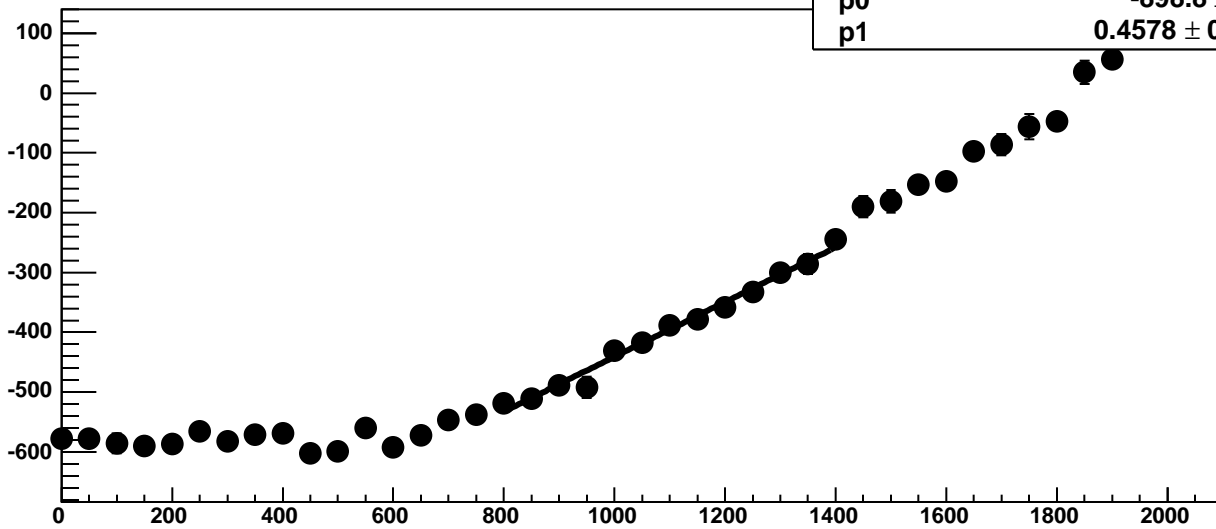
Chip 8, Channel 3, Enable 4, Hold=30, ADC Noise vs DAC



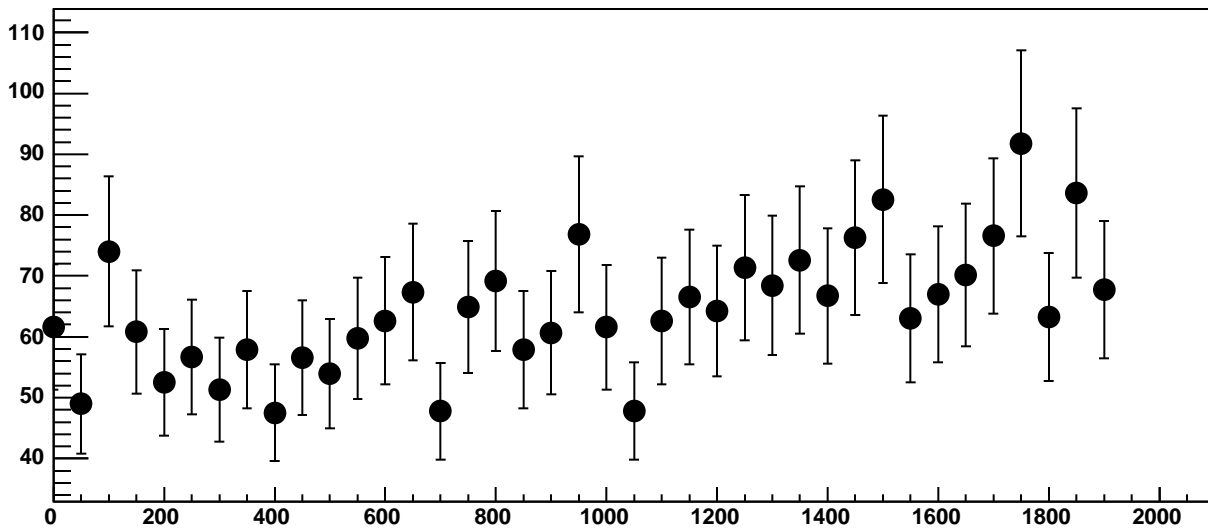
Chip 8, Channel 3, Enable 4, Hold=30, ADC Residuals vs DAC



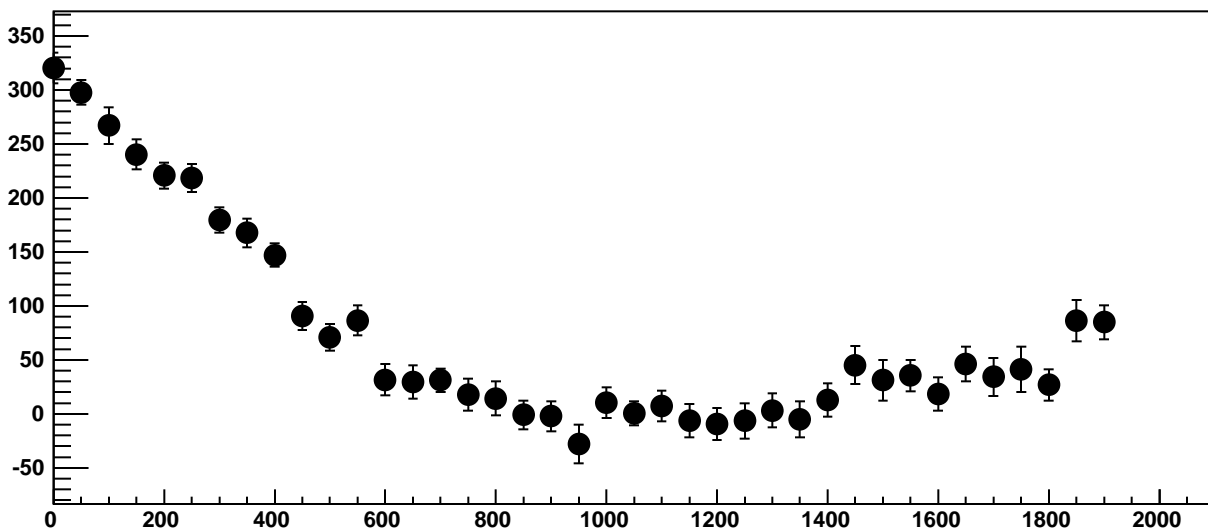
Chip 8, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC



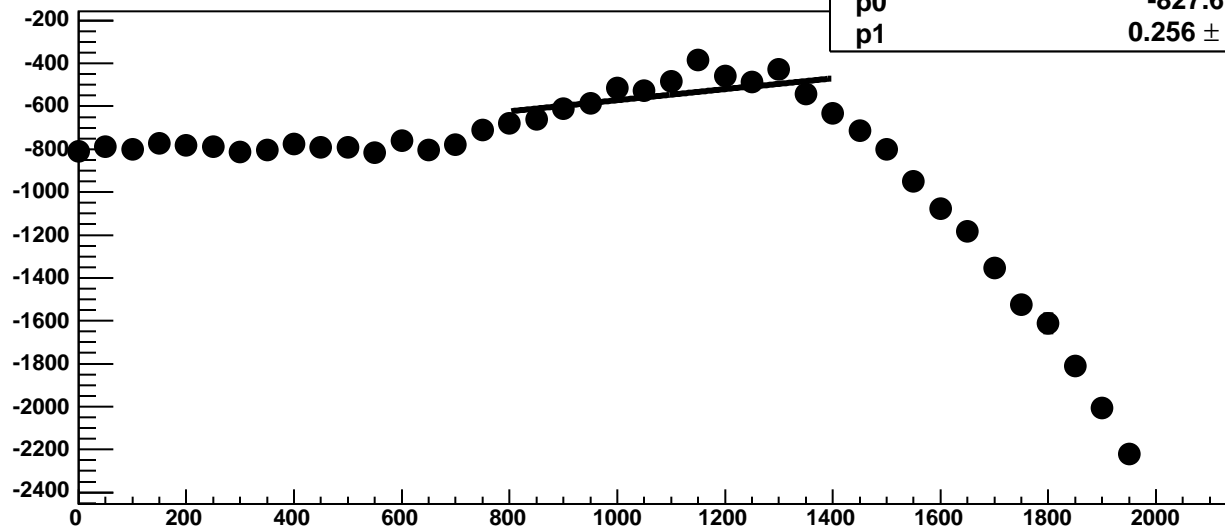
Chip 8, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC



Chip 8, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 8, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

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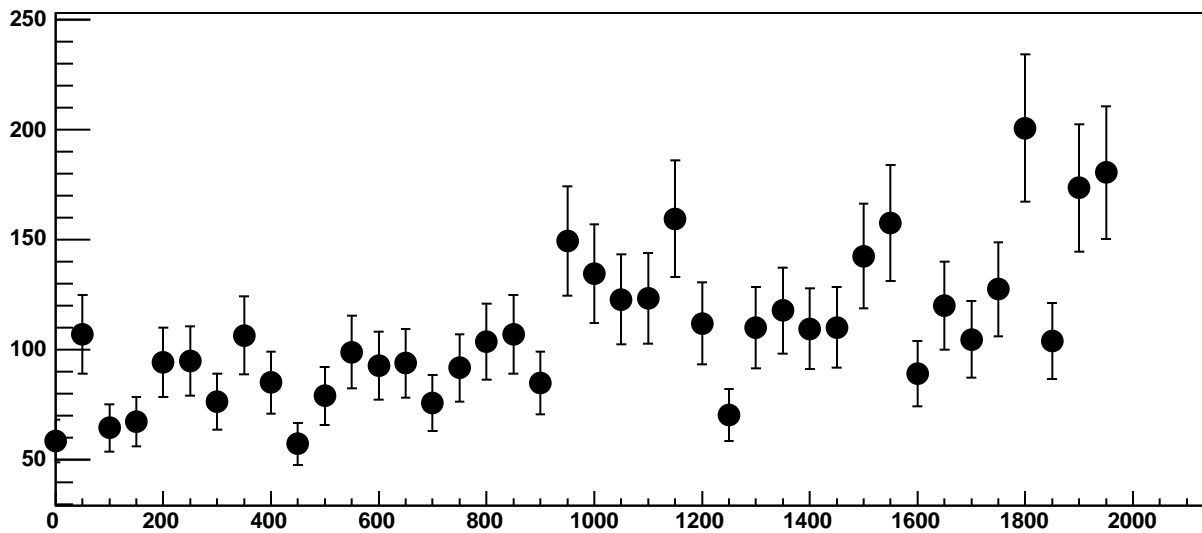
p0

$-827.6 \pm 39.88$

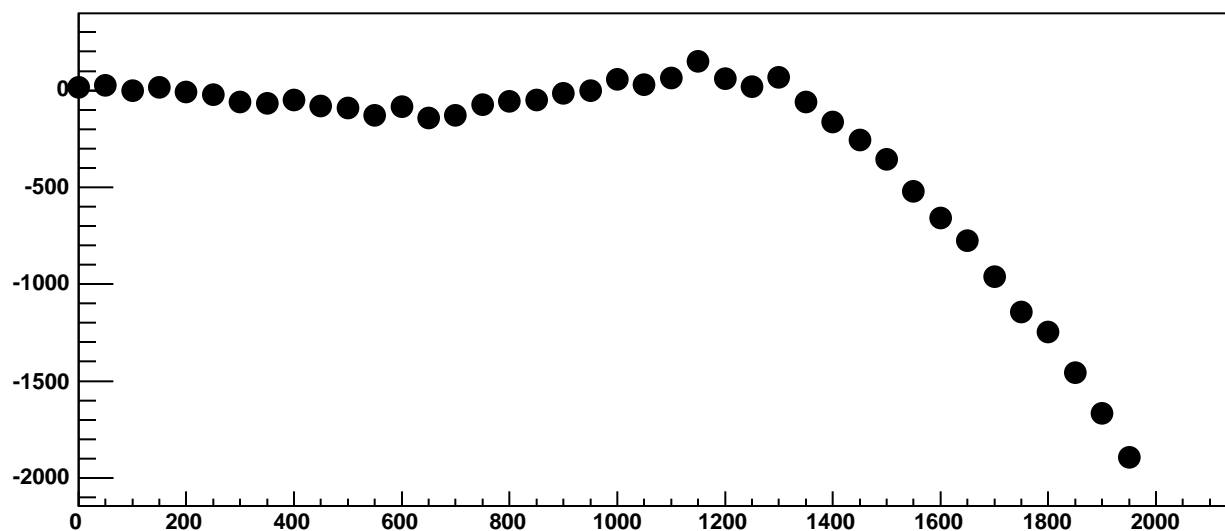
p1

$0.256 \pm 0.03551$

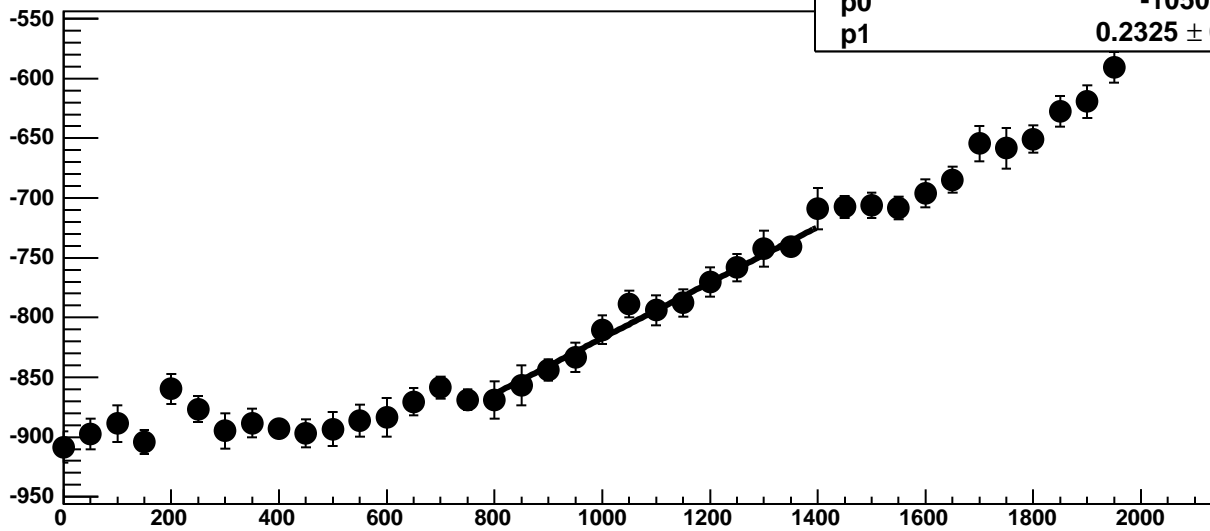
Chip 8, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC



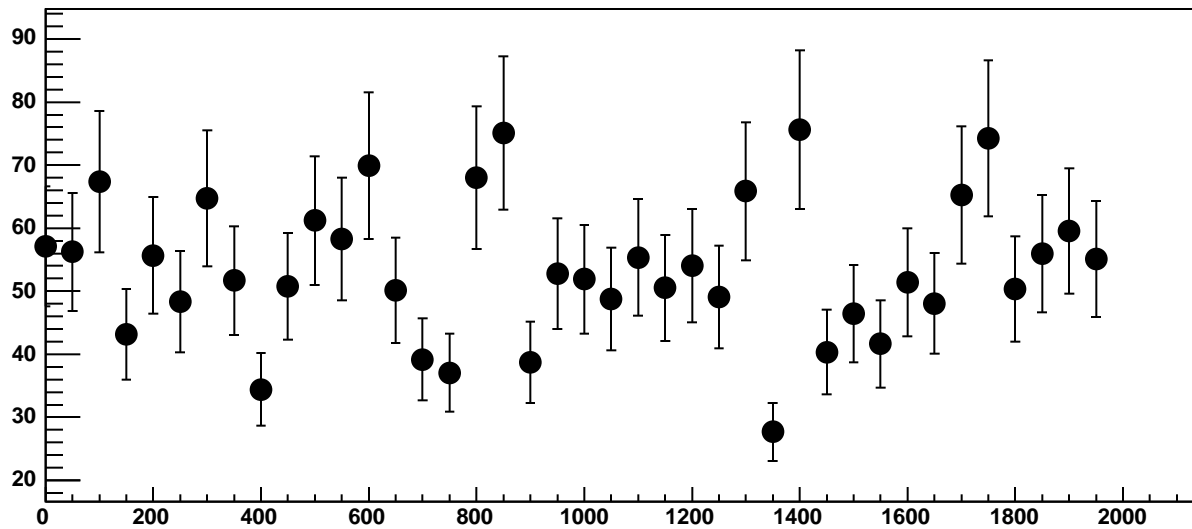
Chip 8, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC



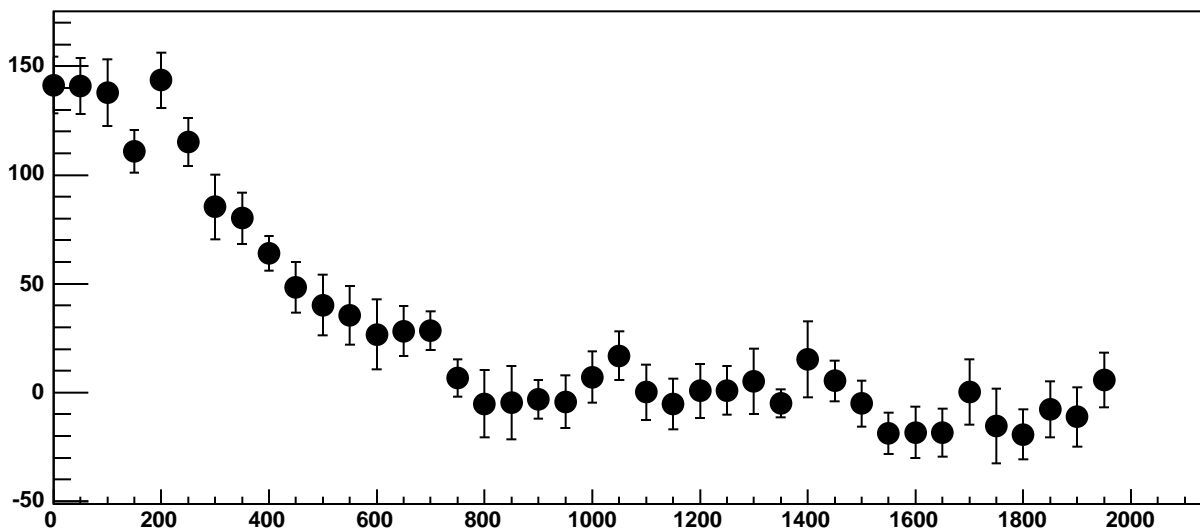
Chip 8, Channel 4, Enable 1, Hold=30, ADC Mean vs DAC



Chip 8, Channel 4, Enable 1, Hold=30, ADC Noise vs DAC

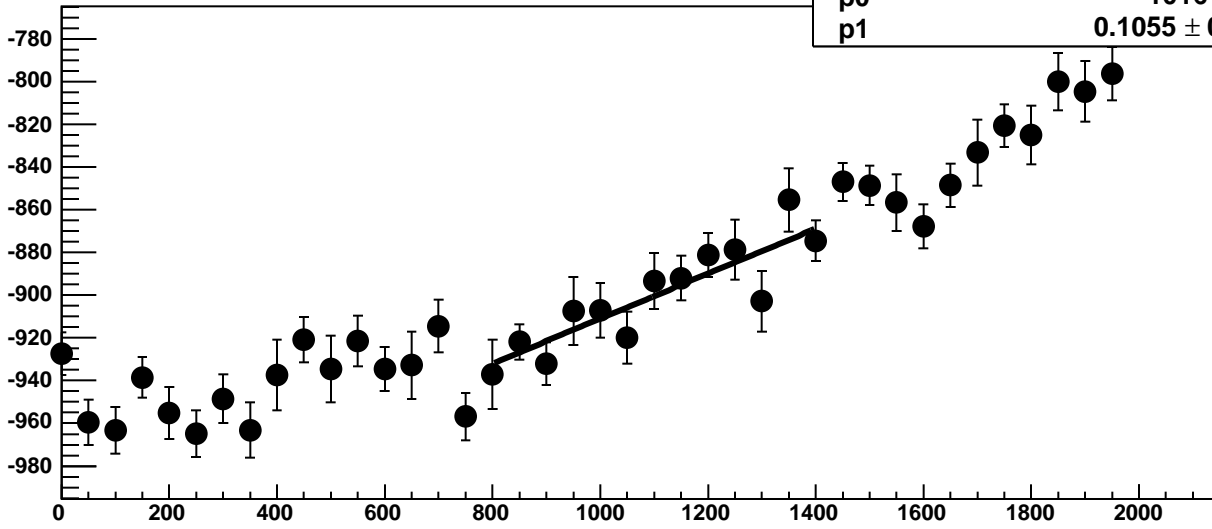


Chip 8, Channel 4, Enable 1, Hold=30, ADC Residuals vs DAC

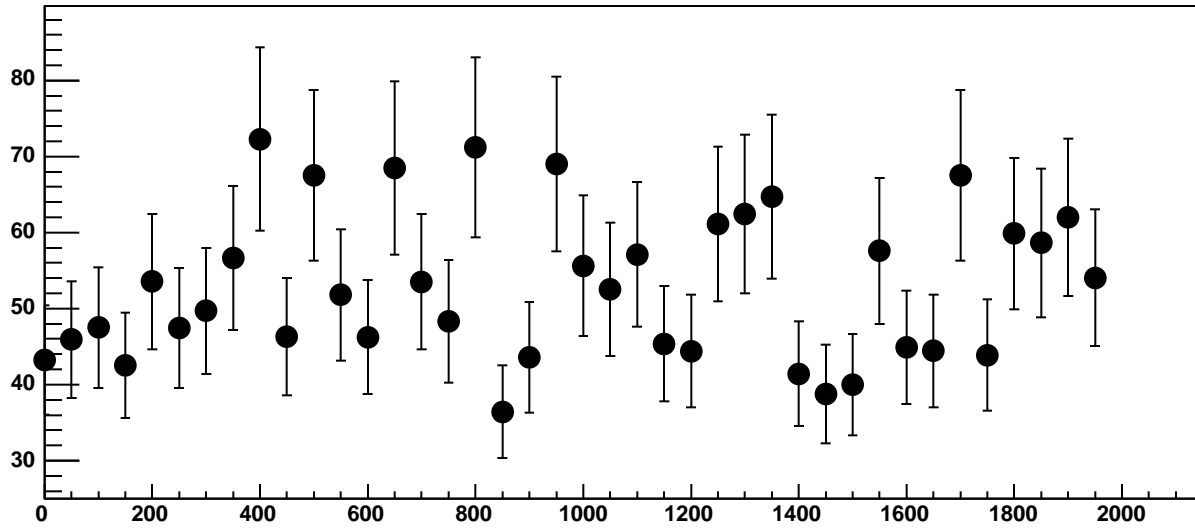




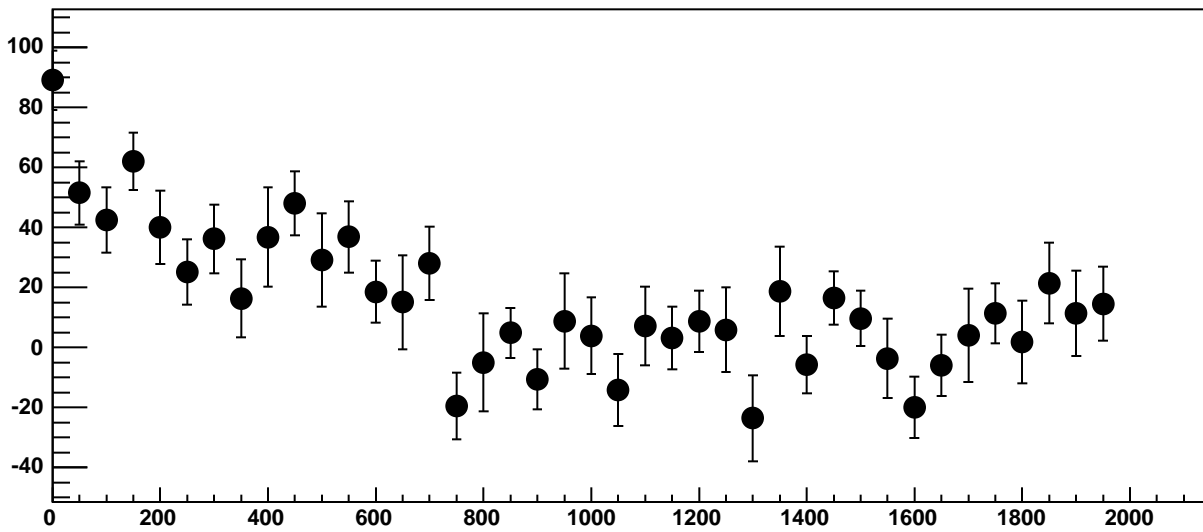
Chip 8, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC



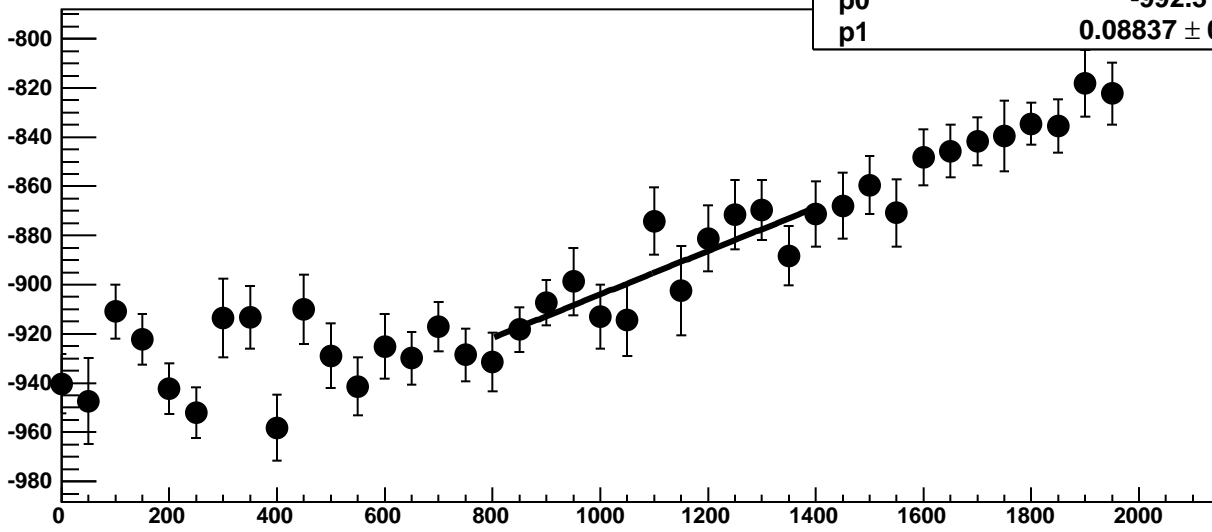
Chip 8, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC



Chip 8, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC

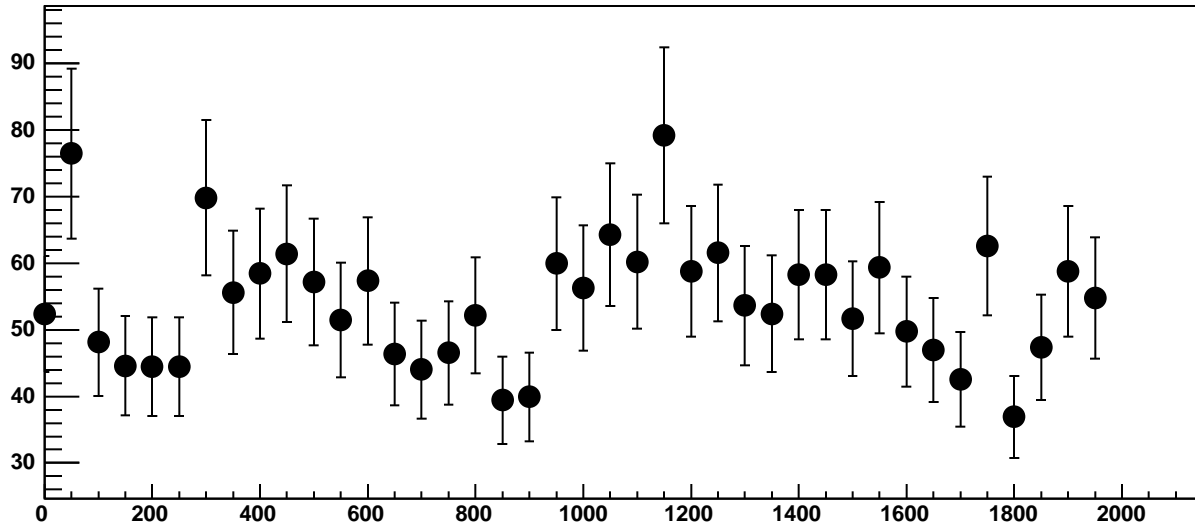


Chip 8, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC

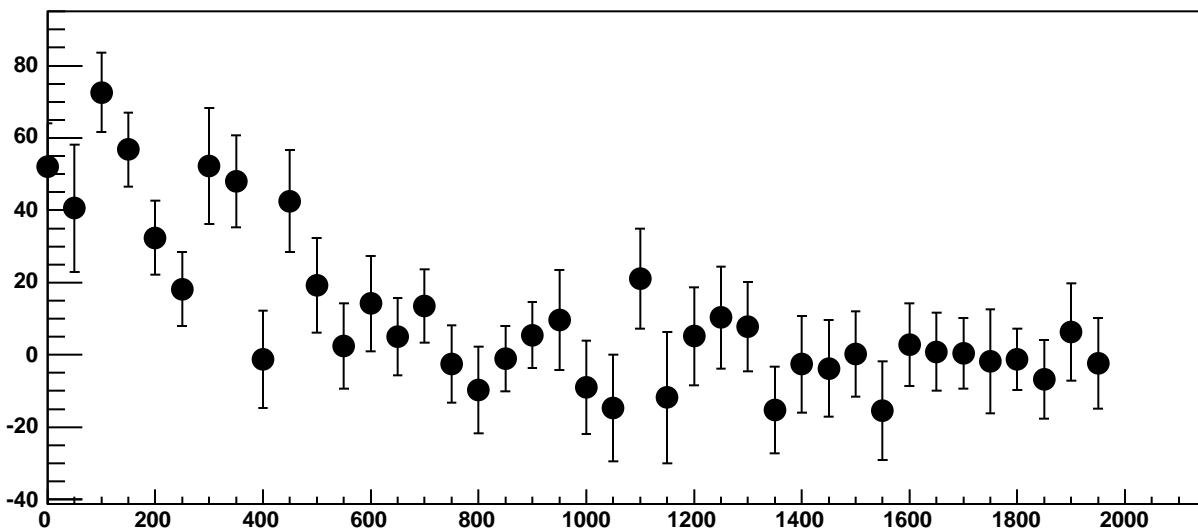


$\chi^2 / \text{ndf}$  8.467 / 11  
p0  $-992.3 \pm 18.77$   
p1  $0.08837 \pm 0.01733$

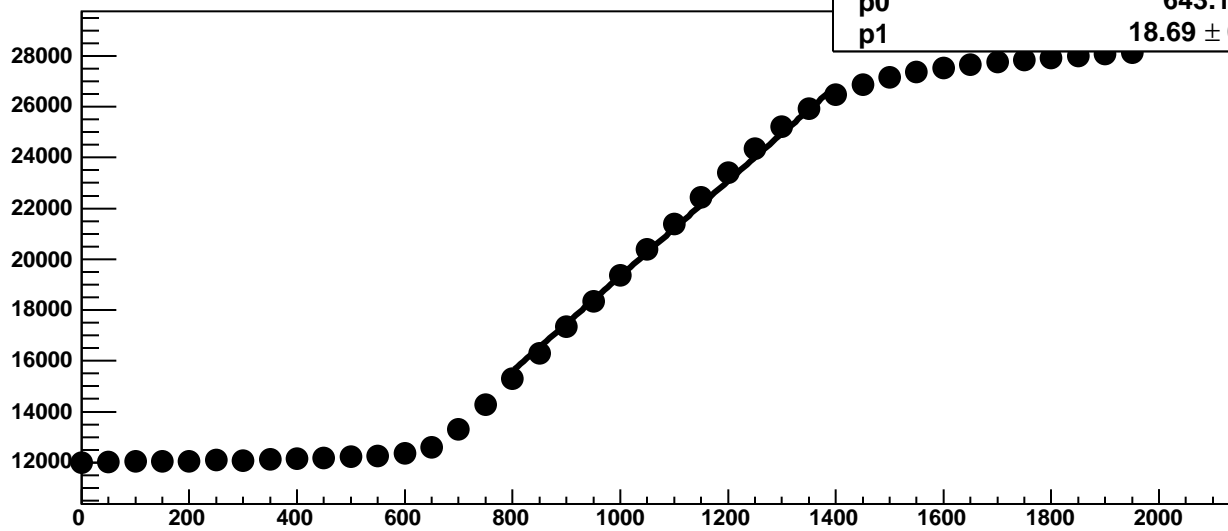
Chip 8, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC



Chip 8, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC



Chip 8, Channel 4, Enable 4!, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

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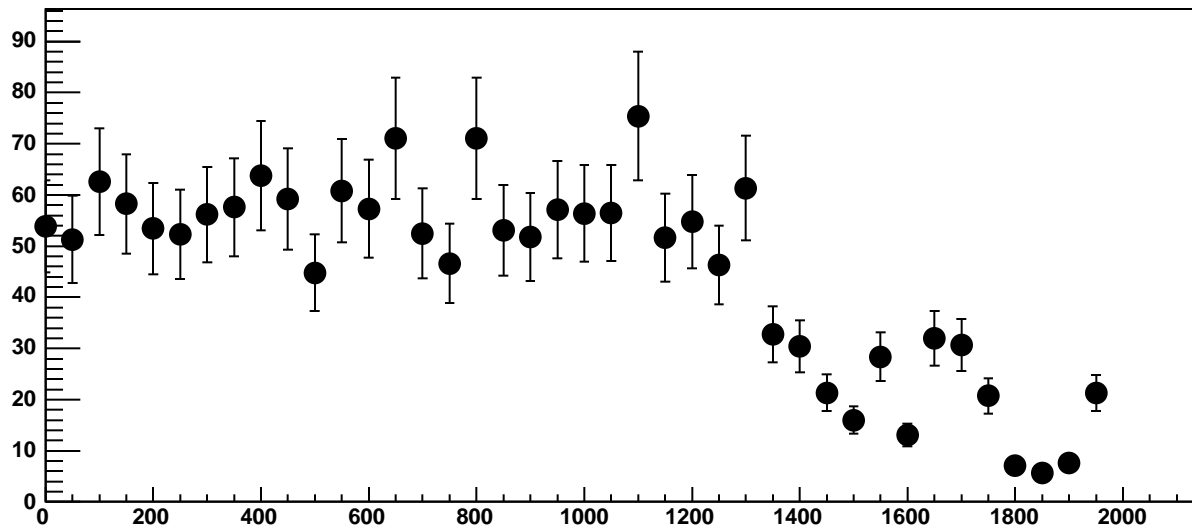
p0

$643.1 \pm 18.66$

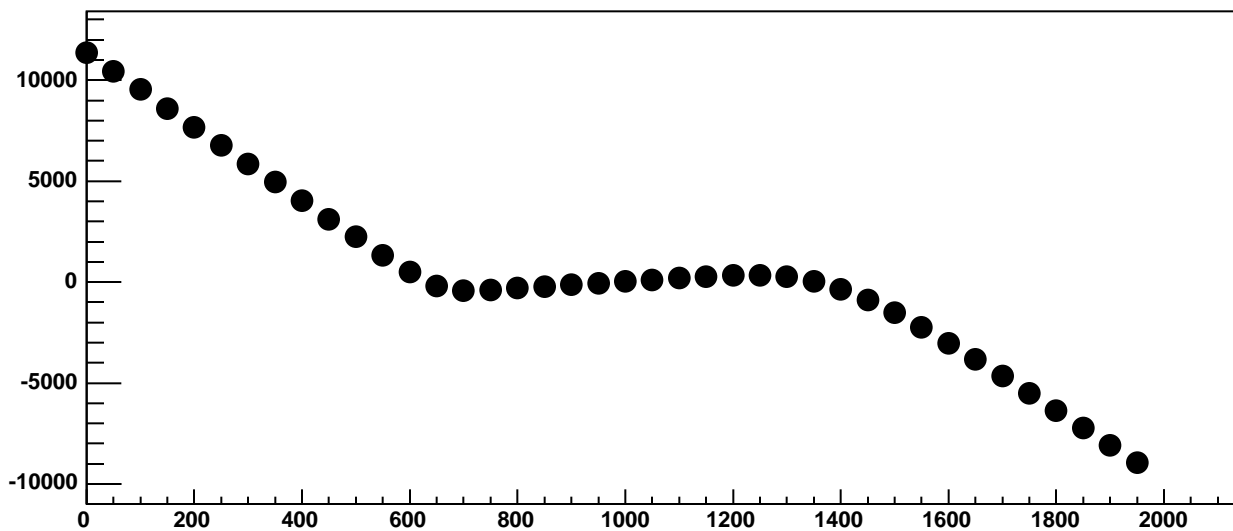
p1

$18.69 \pm 0.01565$

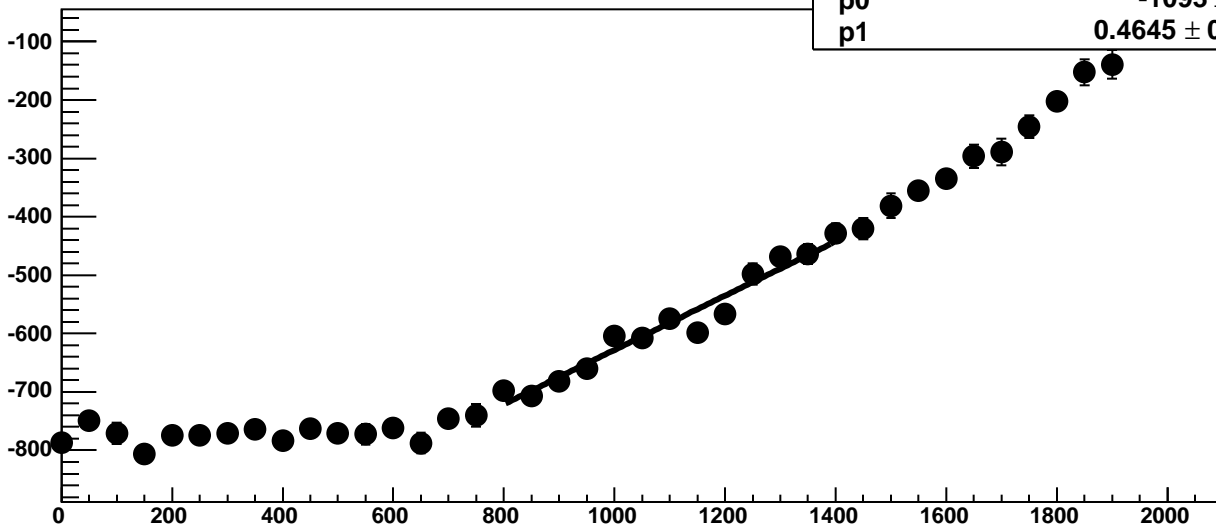
Chip 8, Channel 4, Enable 4!, Hold=30, ADC Noise vs DAC



Chip 8, Channel 4, Enable 4!, Hold=30, ADC Residuals vs DAC

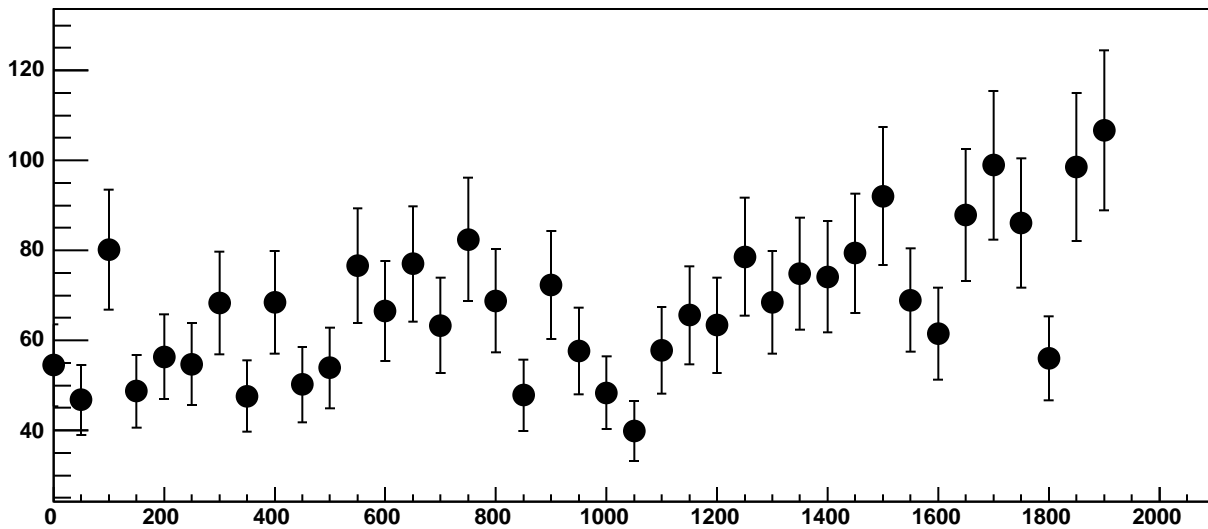


Chip 8, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC

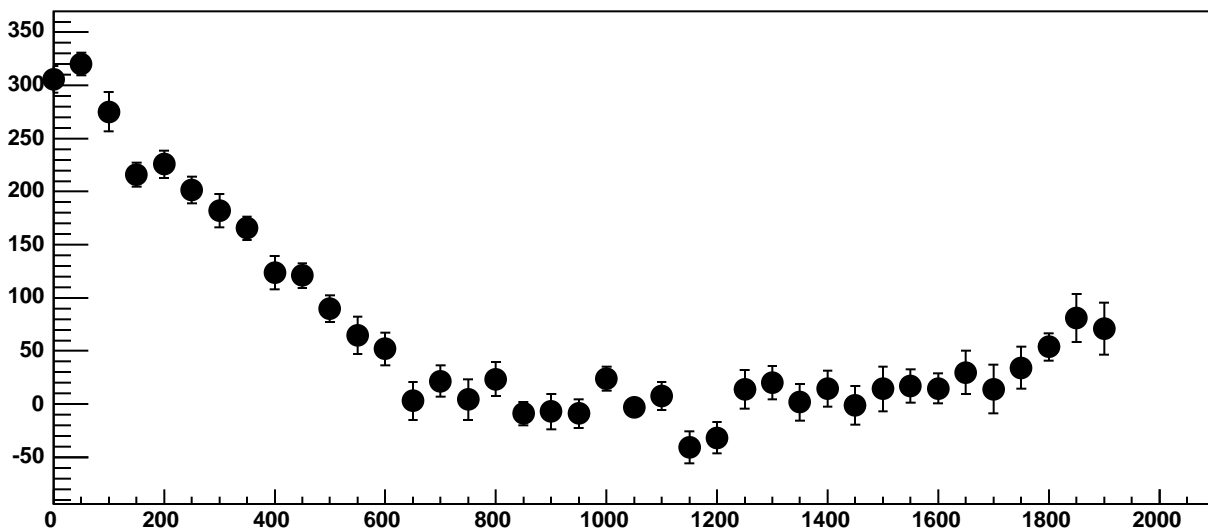


$\chi^2 / \text{ndf}$  23.46 / 11  
p0  $-1093 \pm 24.15$   
p1  $0.4645 \pm 0.02239$

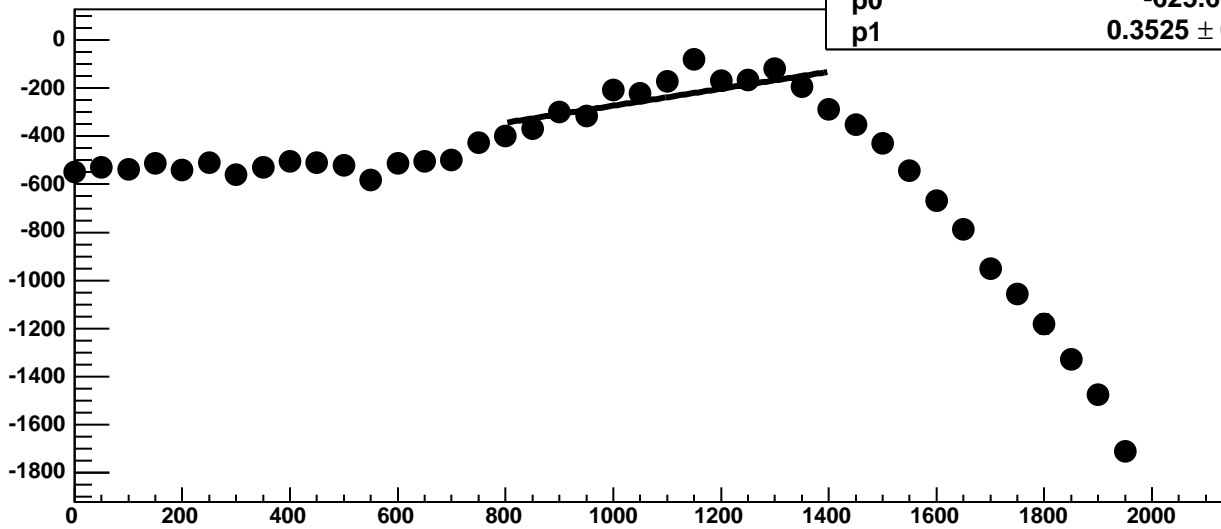
Chip 8, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC



Chip 8, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 8, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

74.67 / 11

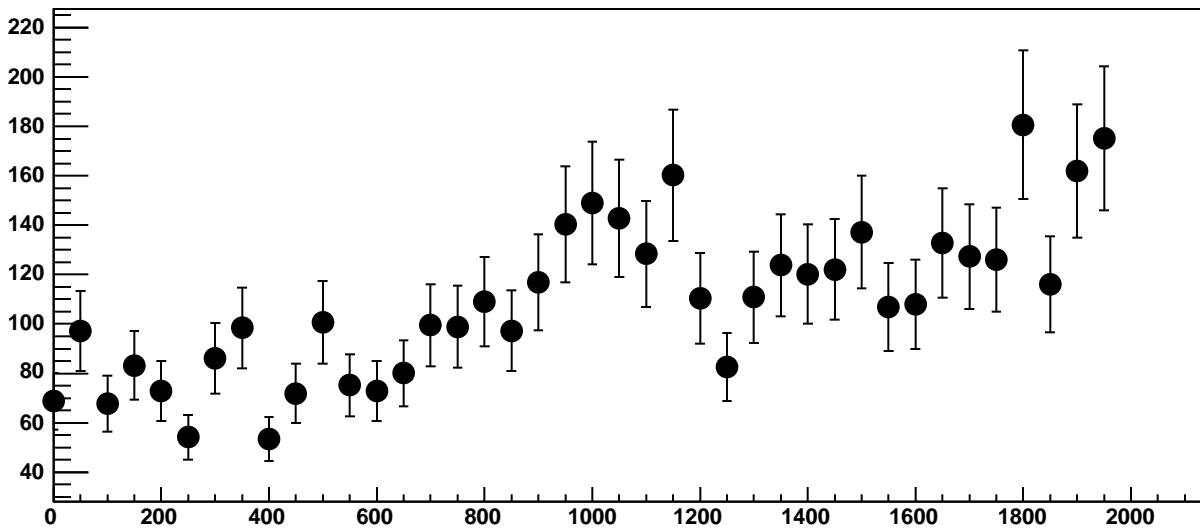
p0

$-625.6 \pm 42.57$

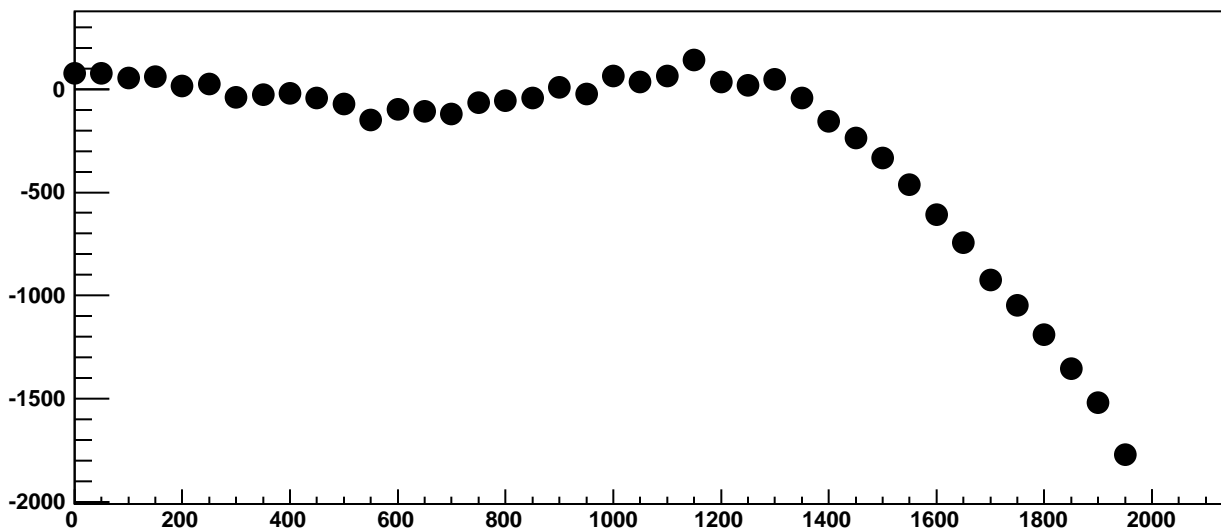
p1

$0.3525 \pm 0.03793$

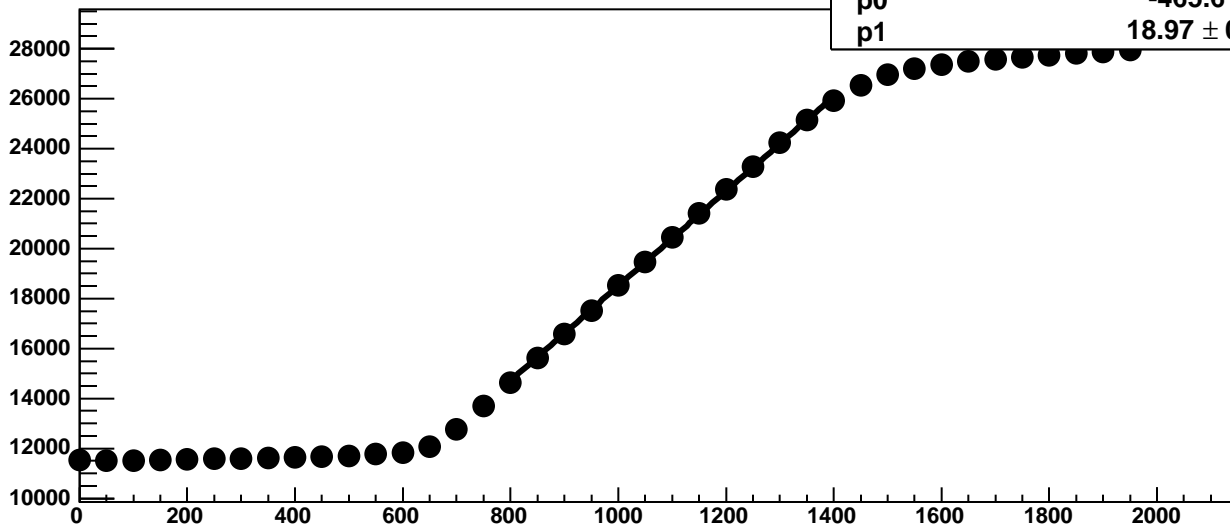
Chip 8, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC



Chip 8, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC

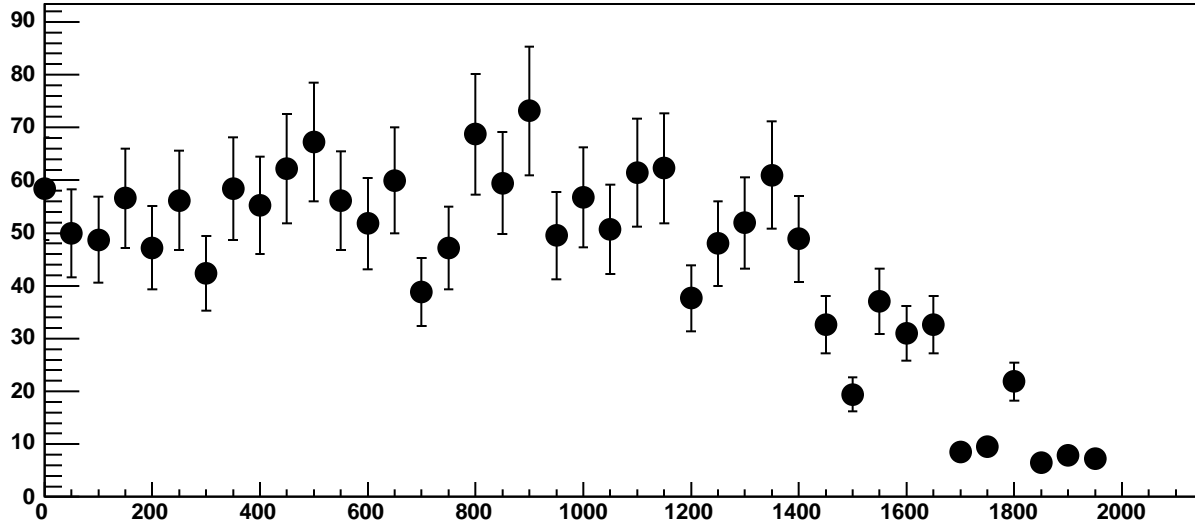


Chip 8, Channel 5, Enable 1!, Hold=30, ADC Mean vs DAC

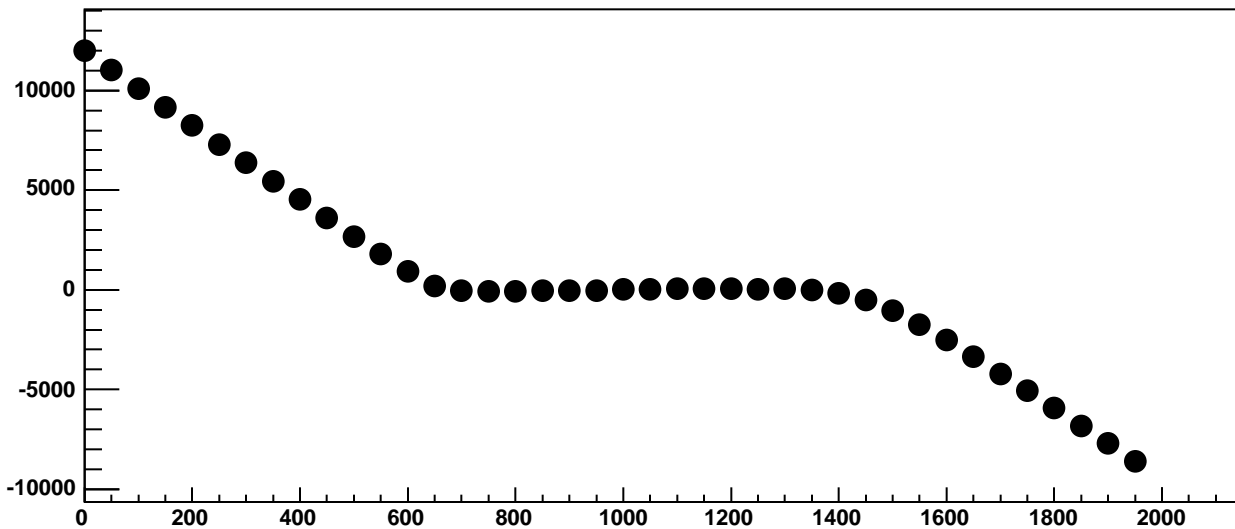


$\chi^2 / \text{ndf}$  408 / 11  
p0  $-465.6 \pm 22.03$   
p1  $18.97 \pm 0.01929$

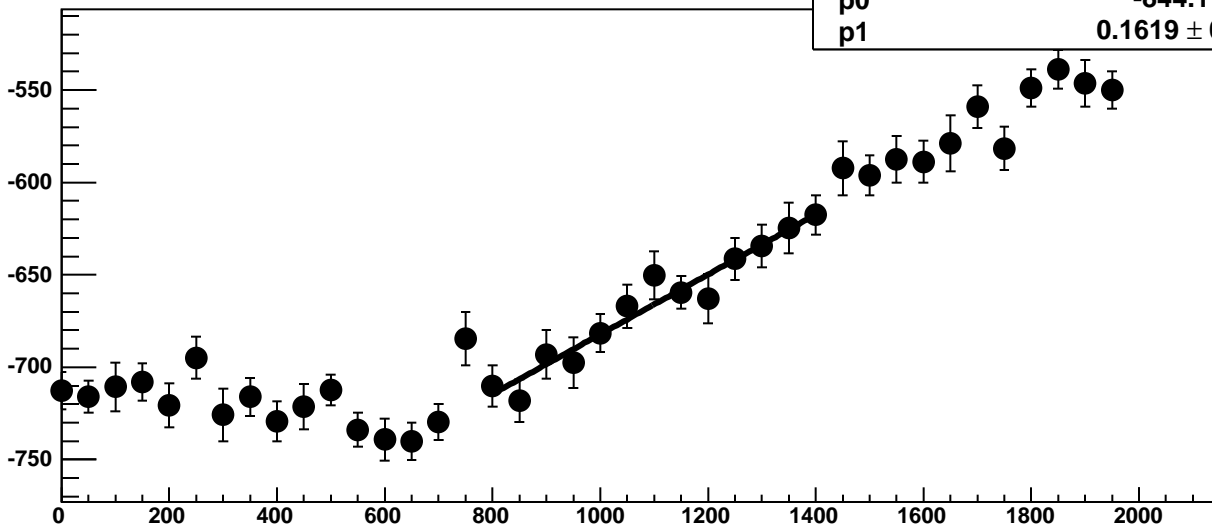
Chip 8, Channel 5, Enable 1!, Hold=30, ADC Noise vs DAC



Chip 8, Channel 5, Enable 1!, Hold=30, ADC Residuals vs DAC

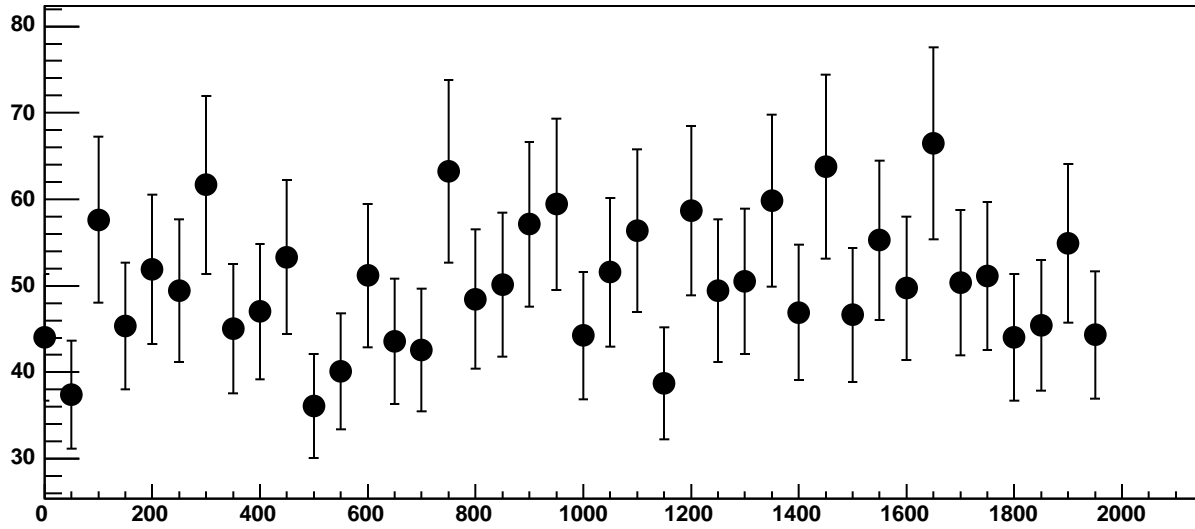


Chip 8, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC

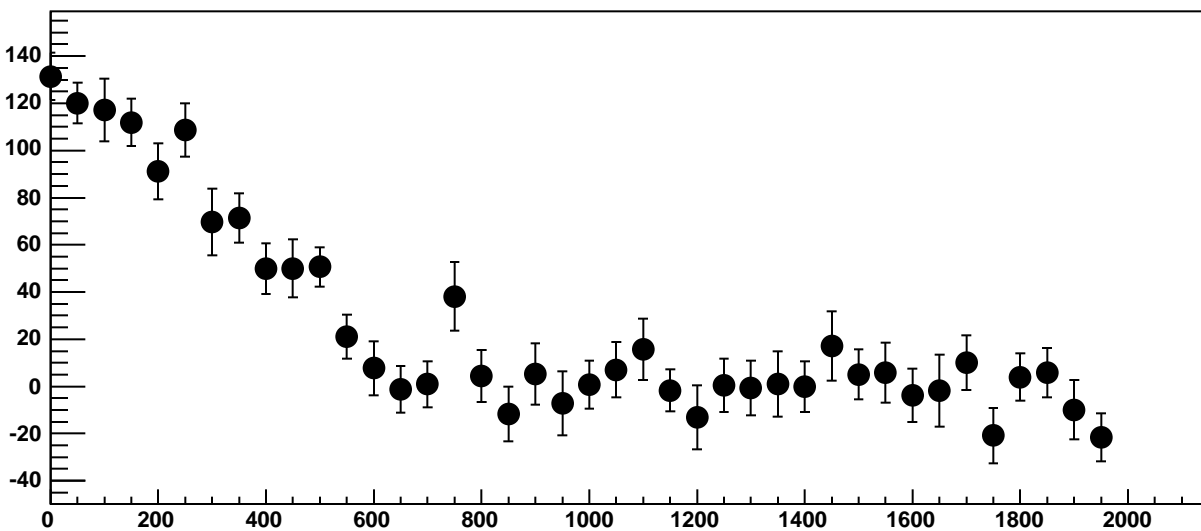


$\chi^2 / \text{ndf}$  4.468 / 11  
p0  $-844.1 \pm 19.33$   
p1  $0.1619 \pm 0.01731$

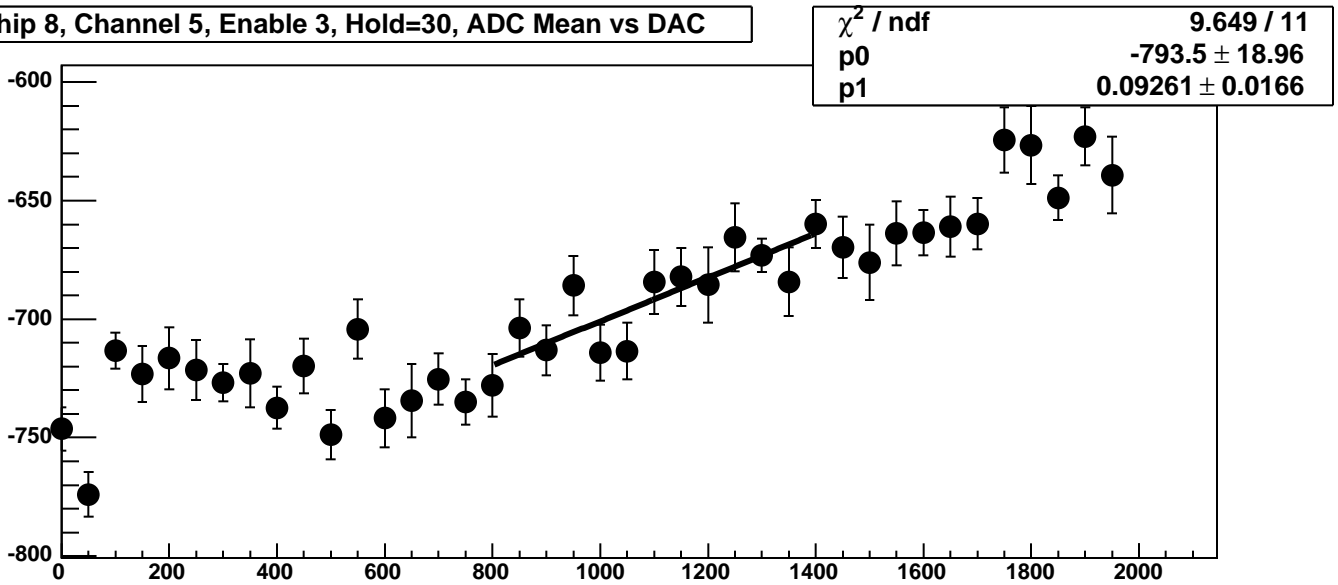
Chip 8, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC



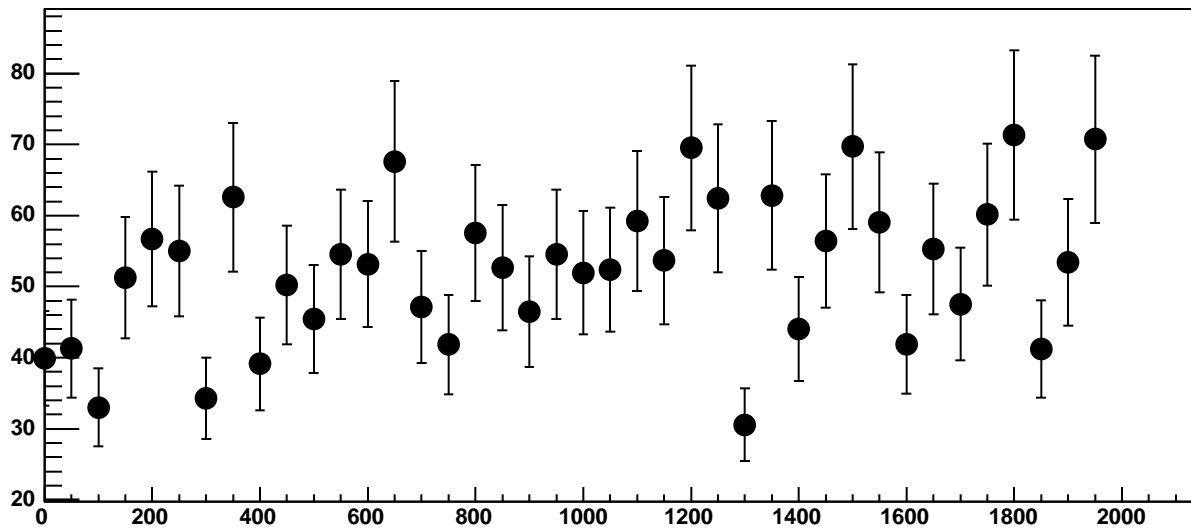
Chip 8, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC



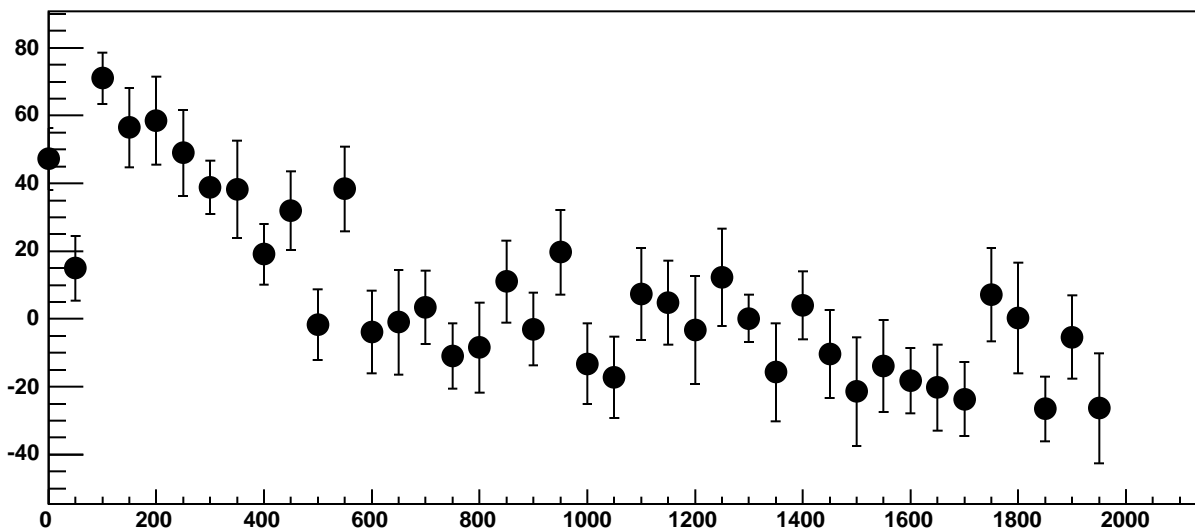
Chip 8, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC



Chip 8, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC

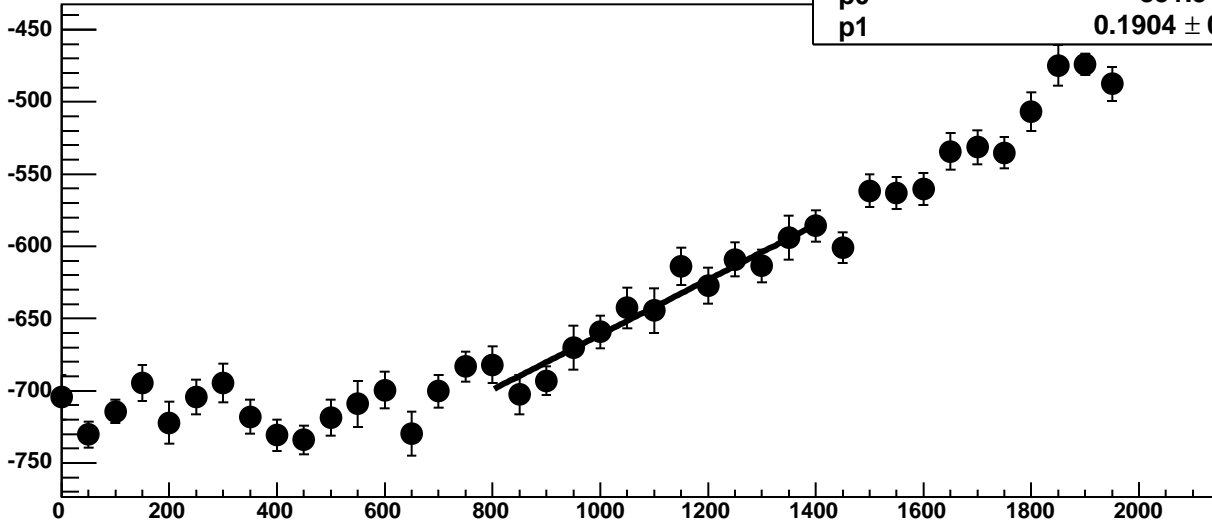


Chip 8, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC



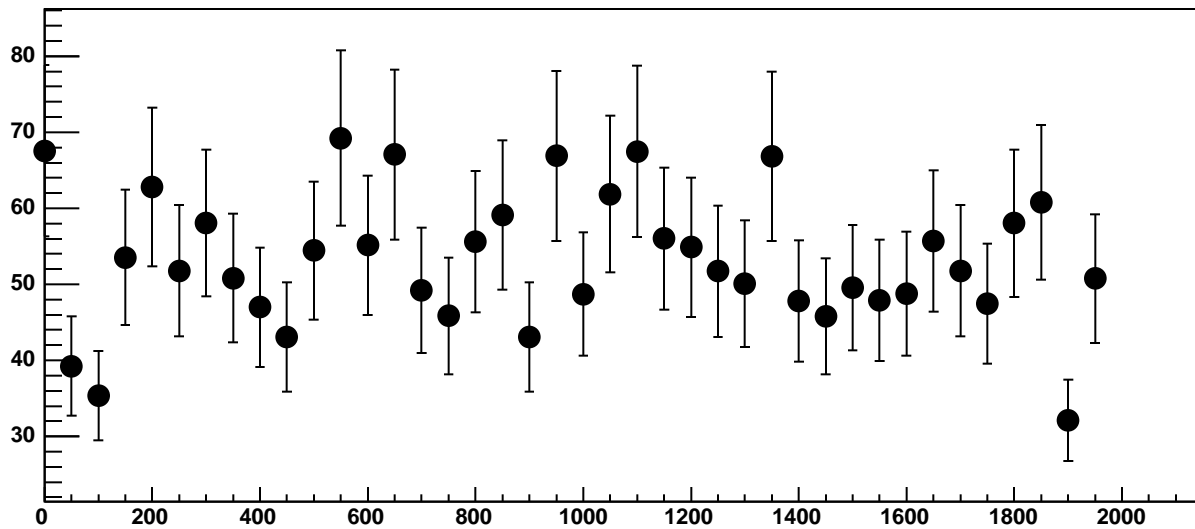


Chip 8, Channel 5, Enable 4, Hold=30, ADC Mean vs DAC

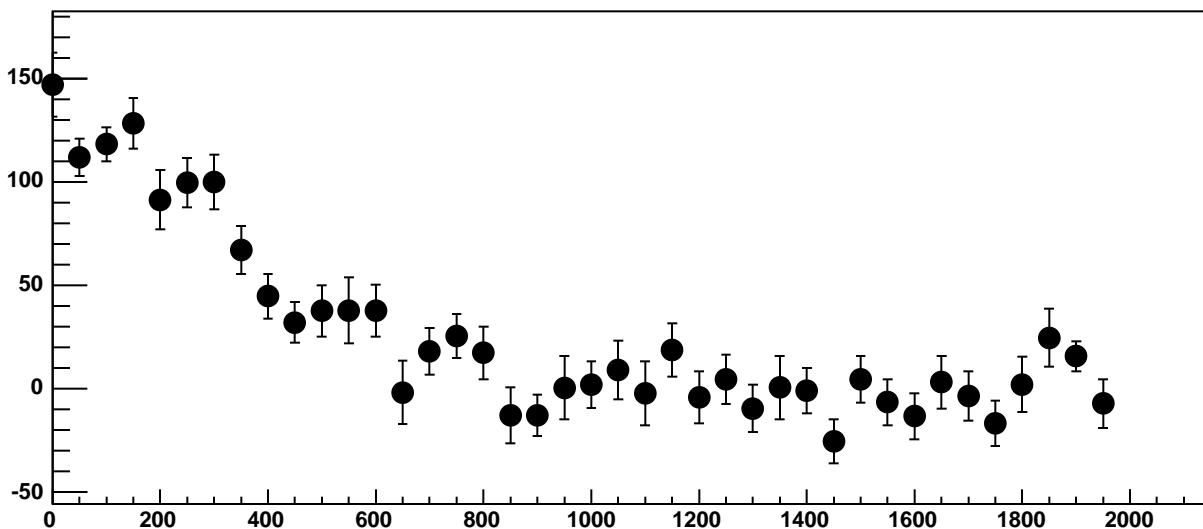


$\chi^2 / \text{ndf}$  7.953 / 11  
p0  $-851.5 \pm 20.27$   
p1  $0.1904 \pm 0.01812$

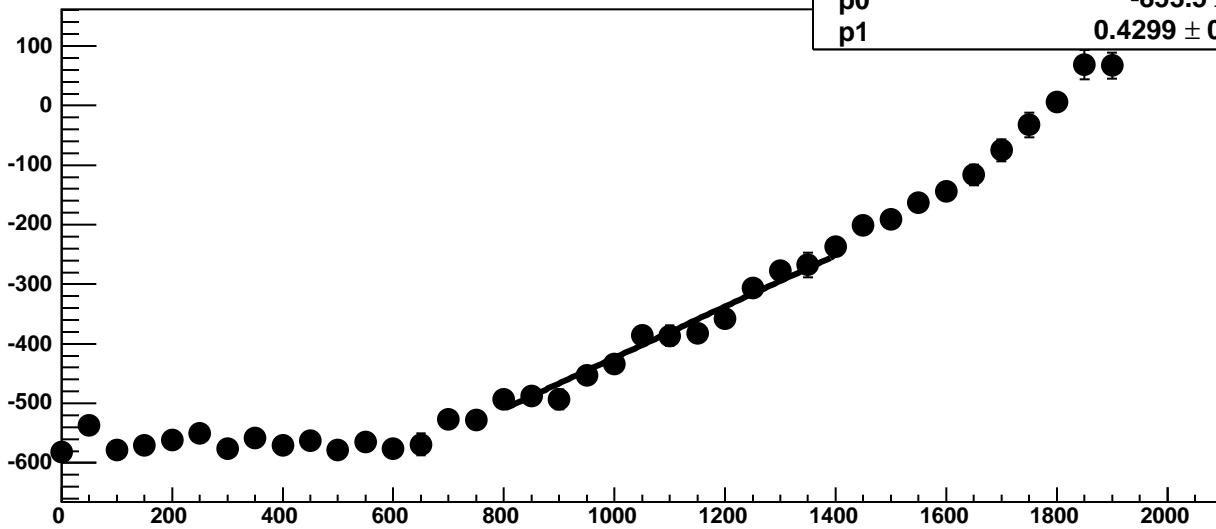
Chip 8, Channel 5, Enable 4, Hold=30, ADC Noise vs DAC



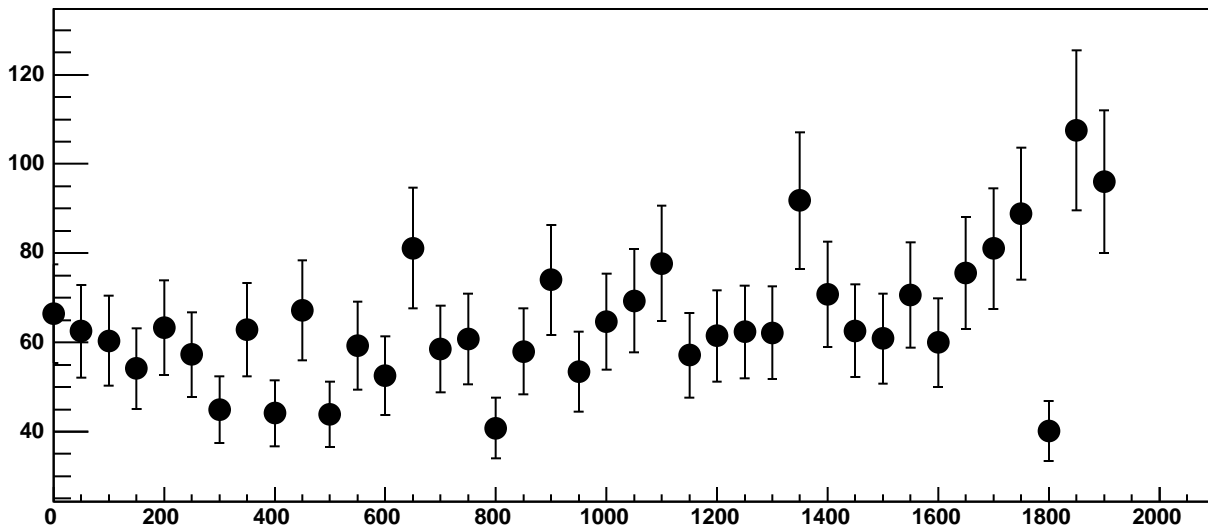
Chip 8, Channel 5, Enable 4, Hold=30, ADC Residuals vs DAC



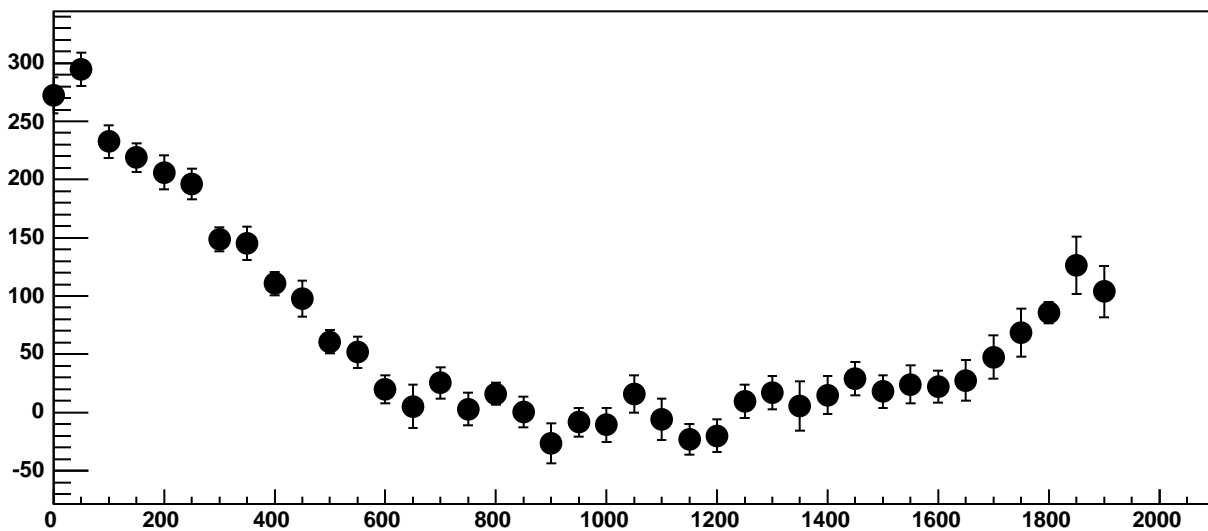
Chip 8, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC



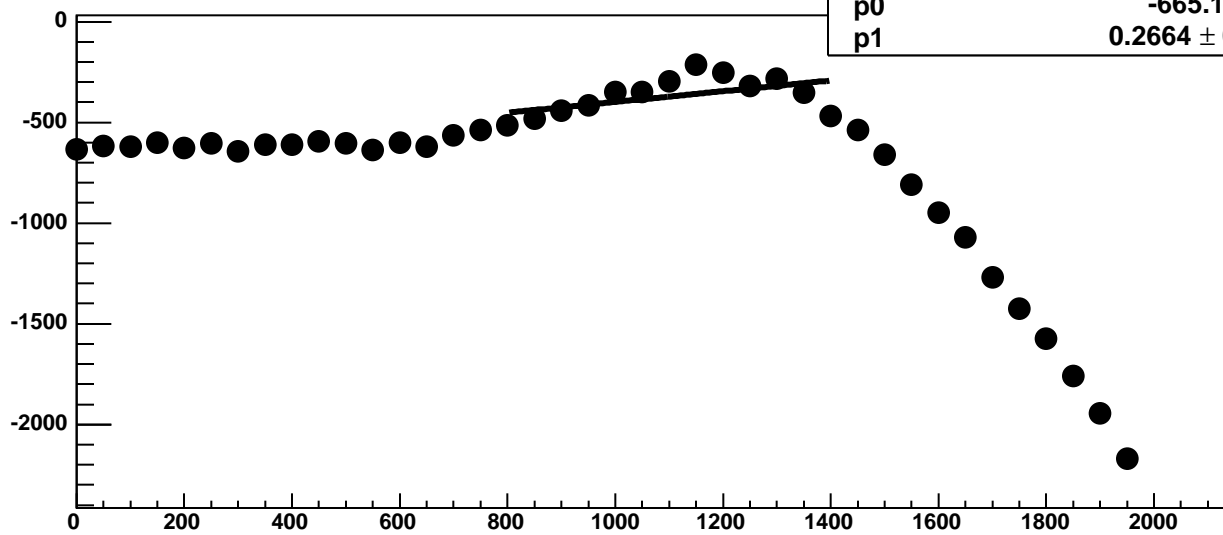
Chip 8, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC



Chip 8, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 8, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

87.75 / 11

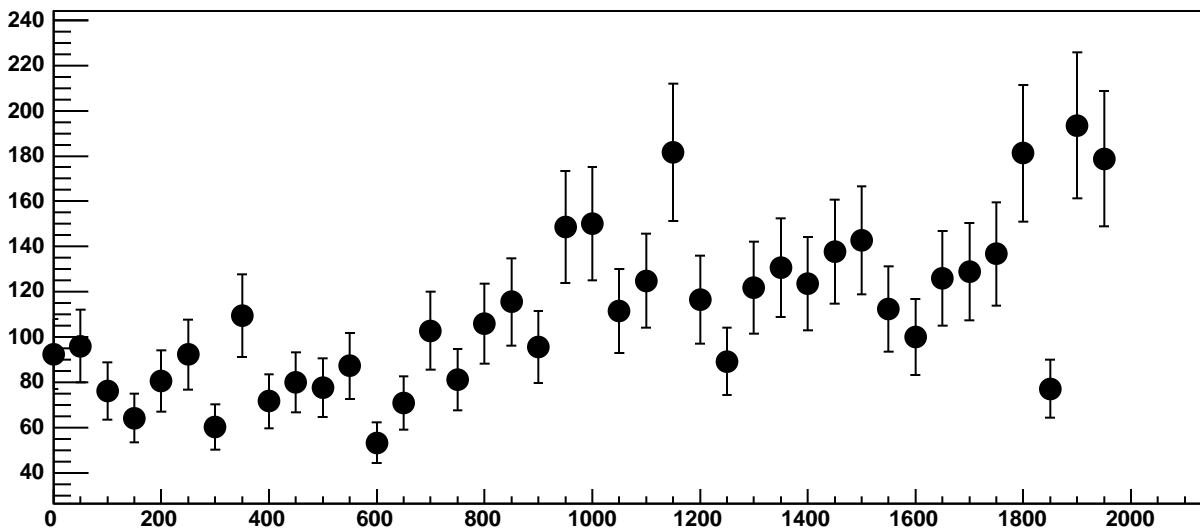
p0

$-665.1 \pm 43.29$

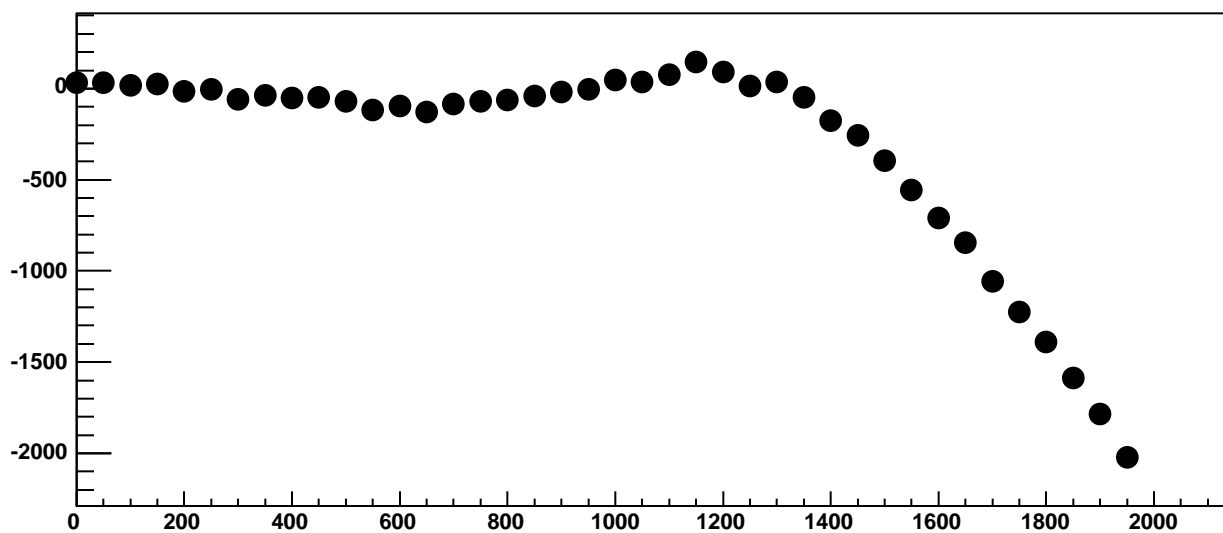
p1

$0.2664 \pm 0.03903$

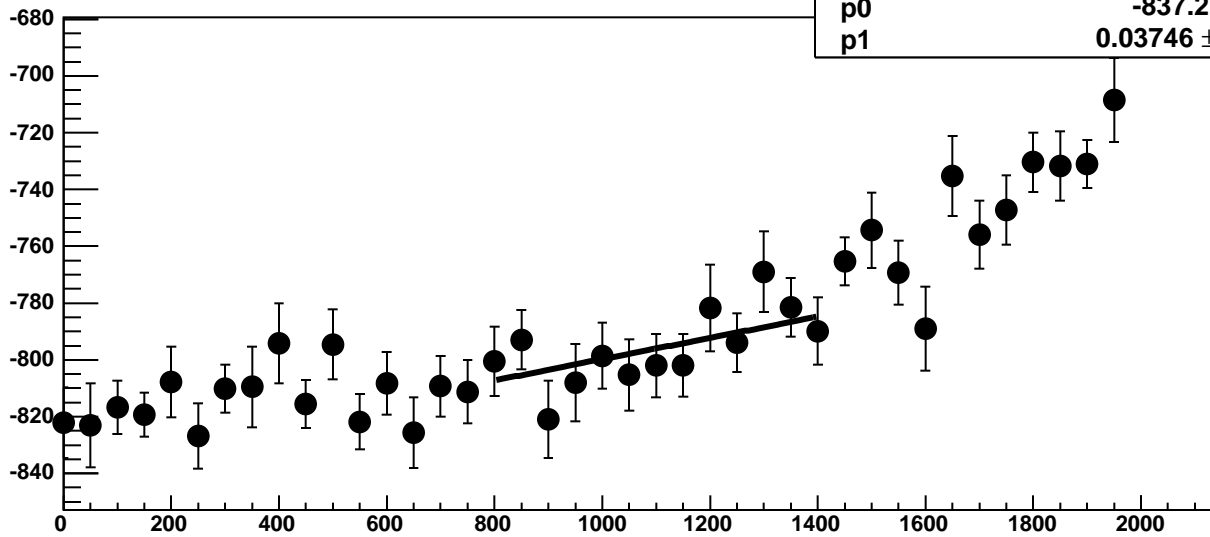
Chip 8, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC



Chip 8, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 8, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

7.645 / 11

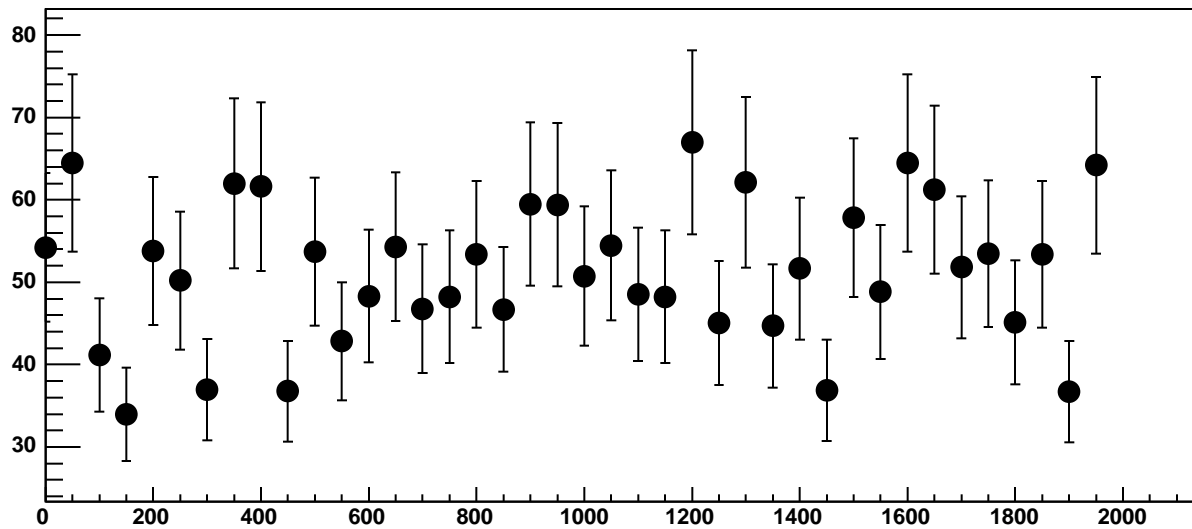
p0

$-837.2 \pm 19.52$

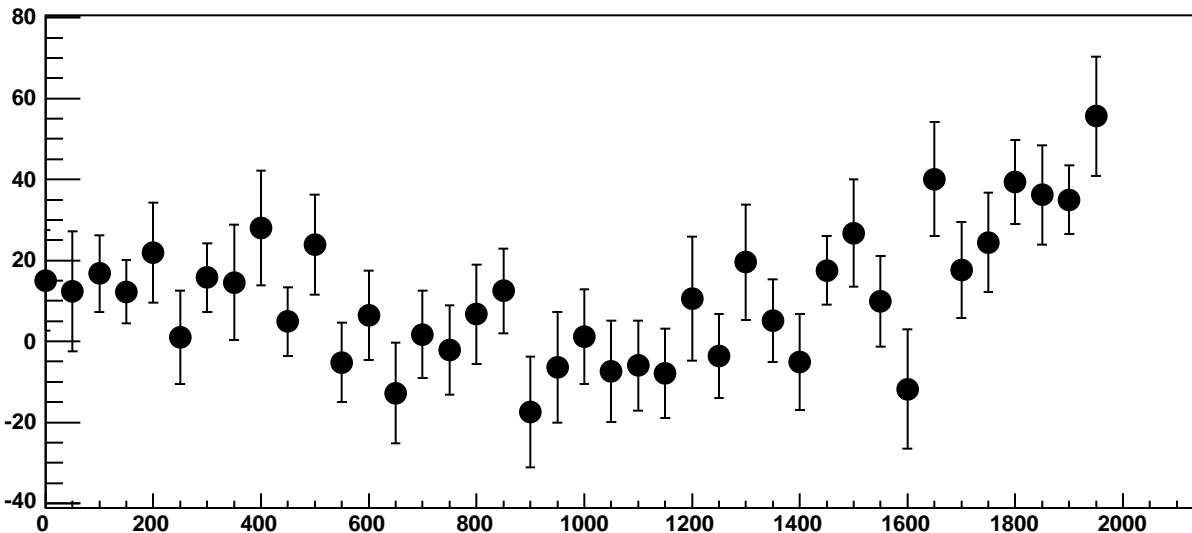
p1

$0.03746 \pm 0.0174$

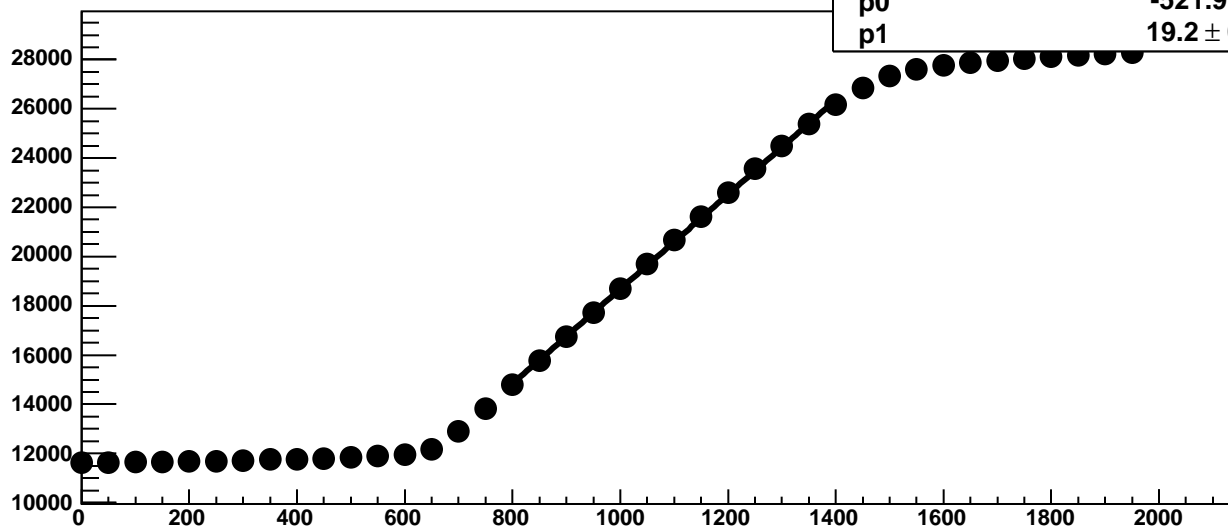
Chip 8, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC



Chip 8, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC

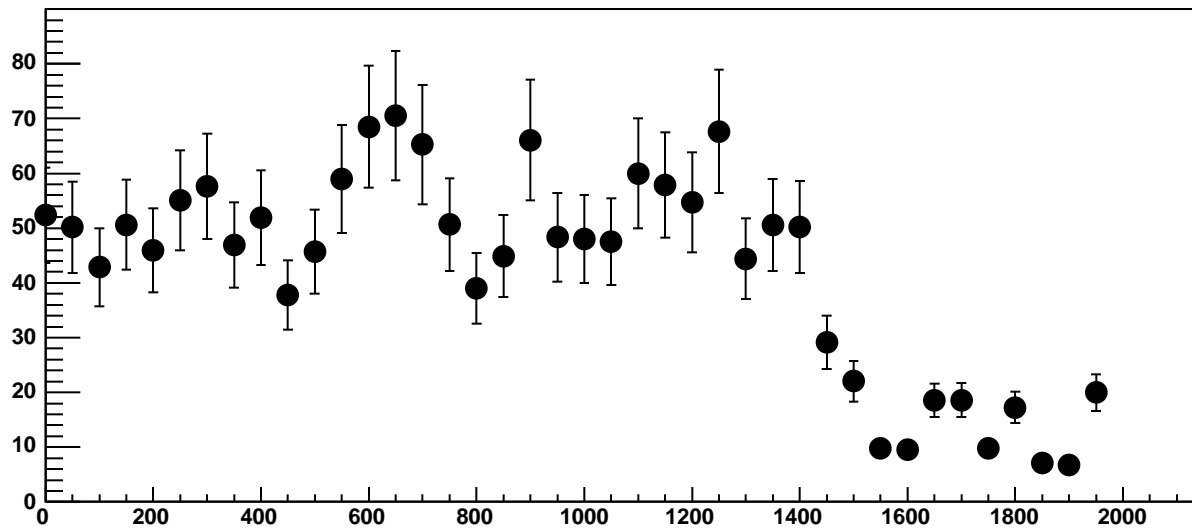


Chip 8, Channel 6, Enable 2!, Hold=30, ADC Mean vs DAC

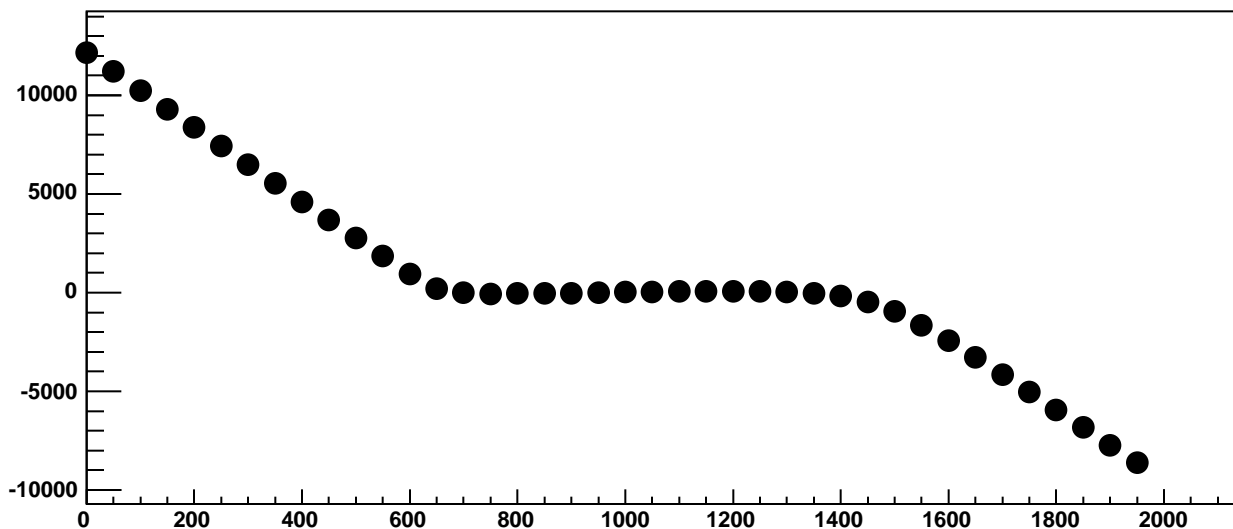


$\chi^2 / \text{ndf}$  432.7 / 11  
p0  $-521.9 \pm 17.74$   
p1  $19.2 \pm 0.01615$

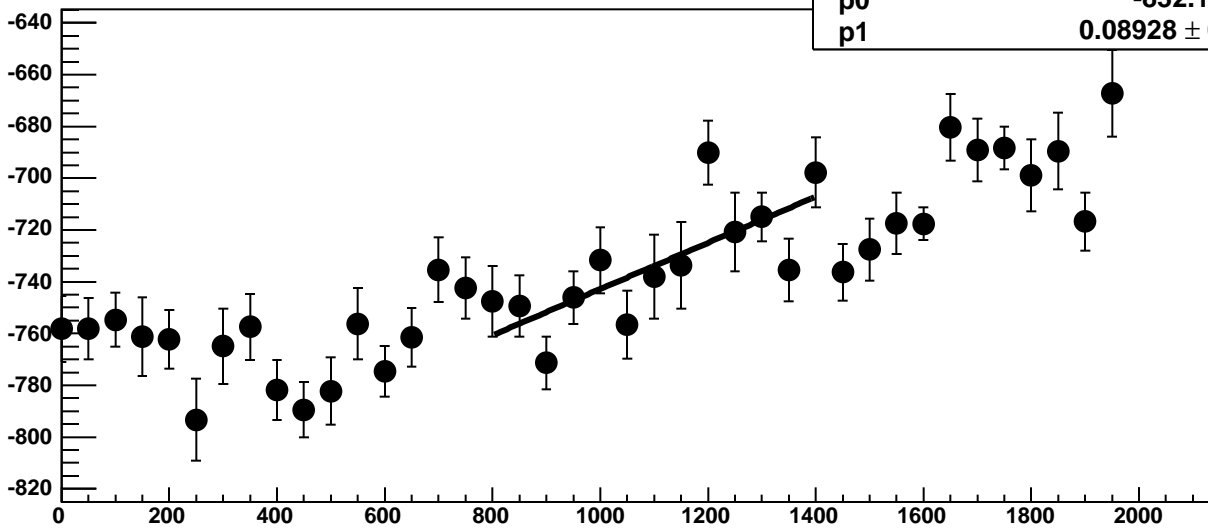
Chip 8, Channel 6, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 8, Channel 6, Enable 2!, Hold=30, ADC Residuals vs DAC



Chip 8, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

20.14 / 11

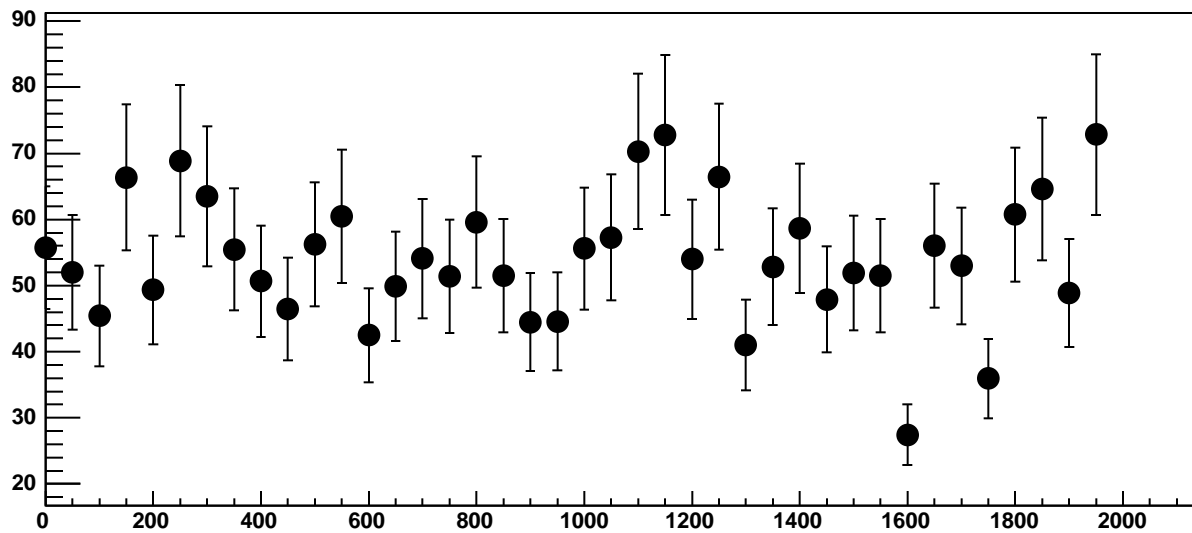
p0

$-832.1 \pm 19.79$

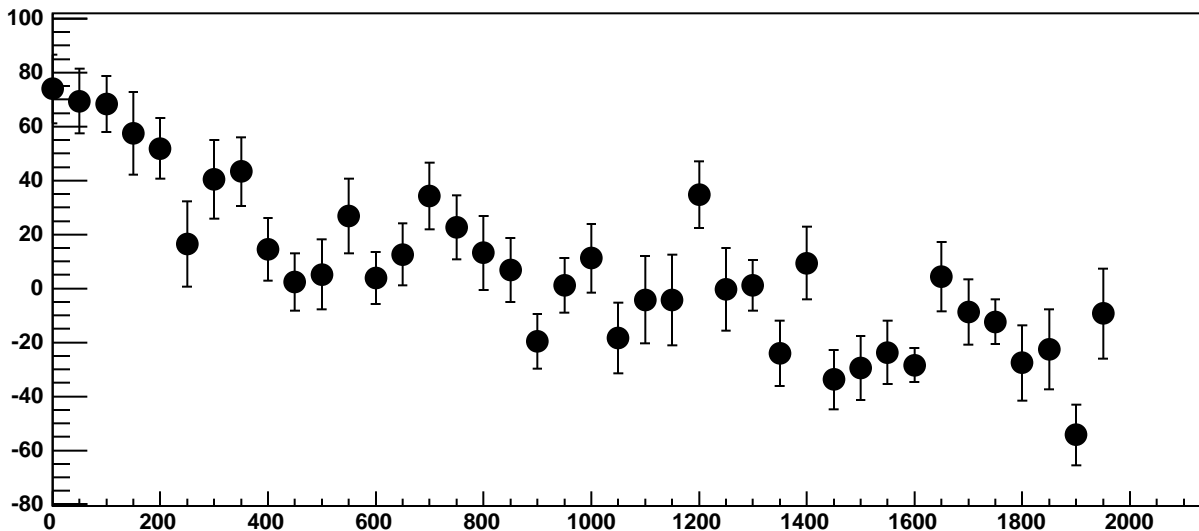
p1

$0.08928 \pm 0.01783$

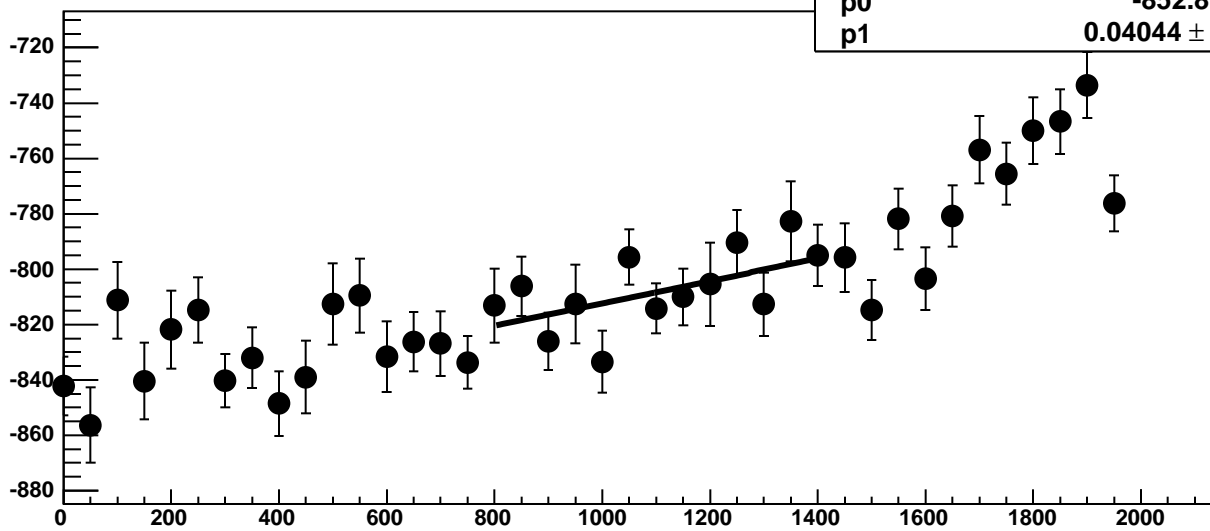
Chip 8, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC



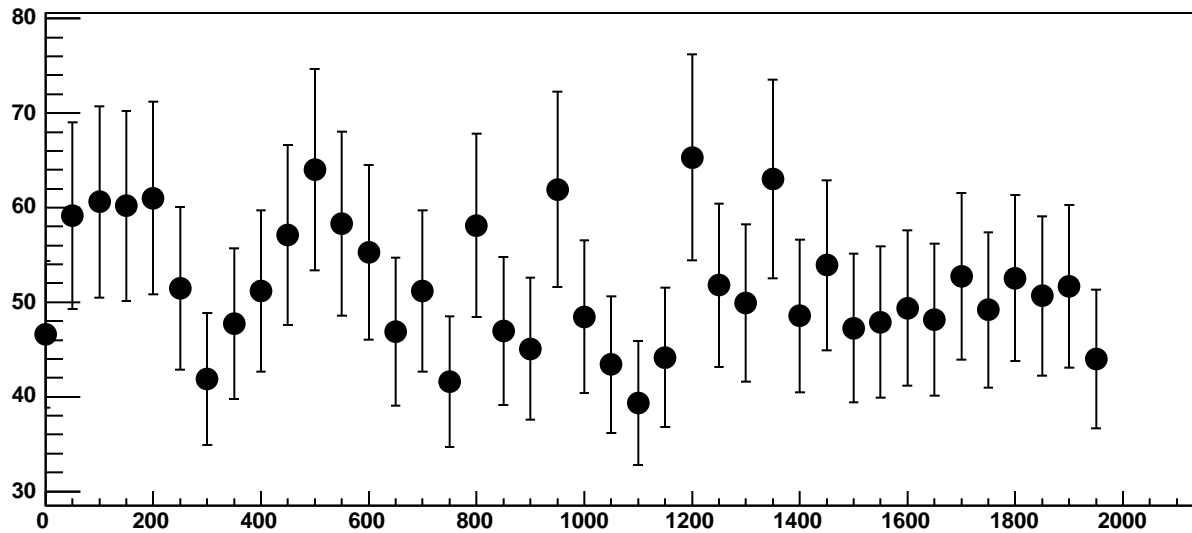
Chip 8, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC



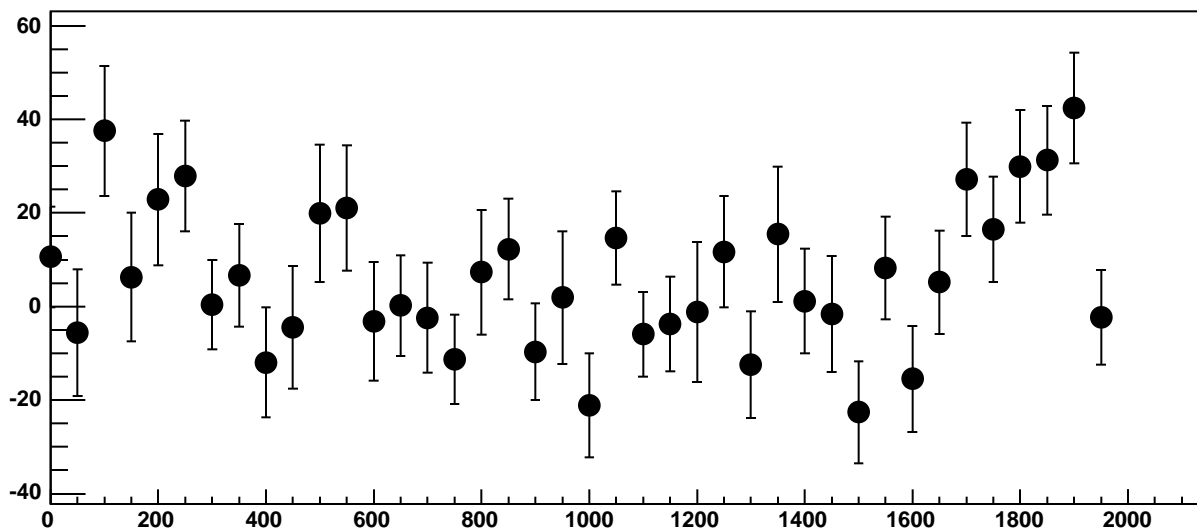
Chip 8, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC



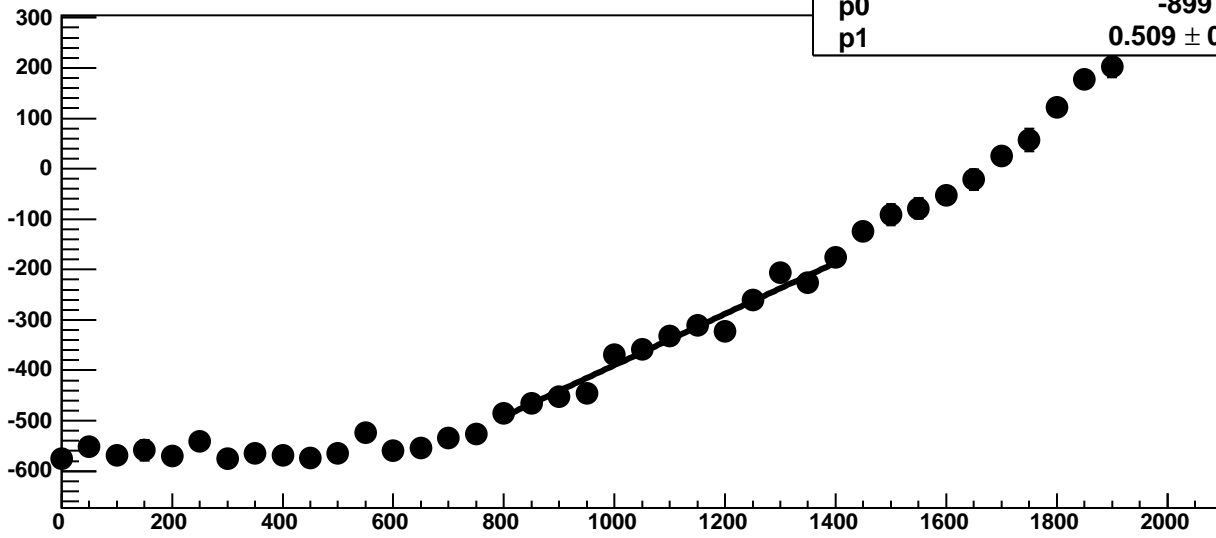
Chip 8, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC



Chip 8, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC

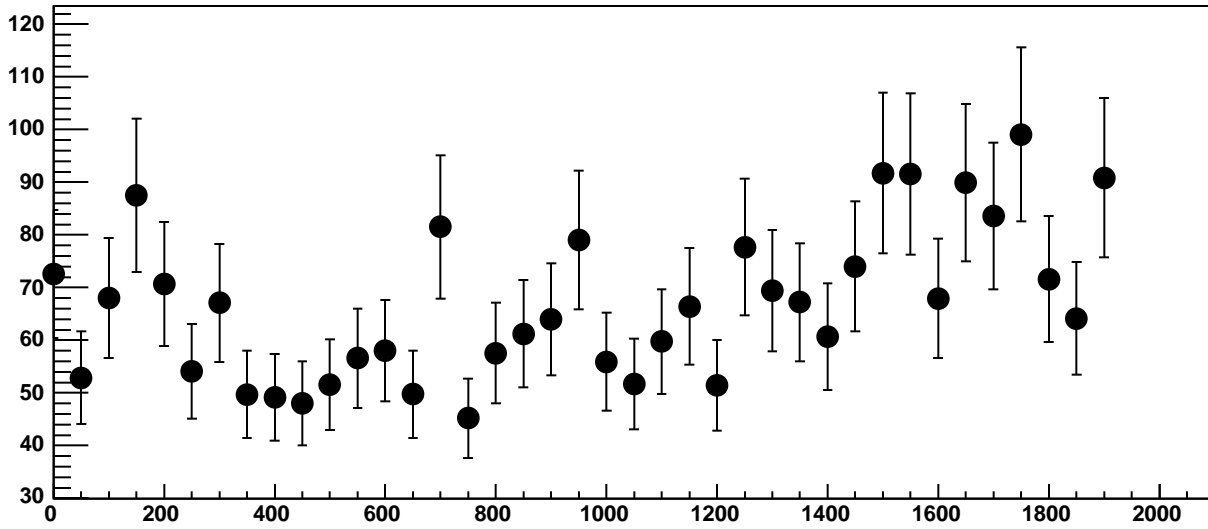


Chip 8, Channel 6, Enable 5, Hold=30, ADC Mean vs DAC

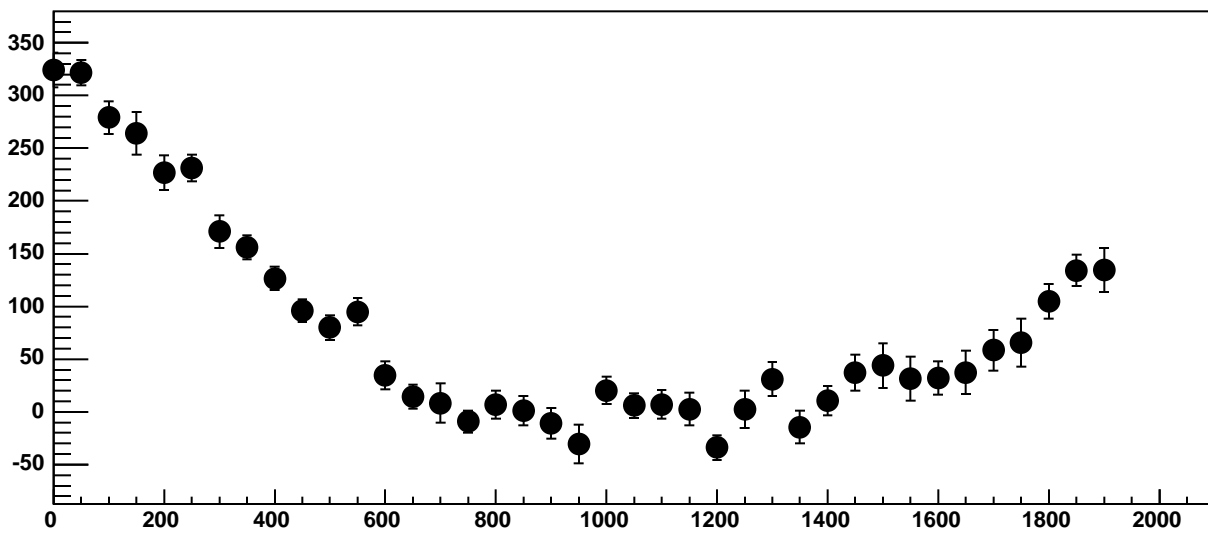


$\chi^2 / \text{ndf}$  20.25 / 11  
p0  $-899 \pm 23.61$   
p1  $0.509 \pm 0.02132$

Chip 8, Channel 6, Enable 5, Hold=30, ADC Noise vs DAC

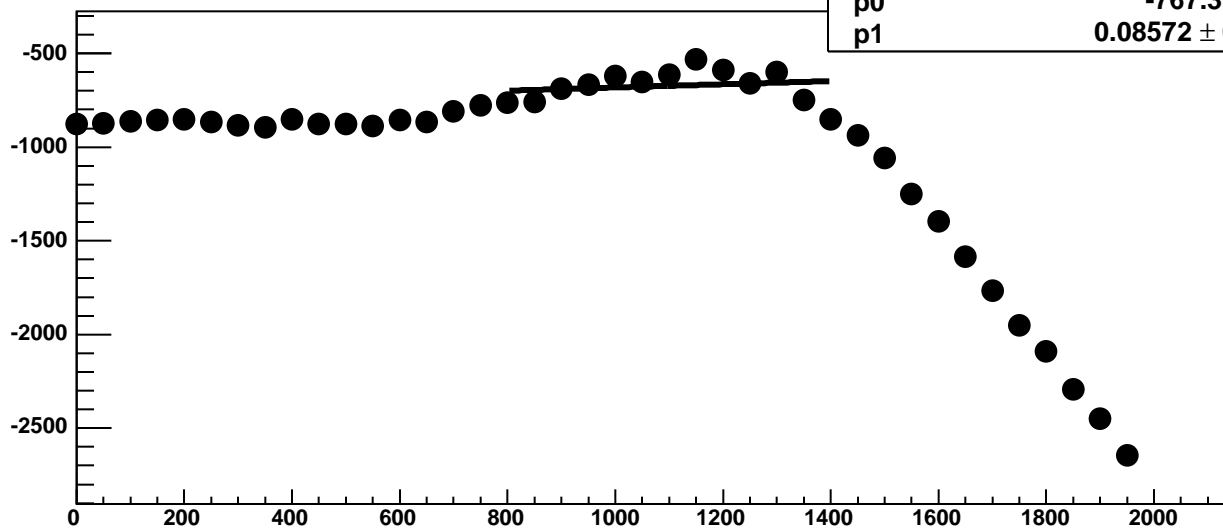


Chip 8, Channel 6, Enable 5, Hold=30, ADC Residuals vs DAC





Chip 8, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

96.31 / 11

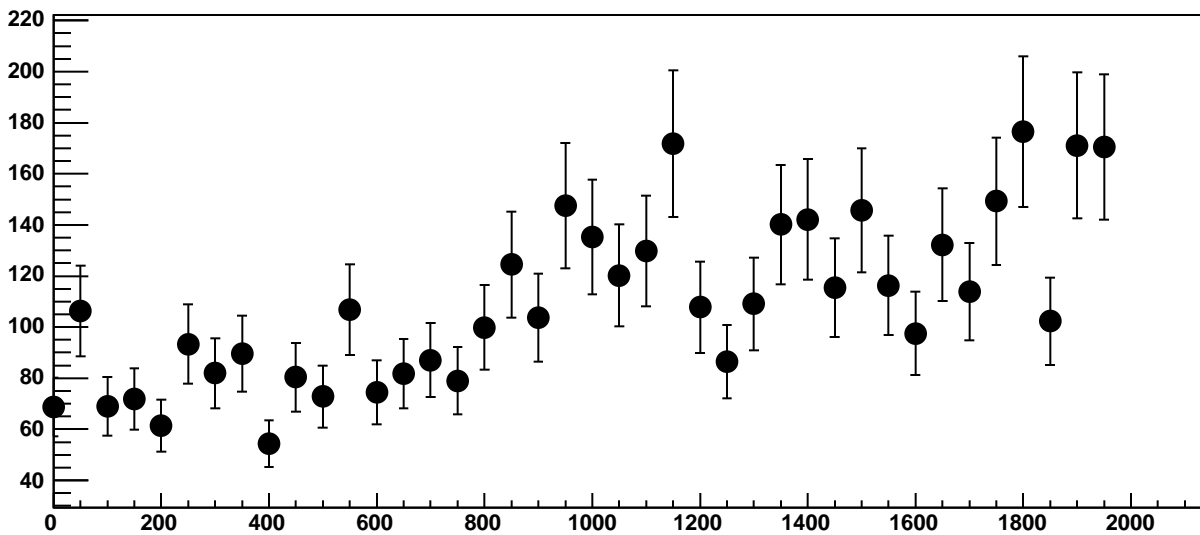
p0

$-767.3 \pm 44.04$

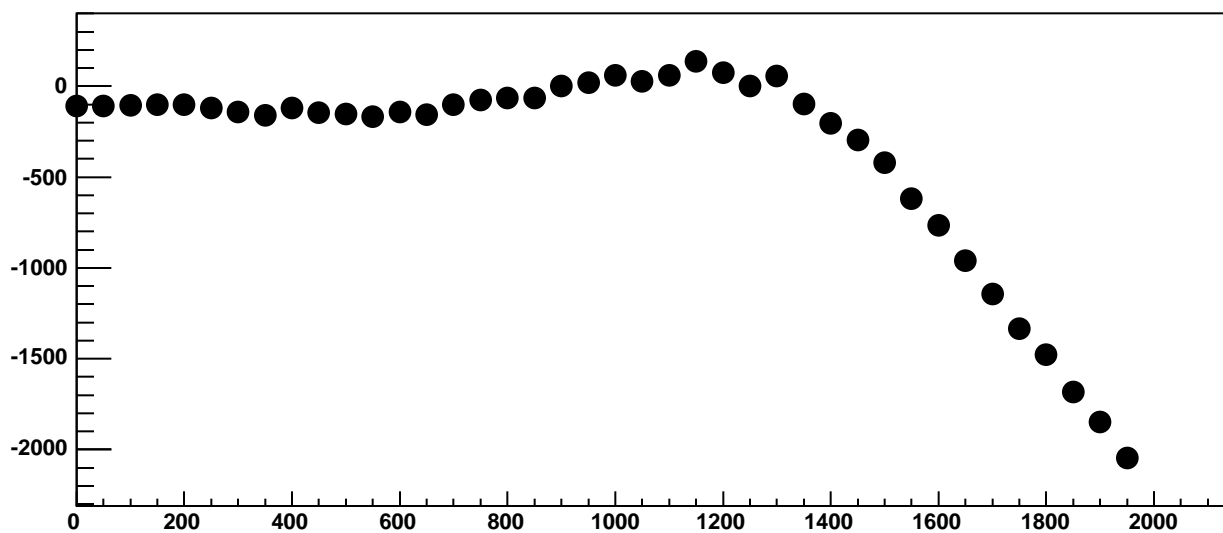
p1

$0.08572 \pm 0.03967$

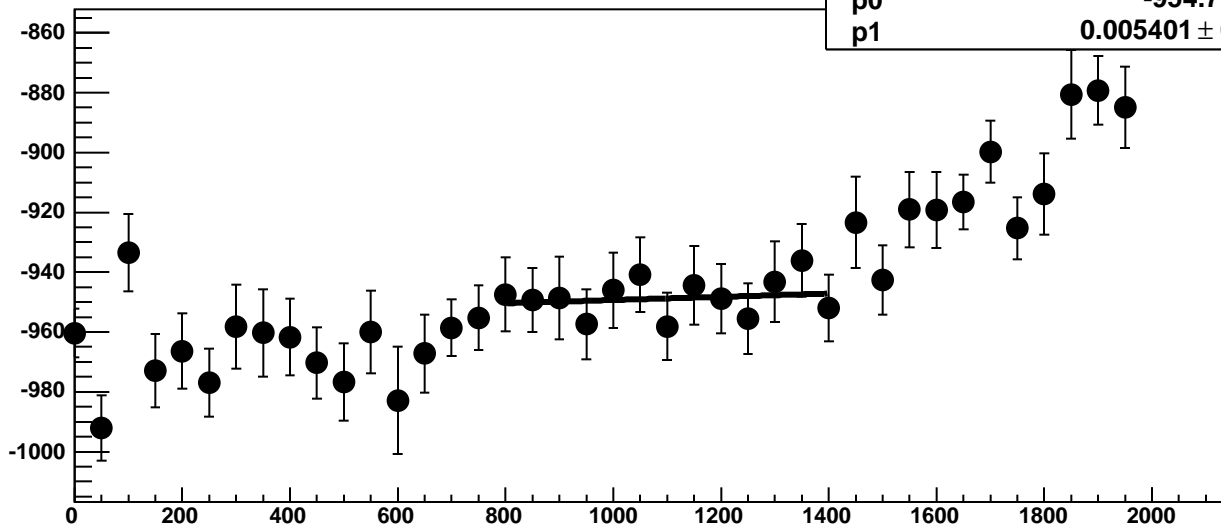
Chip 8, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC



Chip 8, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC

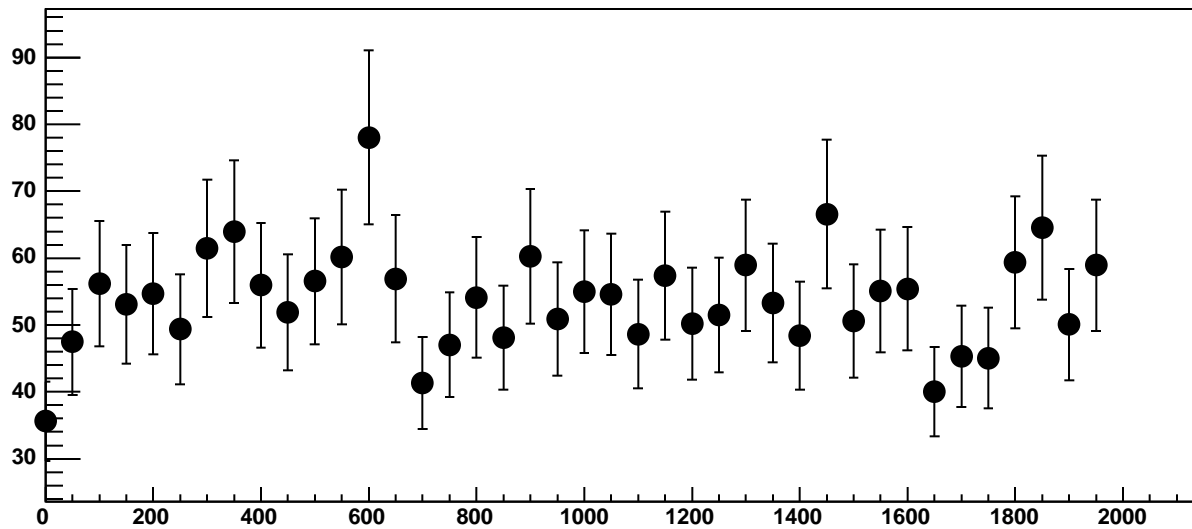


Chip 8, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC

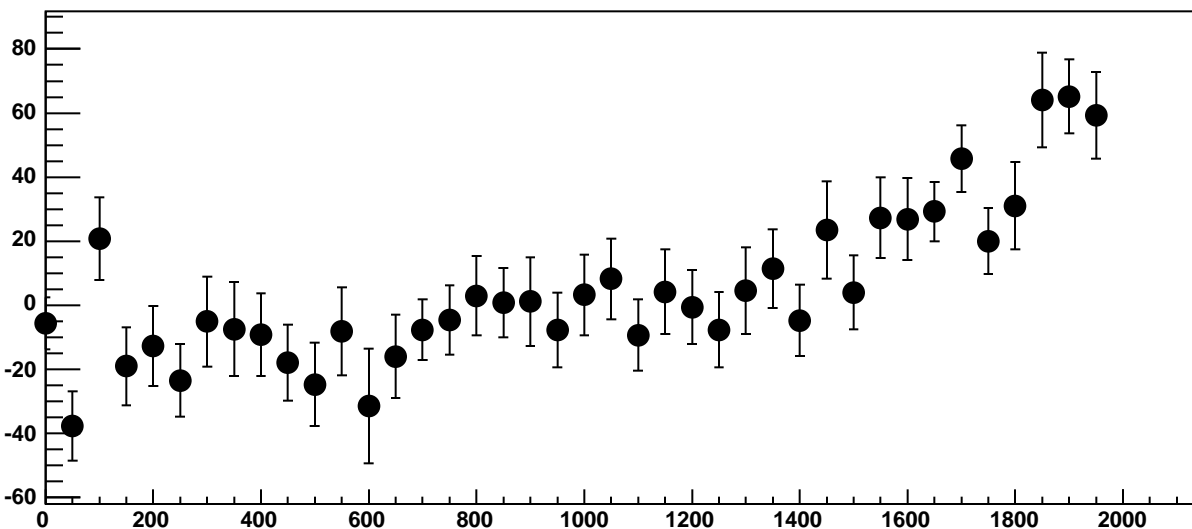


$\chi^2 / \text{ndf}$  3.392 / 11  
p0  $-954.7 \pm 19.77$   
p1  $0.005401 \pm 0.01769$

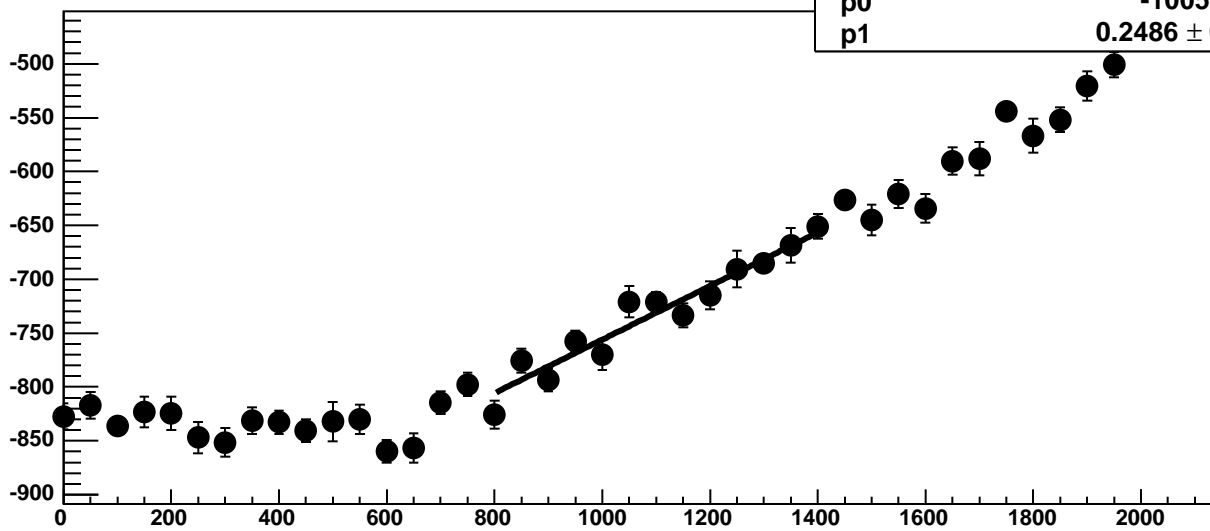
Chip 8, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC



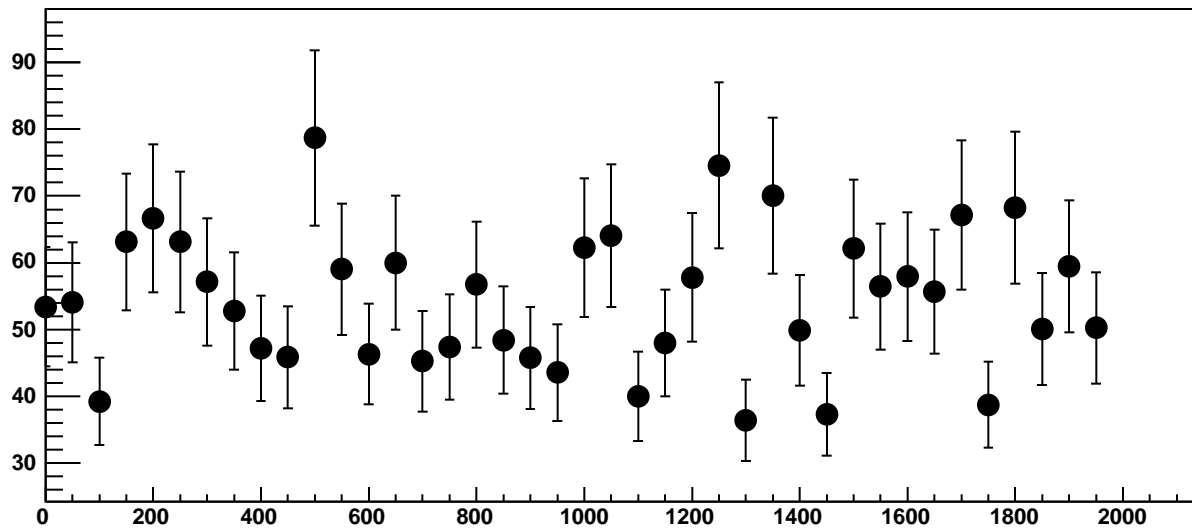
Chip 8, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC



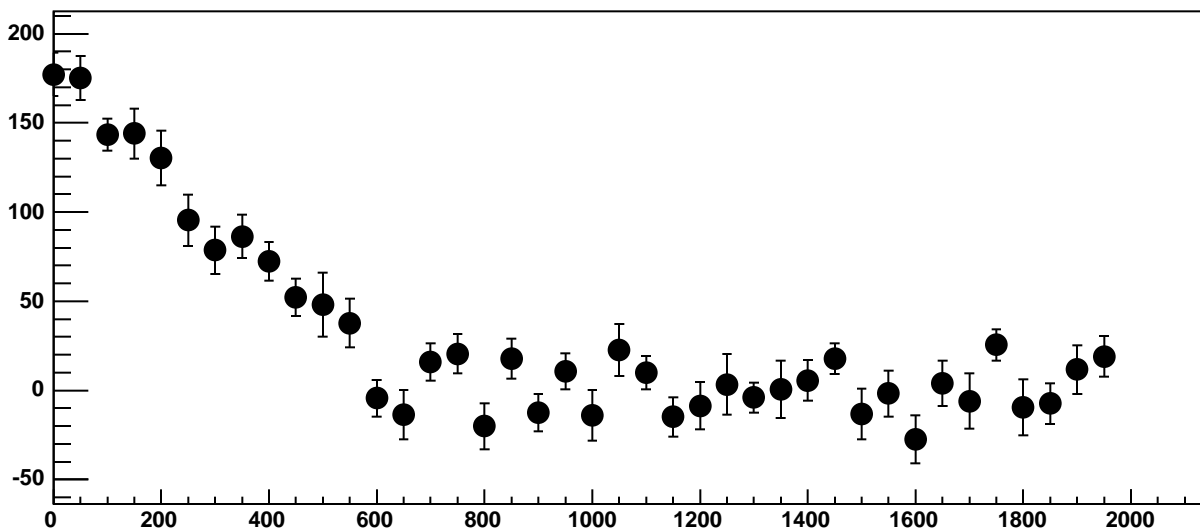
Chip 8, Channel 7, Enable 2, Hold=30, ADC Mean vs DAC



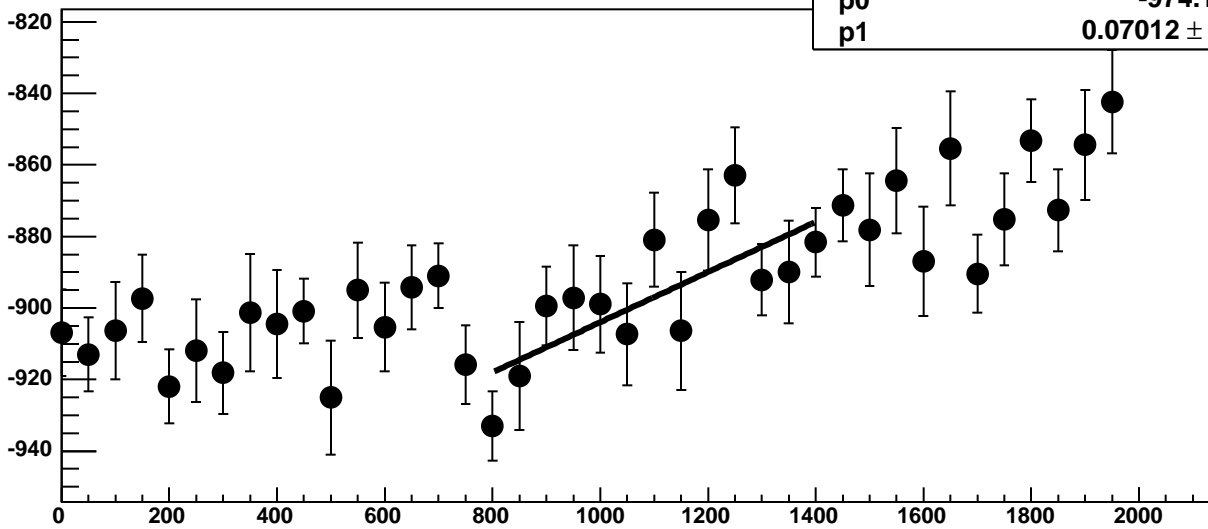
Chip 8, Channel 7, Enable 2, Hold=30, ADC Noise vs DAC



Chip 8, Channel 7, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 8, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

12.39 / 11

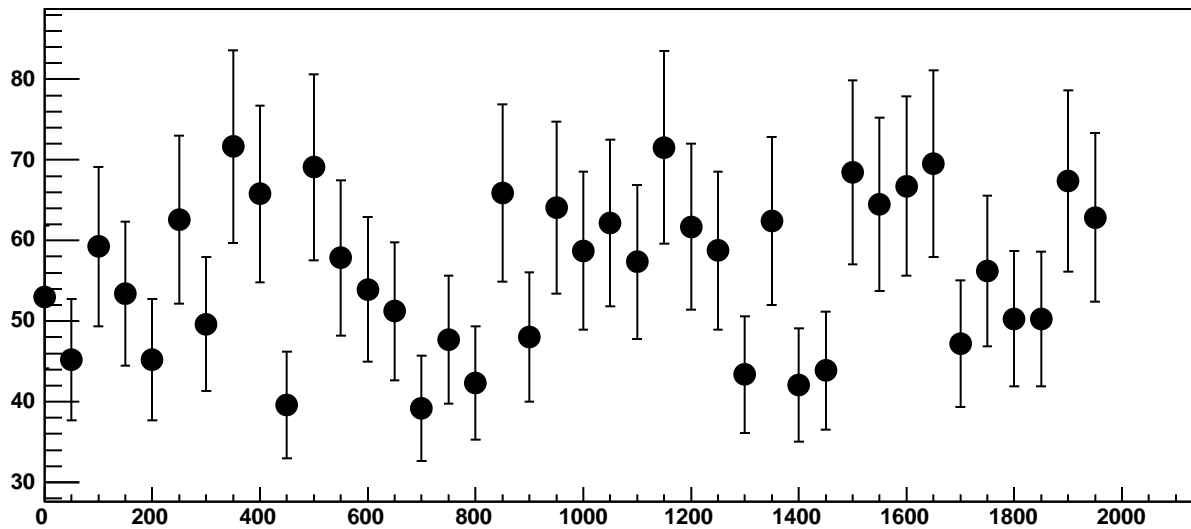
p0

$-974.1 \pm 18.81$

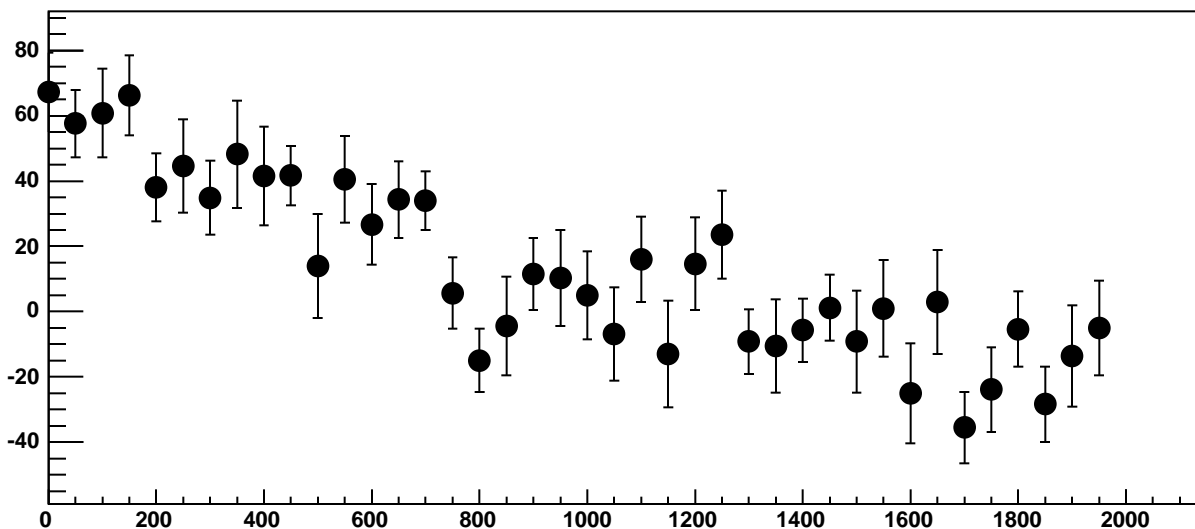
p1

$0.07012 \pm 0.01671$

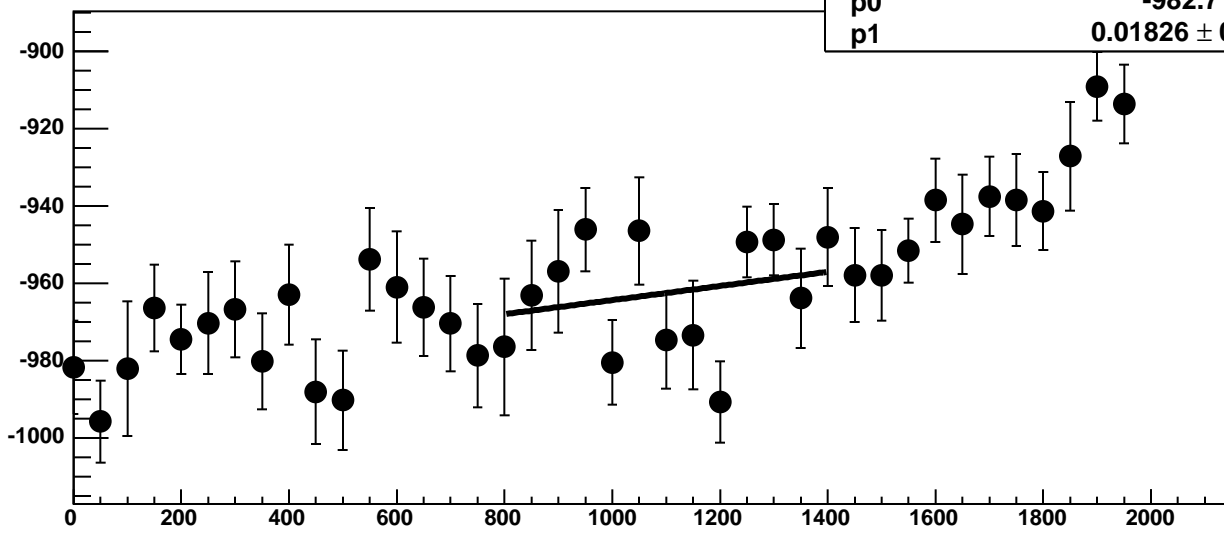
Chip 8, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC



Chip 8, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC

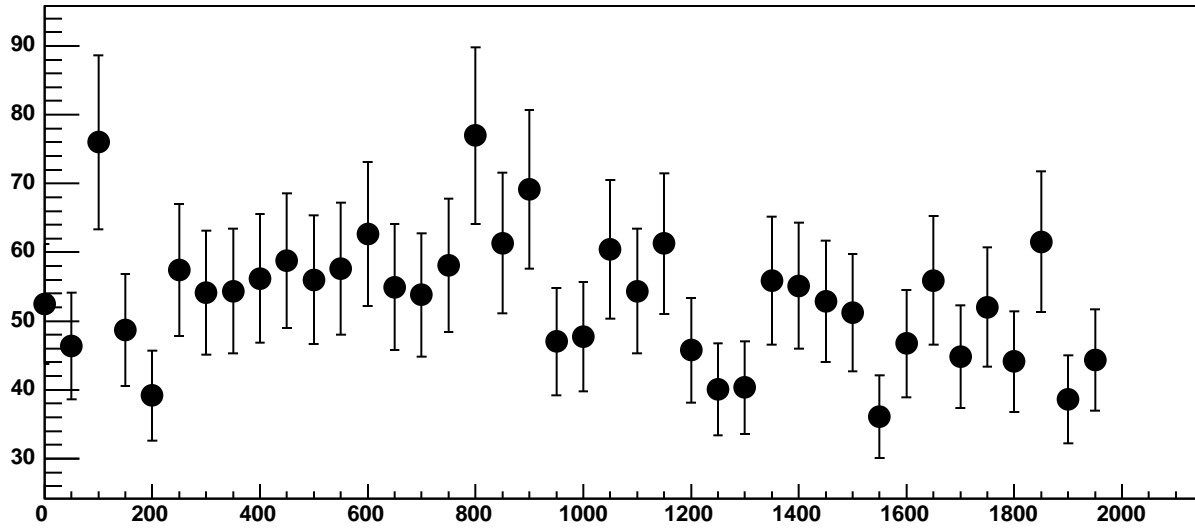


Chip 8, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC

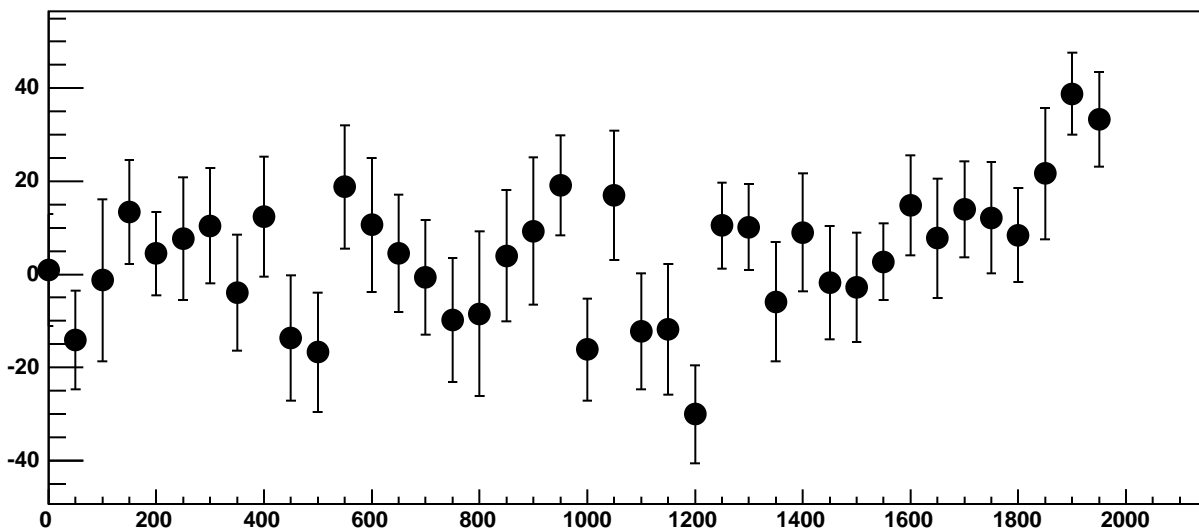


$\chi^2 / \text{ndf}$  20.55 / 11  
p0  $-982.7 \pm 22.05$   
p1  $0.01826 \pm 0.01919$

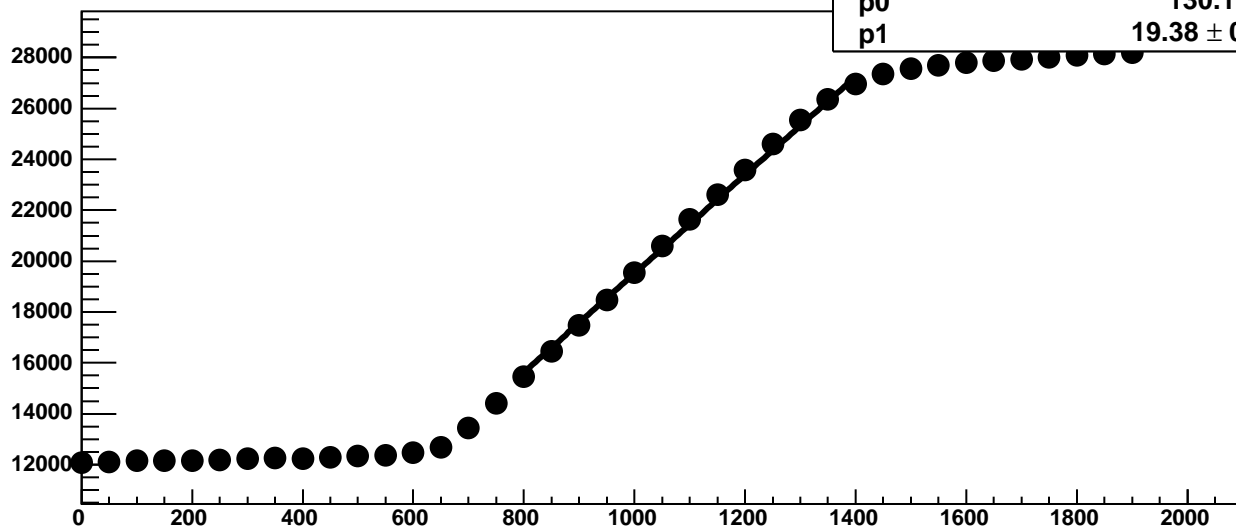
Chip 8, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC



Chip 8, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC

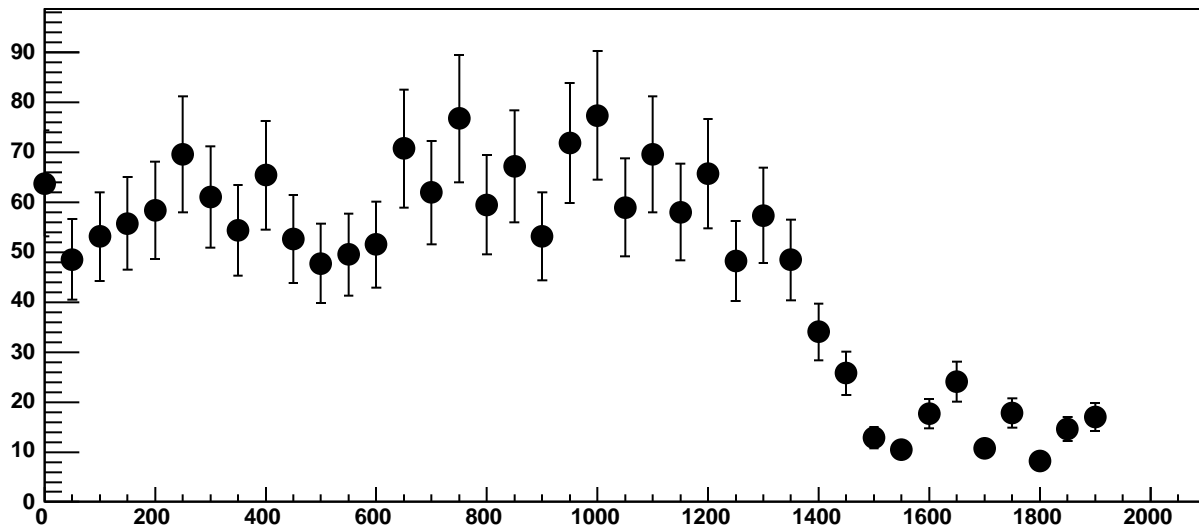


Chip 8, Channel 7, Enable 5!, Hold=30, ADC Mean vs DAC

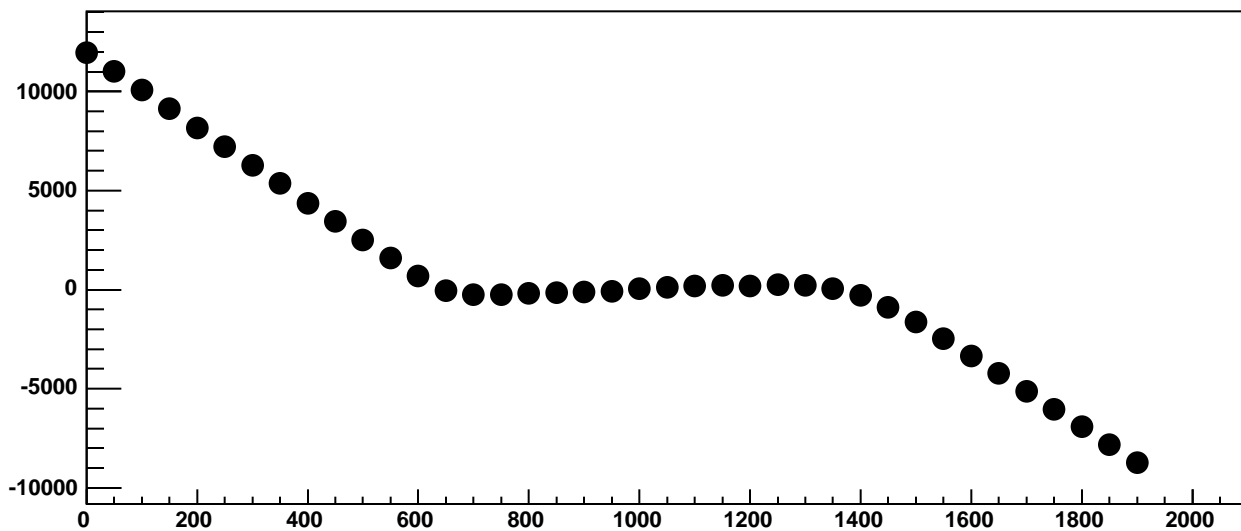


$\chi^2 / \text{ndf}$	3366 / 11
p0	$130.1 \pm 20.51$
p1	$19.38 \pm 0.01741$

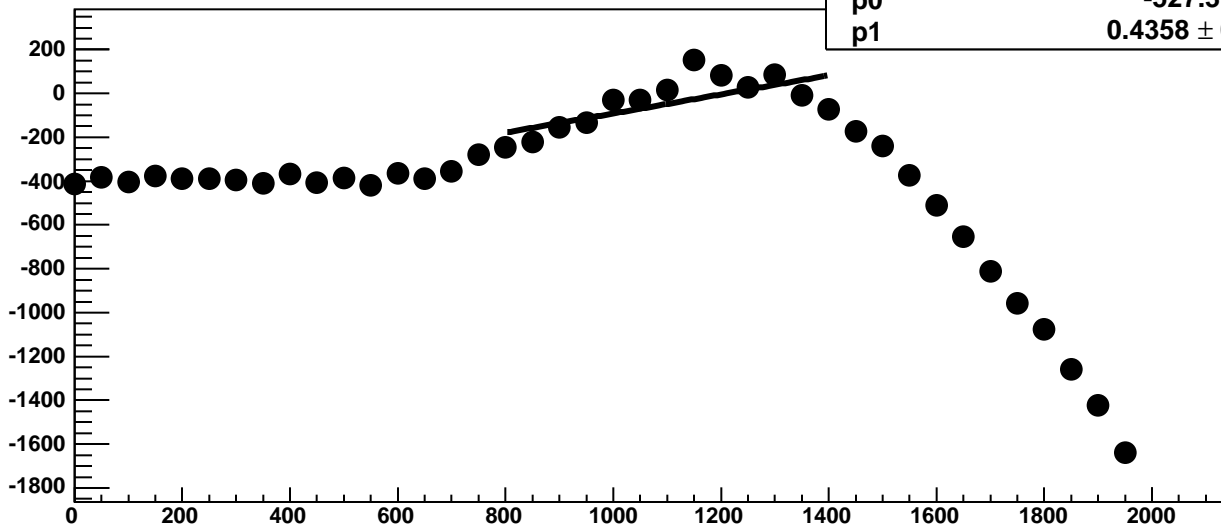
Chip 8, Channel 7, Enable 5!, Hold=30, ADC Noise vs DAC



Chip 8, Channel 7, Enable 5!, Hold=30, ADC Residuals vs DAC



Chip 8, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

90.21 / 11

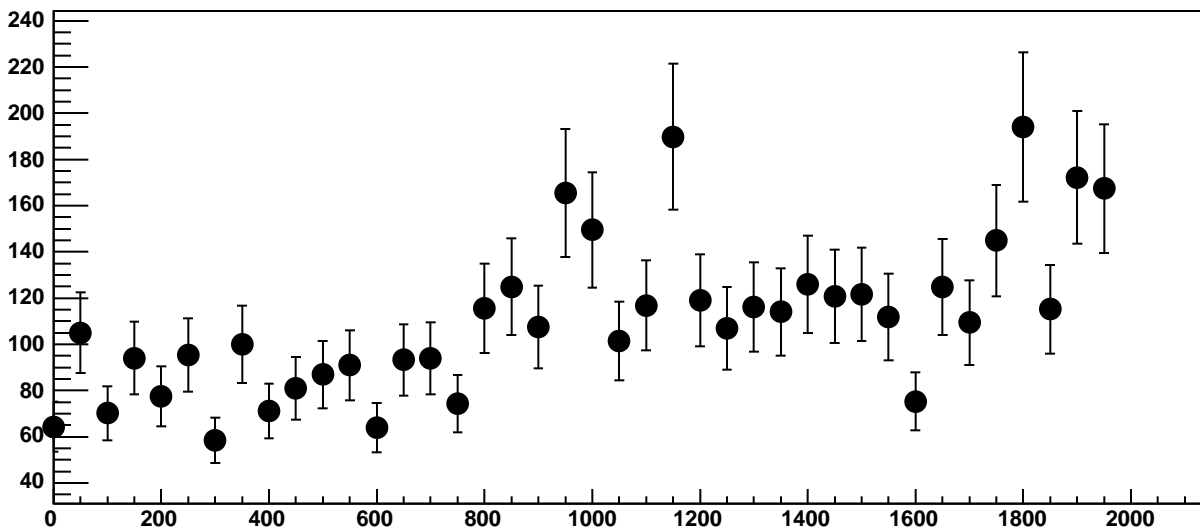
p0

$-527.3 \pm 45.55$

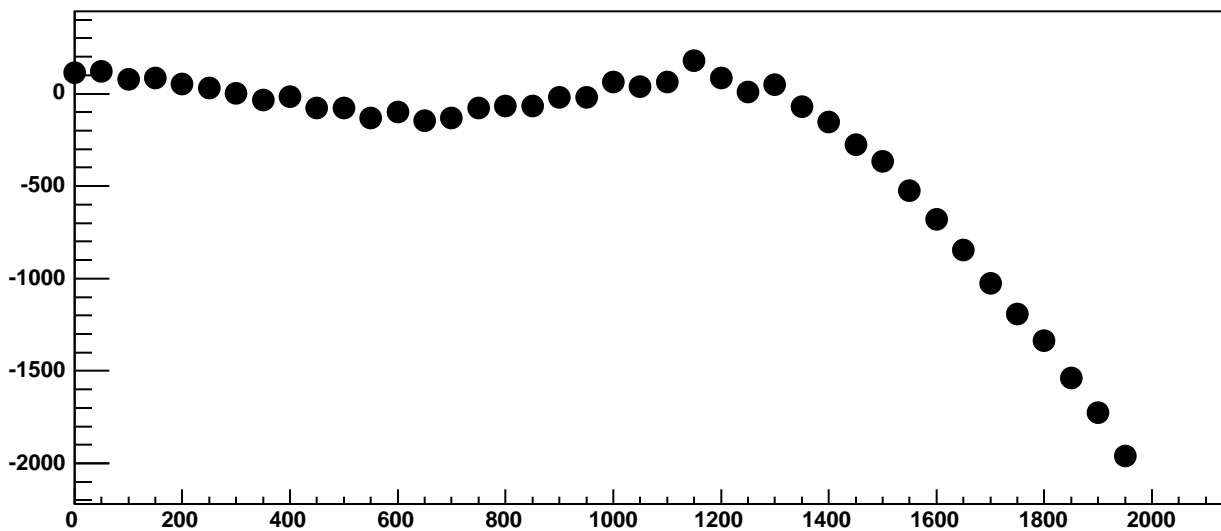
p1

$0.4358 \pm 0.04063$

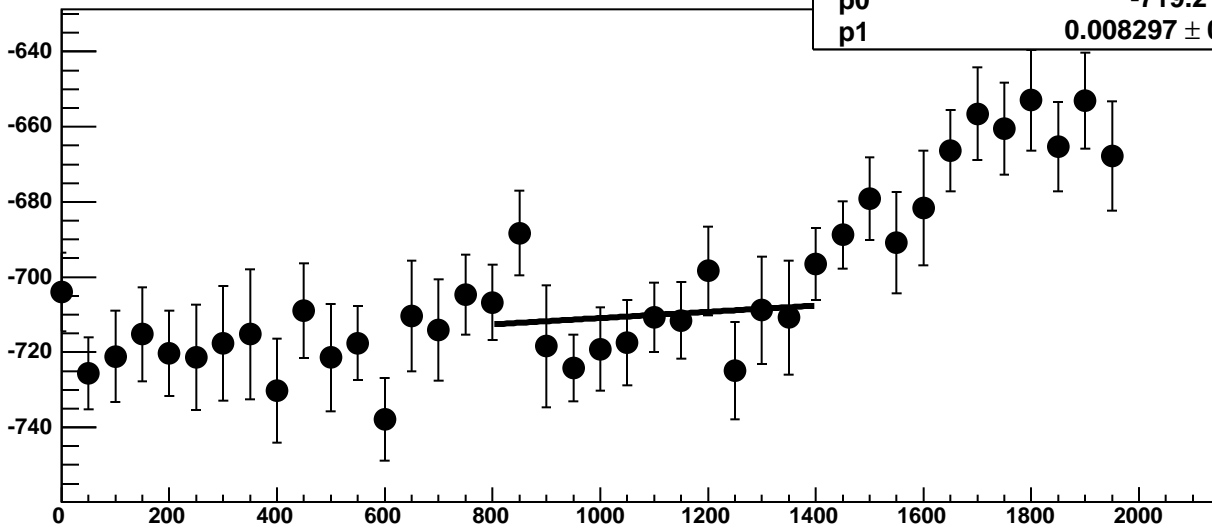
Chip 8, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC



Chip 8, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC

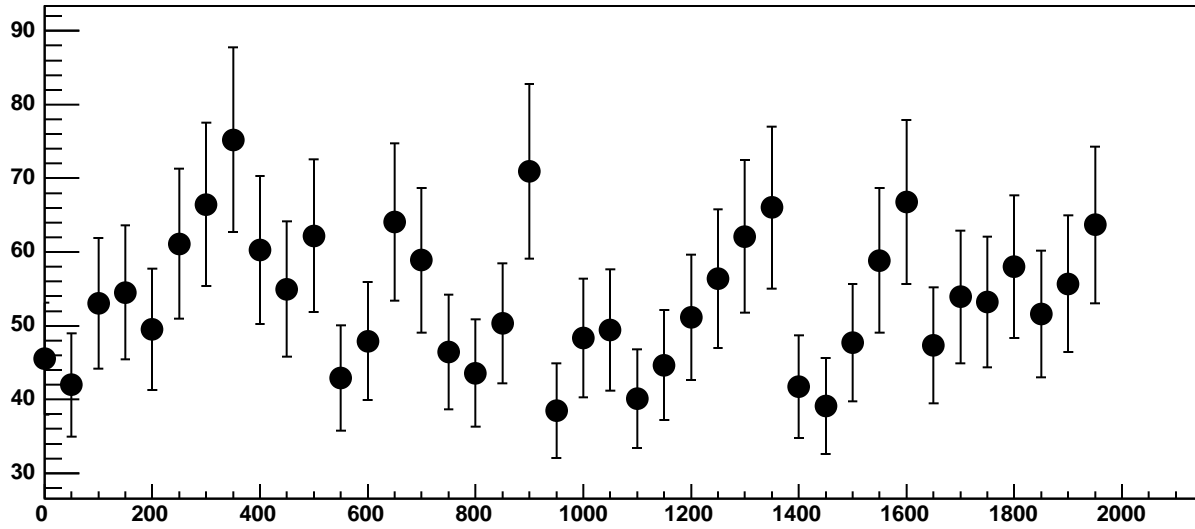


Chip 8, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC

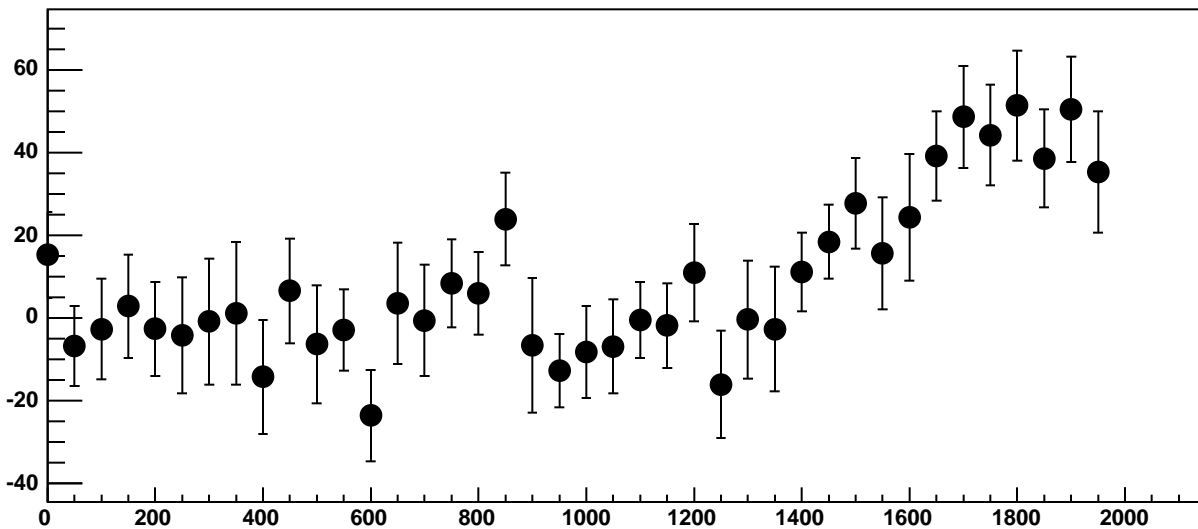


$\chi^2 / \text{ndf}$  11.87 / 11  
p0  $-719.2 \pm 18.38$   
p1  $0.008297 \pm 0.01667$

Chip 8, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC

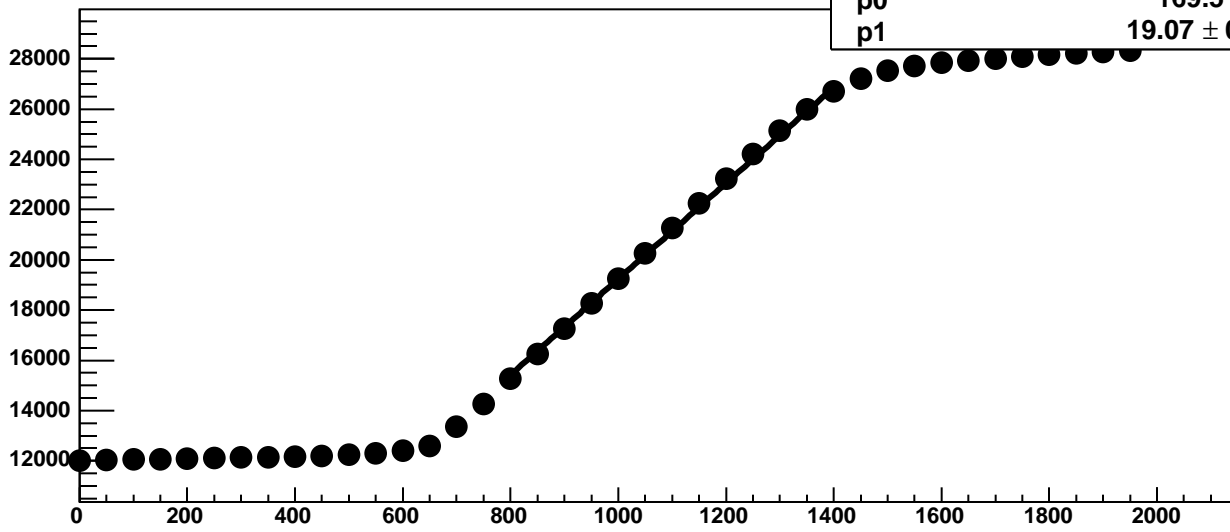


Chip 8, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC



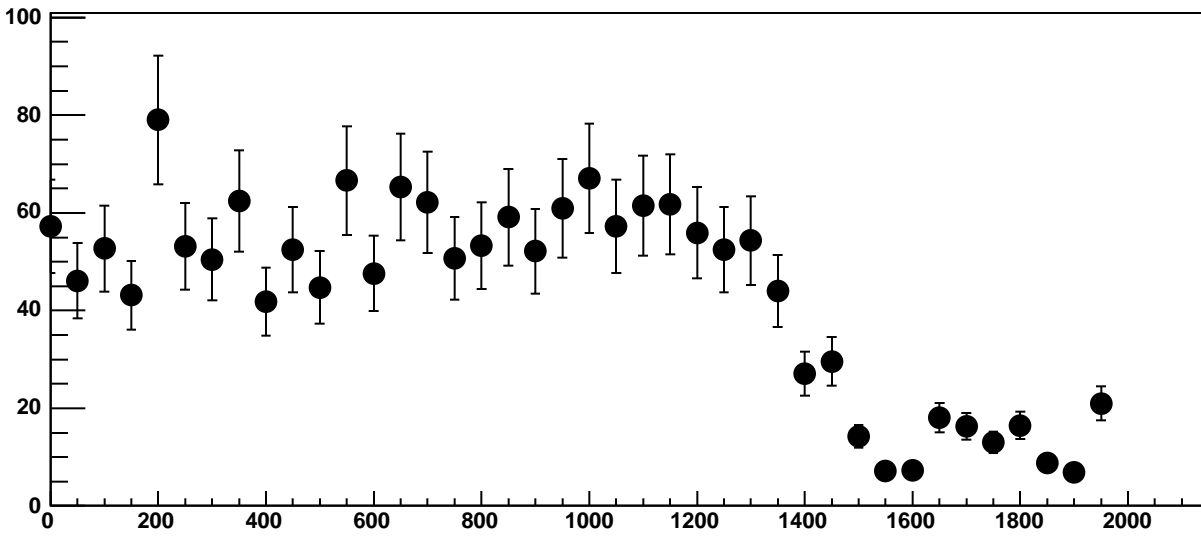


Chip 8, Channel 8, Enable 2!, Hold=30, ADC Mean vs DAC

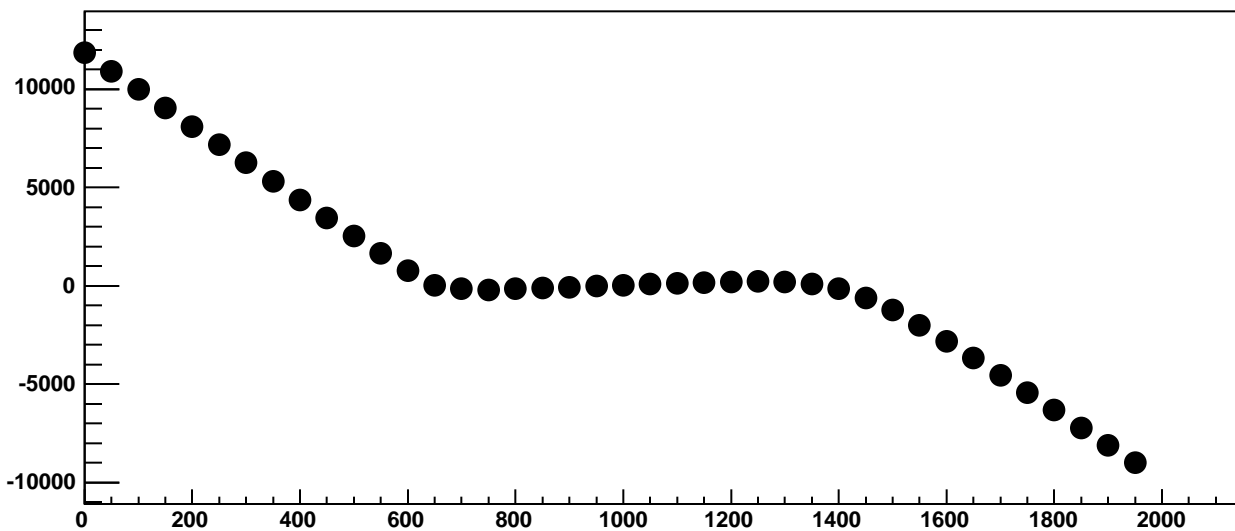


$\chi^2 / \text{ndf}$  1966 / 11  
p0  $169.5 \pm 18.13$   
p1  $19.07 \pm 0.01523$

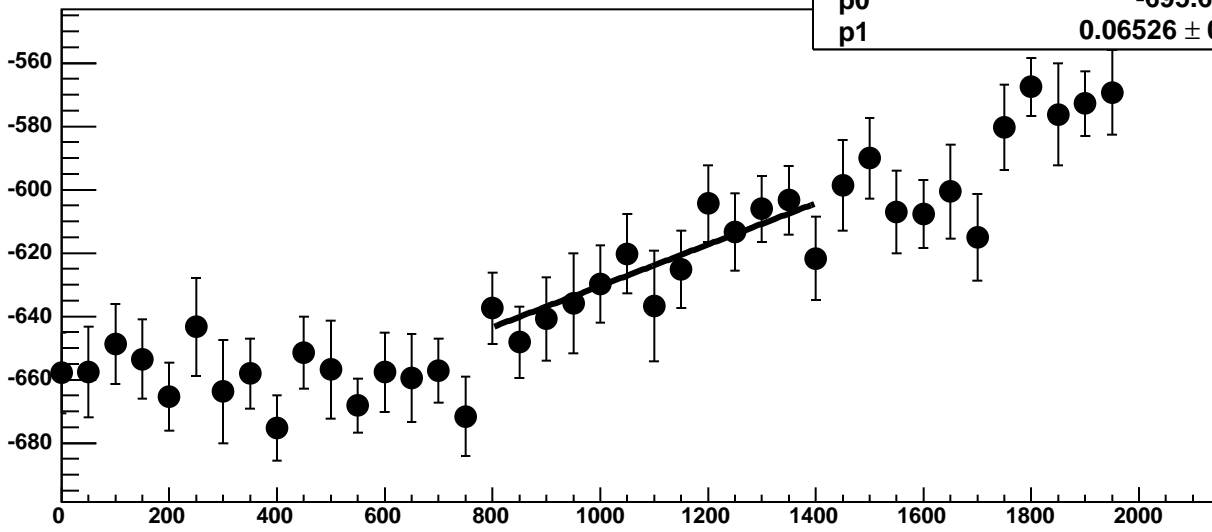
Chip 8, Channel 8, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 8, Channel 8, Enable 2!, Hold=30, ADC Residuals vs DAC

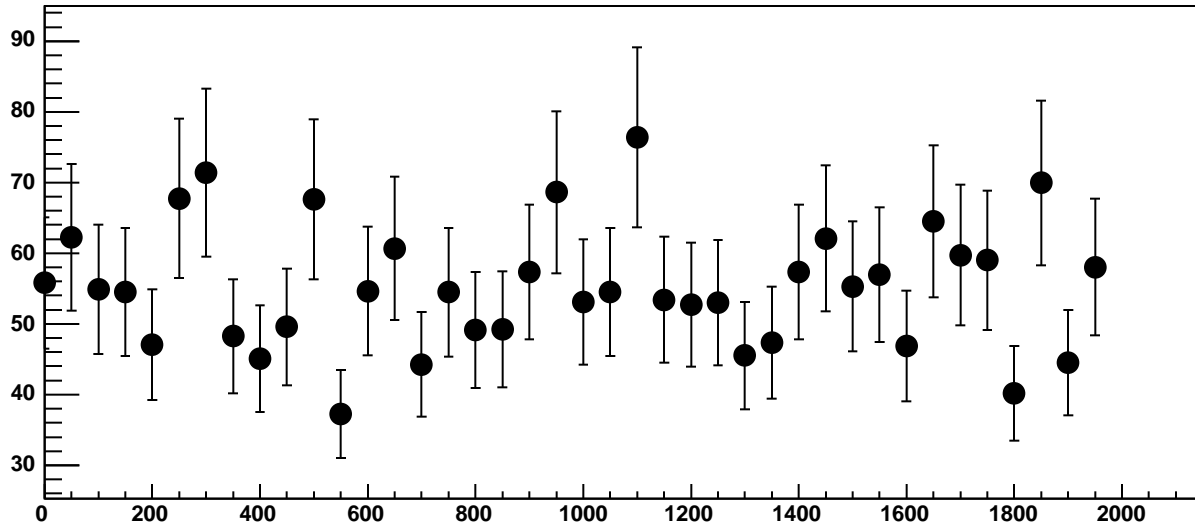


Chip 8, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC

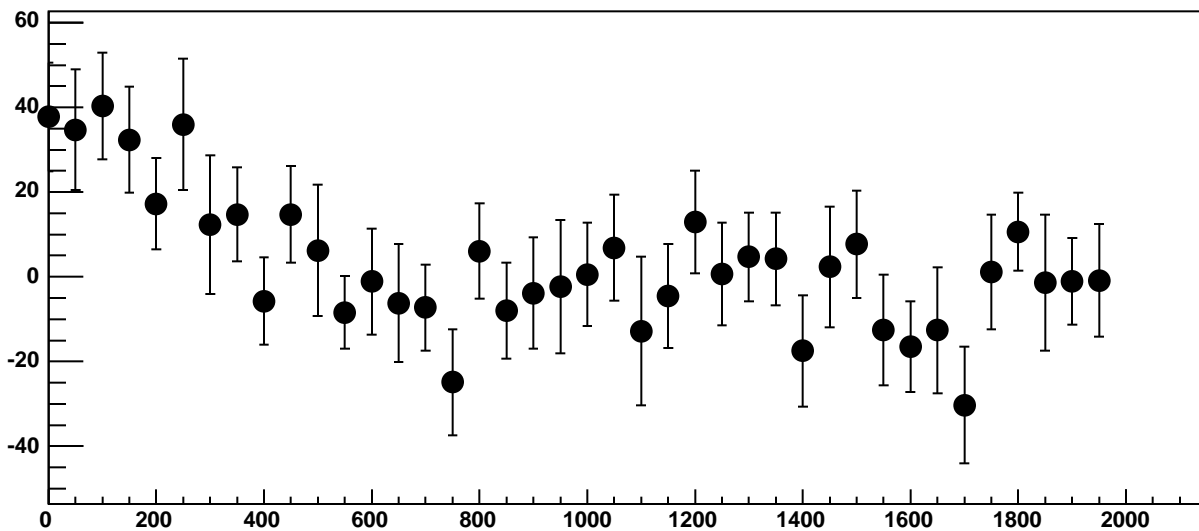


$\chi^2 / \text{ndf}$  5.145 / 11  
p0  $-695.6 \pm 19.71$   
p1  $0.06526 \pm 0.01753$

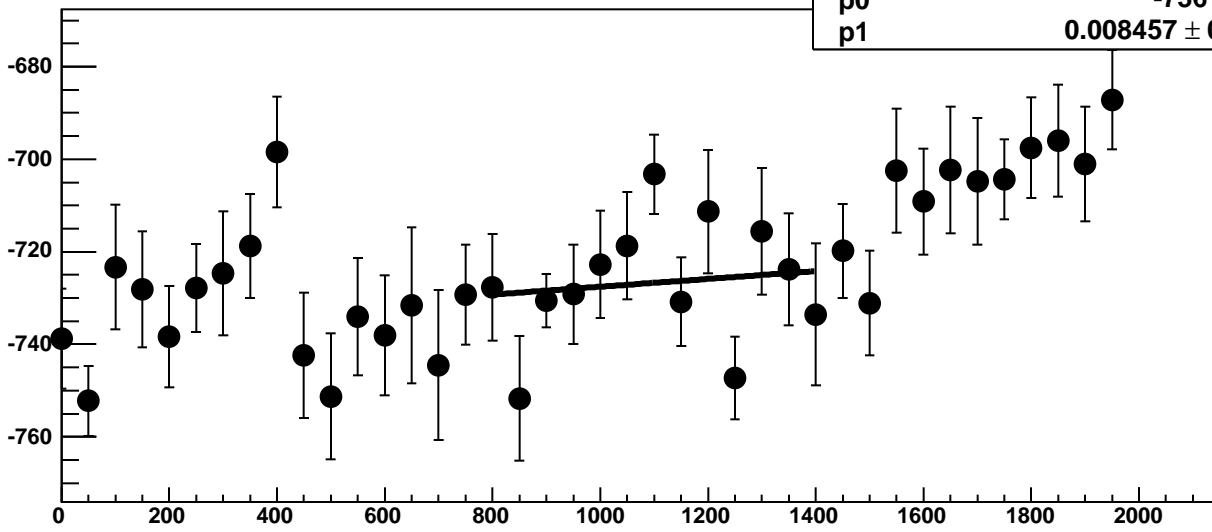
Chip 8, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC



Chip 8, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC

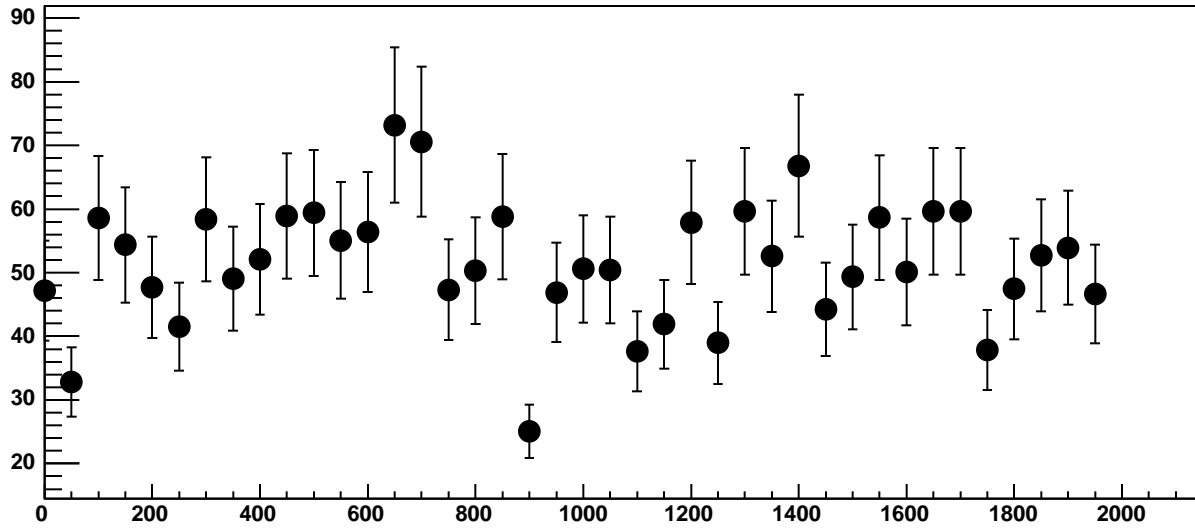


Chip 8, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC

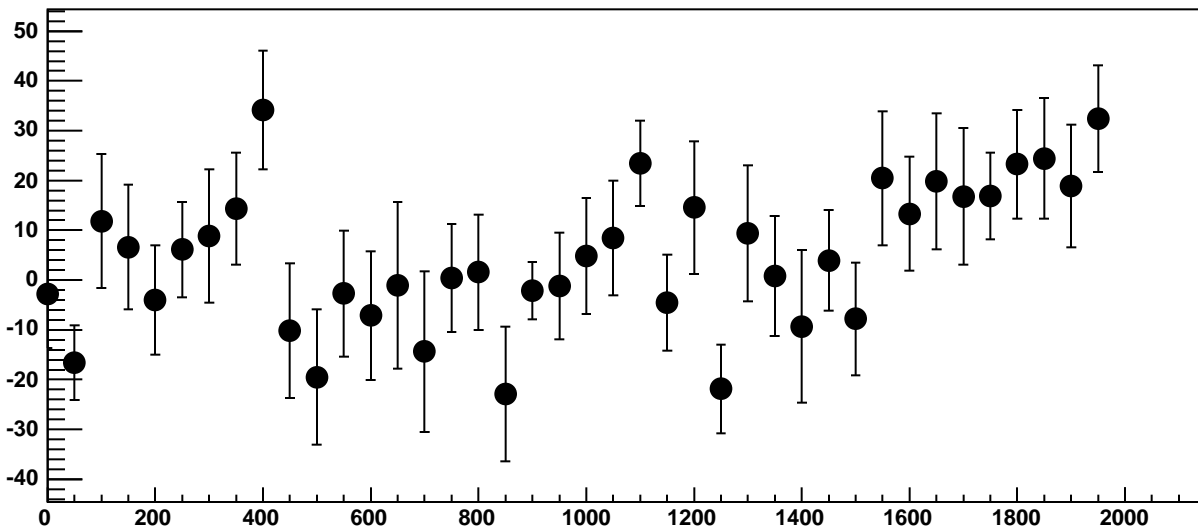


$\chi^2 / \text{ndf}$  19.39 / 11  
p0  $-736 \pm 17.64$   
p1  $0.008457 \pm 0.01643$

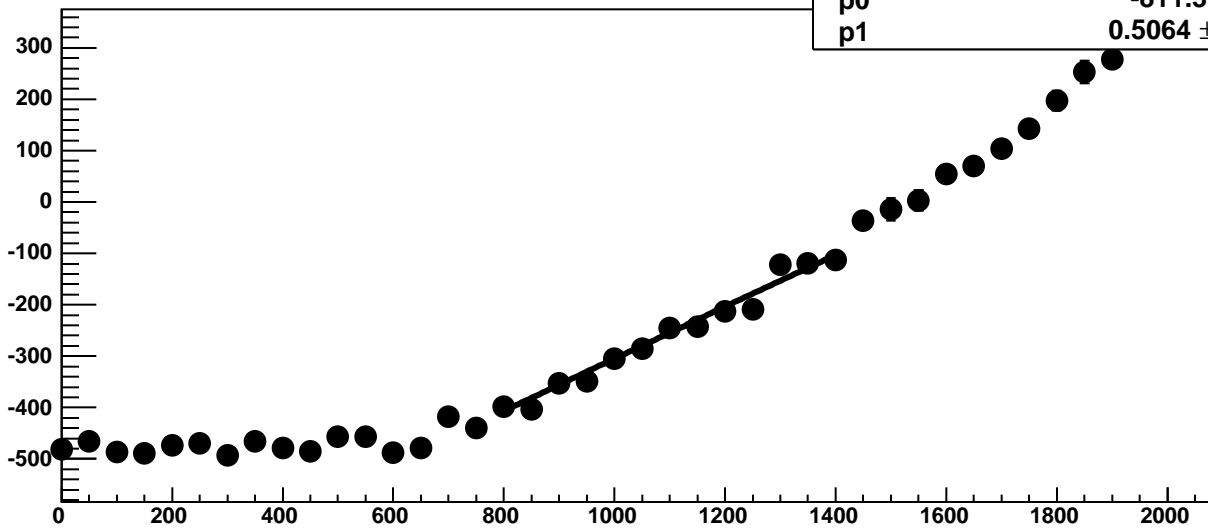
Chip 8, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC



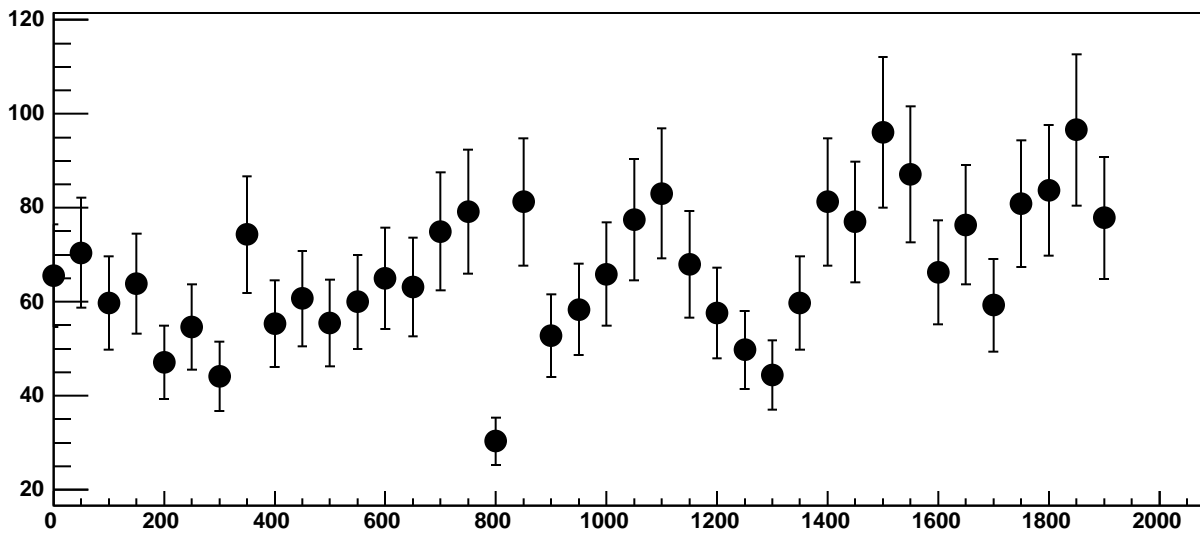
Chip 8, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC



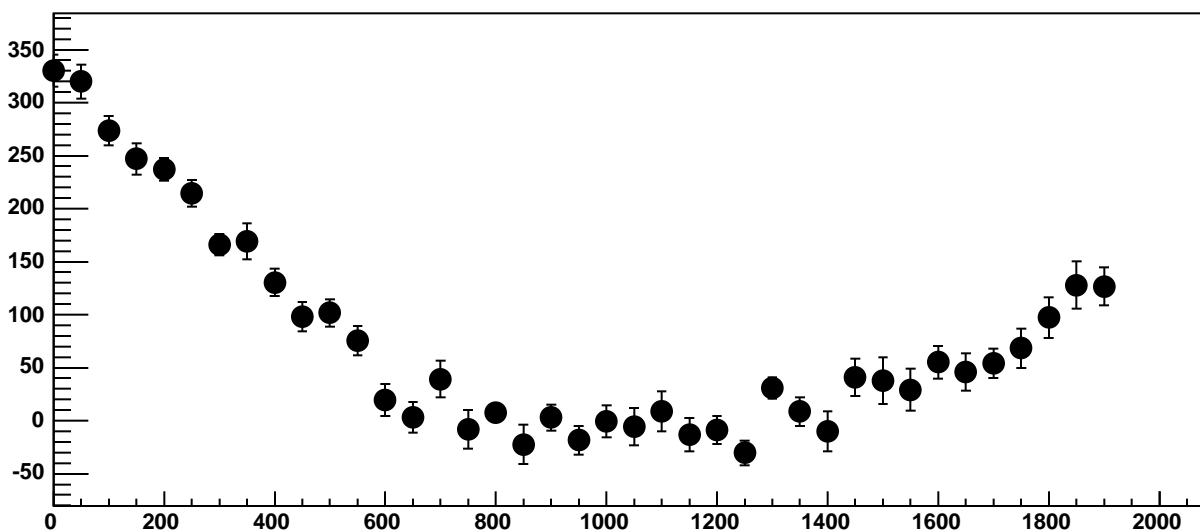
Chip 8, Channel 8, Enable 5, Hold=30, ADC Mean vs DAC



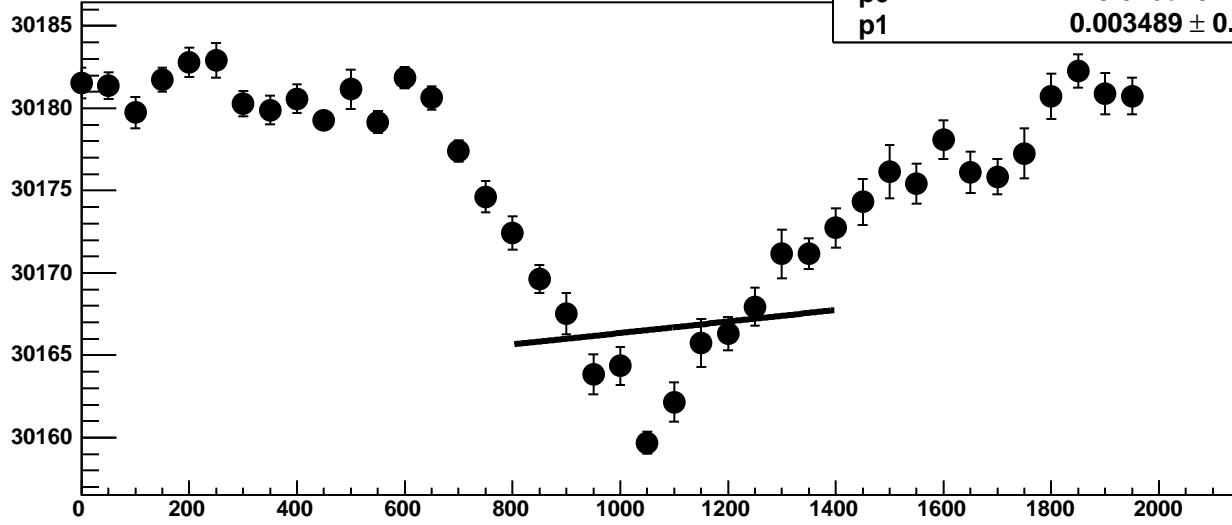
Chip 8, Channel 8, Enable 5, Hold=30, ADC Noise vs DAC



Chip 8, Channel 8, Enable 5, Hold=30, ADC Residuals vs DAC

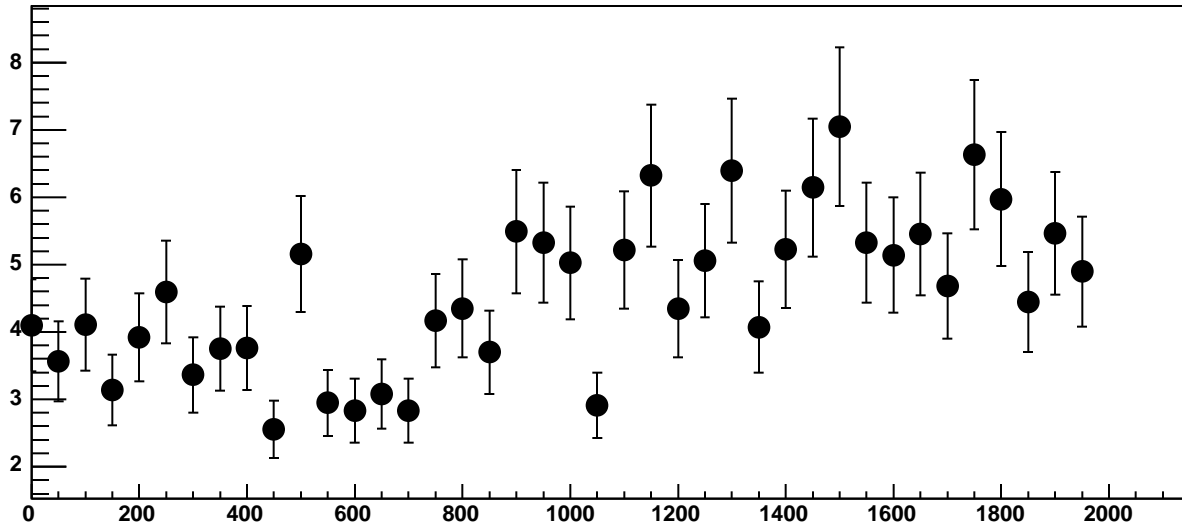


Chip 8, Channel 9, Enable 0!, Hold=30, ADC Mean vs DAC

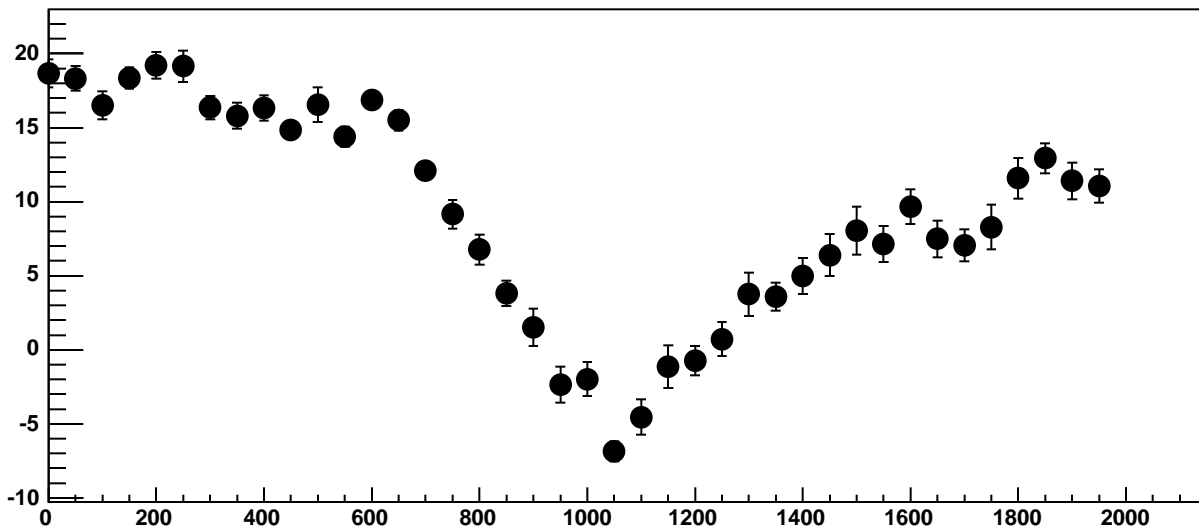


$\chi^2 / \text{ndf}$  233.9 / 11  
p0  $3.016\text{e}+04 \pm 1.719$   
p1  $0.003489 \pm 0.001569$

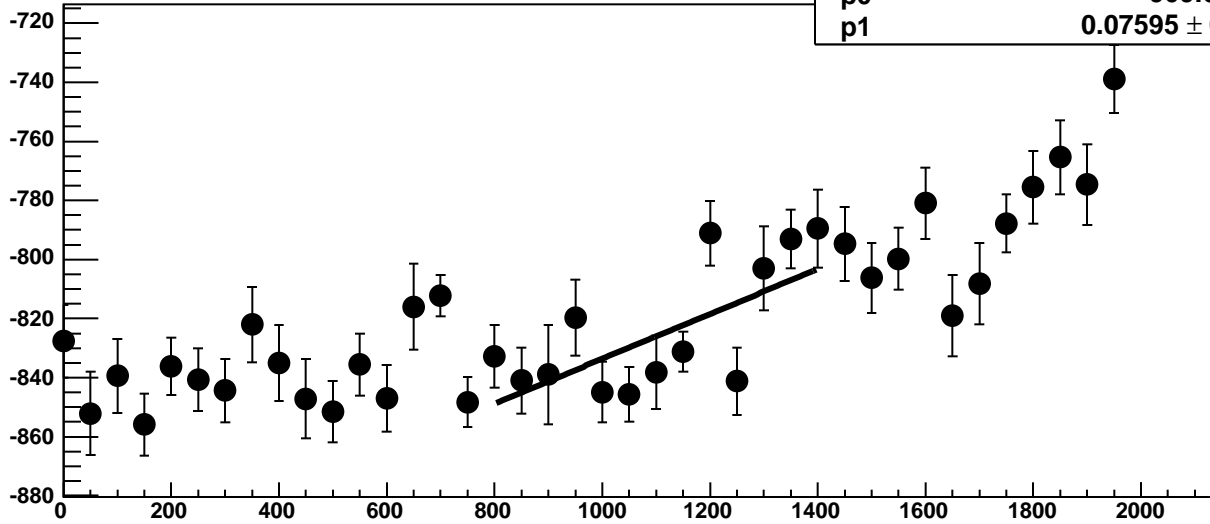
Chip 8, Channel 9, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 8, Channel 9, Enable 0!, Hold=30, ADC Residuals vs DAC



Chip 8, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

26.22 / 11

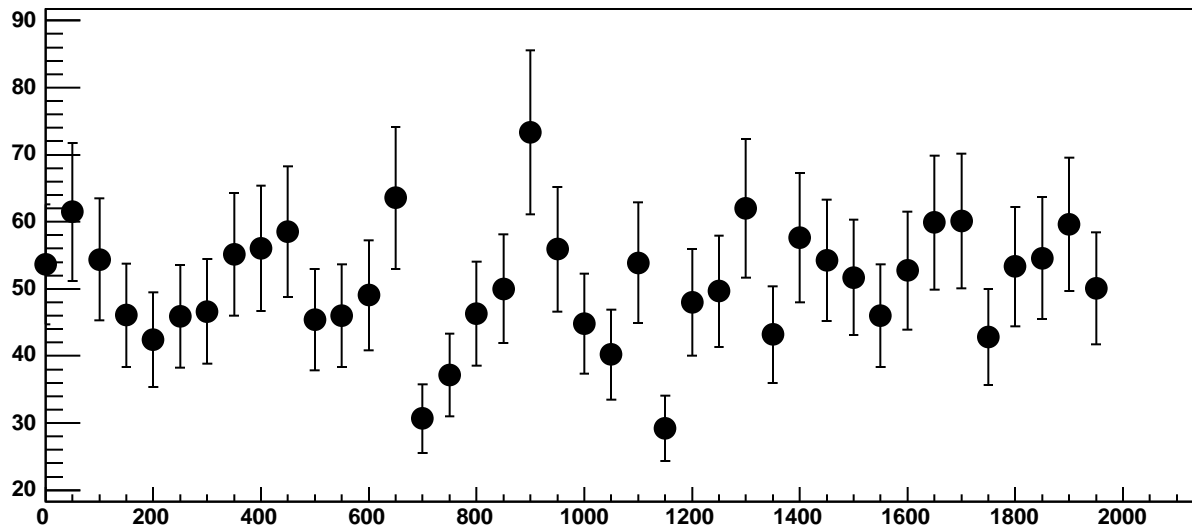
p0

$-909.6 \pm 19.3$

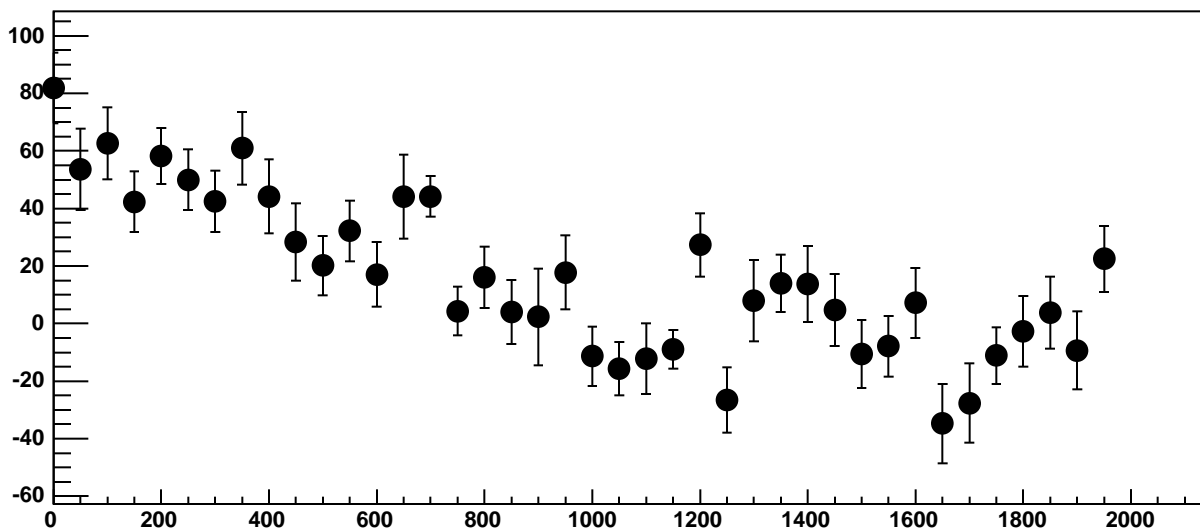
p1

$0.07595 \pm 0.01726$

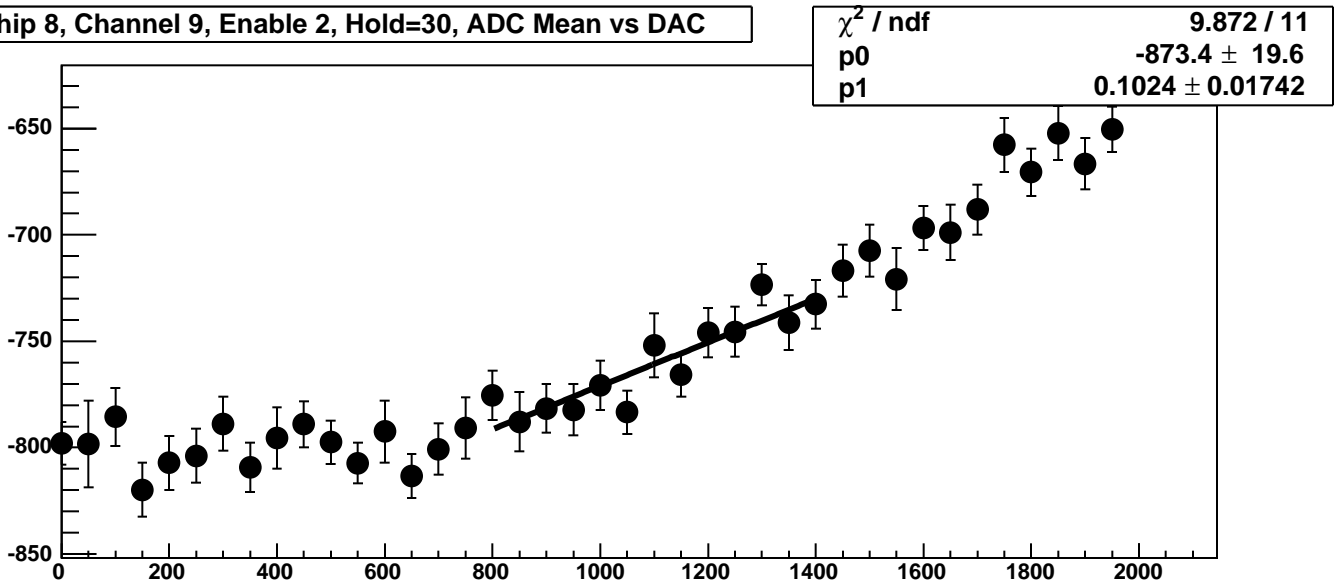
Chip 8, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC



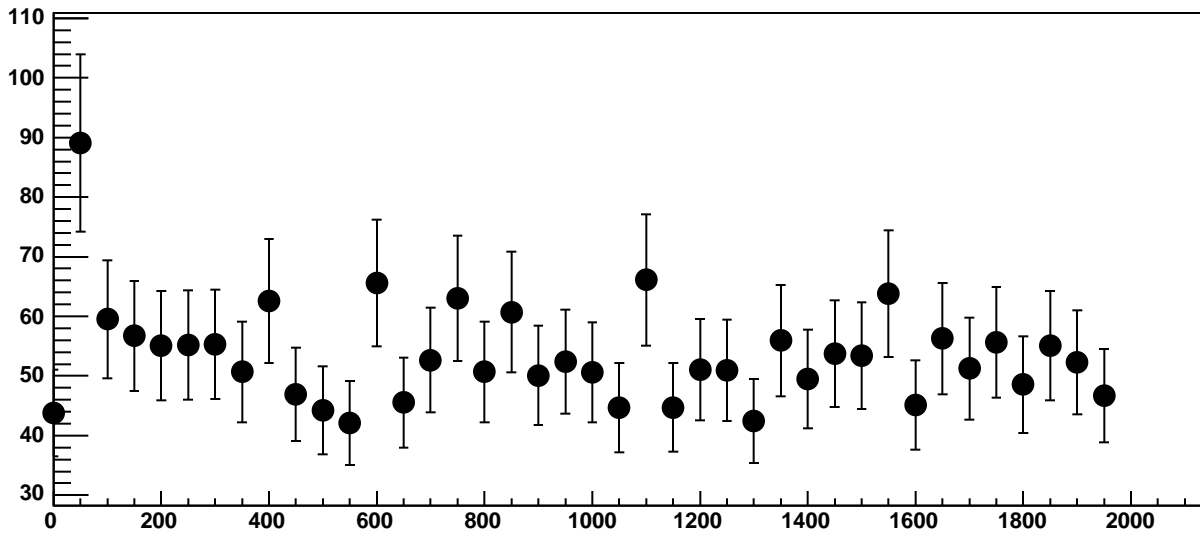
Chip 8, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC



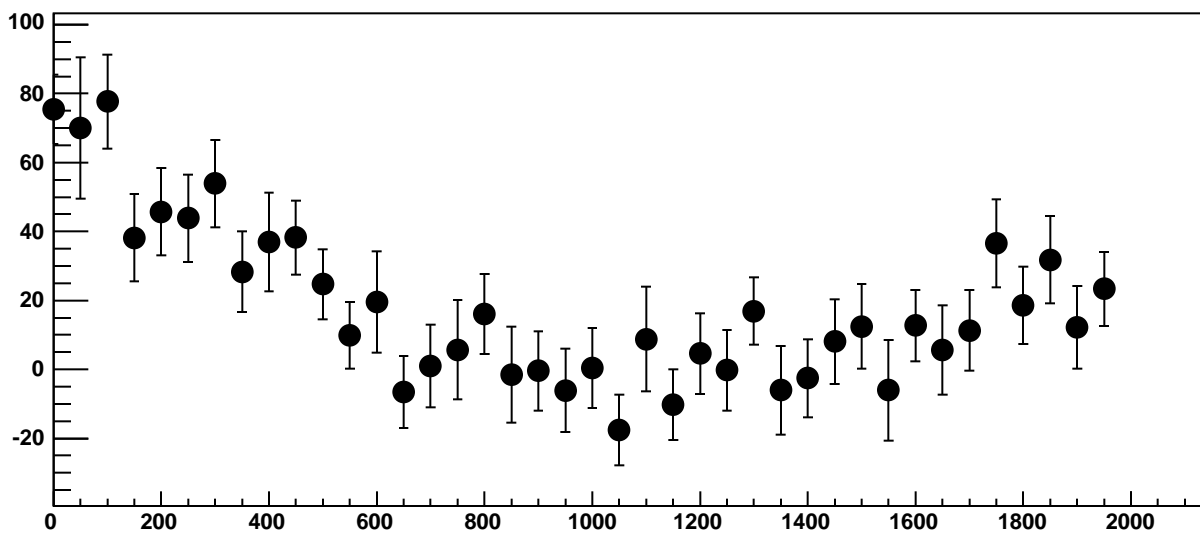
Chip 8, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC



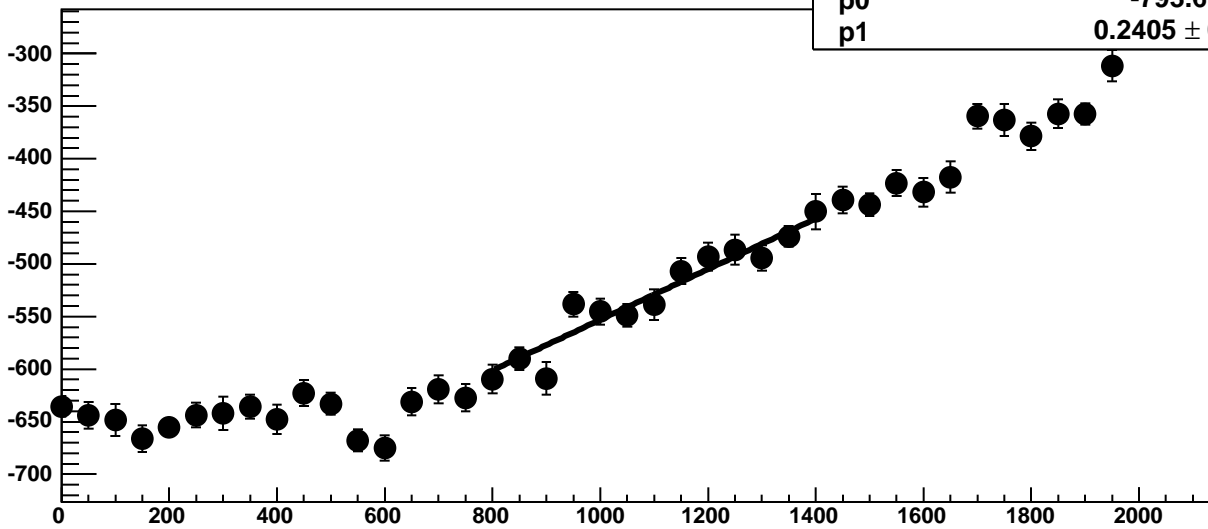
Chip 8, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC



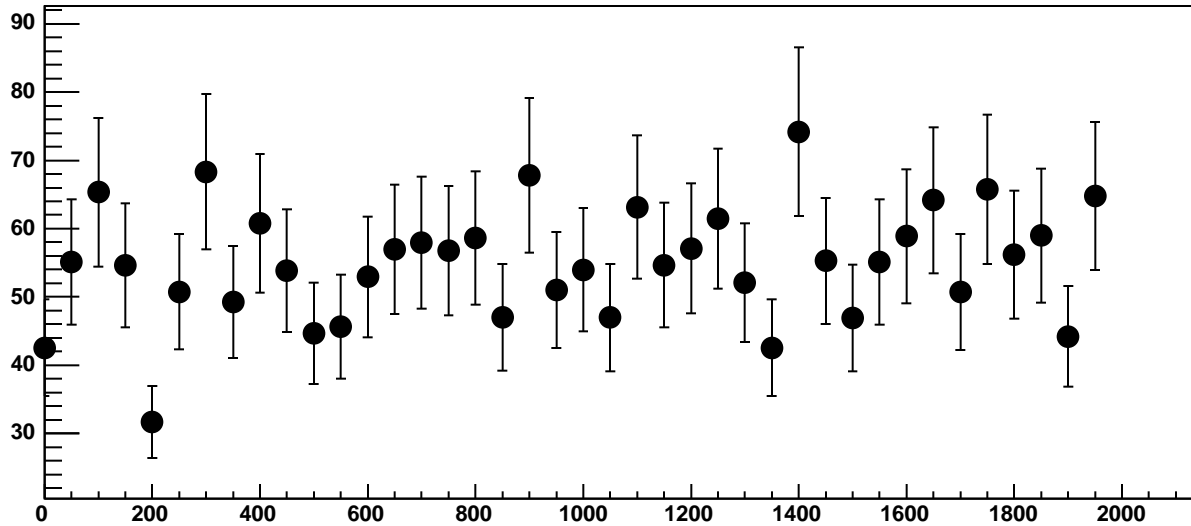
Chip 8, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC



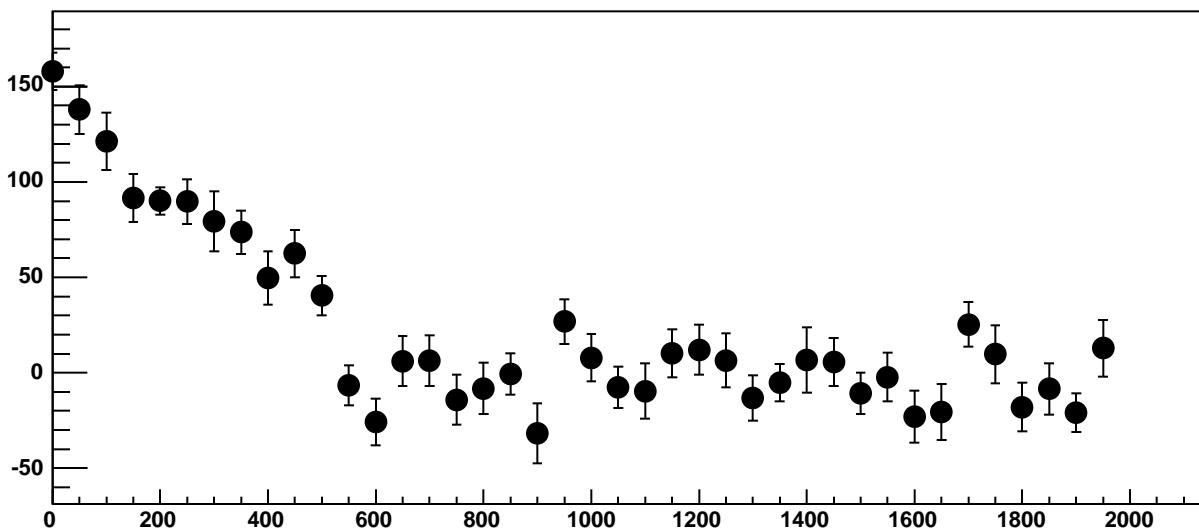
Chip 8, Channel 9, Enable 3, Hold=30, ADC Mean vs DAC



Chip 8, Channel 9, Enable 3, Hold=30, ADC Noise vs DAC

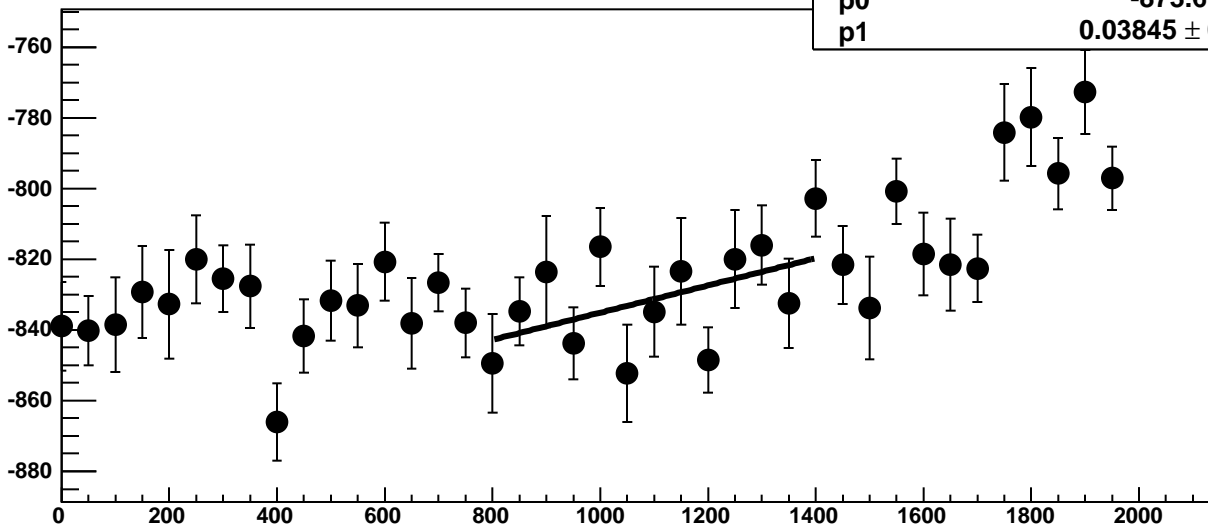


Chip 8, Channel 9, Enable 3, Hold=30, ADC Residuals vs DAC



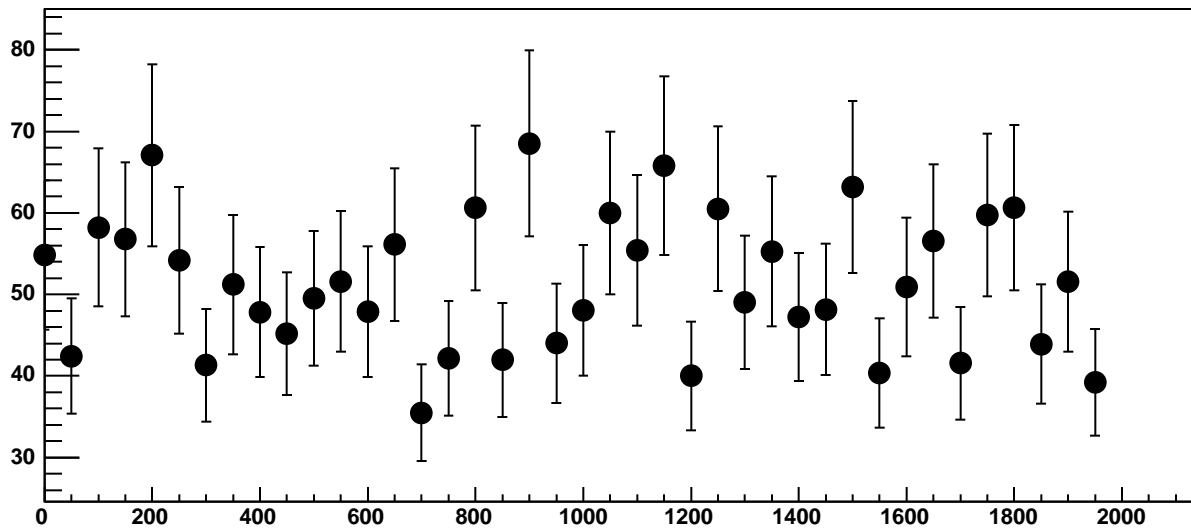


Chip 8, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC

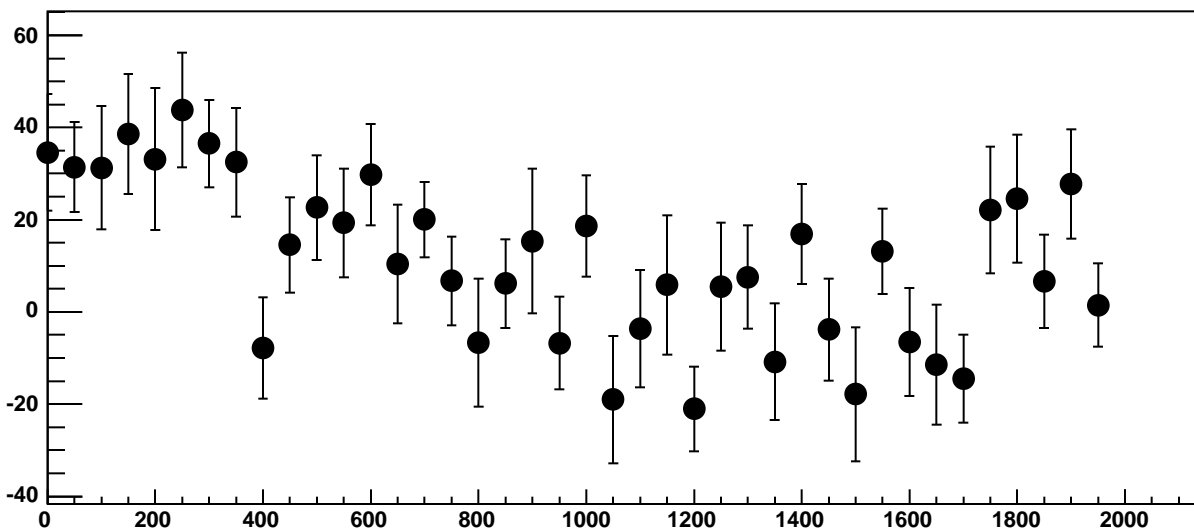


$\chi^2 / \text{ndf}$  16.08 / 11  
p0  $-873.6 \pm 19.38$   
p1  $0.03845 \pm 0.01732$

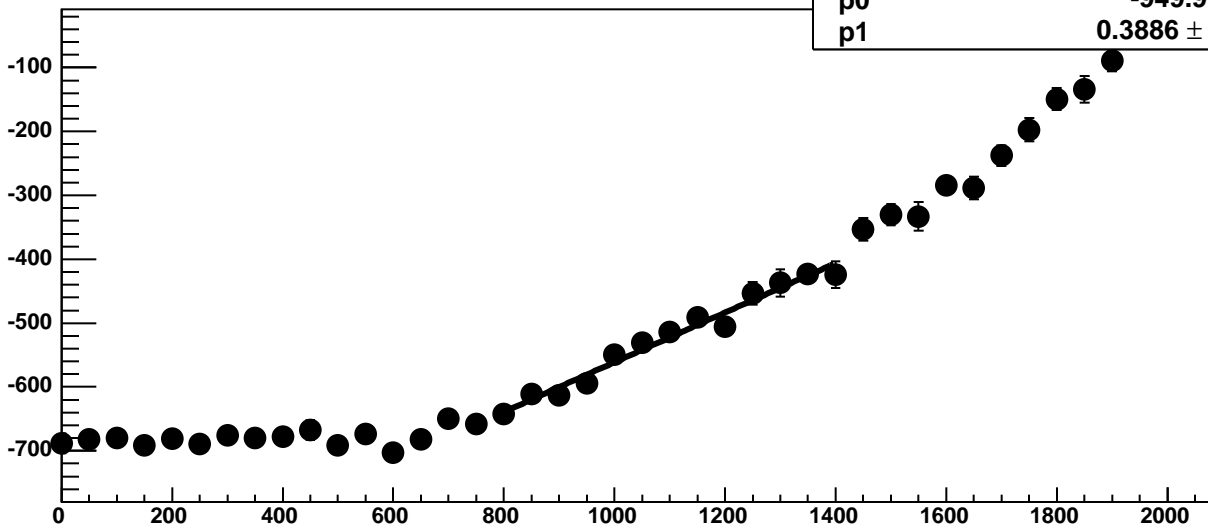
Chip 8, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC



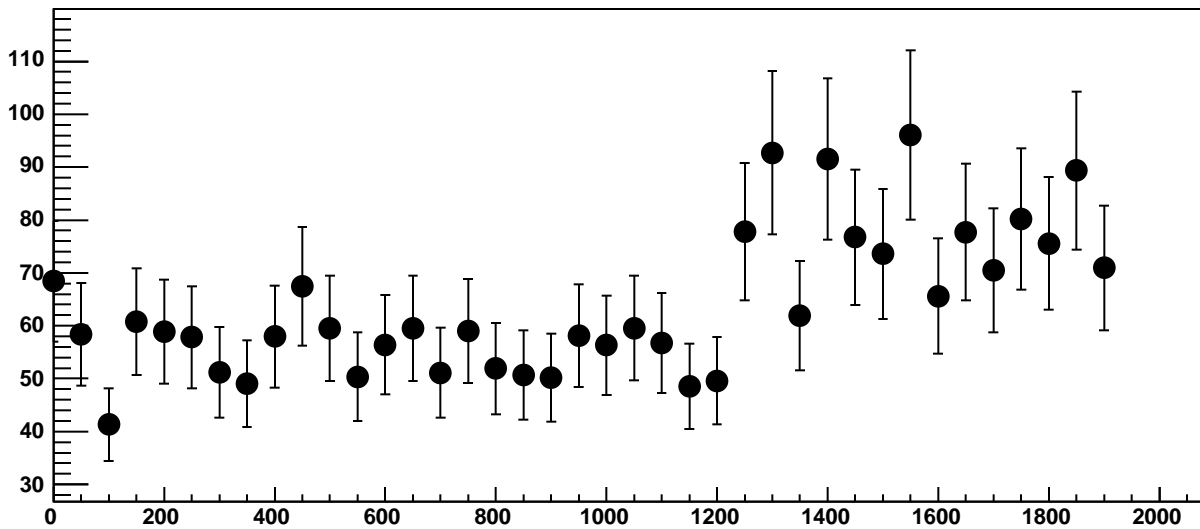
Chip 8, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC



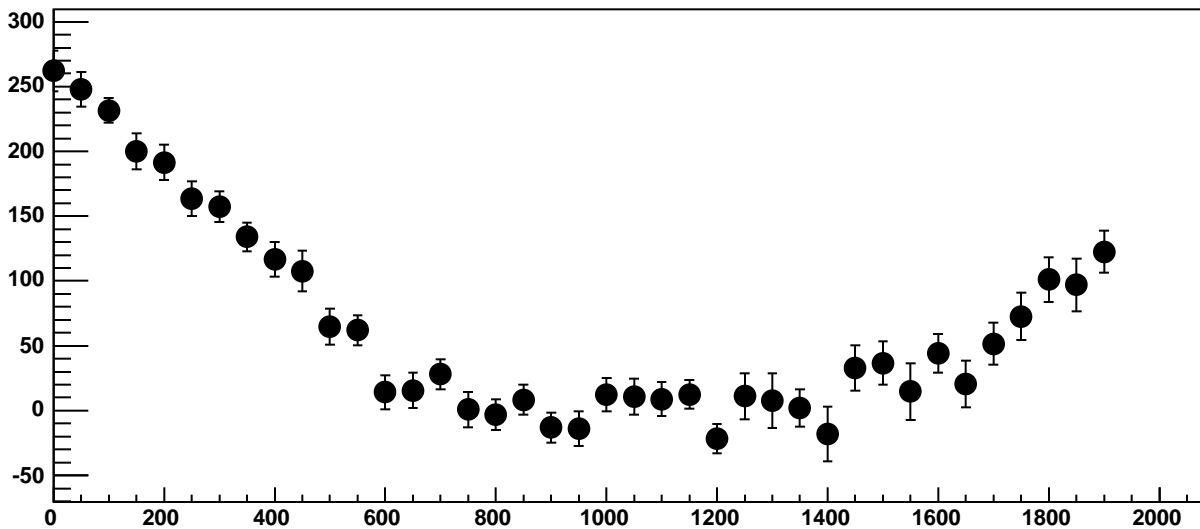
Chip 8, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC



Chip 8, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC

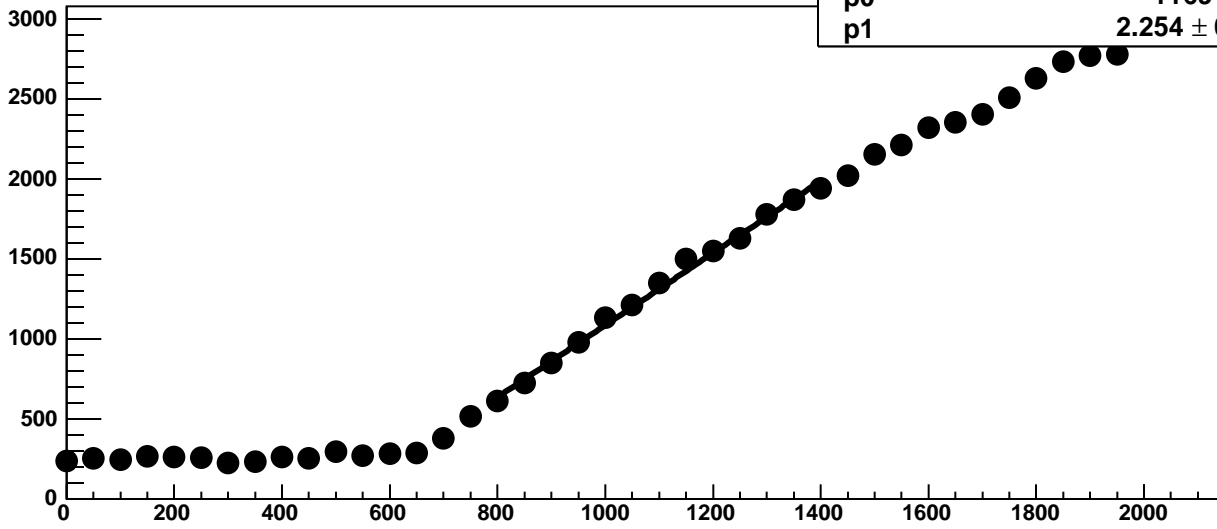


Chip 8, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC

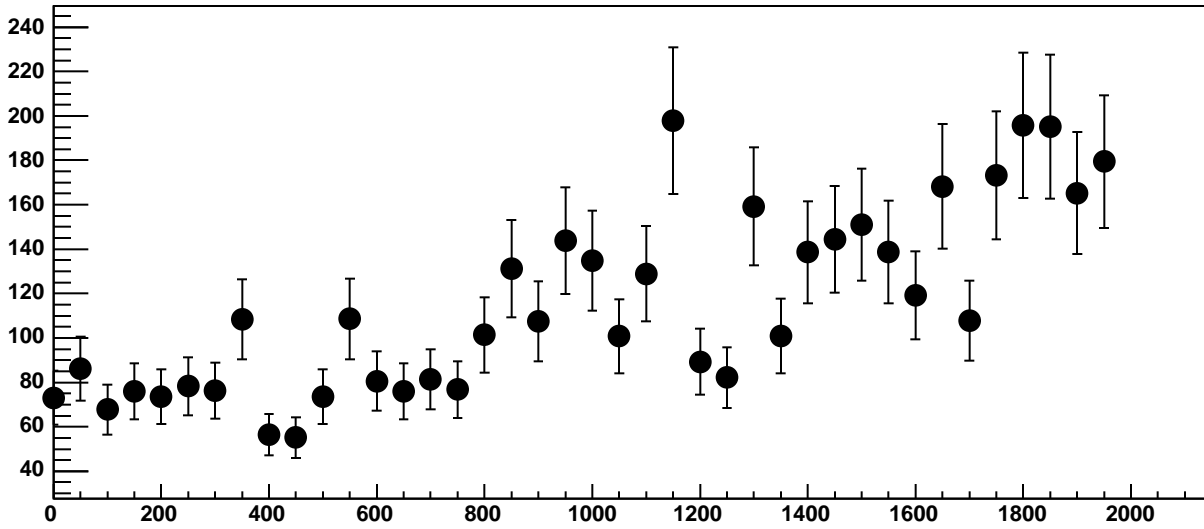


Chip 8, Channel 10, Enable 0, Hold=30, ADC Mean vs DAC

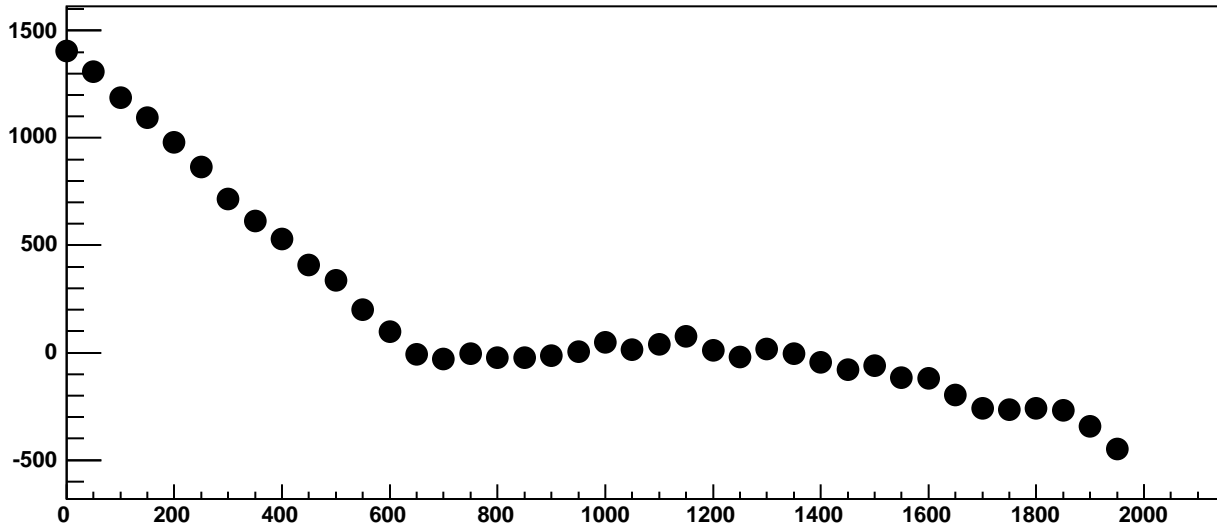
$\chi^2 / \text{ndf}$  12.69 / 11  
p0  $-1169 \pm 43.63$   
p1  $2.254 \pm 0.03891$



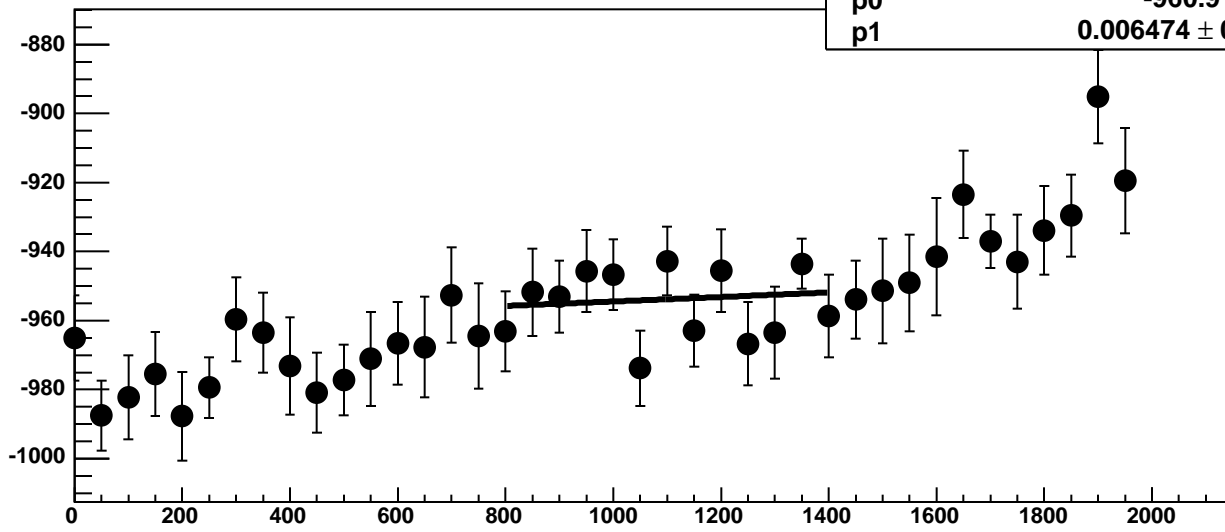
Chip 8, Channel 10, Enable 0, Hold=30, ADC Noise vs DAC



Chip 8, Channel 10, Enable 0, Hold=30, ADC Residuals vs DAC

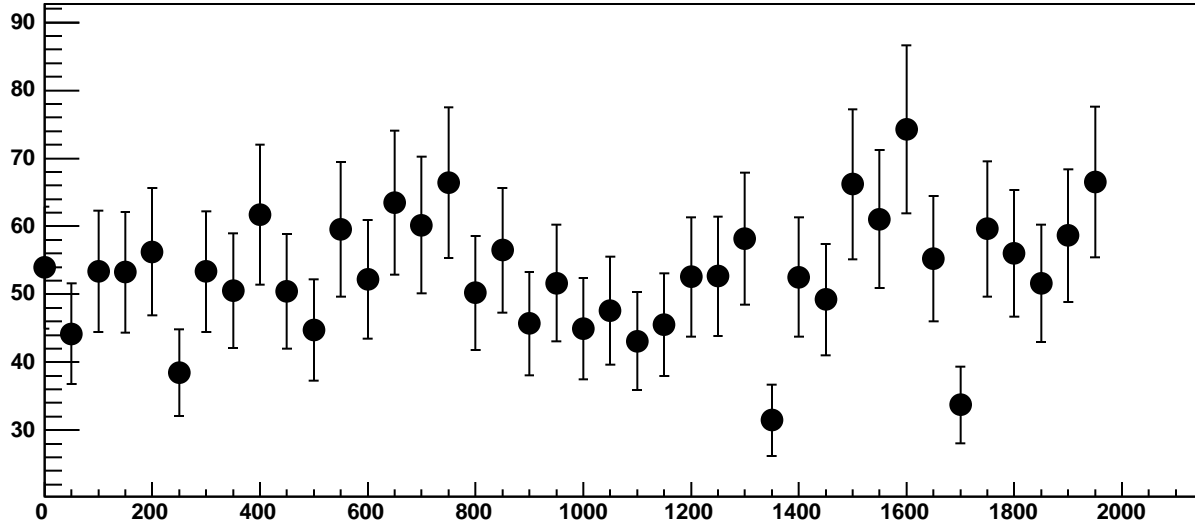


Chip 8, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC

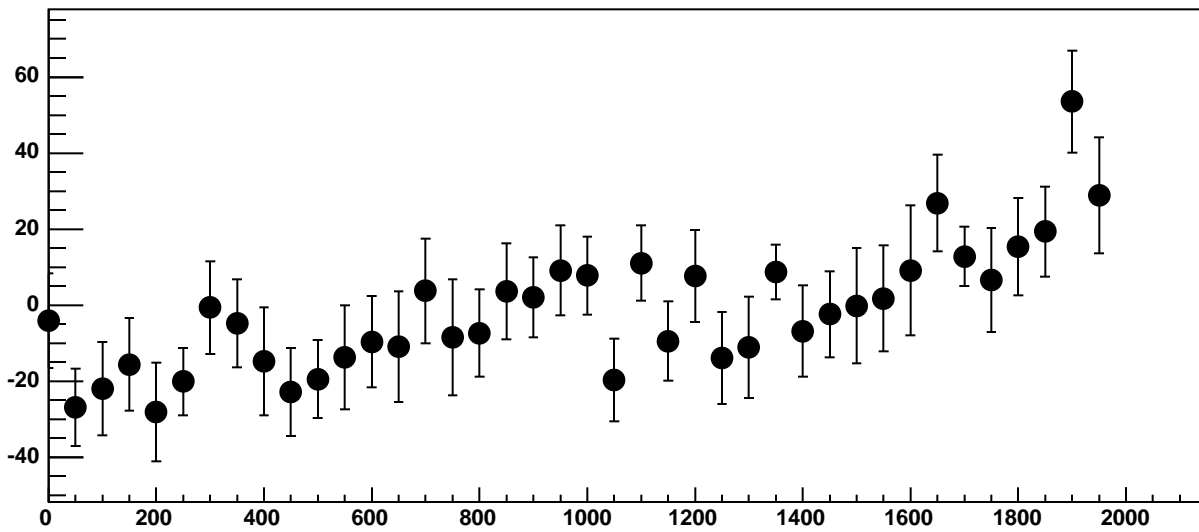


$\chi^2 / \text{ndf}$  11.19 / 11  
p0  $-960.9 \pm 18.02$   
p1  $0.006474 \pm 0.01589$

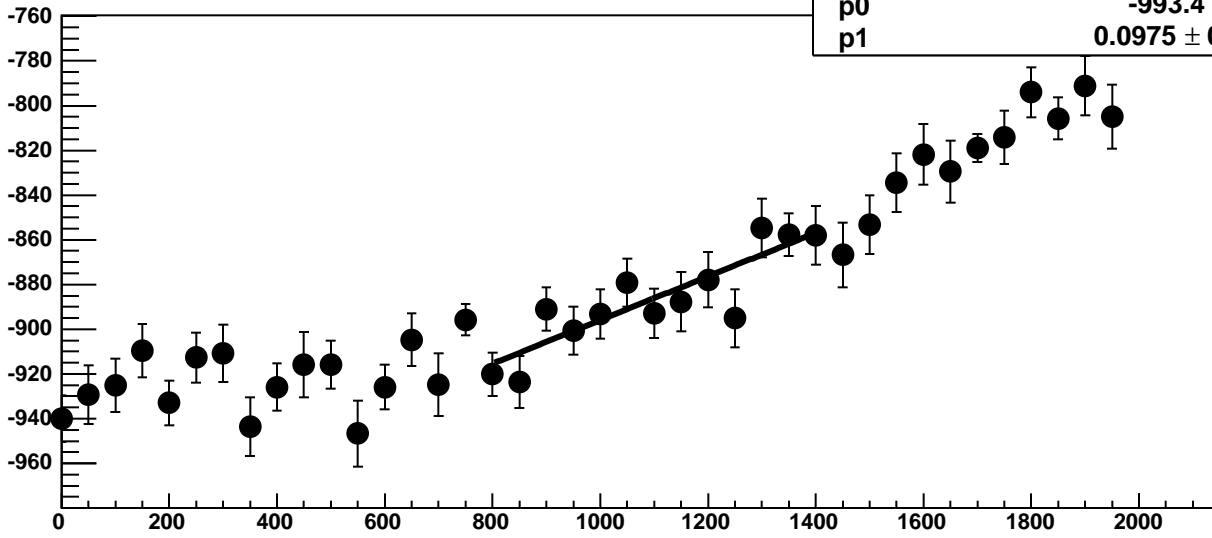
Chip 8, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC



Chip 8, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC

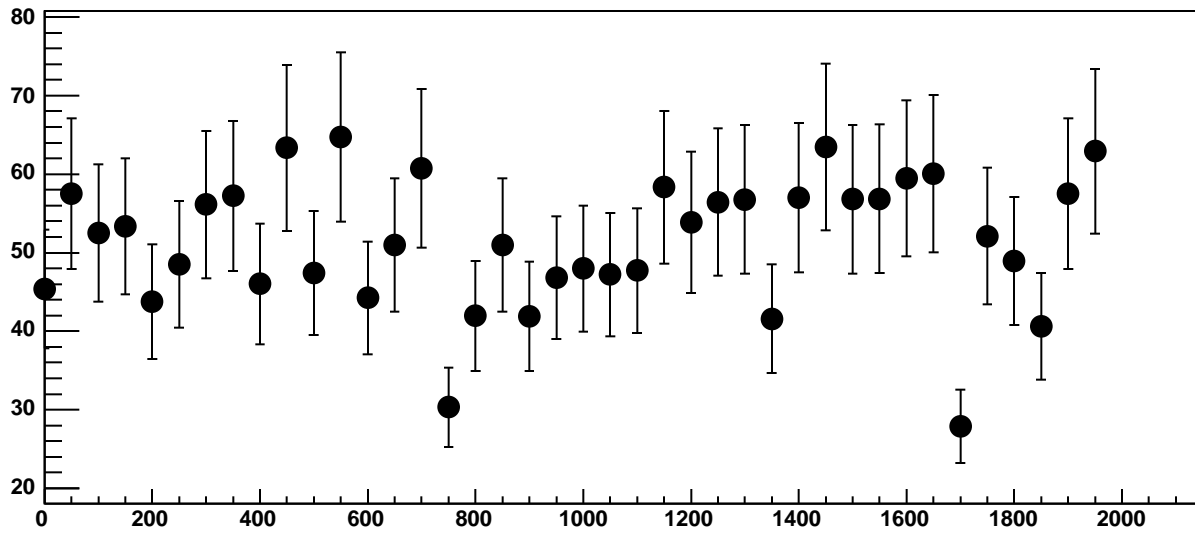


Chip 8, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC

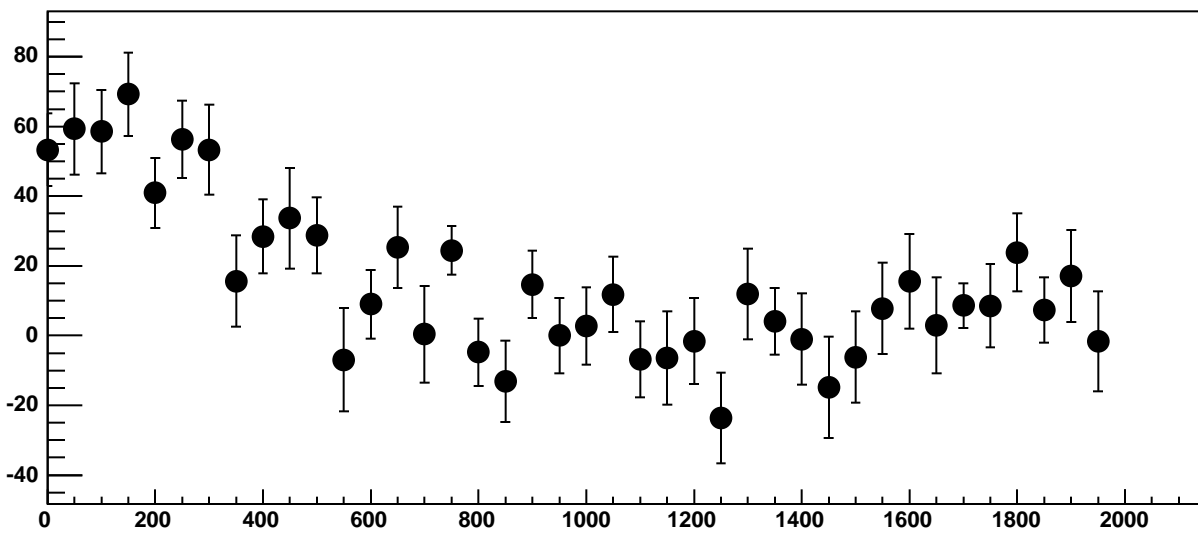


$\chi^2 / \text{ndf}$  10.1 / 11  
p0  $-993.4 \pm 17.82$   
p1  $0.0975 \pm 0.01628$

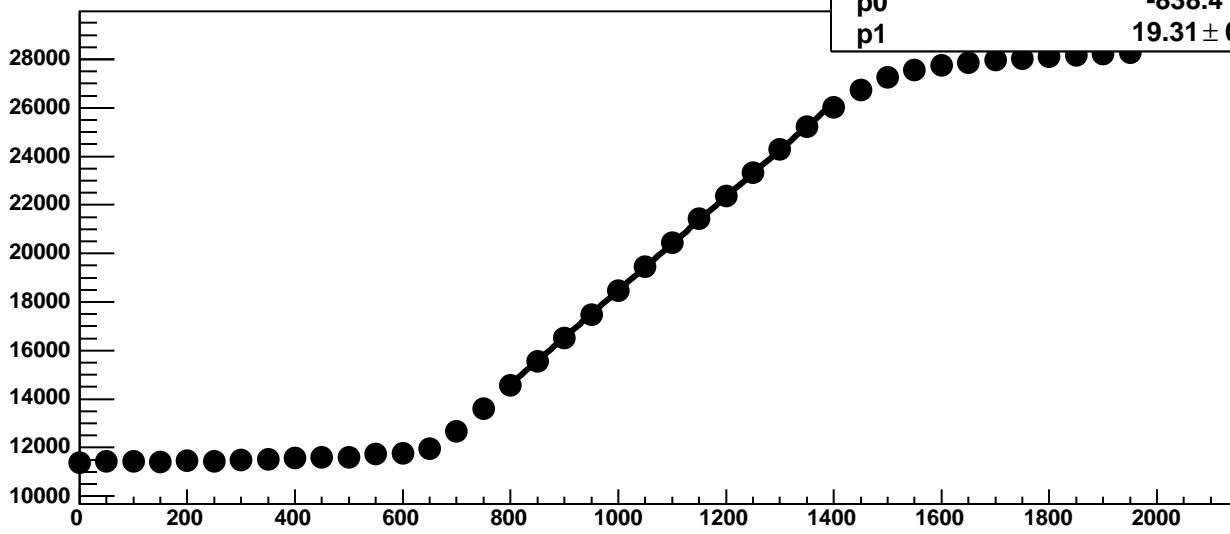
Chip 8, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC



Chip 8, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC

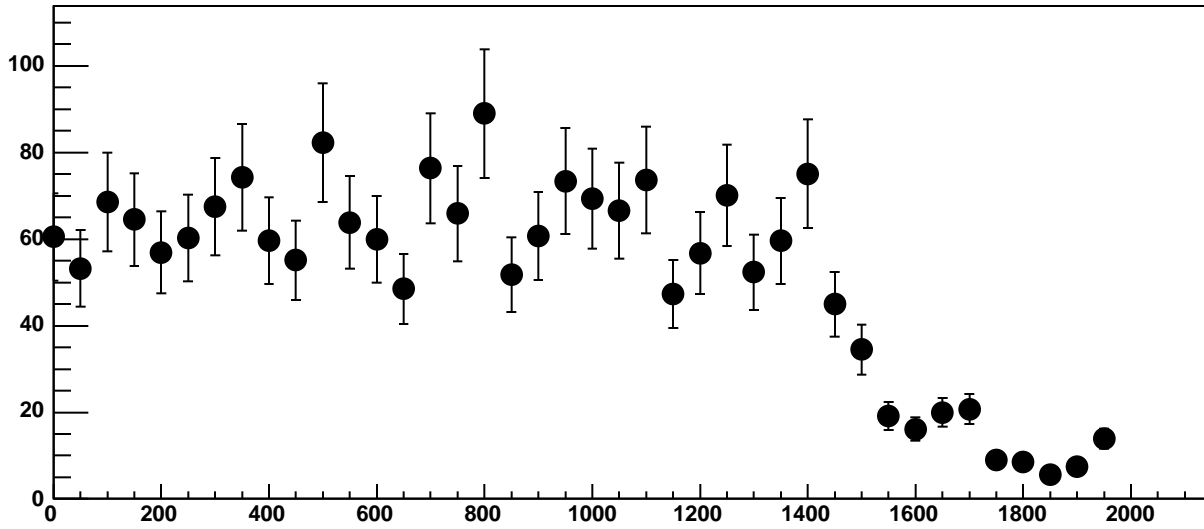


Chip 8, Channel 10, Enable 3!, Hold=30, ADC Mean vs DAC

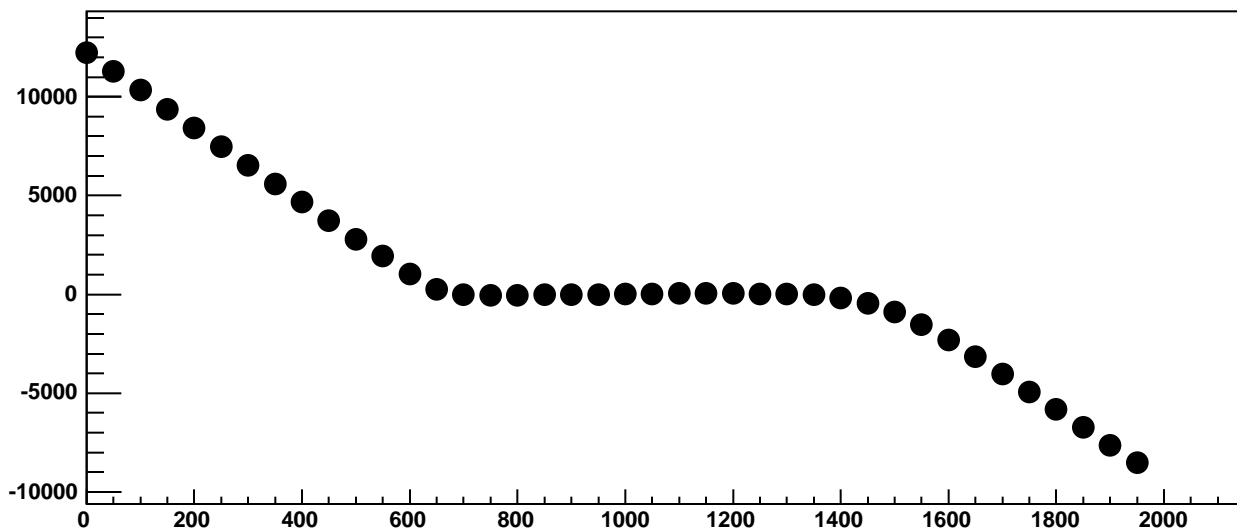


$\chi^2 / \text{ndf}$  169.2 / 11  
p0  $-838.4 \pm 24.72$   
p1  $19.31 \pm 0.02198$

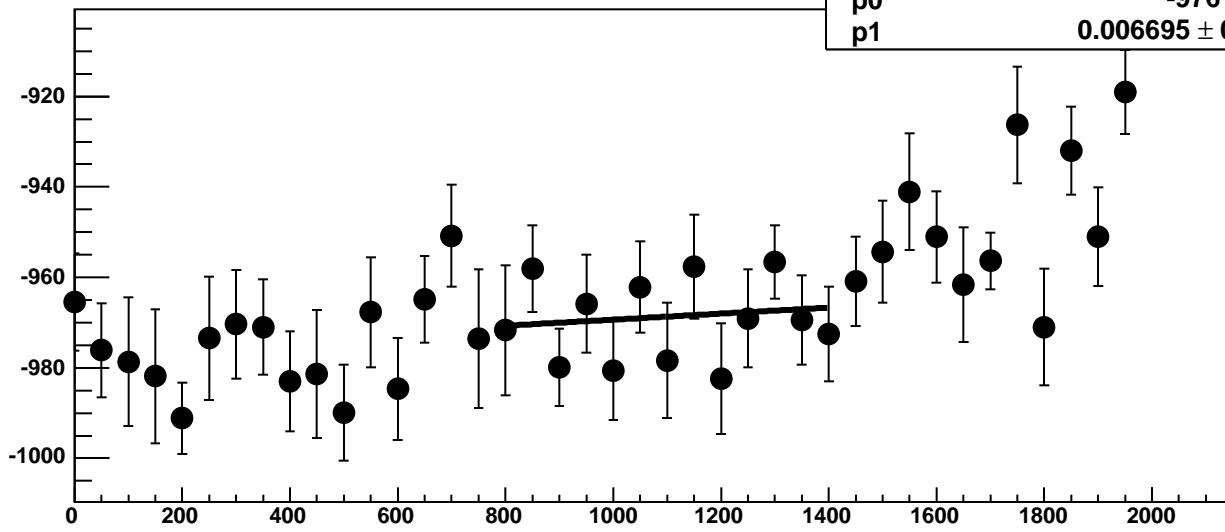
Chip 8, Channel 10, Enable 3!, Hold=30, ADC Noise vs DAC



Chip 8, Channel 10, Enable 3!, Hold=30, ADC Residuals vs DAC

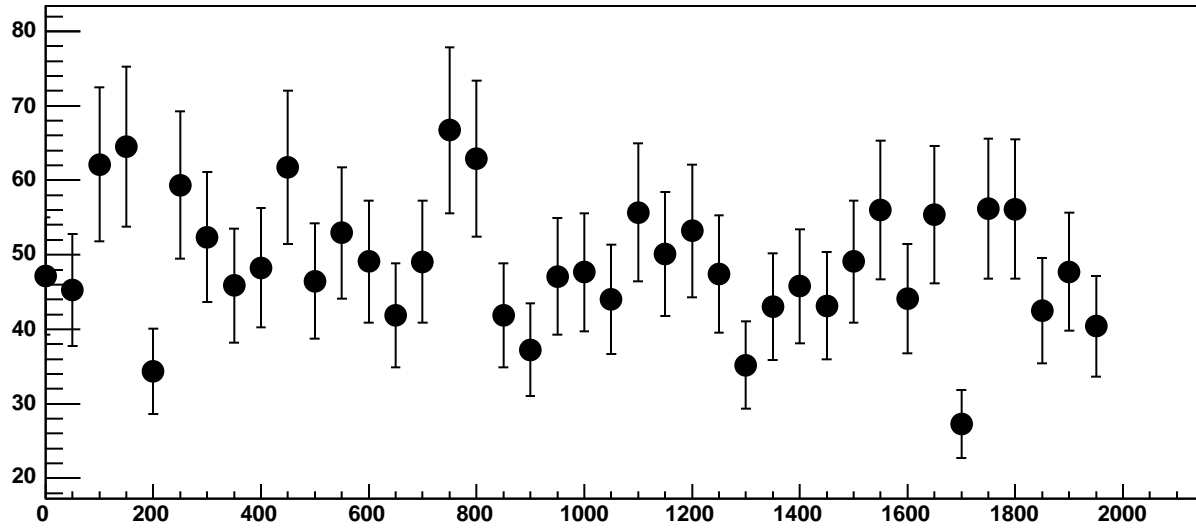


Chip 8, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC

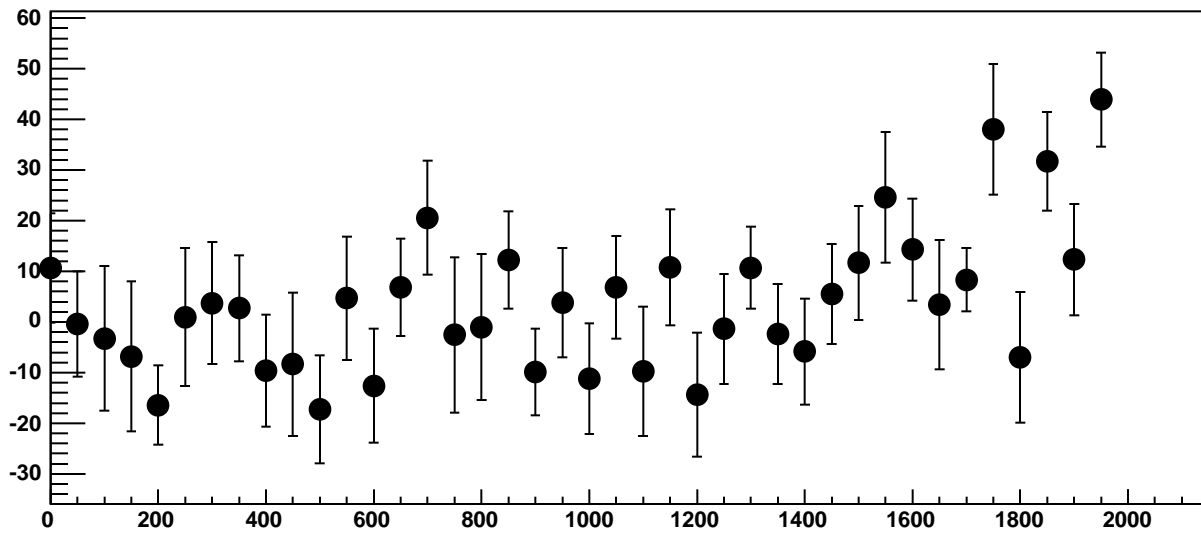


$\chi^2 / \text{ndf}$  9.618 / 11  
p0  $-976 \pm 17.18$   
p1  $0.006695 \pm 0.01526$

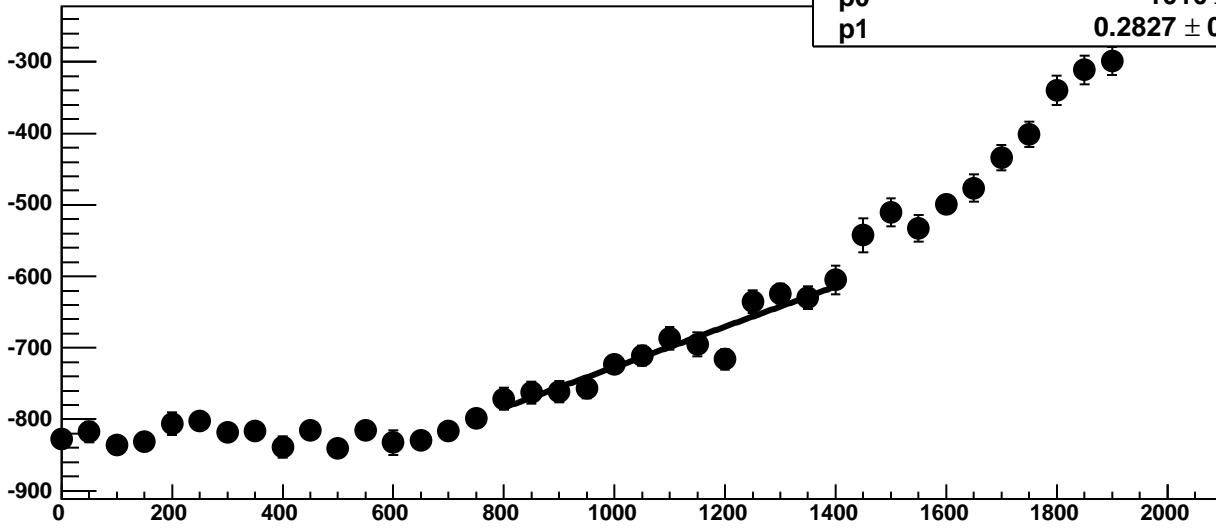
Chip 8, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC



Chip 8, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC

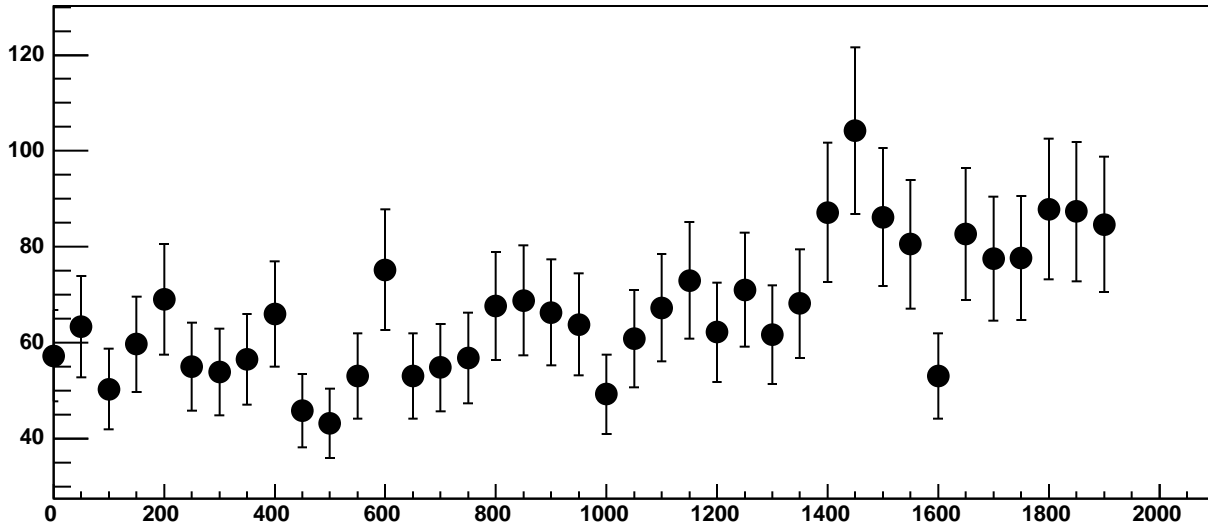


Chip 8, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC

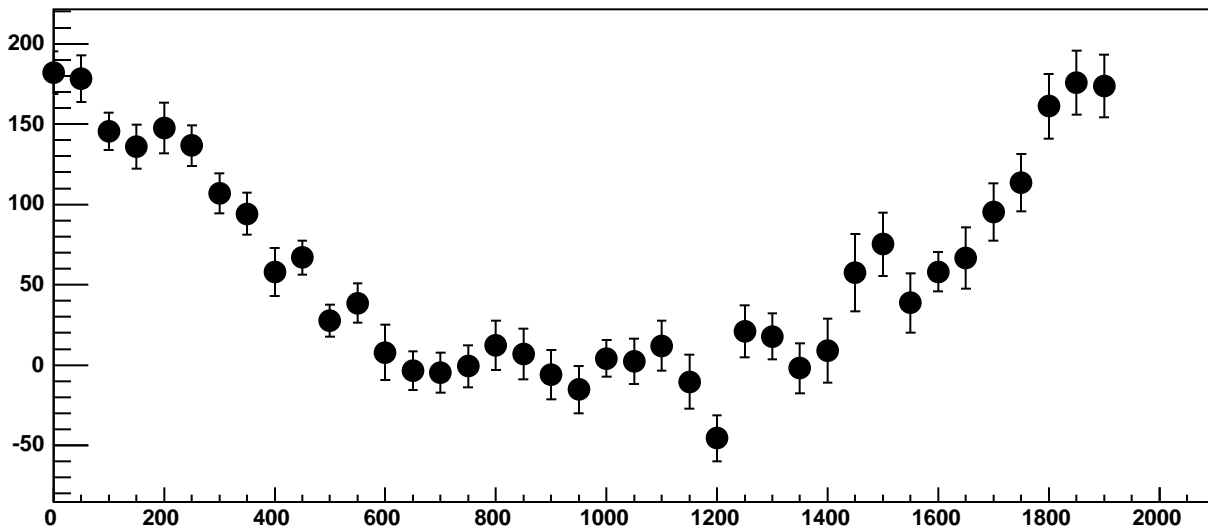


$\chi^2 / \text{ndf}$  16.82 / 11  
p0  $-1010 \pm 25.89$   
p1  $0.2827 \pm 0.02355$

Chip 8, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC

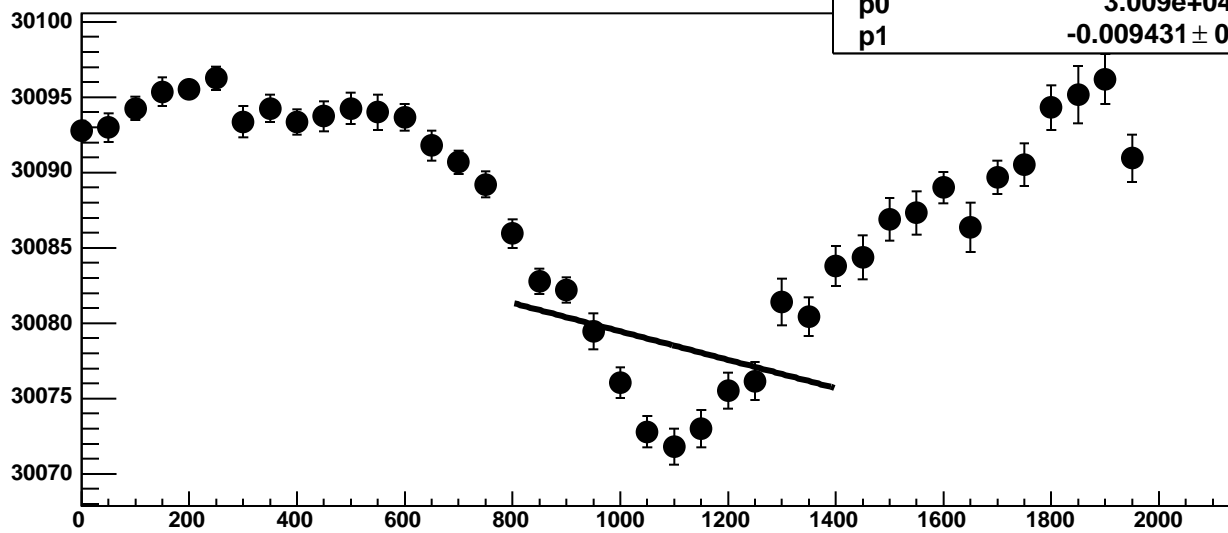


Chip 8, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC





Chip 8, Channel 11, Enable 0!, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

188.3 / 11

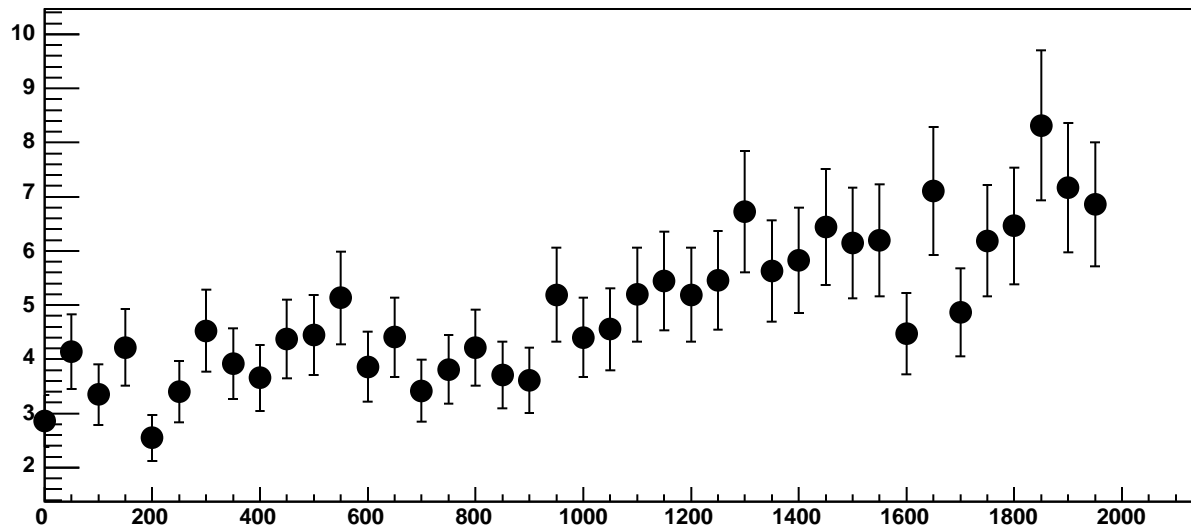
p0

$3.009\text{e}+04 \pm 1.76$

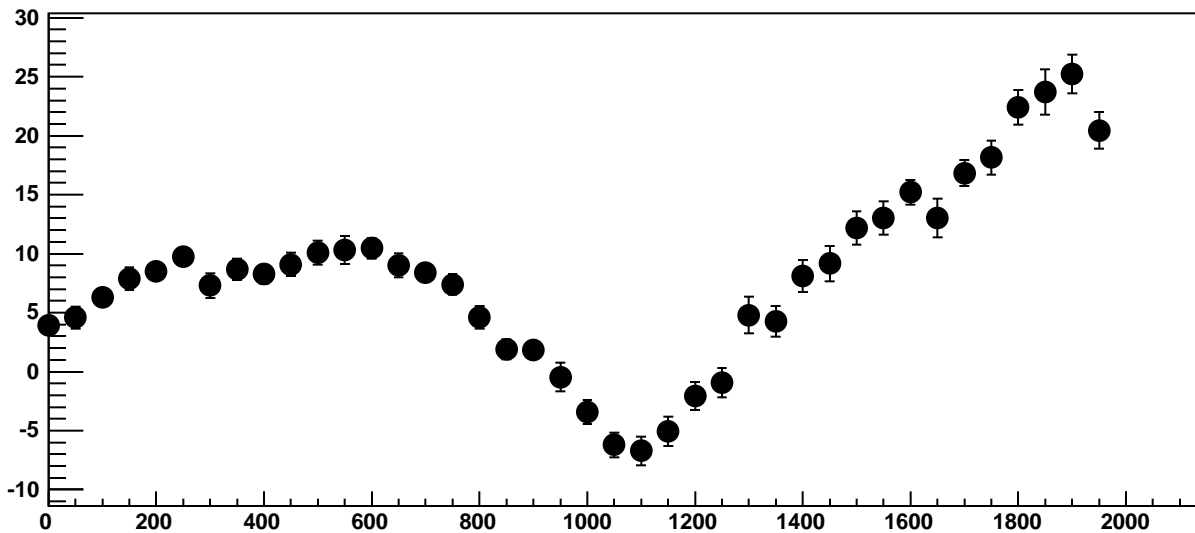
p1

$-0.009431 \pm 0.001661$

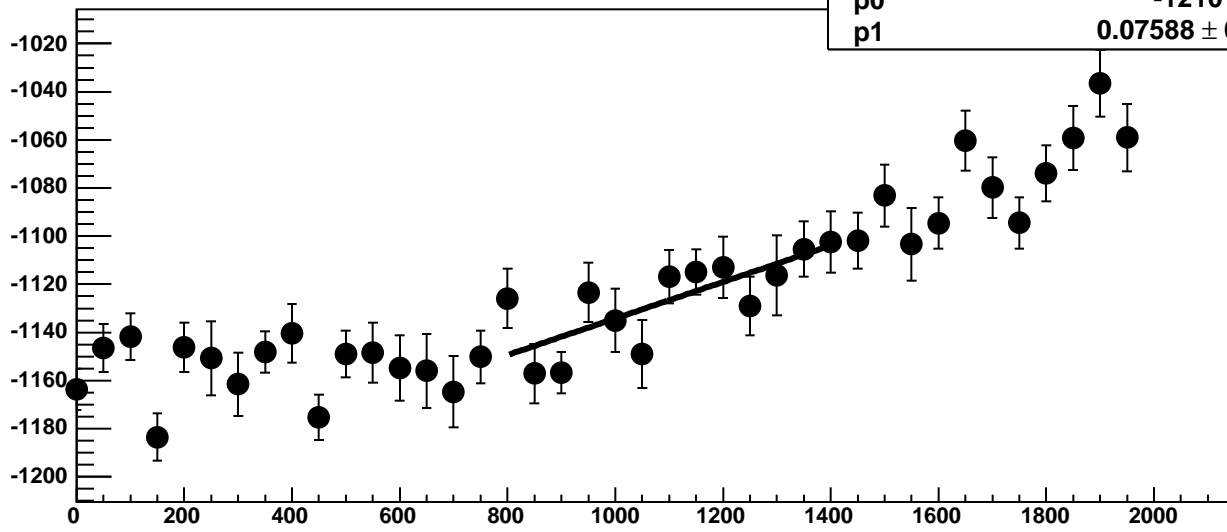
Chip 8, Channel 11, Enable 0!, Hold=30, ADC Noise vs DAC



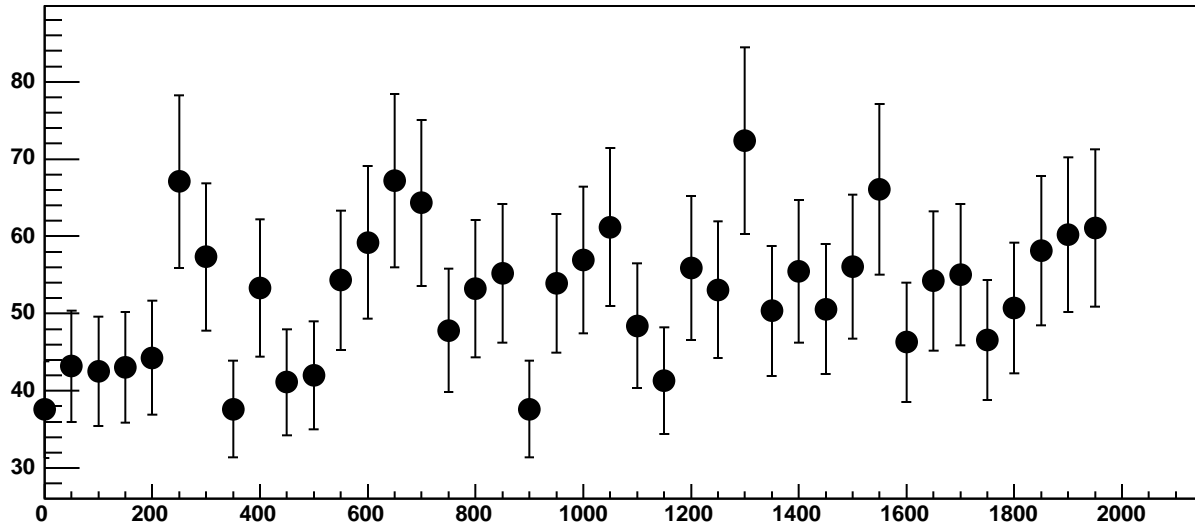
Chip 8, Channel 11, Enable 0!, Hold=30, ADC Residuals vs DAC



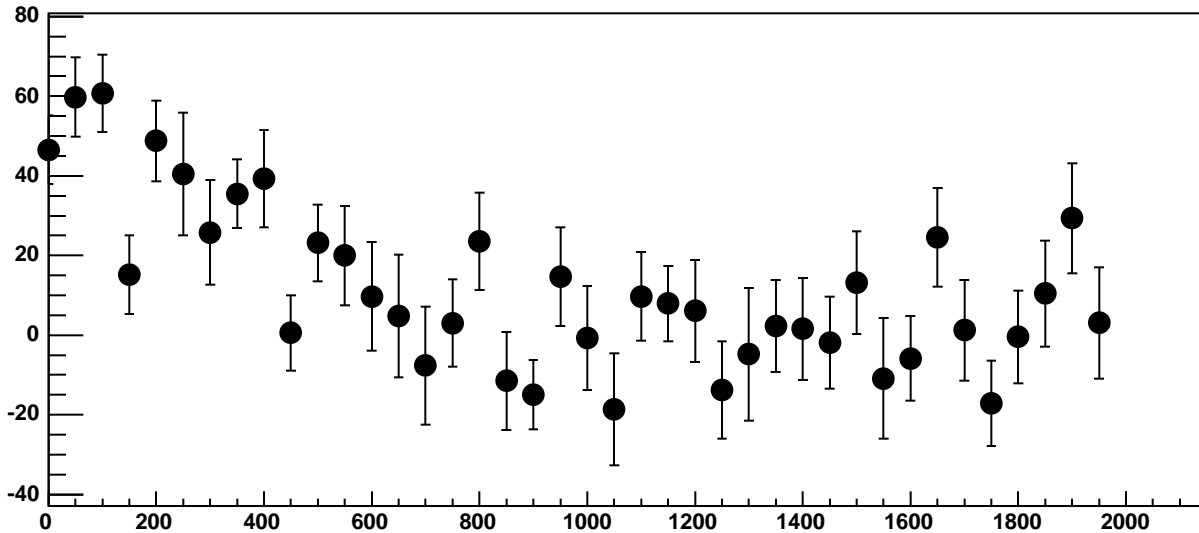
Chip 8, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC



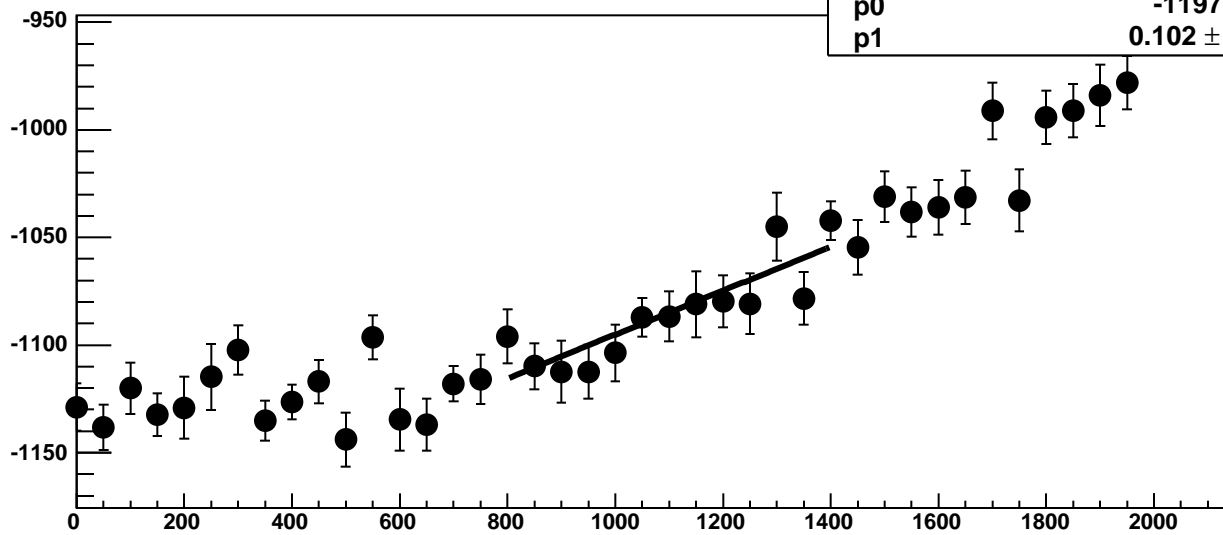
Chip 8, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC



Chip 8, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC



Chip 8, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

10.7 / 11

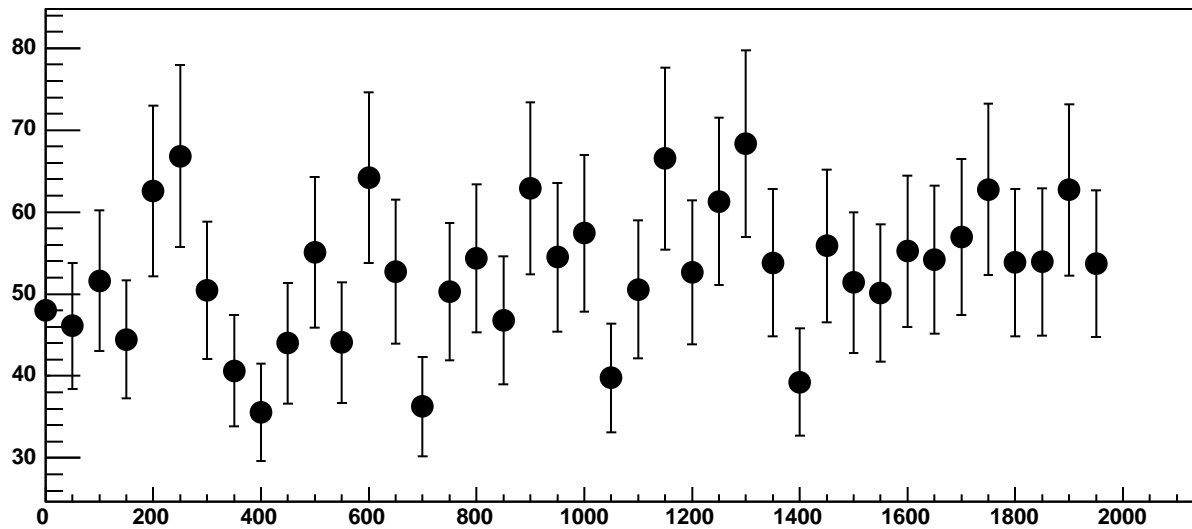
p0

$-1197 \pm 19.23$

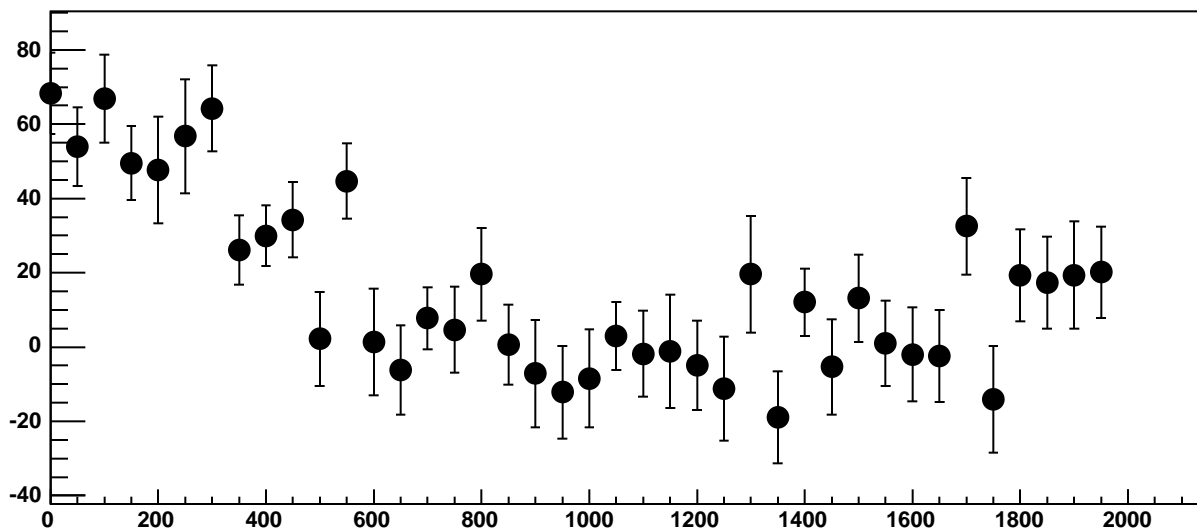
p1

$0.102 \pm 0.01711$

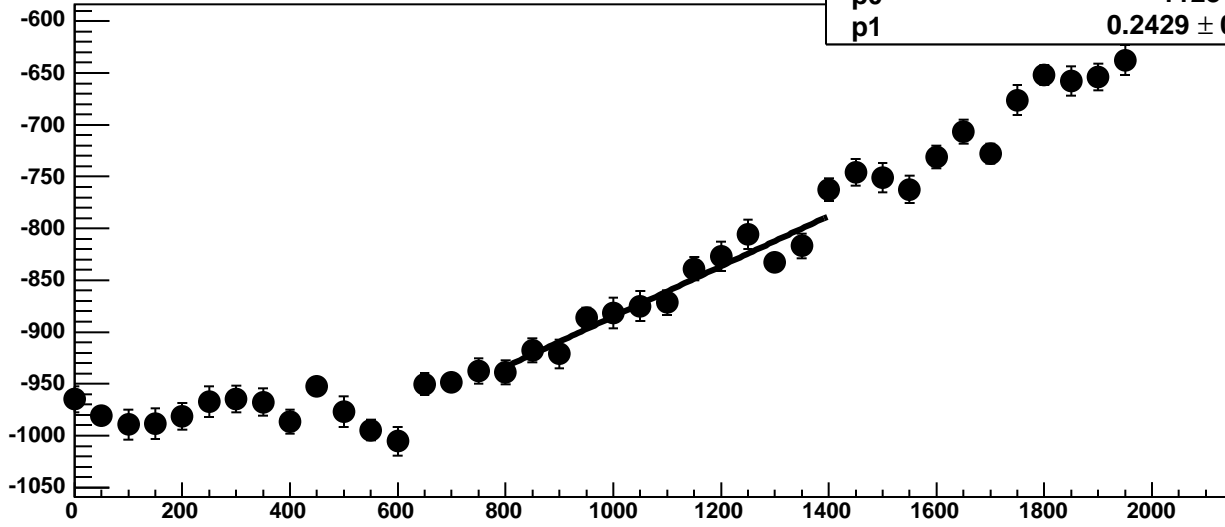
Chip 8, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC



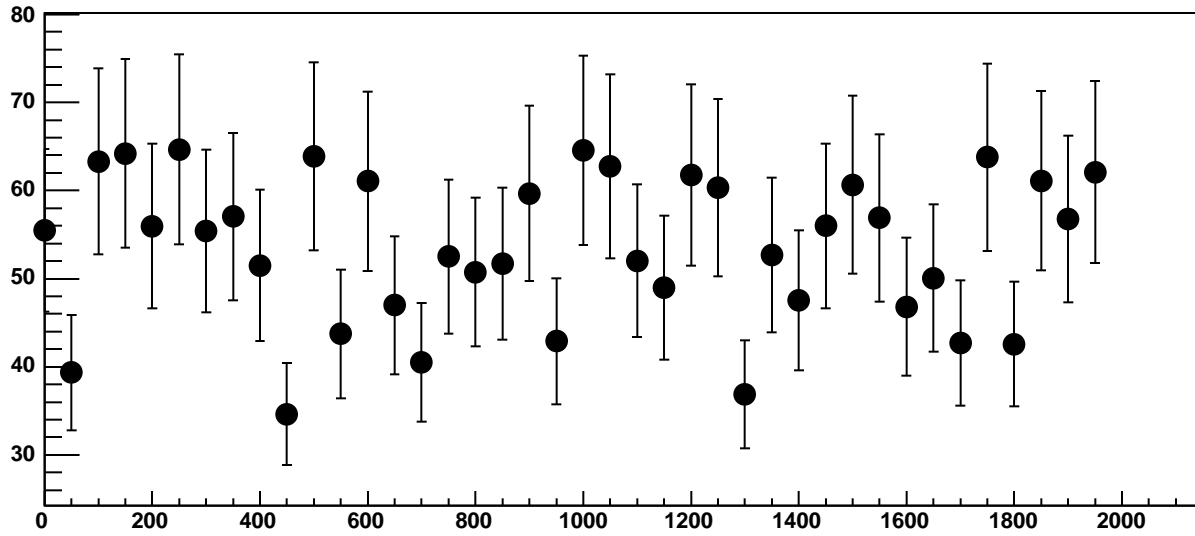
Chip 8, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC



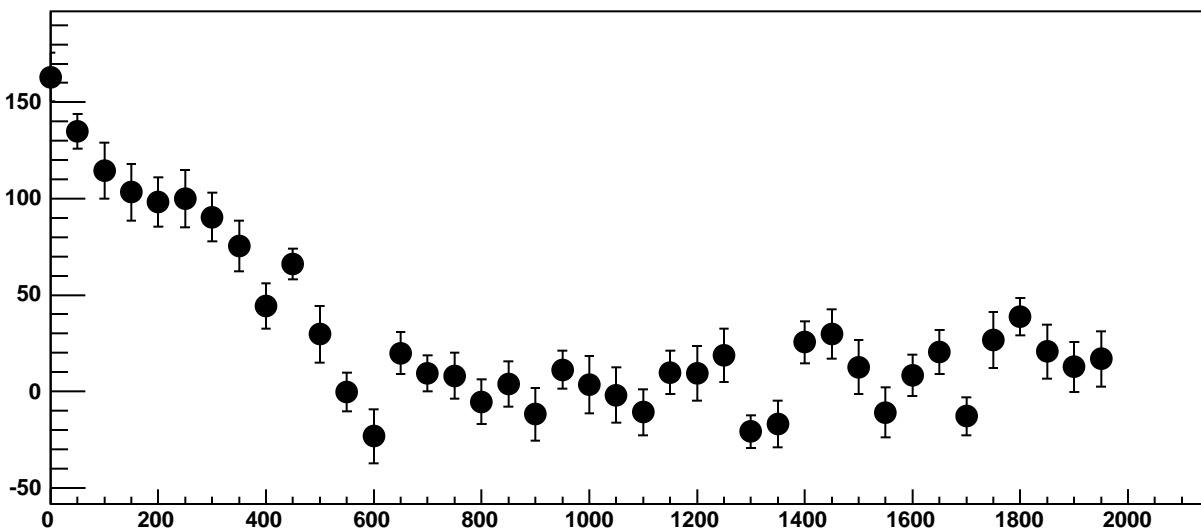
Chip 8, Channel 11, Enable 3, Hold=30, ADC Mean vs DAC



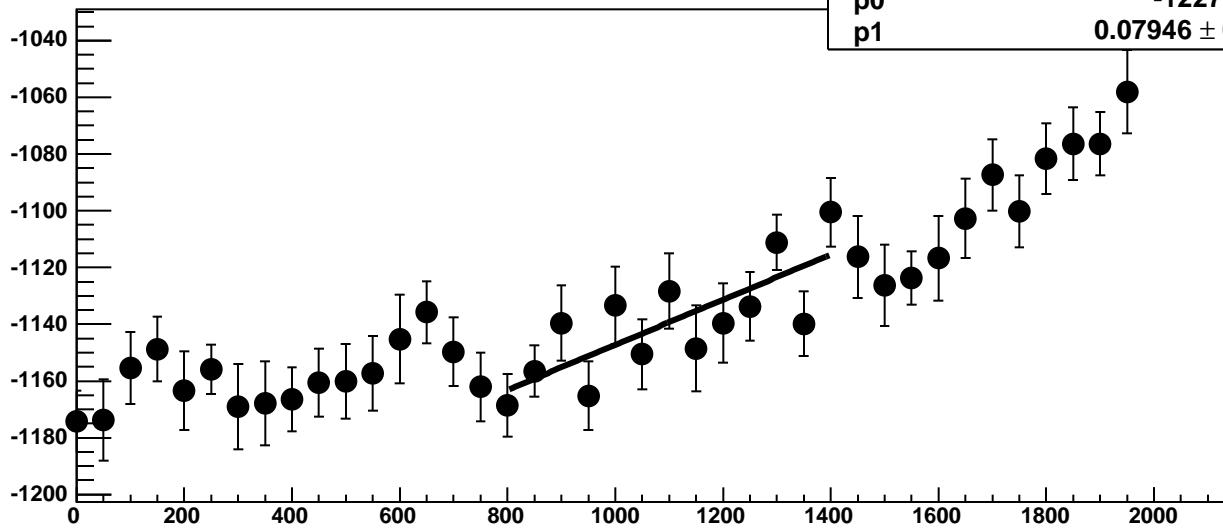
Chip 8, Channel 11, Enable 3, Hold=30, ADC Noise vs DAC



Chip 8, Channel 11, Enable 3, Hold=30, ADC Residuals vs DAC



Chip 8, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

12.84 / 11

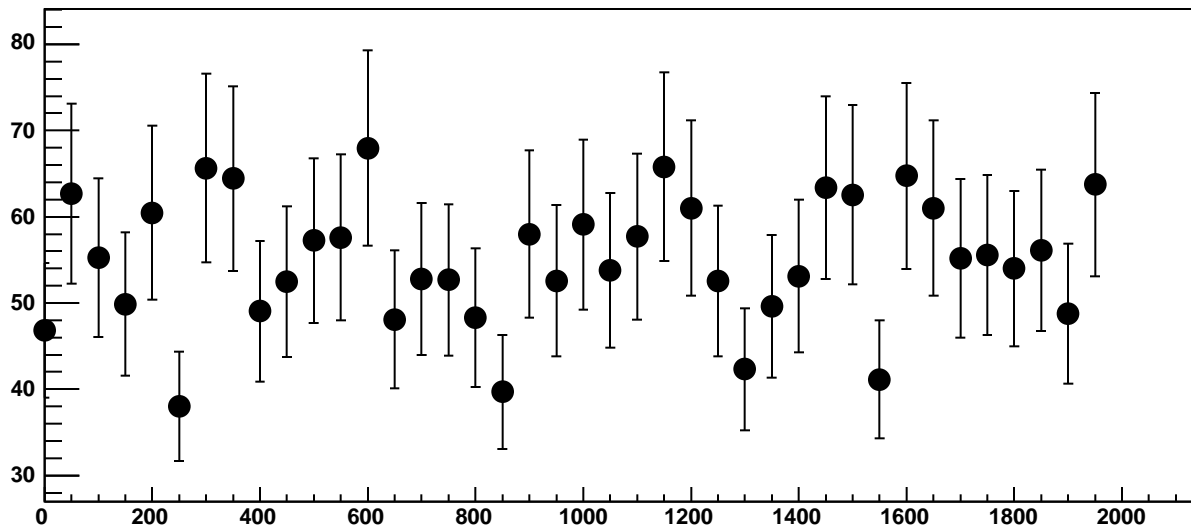
p0

$-1227 \pm 18.35$

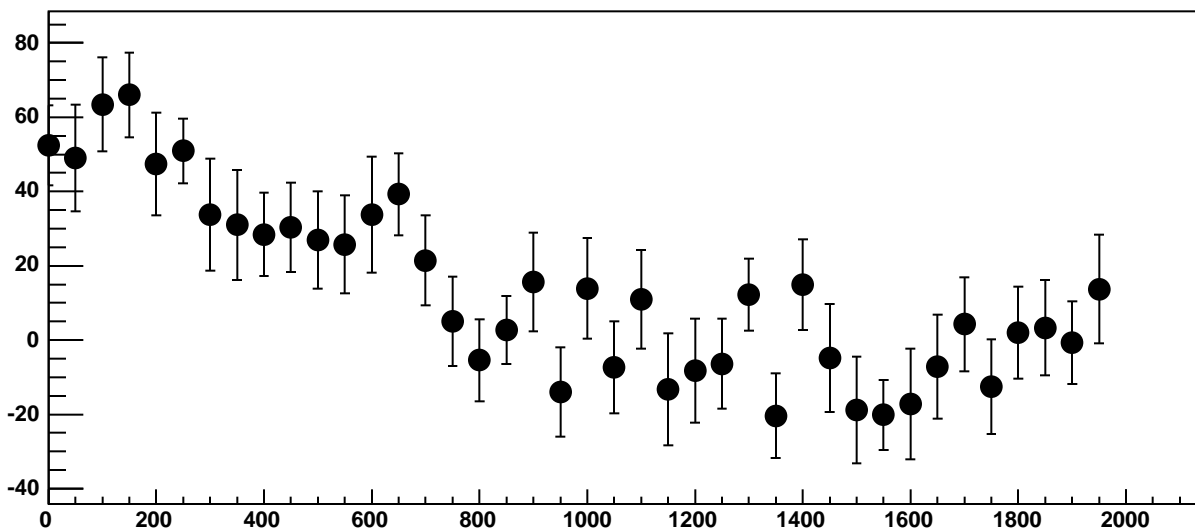
p1

$0.07946 \pm 0.01652$

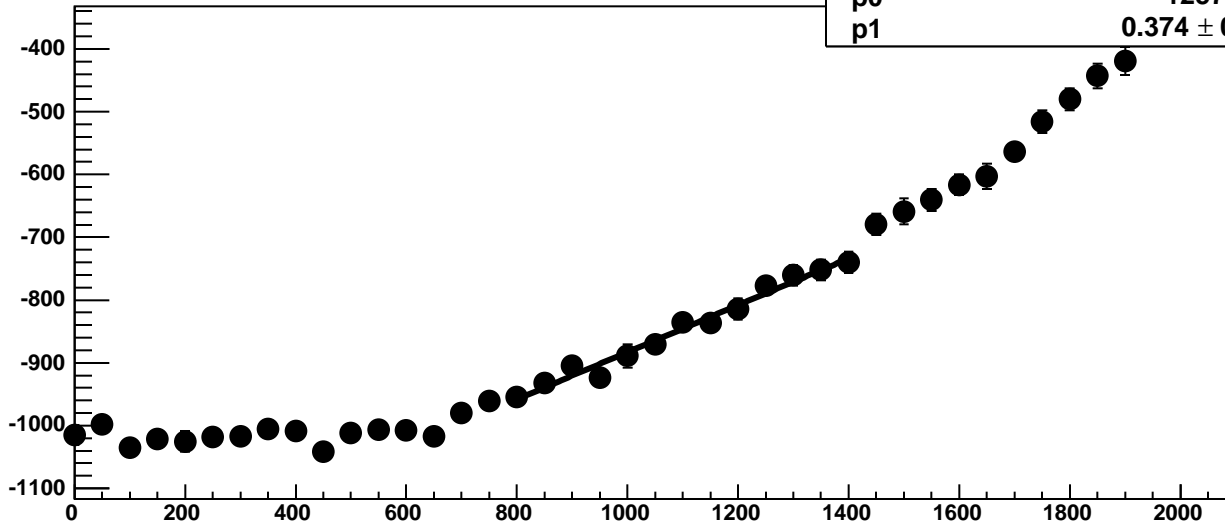
Chip 8, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC



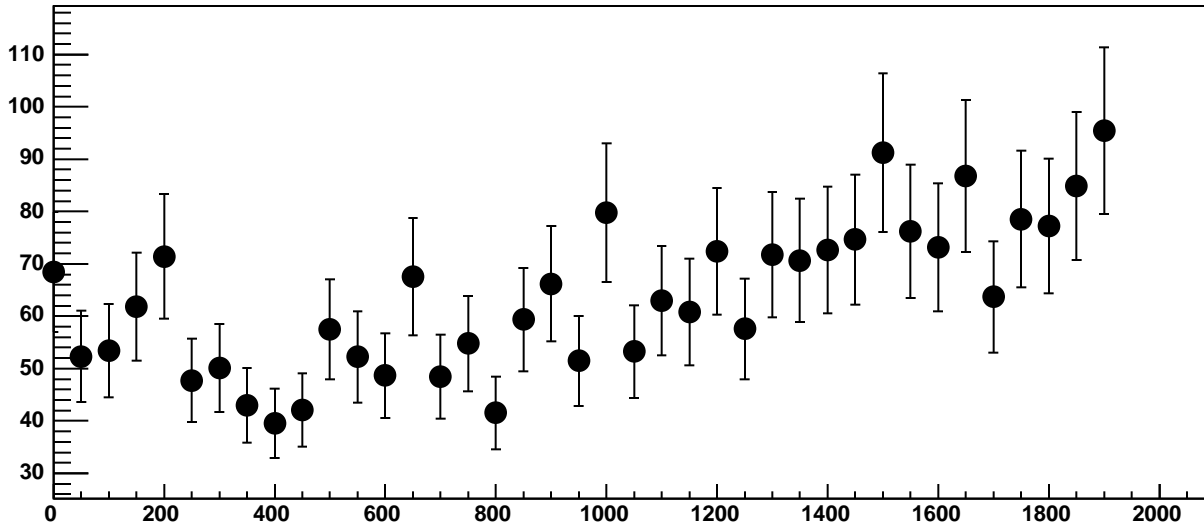
Chip 8, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC



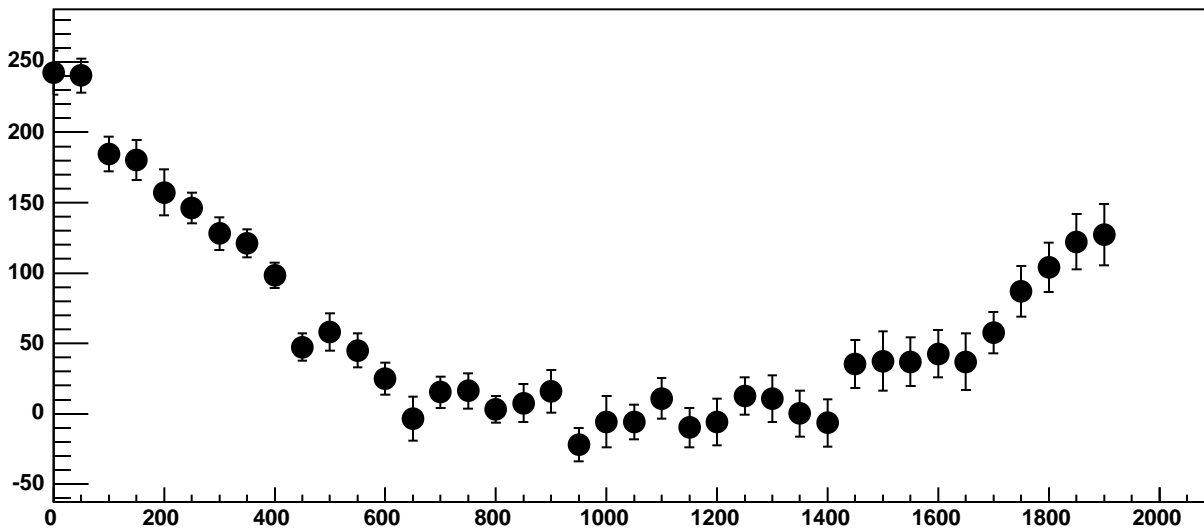
Chip 8, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC



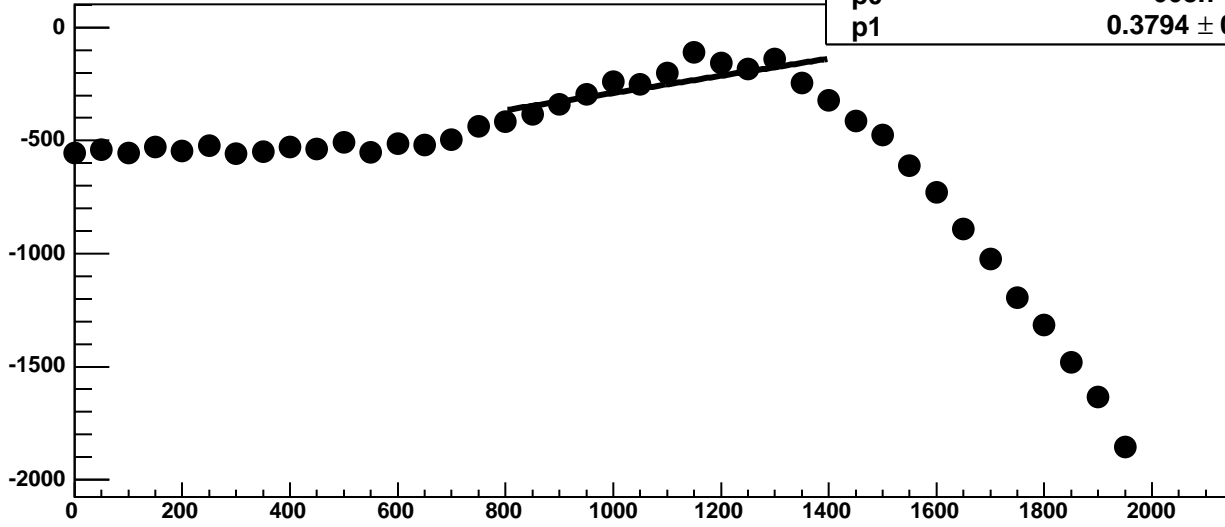
Chip 8, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC



Chip 8, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC

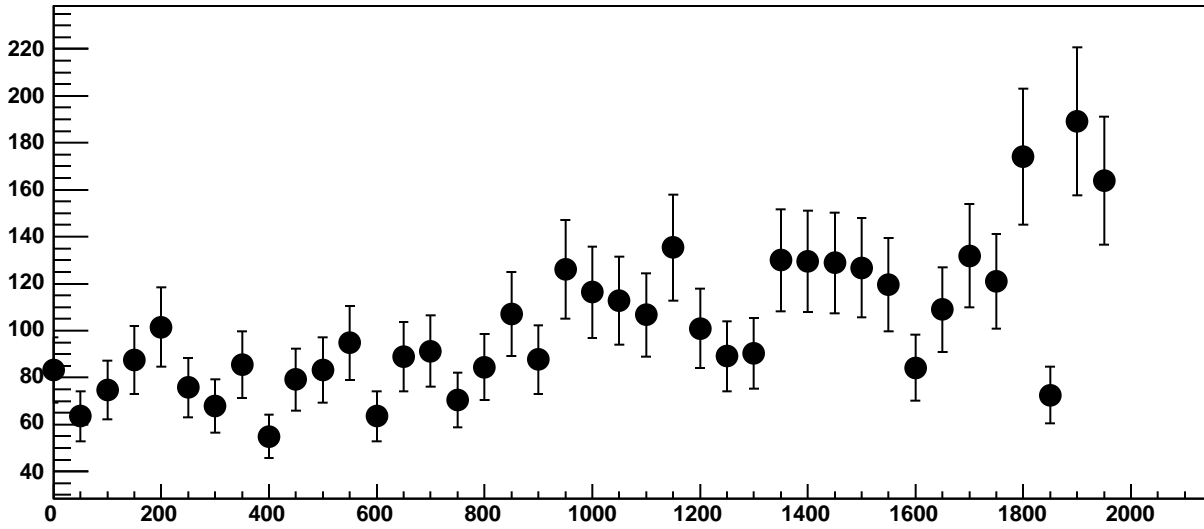


Chip 8, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC

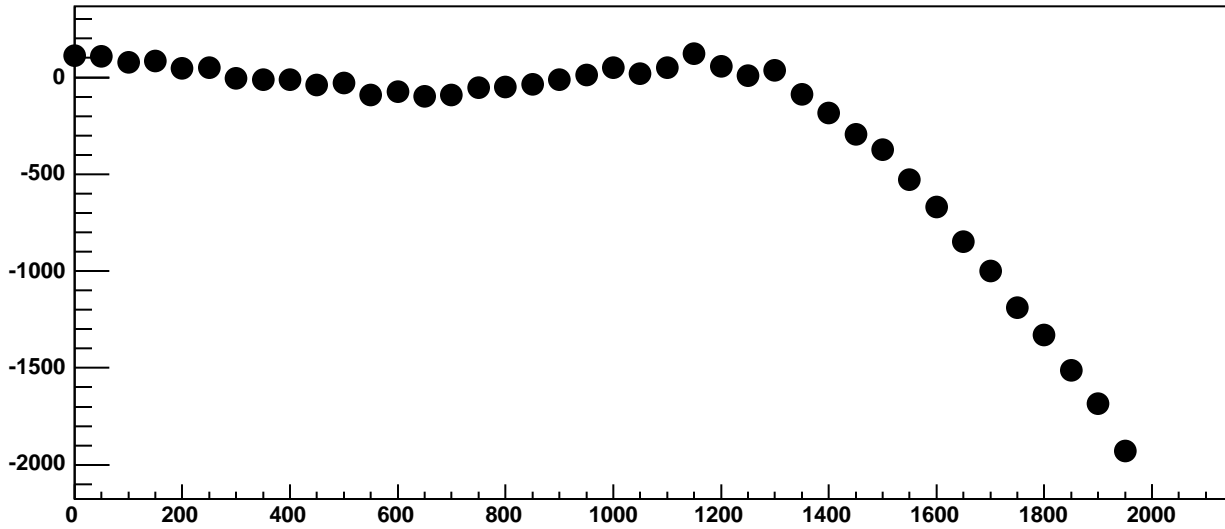


$\chi^2 / \text{ndf}$  89.49 / 11  
p0  $-668.7 \pm 38.37$   
p1  $0.3794 \pm 0.03494$

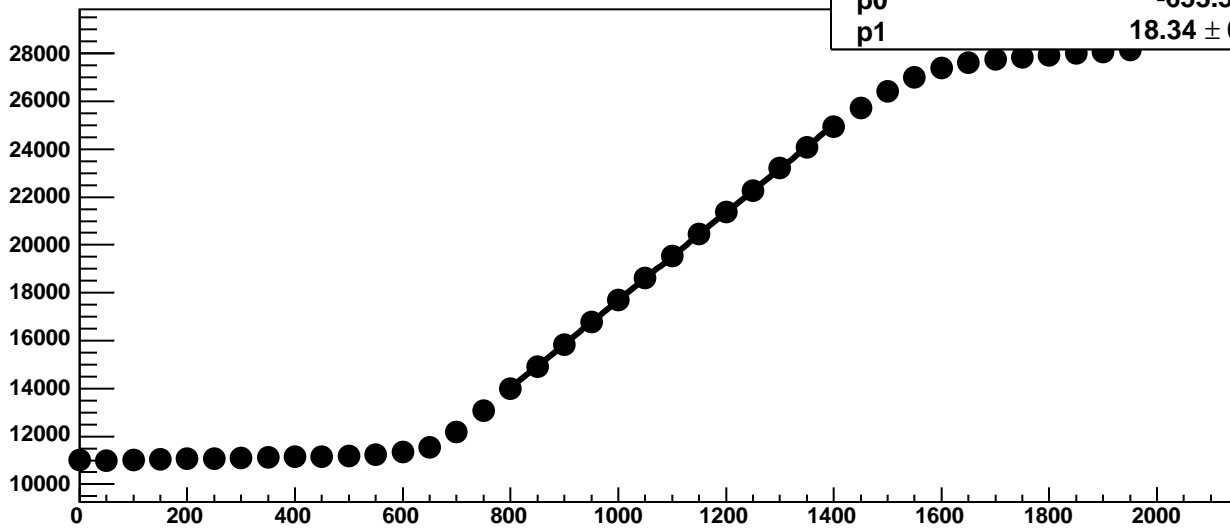
Chip 8, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC



Chip 8, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC

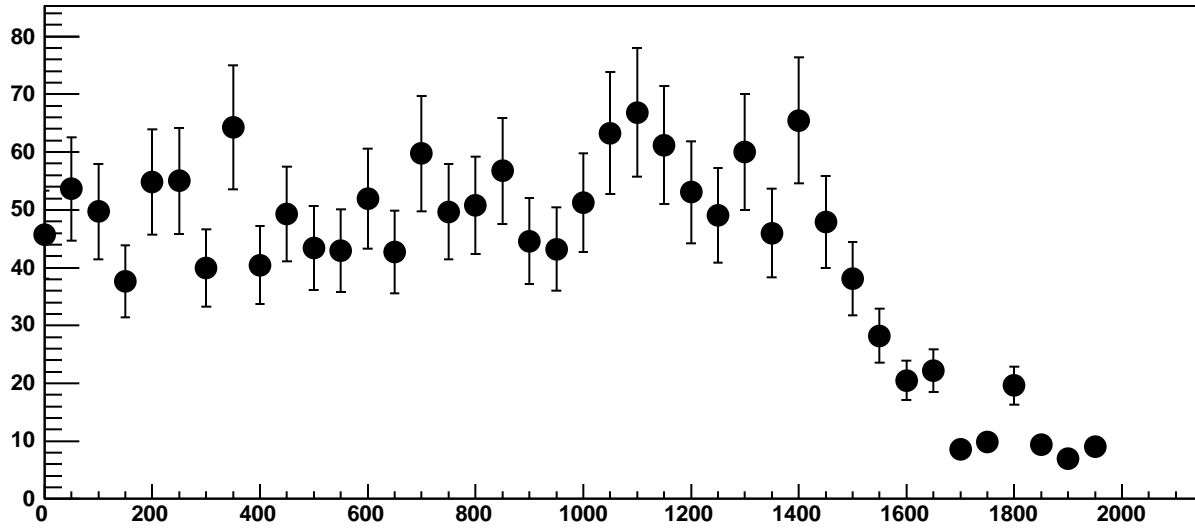


Chip 8, Channel 12, Enable 1!, Hold=30, ADC Mean vs DAC

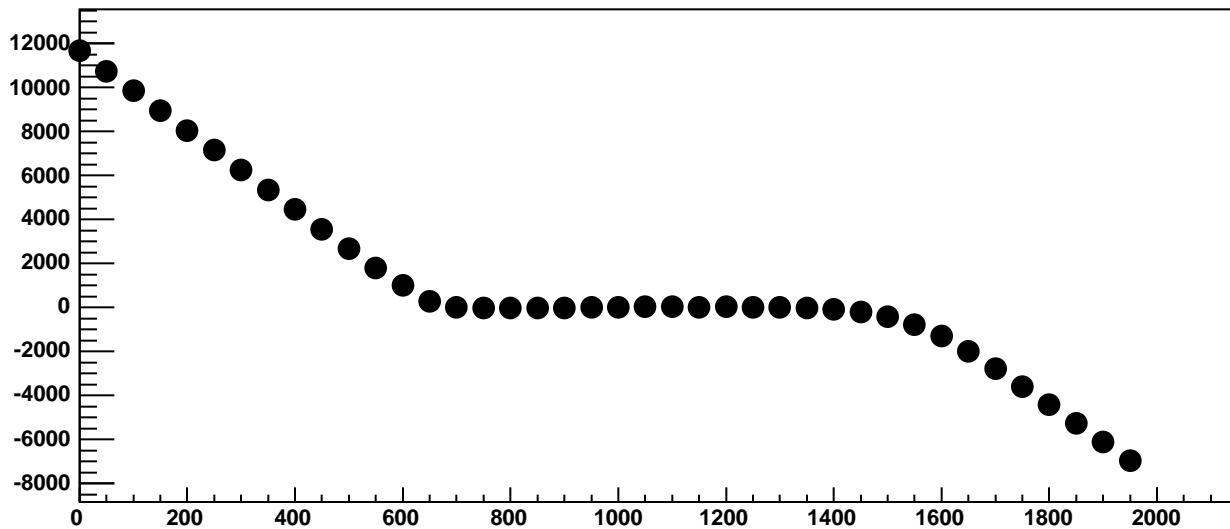


$\chi^2 / \text{ndf}$  71.23 / 11  
p0  $-653.5 \pm 19.6$   
p1  $18.34 \pm 0.01781$

Chip 8, Channel 12, Enable 1!, Hold=30, ADC Noise vs DAC

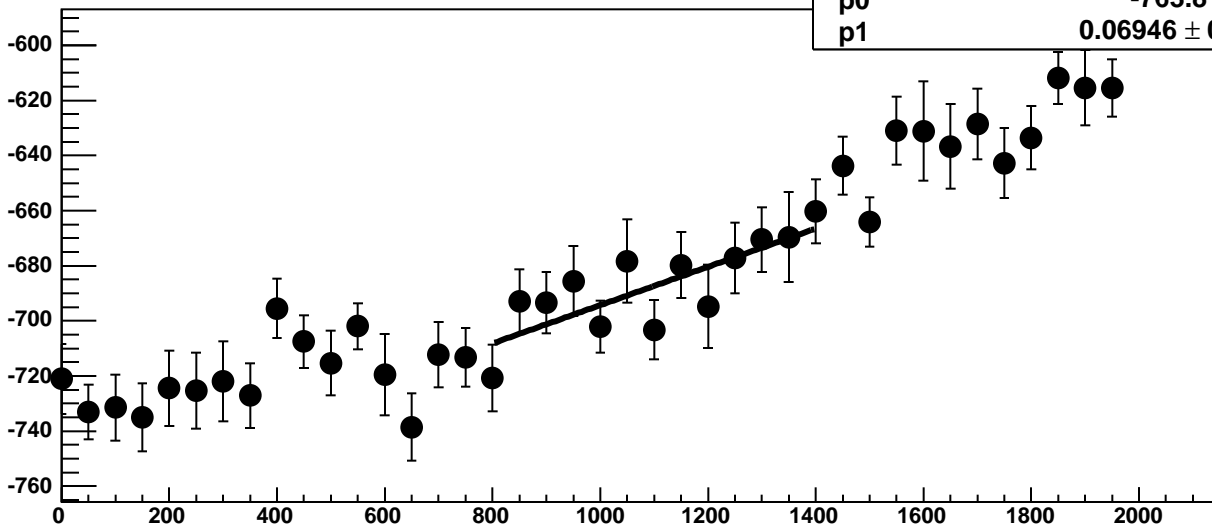


Chip 8, Channel 12, Enable 1!, Hold=30, ADC Residuals vs DAC



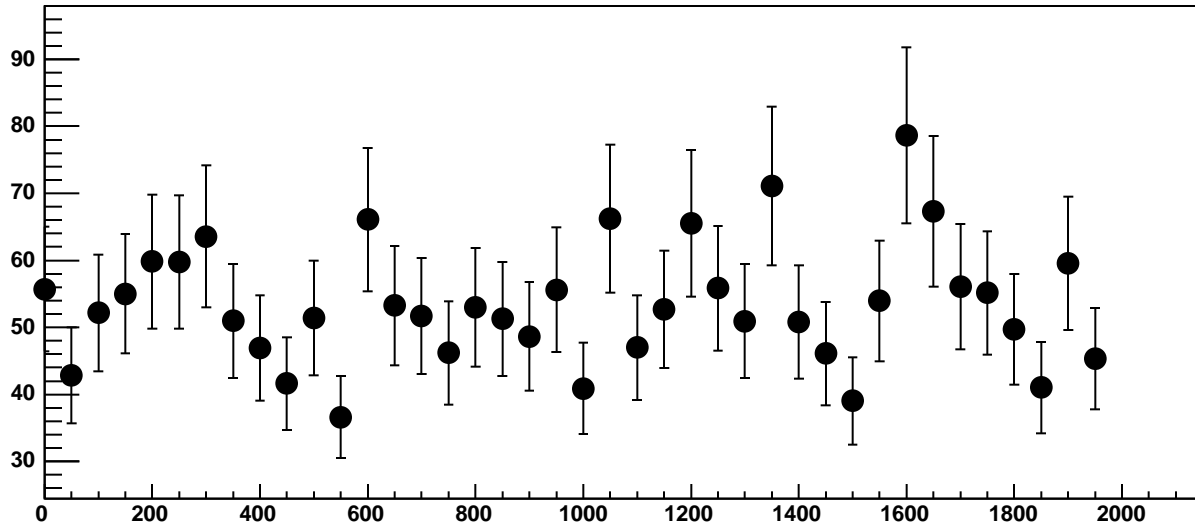


Chip 8, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC

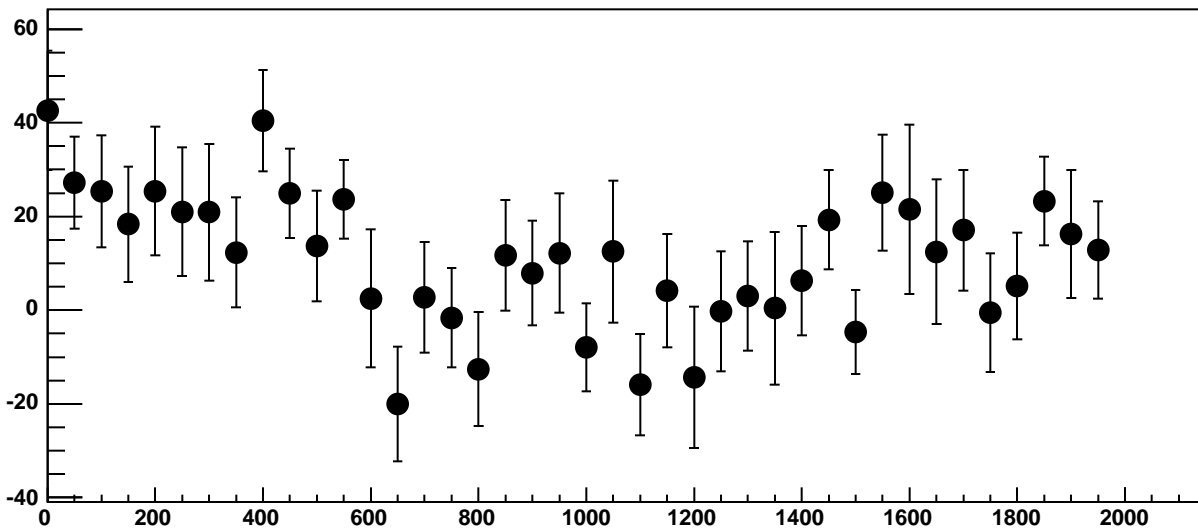


$\chi^2 / \text{ndf}$  8.399 / 11  
p0  $-763.8 \pm 20.04$   
p1  $0.06946 \pm 0.01822$

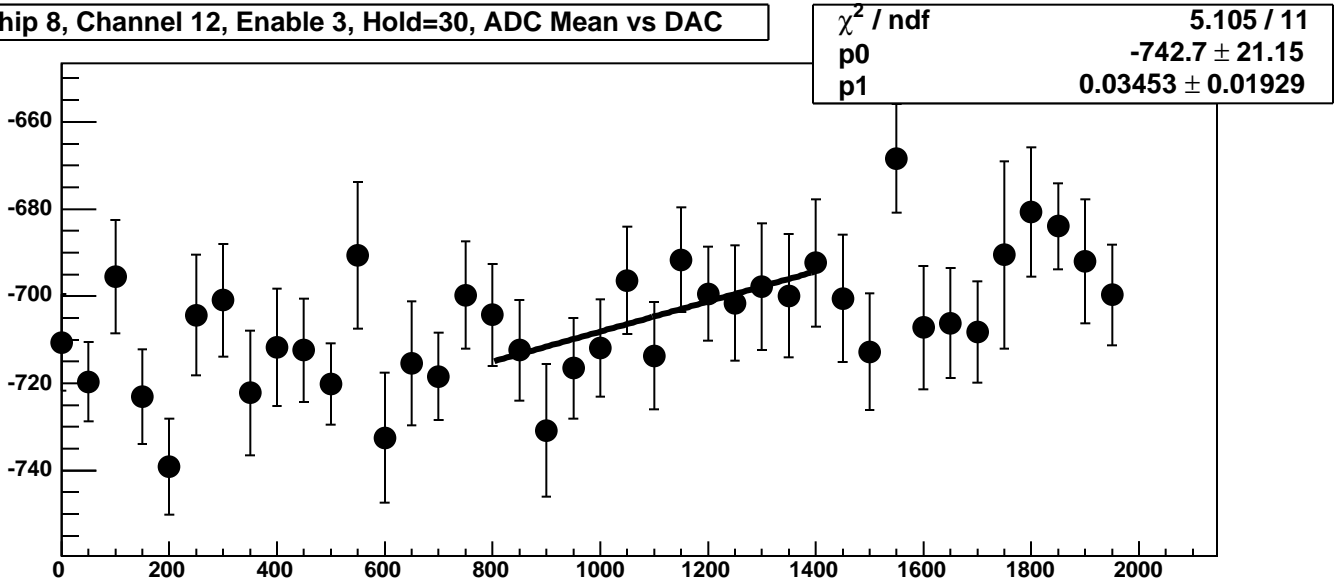
Chip 8, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC



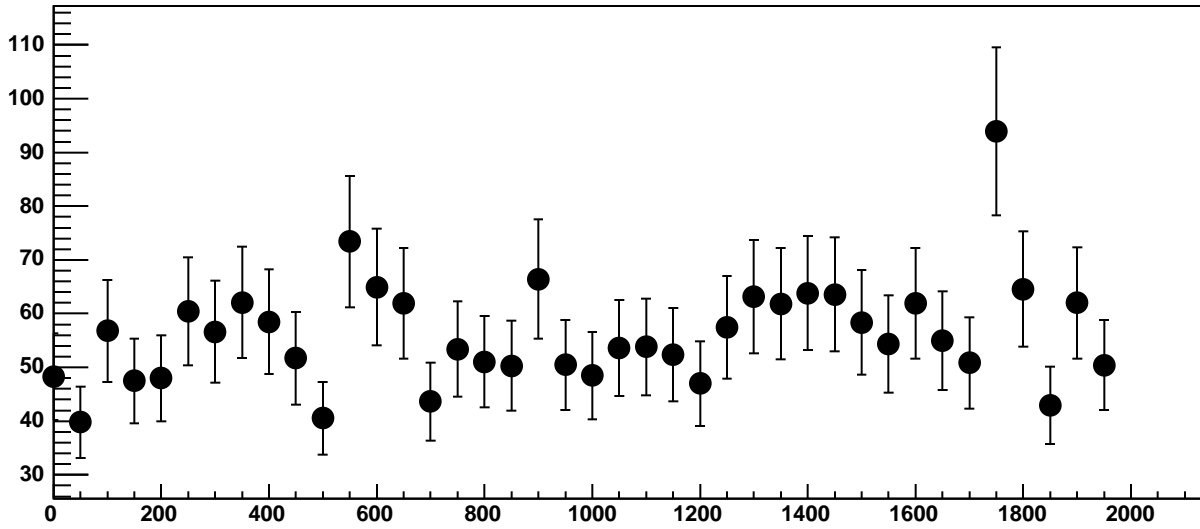
Chip 8, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC



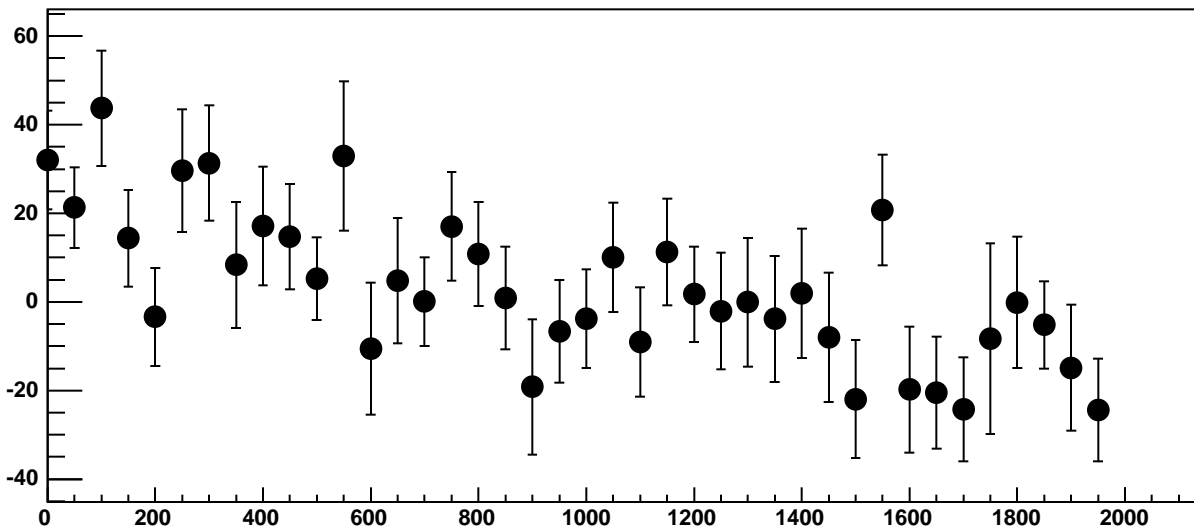
Chip 8, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC



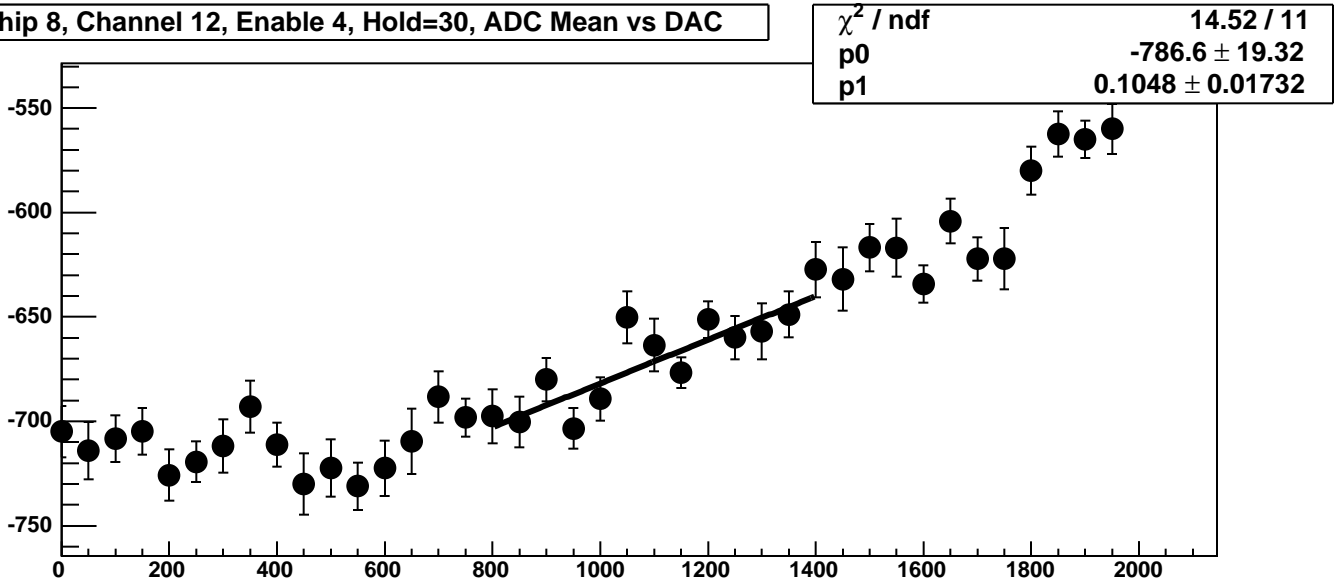
Chip 8, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC



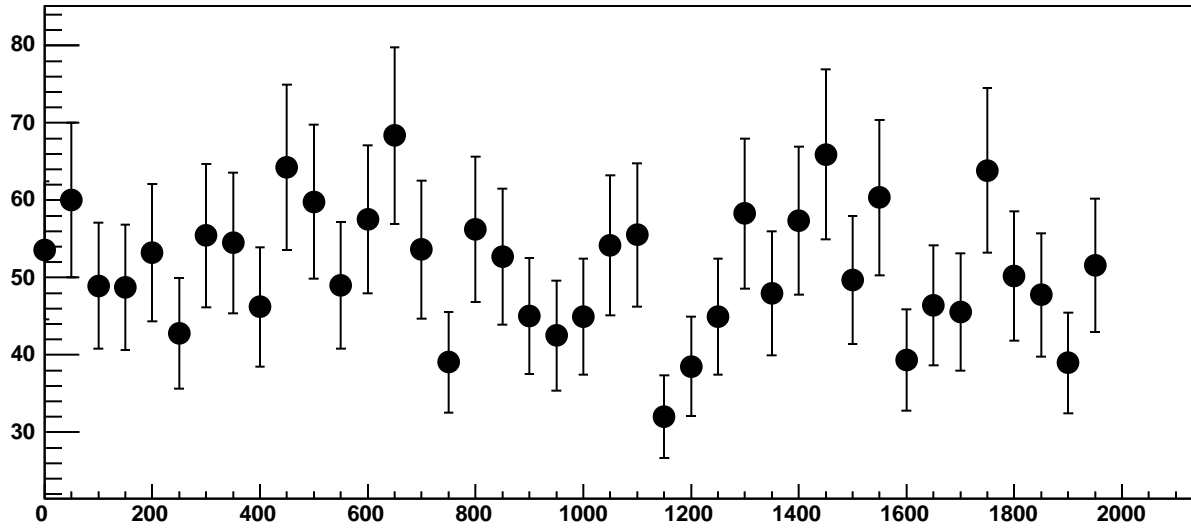
Chip 8, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC



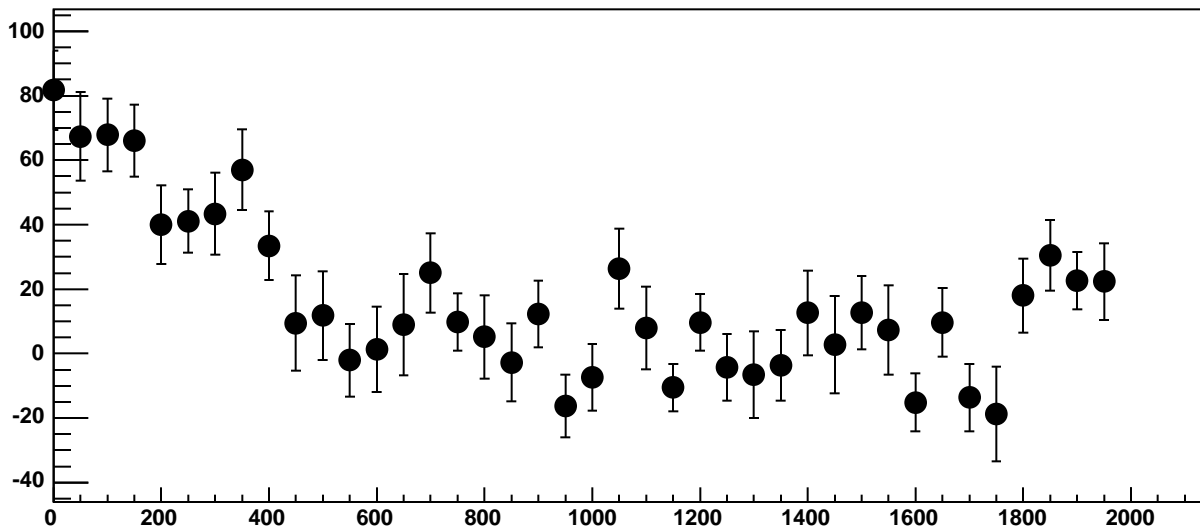
Chip 8, Channel 12, Enable 4, Hold=30, ADC Mean vs DAC



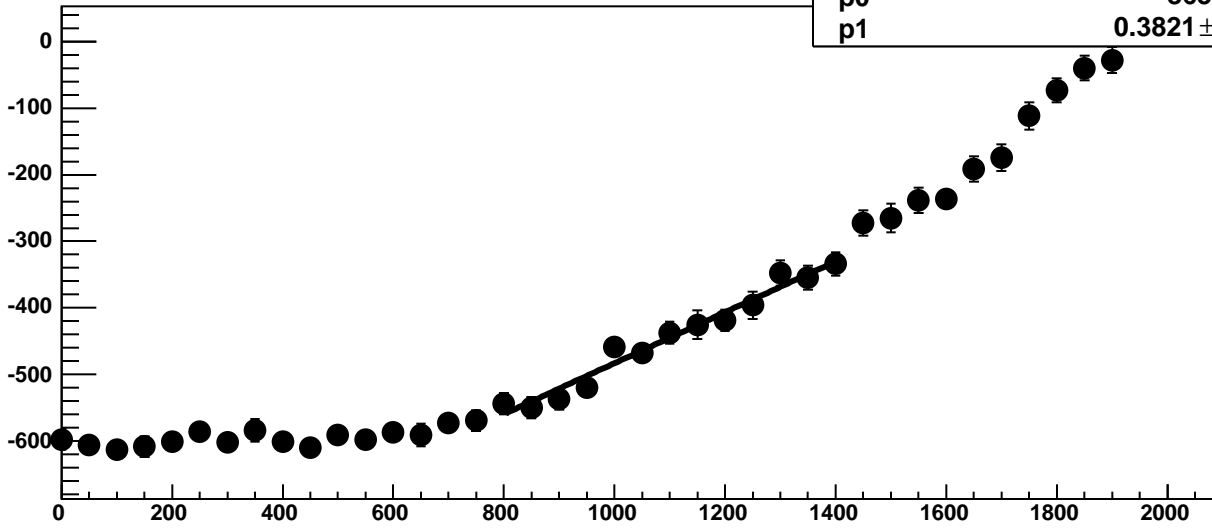
Chip 8, Channel 12, Enable 4, Hold=30, ADC Noise vs DAC



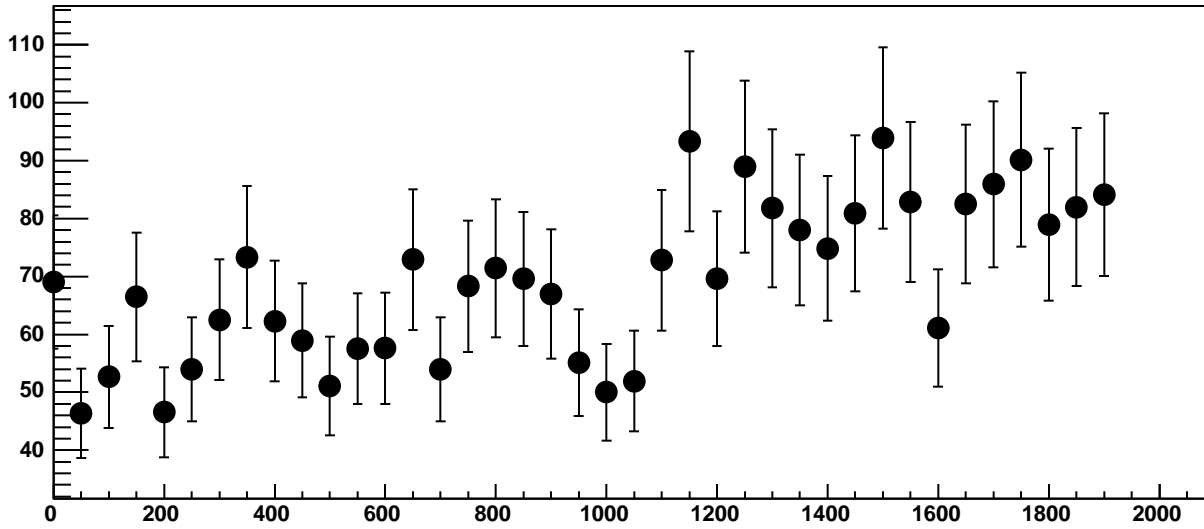
Chip 8, Channel 12, Enable 4, Hold=30, ADC Residuals vs DAC



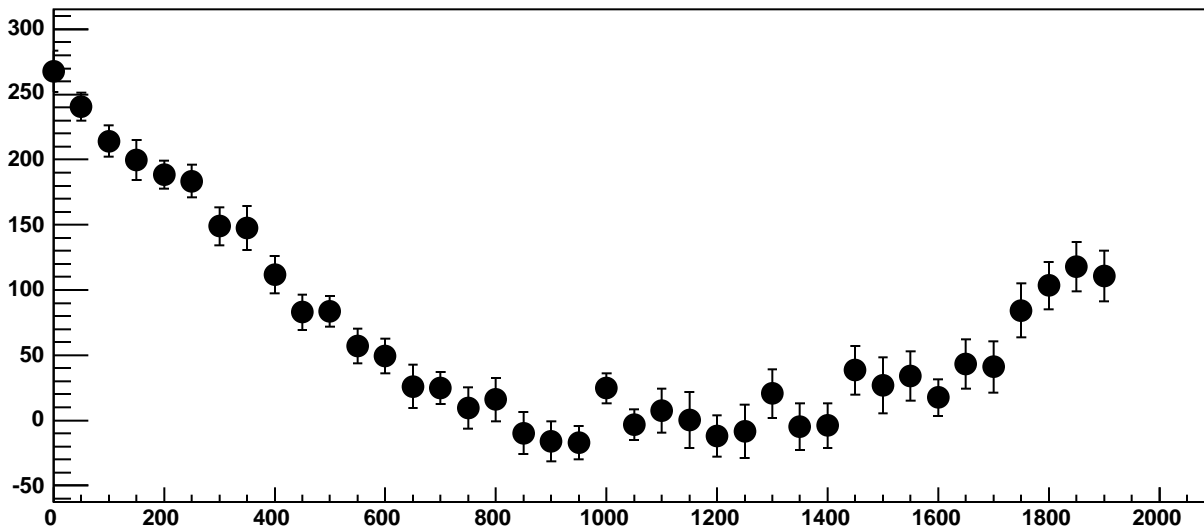
Chip 8, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC



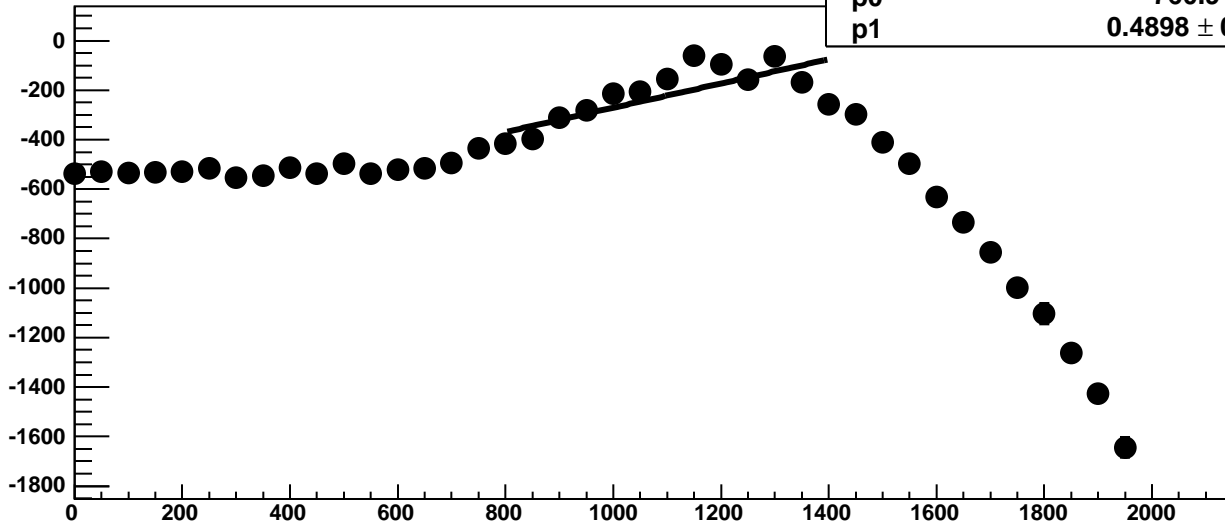
Chip 8, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC



Chip 8, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC

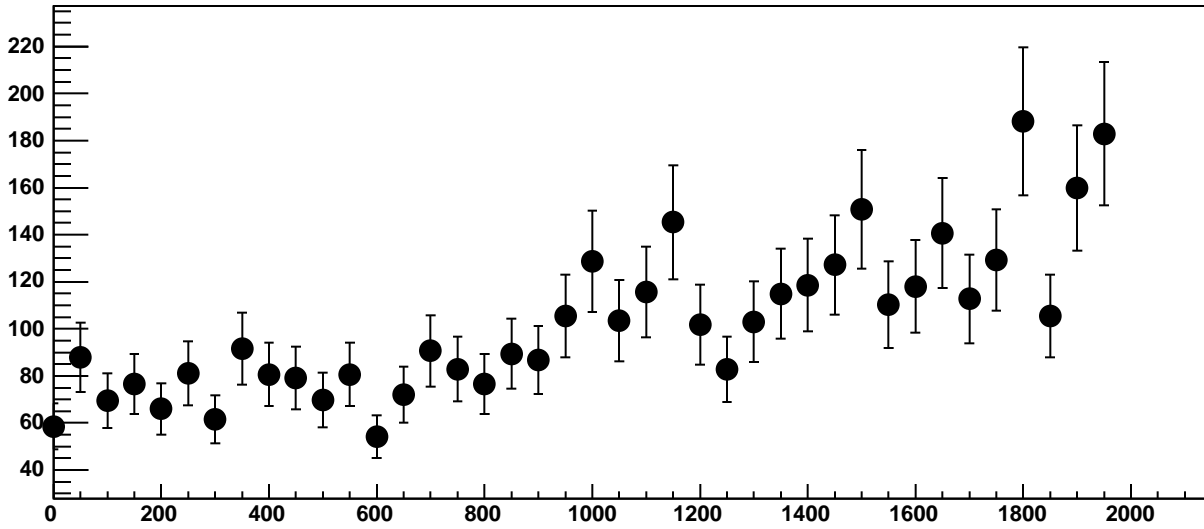


Chip 8, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC

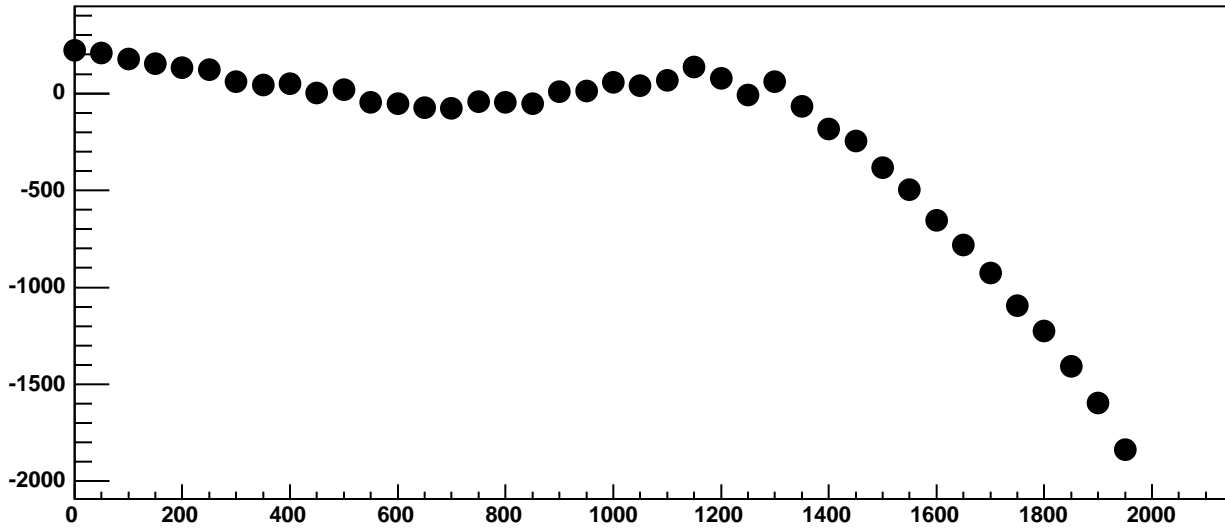


$\chi^2 / \text{ndf}$  114.1 / 11  
p0  $-760.9 \pm 35.49$   
p1  $0.4898 \pm 0.03266$

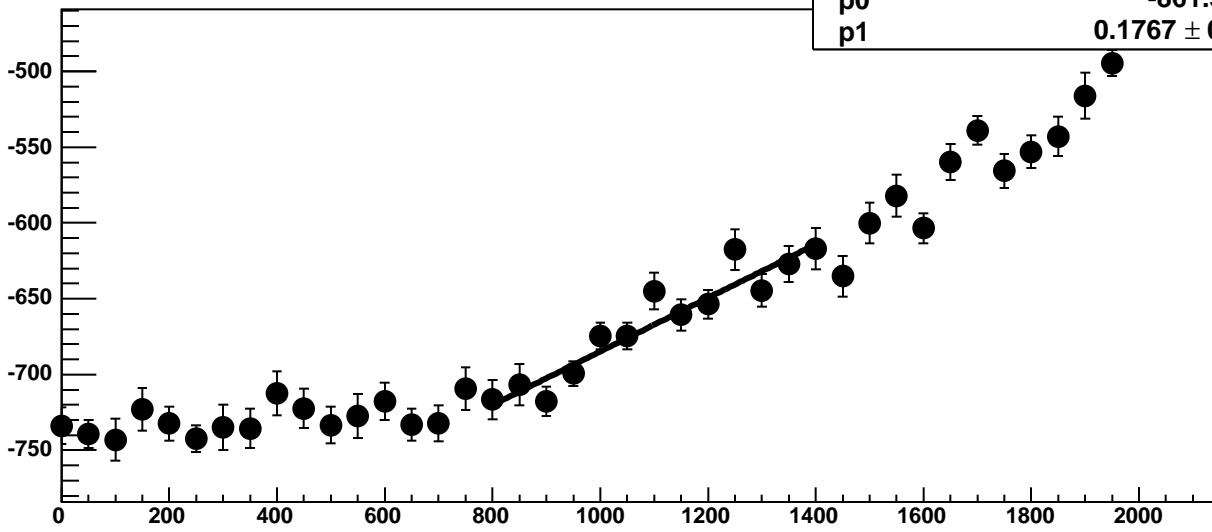
Chip 8, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC



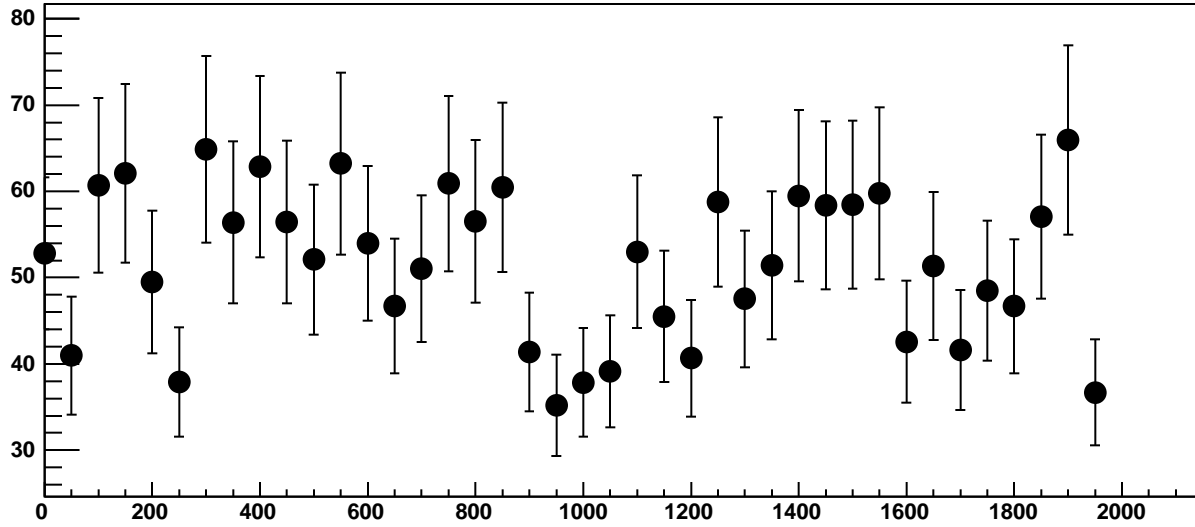
Chip 8, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC



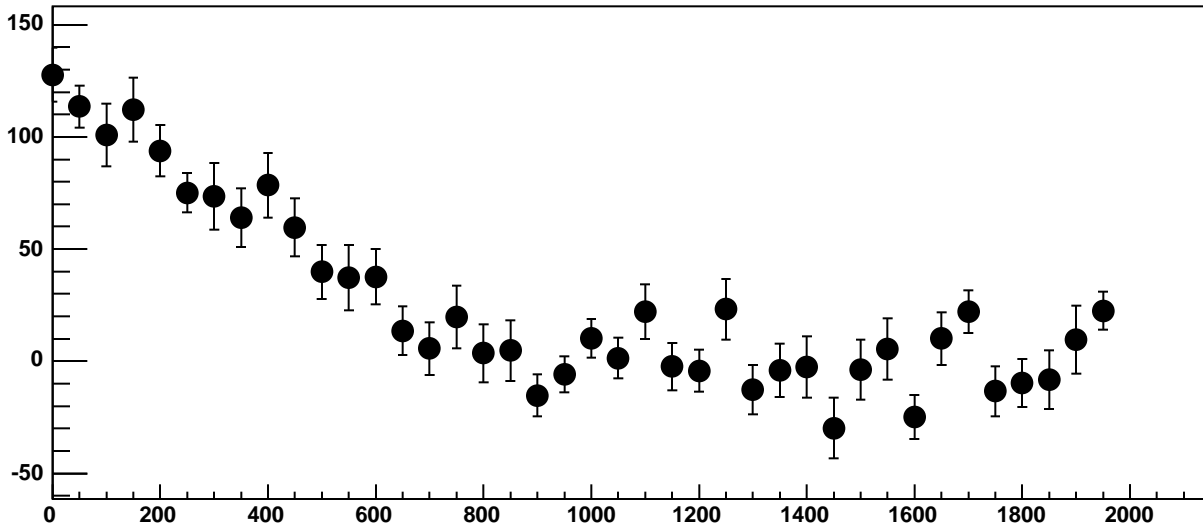
Chip 8, Channel 13, Enable 1, Hold=30, ADC Mean vs DAC



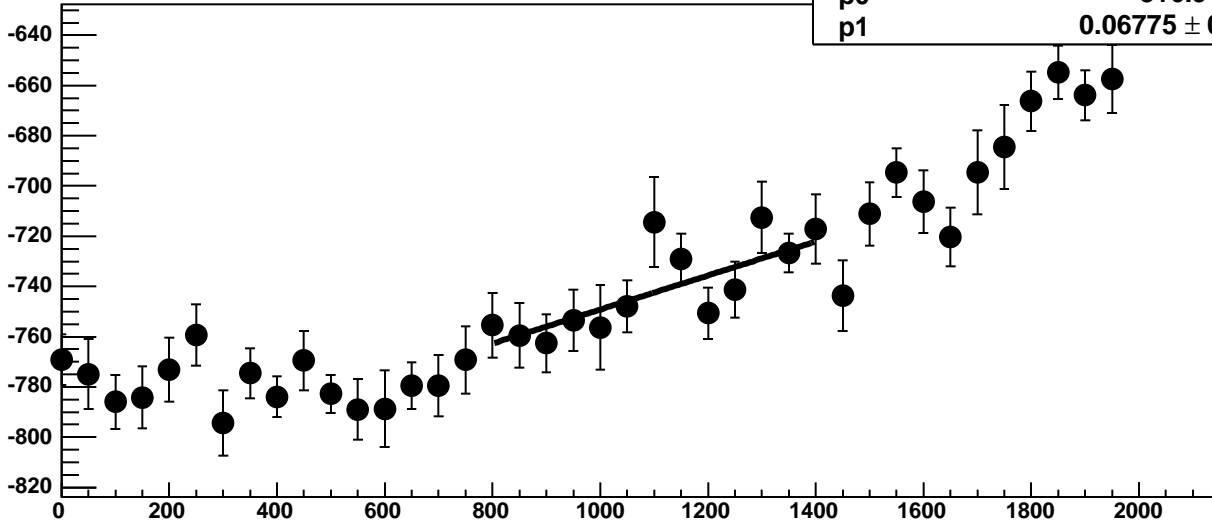
Chip 8, Channel 13, Enable 1, Hold=30, ADC Noise vs DAC



Chip 8, Channel 13, Enable 1, Hold=30, ADC Residuals vs DAC

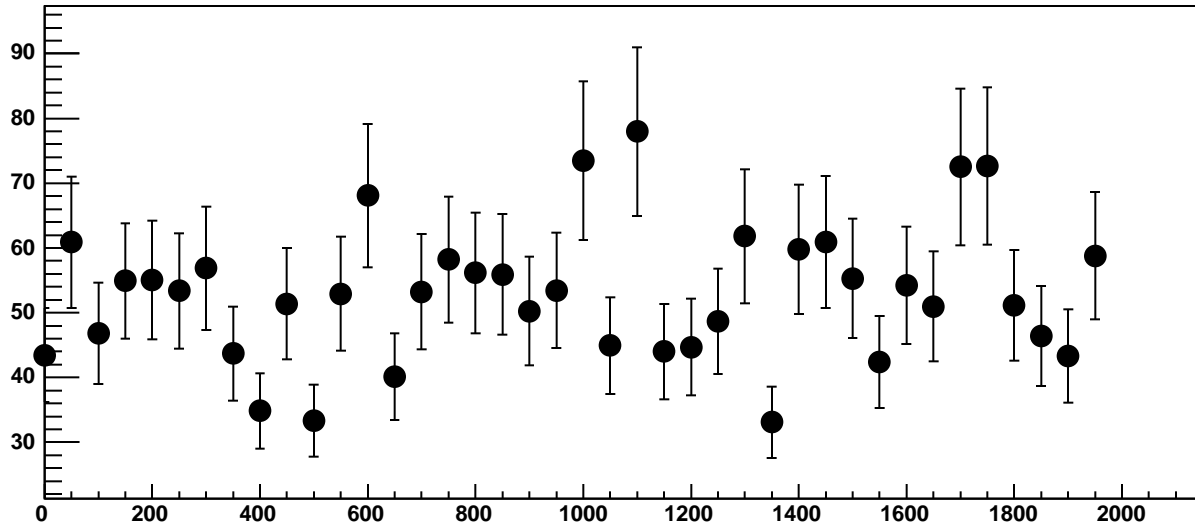


Chip 8, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC

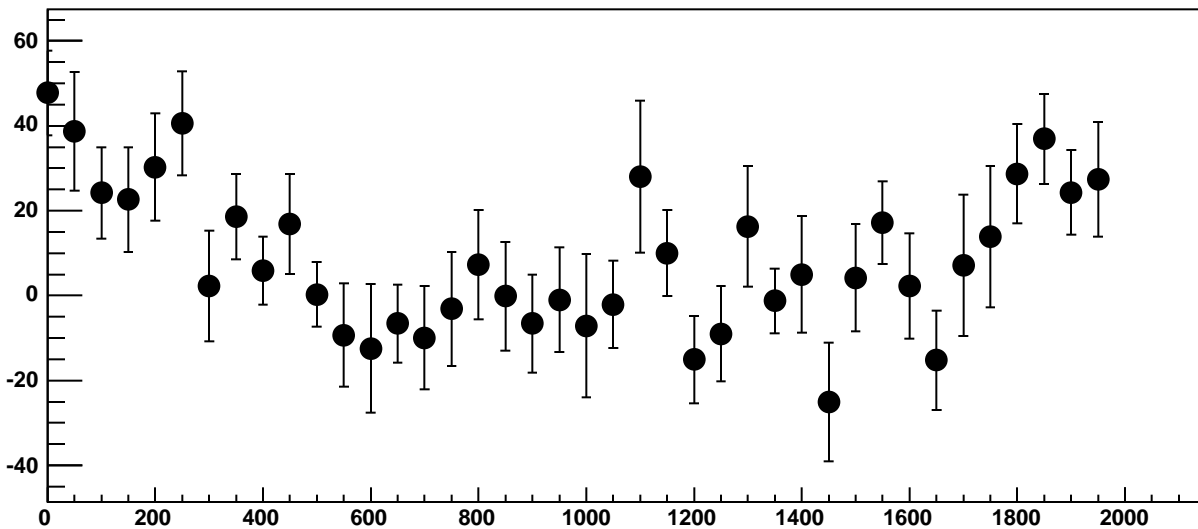


$\chi^2 / \text{ndf}$  8.589 / 11  
p0  $-816.9 \pm 19.63$   
p1  $0.06775 \pm 0.01714$

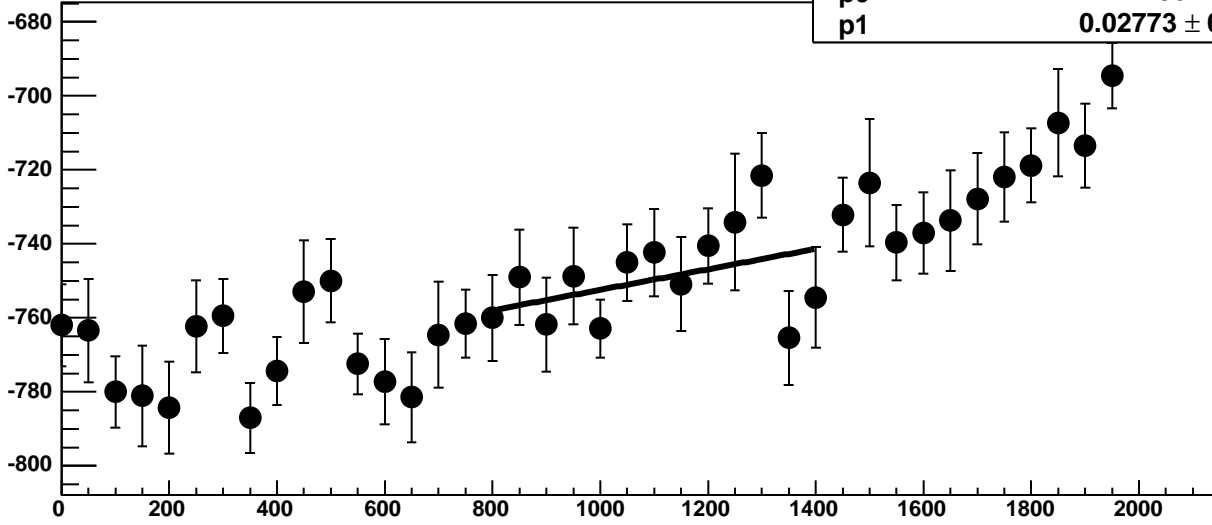
Chip 8, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC



Chip 8, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 8, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

12.19 / 11

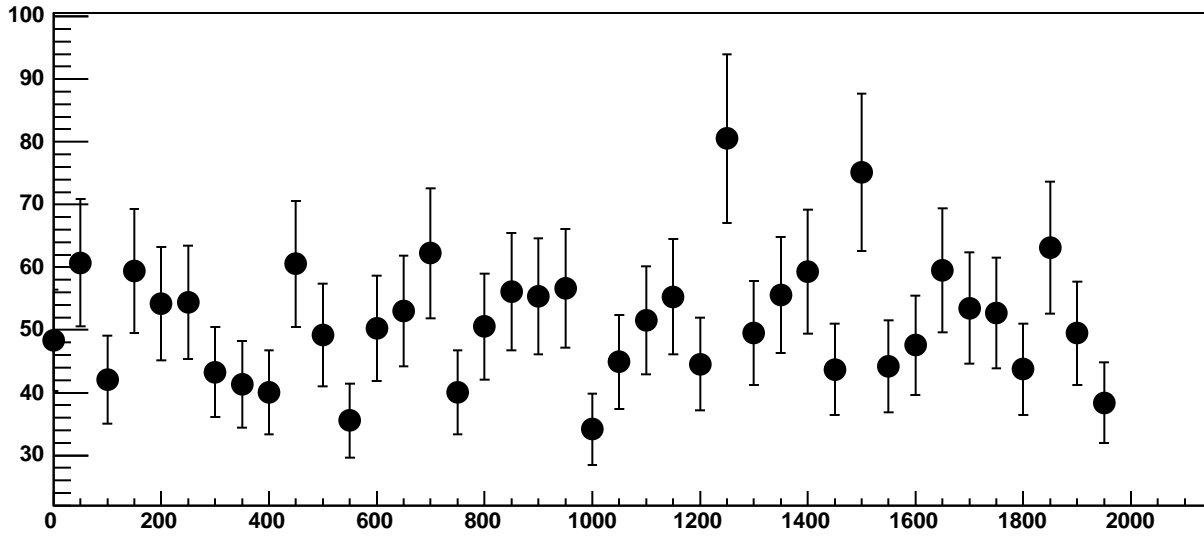
p0

$-780.1 \pm 20.22$

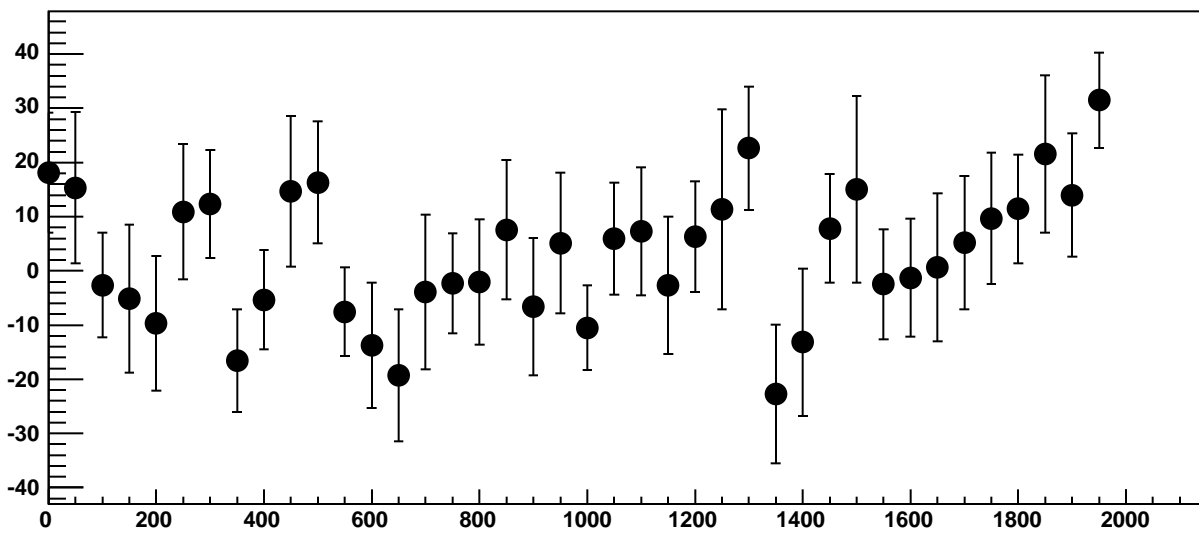
p1

$0.02773 \pm 0.01842$

Chip 8, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC

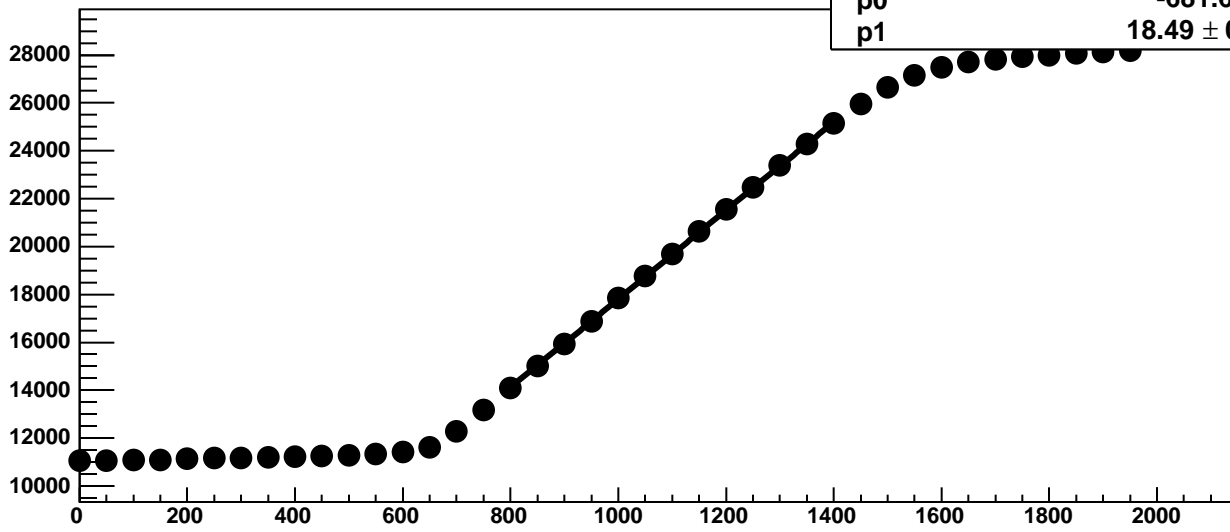


Chip 8, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC



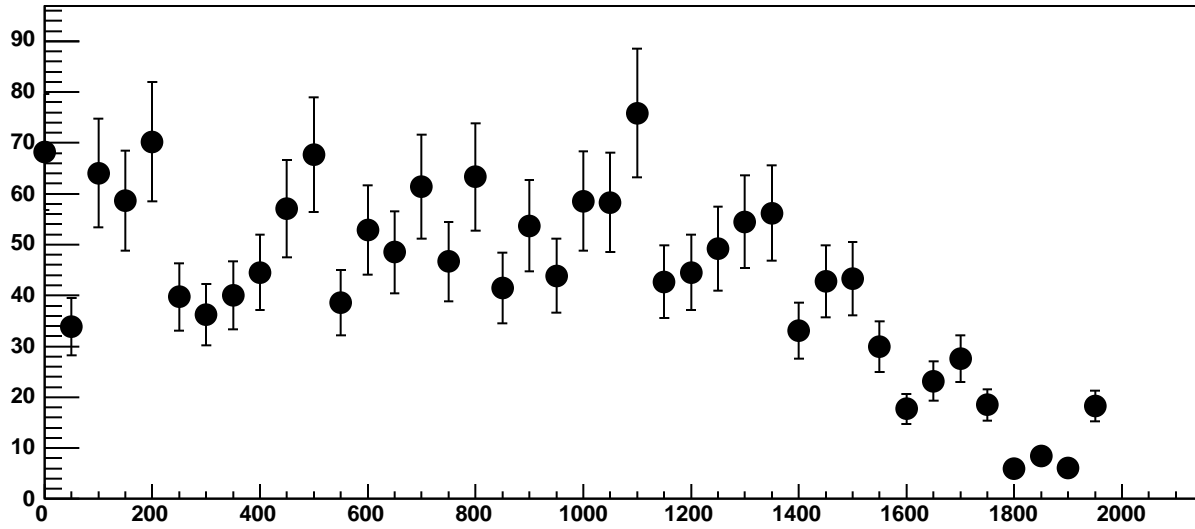


Chip 8, Channel 13, Enable 4!, Hold=30, ADC Mean vs DAC

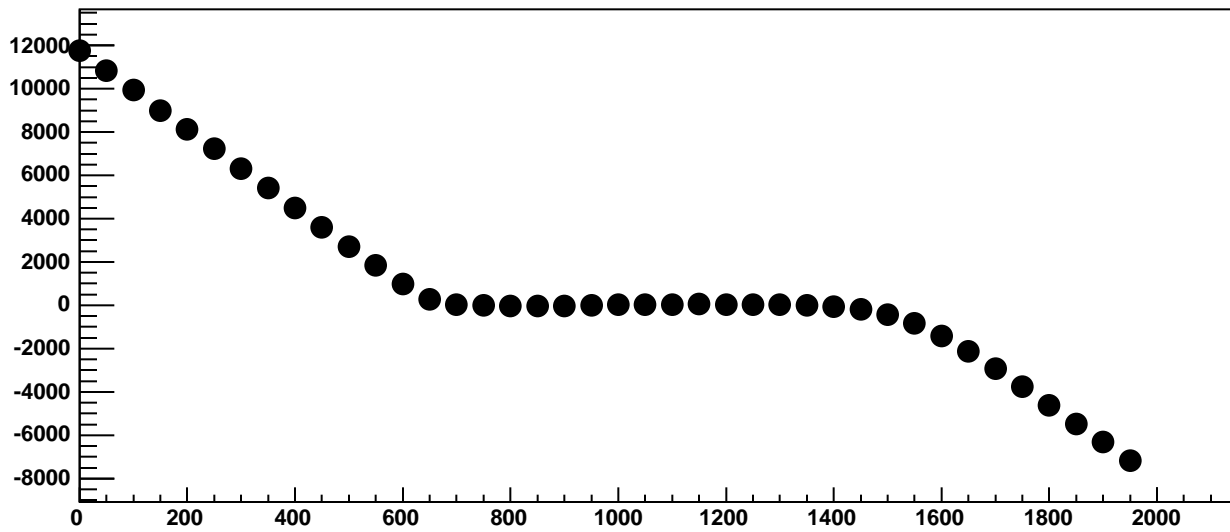


$\chi^2 / \text{ndf}$  141.3 / 11  
p0  $-681.6 \pm 17.9$   
p1  $18.49 \pm 0.01564$

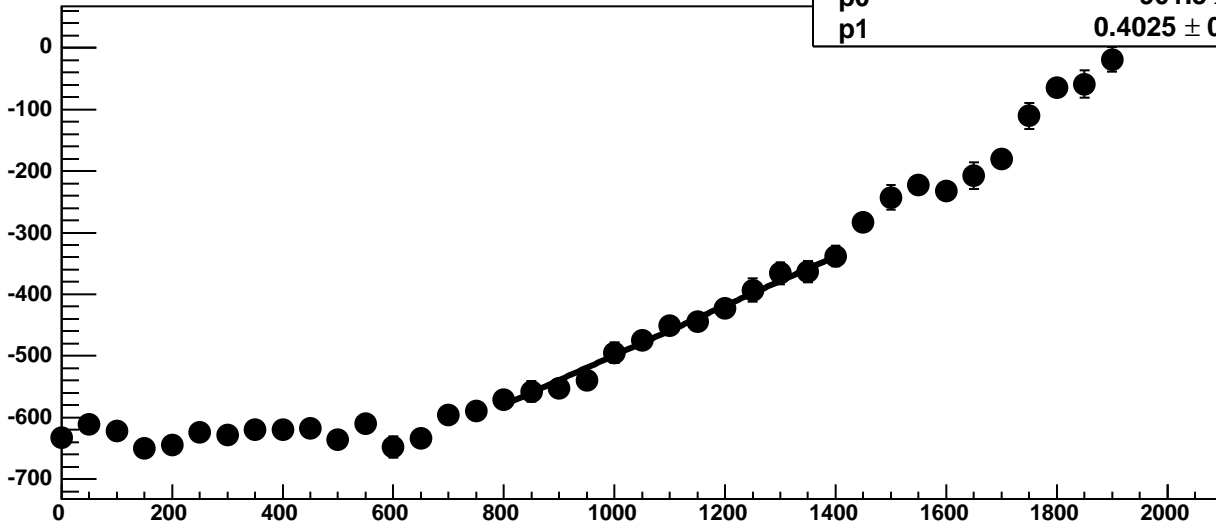
Chip 8, Channel 13, Enable 4!, Hold=30, ADC Noise vs DAC



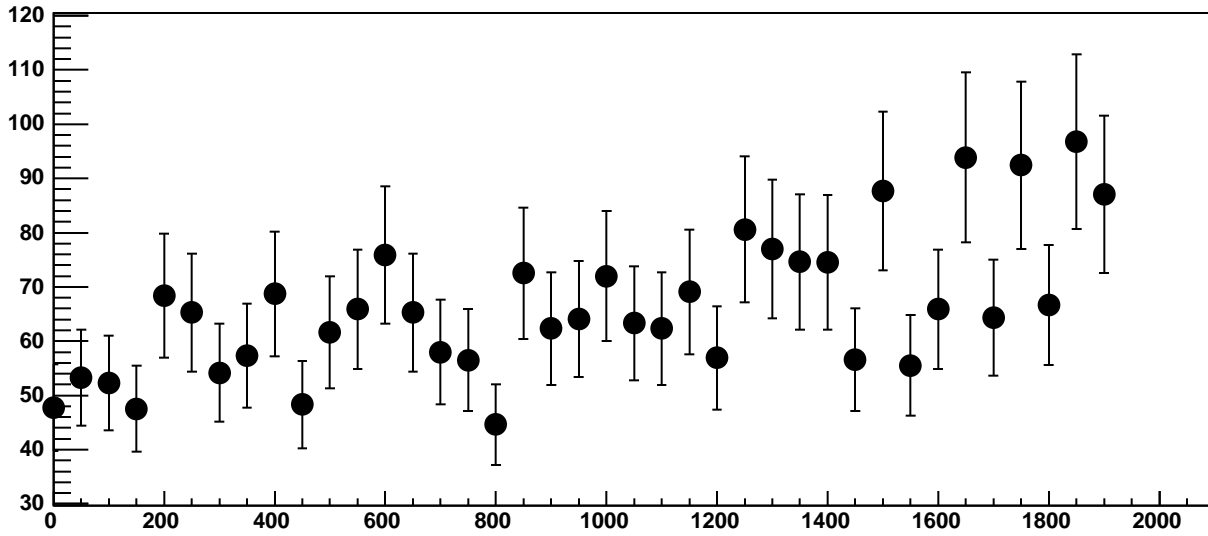
Chip 8, Channel 13, Enable 4!, Hold=30, ADC Residuals vs DAC



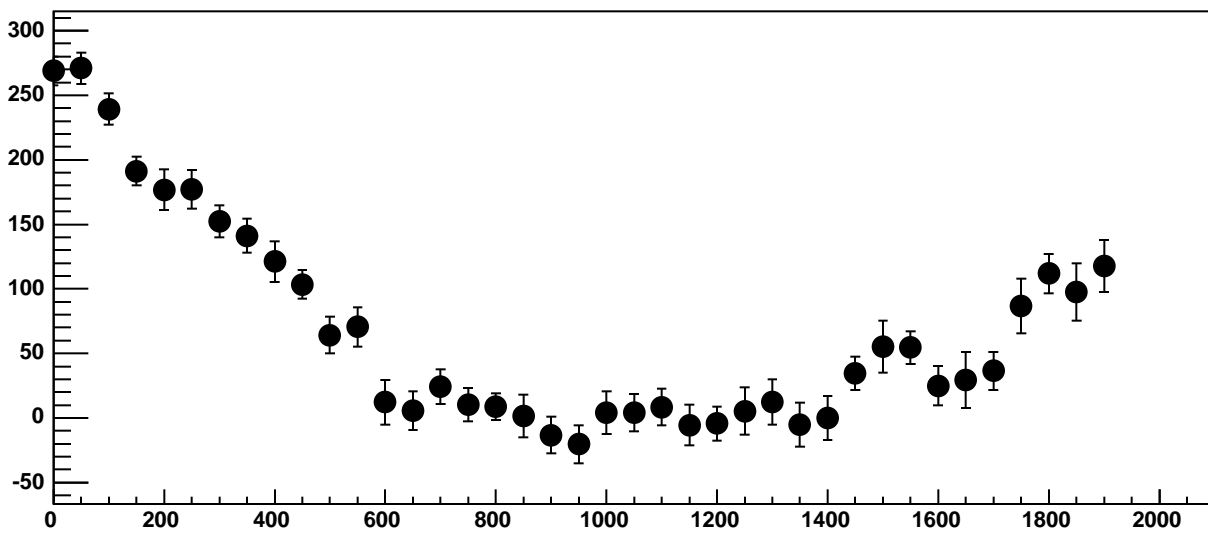
Chip 8, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC



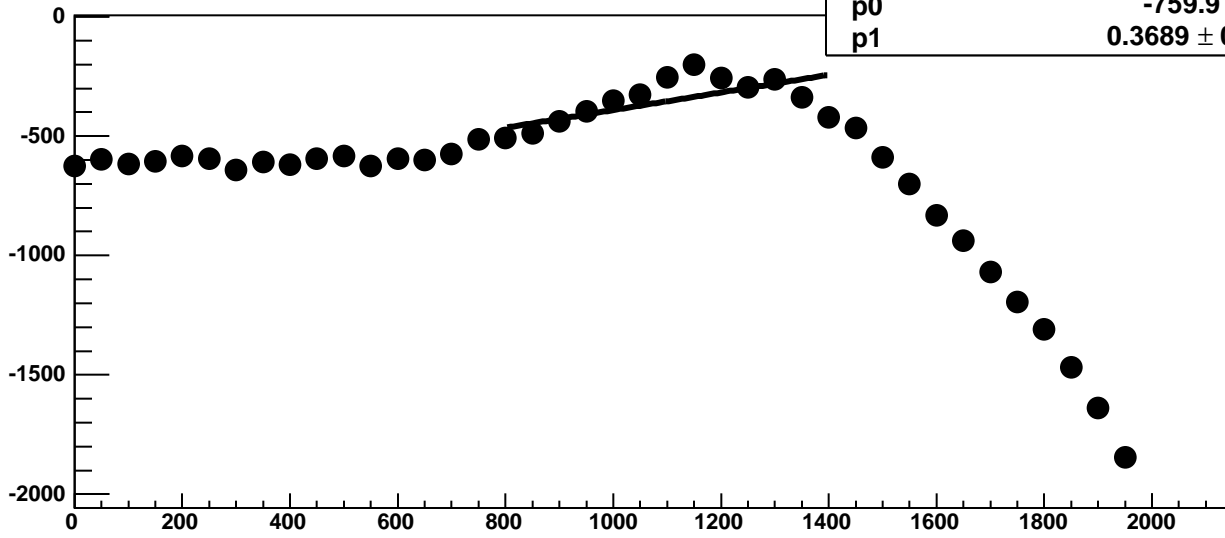
Chip 8, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC



Chip 8, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC

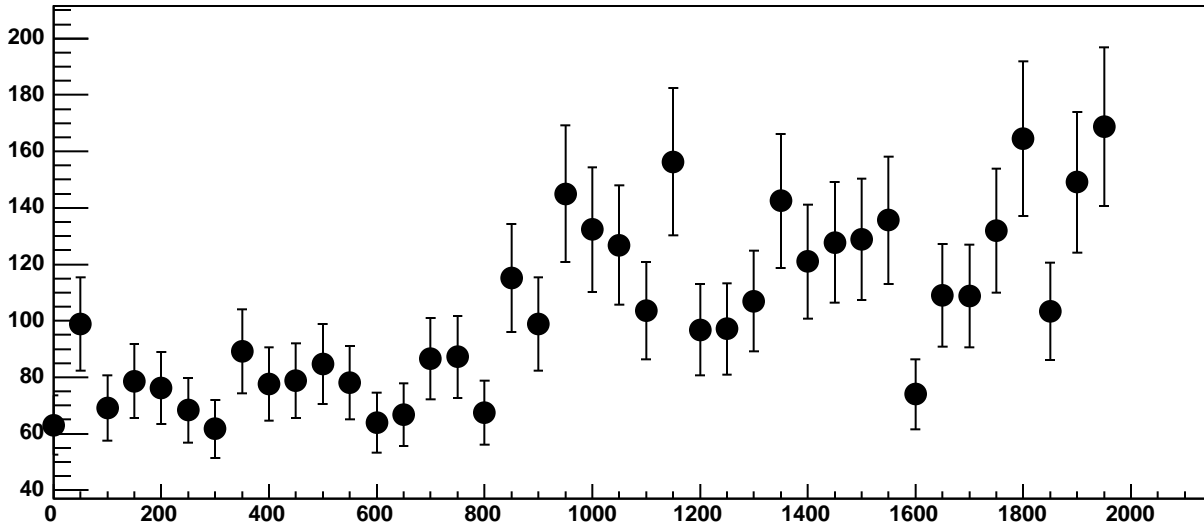


Chip 8, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC

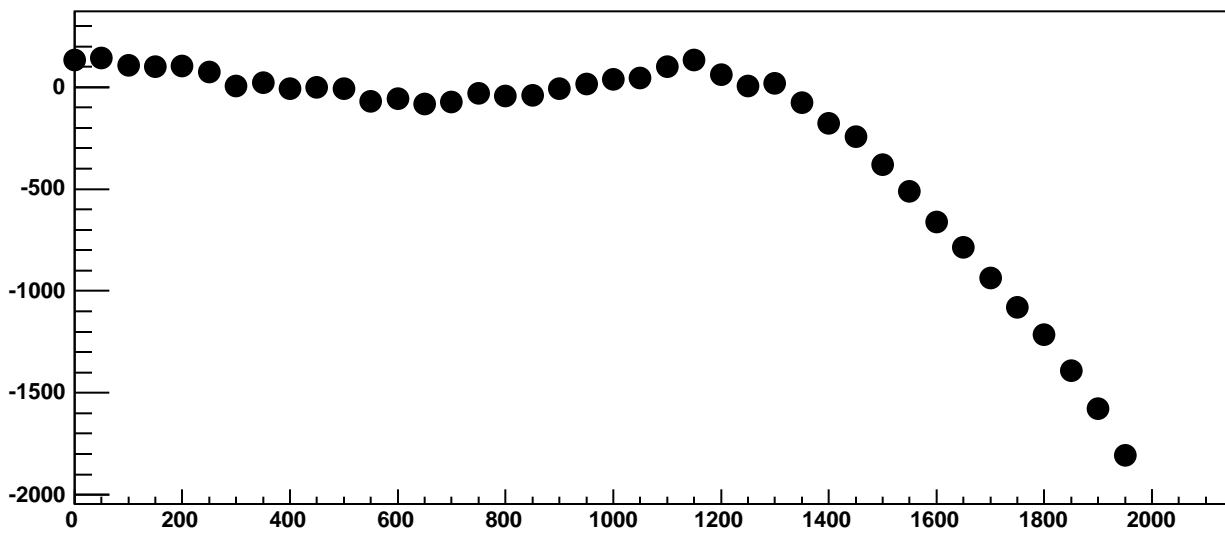


$\chi^2 / \text{ndf}$	99.98 / 11
p0	$-759.9 \pm 36.96$
p1	$0.3689 \pm 0.03419$

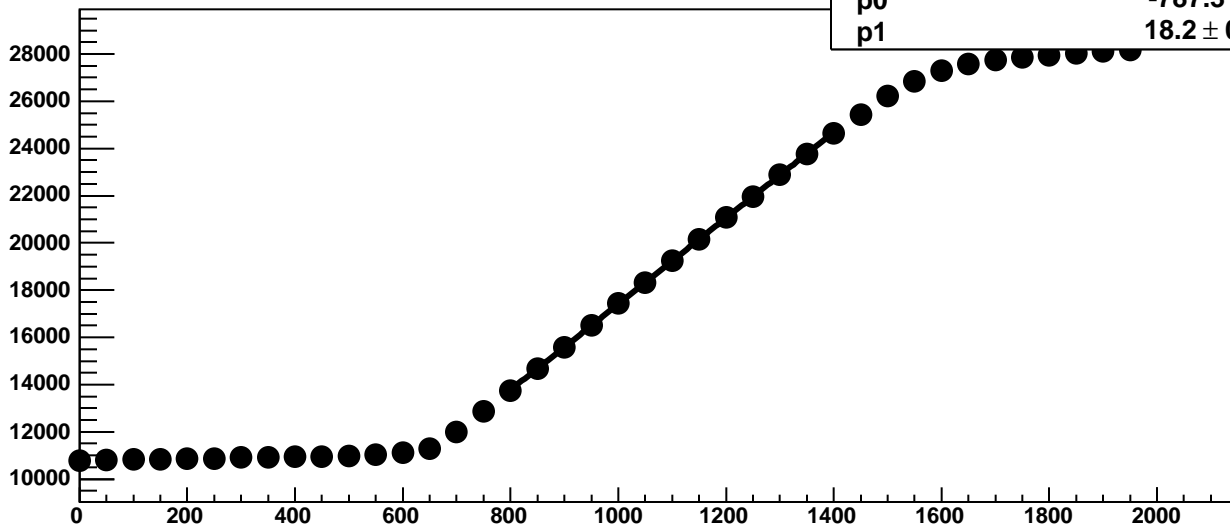
Chip 8, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC



Chip 8, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC

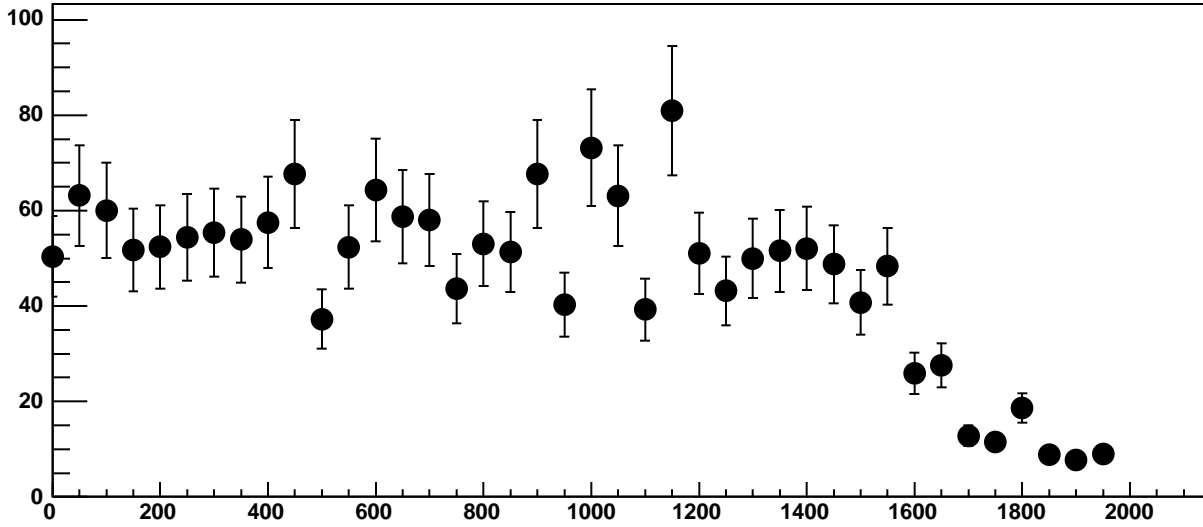


Chip 8, Channel 14, Enable 1!, Hold=30, ADC Mean vs DAC

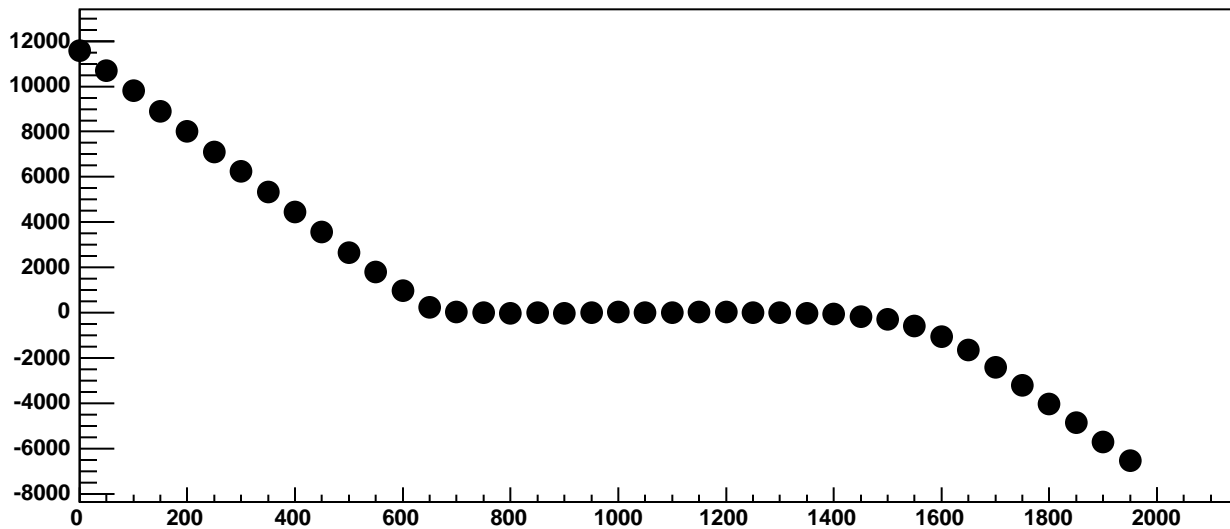


$\chi^2 / \text{ndf}$	33.73 / 11
p0	$-787.3 \pm 19.72$
p1	$18.2 \pm 0.01756$

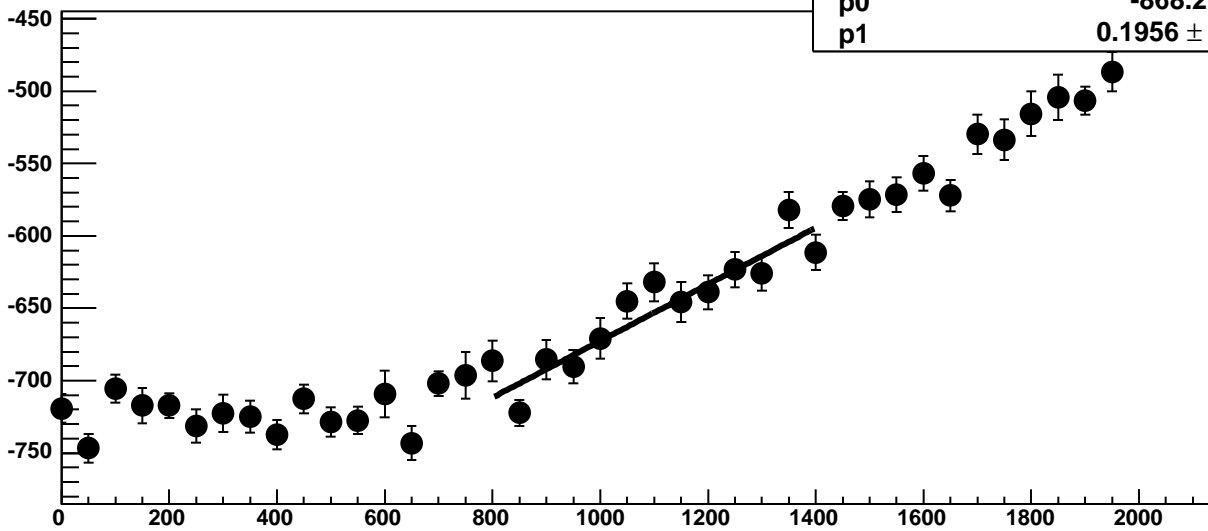
Chip 8, Channel 14, Enable 1!, Hold=30, ADC Noise vs DAC



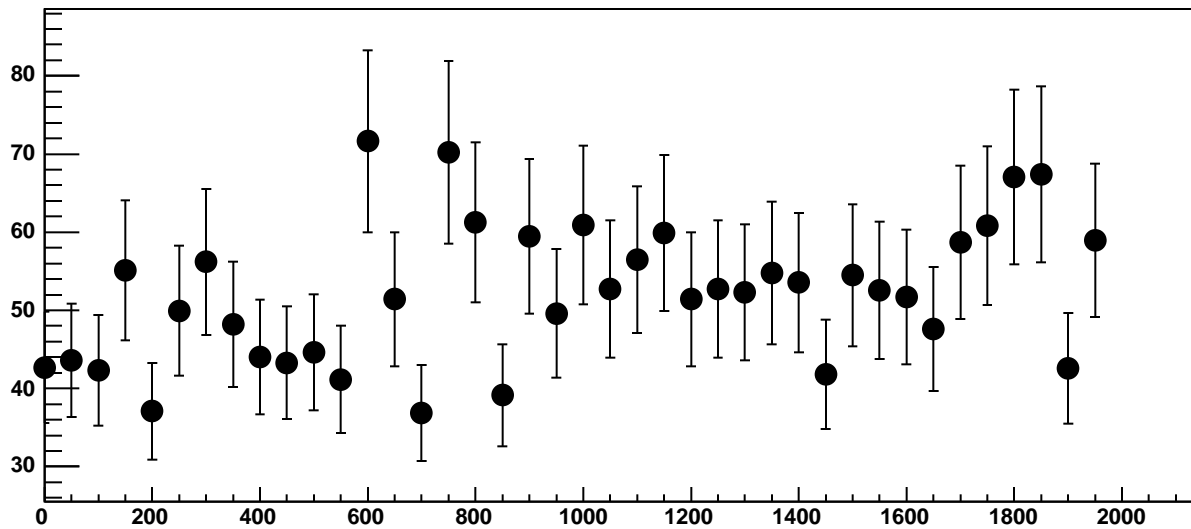
Chip 8, Channel 14, Enable 1!, Hold=30, ADC Residuals vs DAC



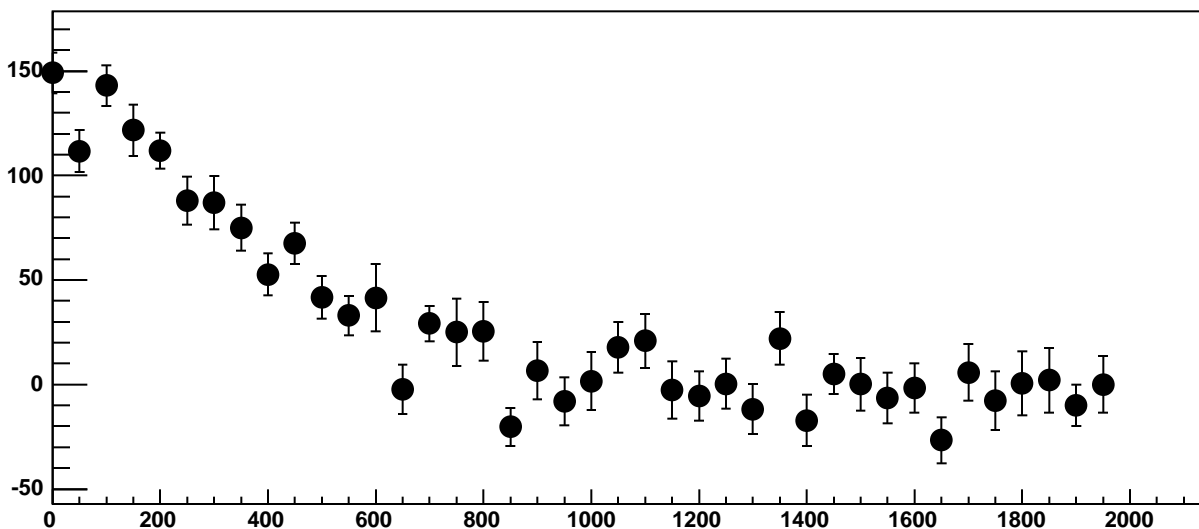
Chip 8, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC



Chip 8, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC

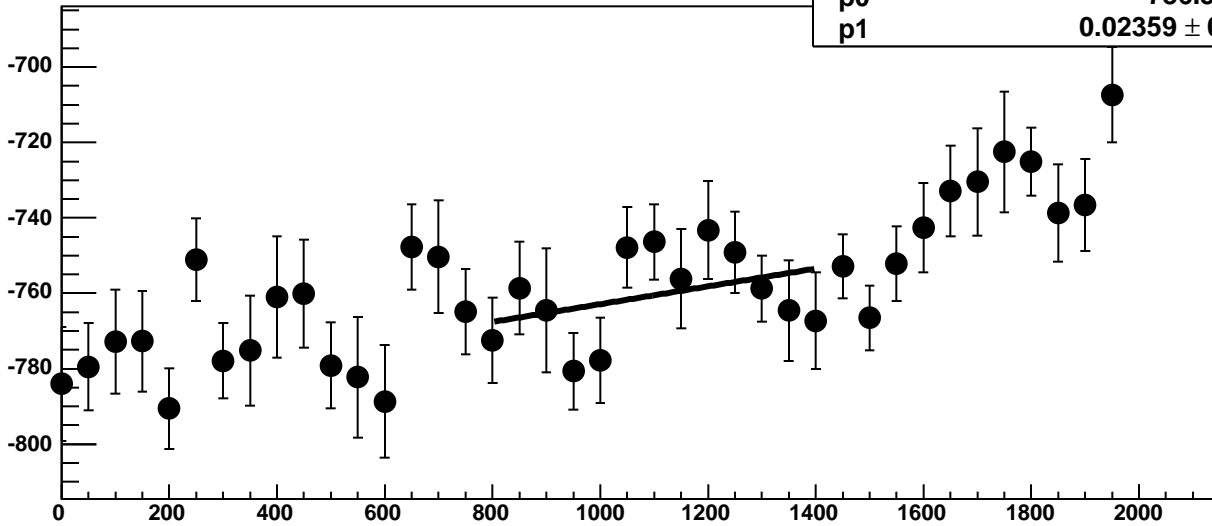


Chip 8, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC

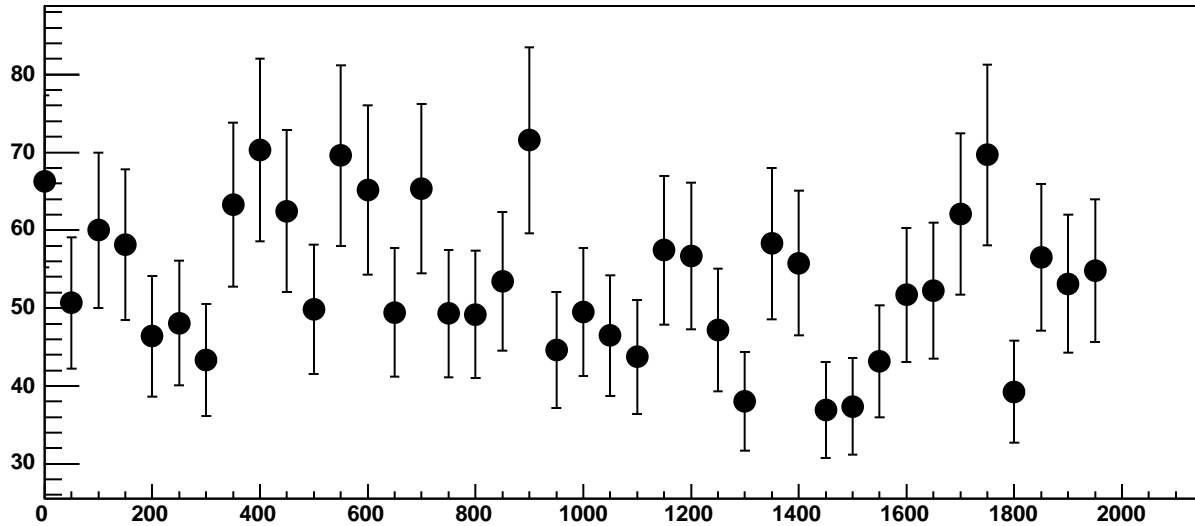


Chip 8, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC

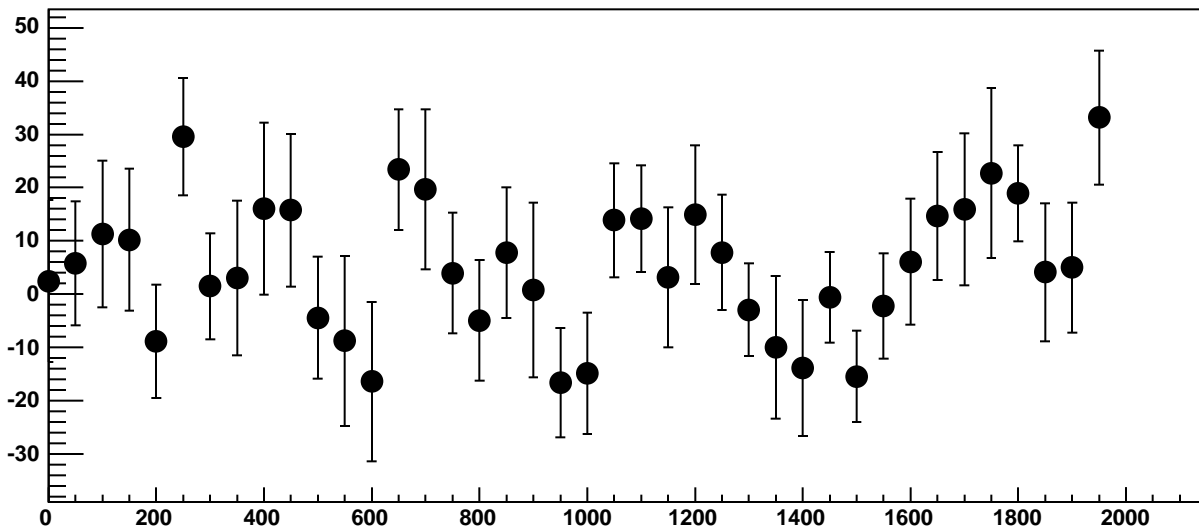
$\chi^2 / \text{ndf}$  12.36 / 11  
p0  $-786.5 \pm 19.6$   
p1  $0.02359 \pm 0.01748$



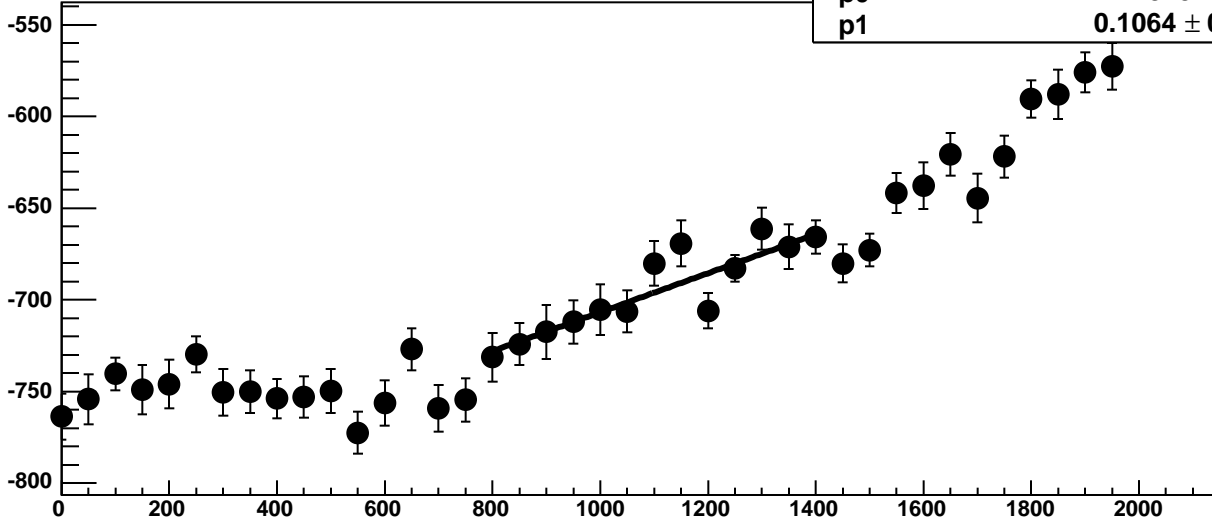
Chip 8, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC



Chip 8, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC

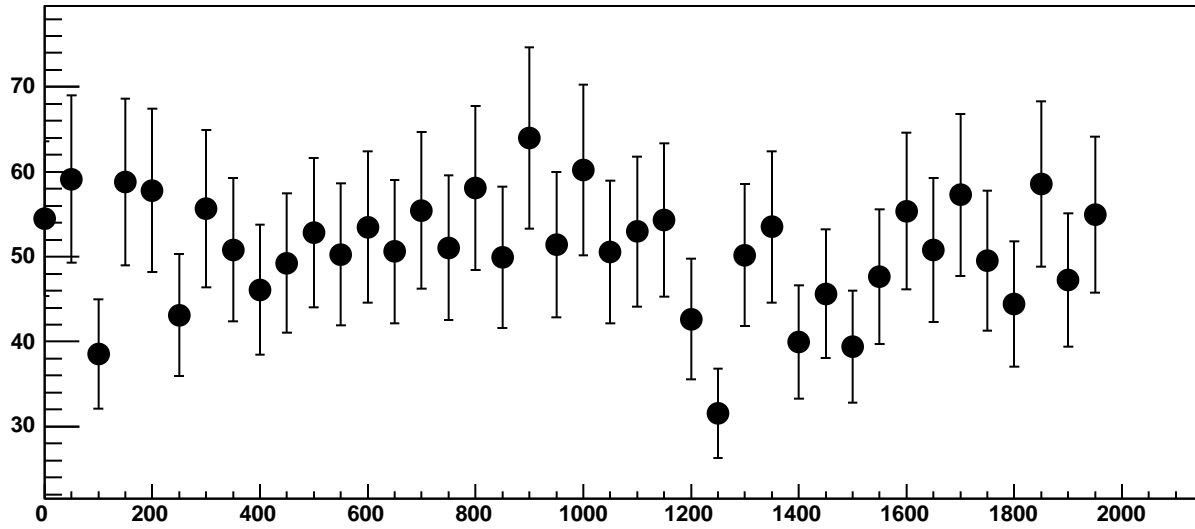


Chip 8, Channel 14, Enable 4, Hold=30, ADC Mean vs DAC

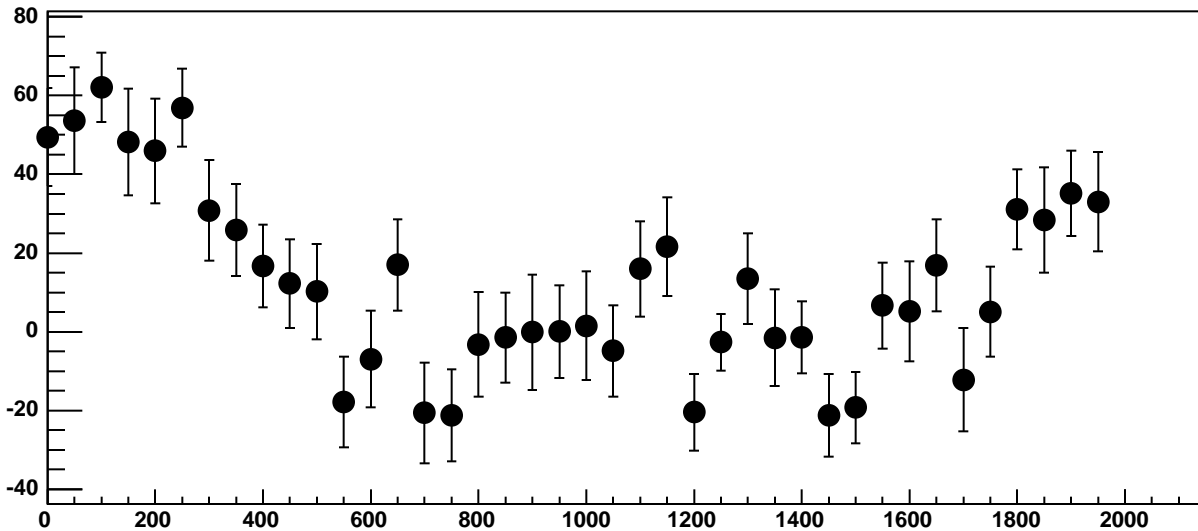


$\chi^2 / \text{ndf}$  10.92 / 11  
p0  $-813.1 \pm 19.41$   
p1  $0.1064 \pm 0.01678$

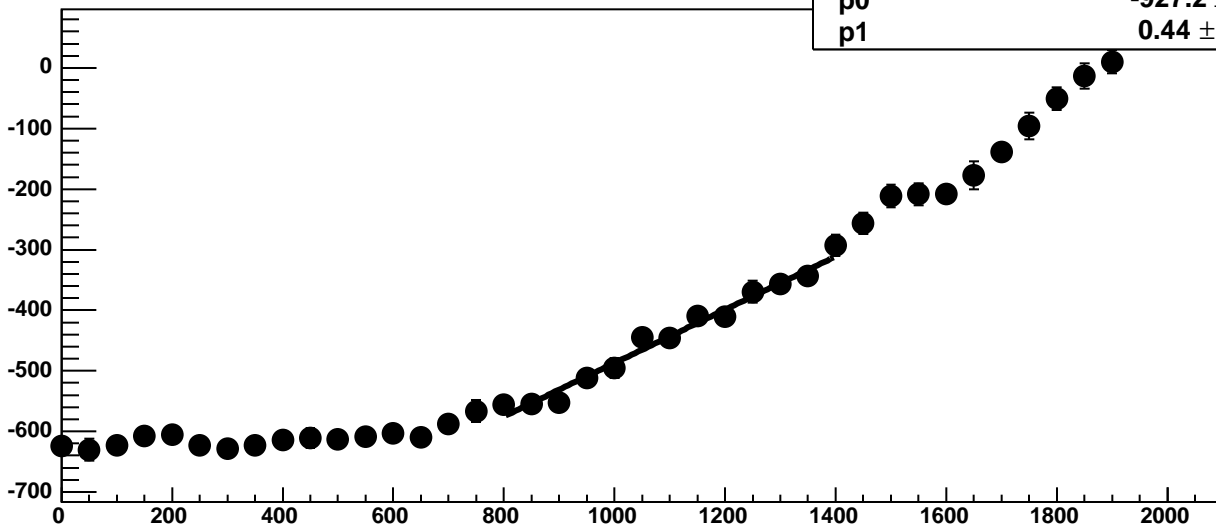
Chip 8, Channel 14, Enable 4, Hold=30, ADC Noise vs DAC



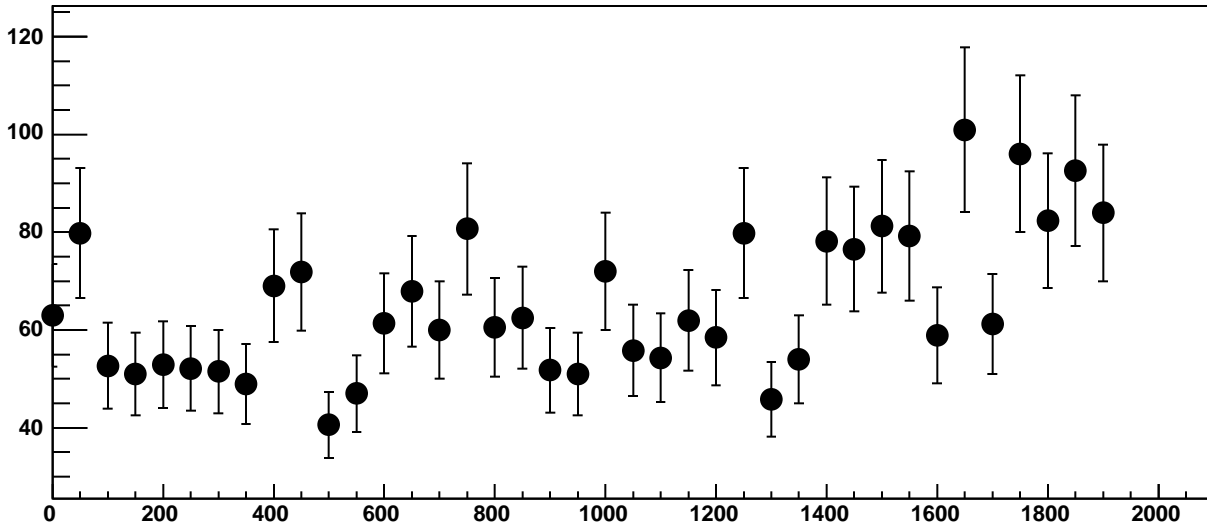
Chip 8, Channel 14, Enable 4, Hold=30, ADC Residuals vs DAC



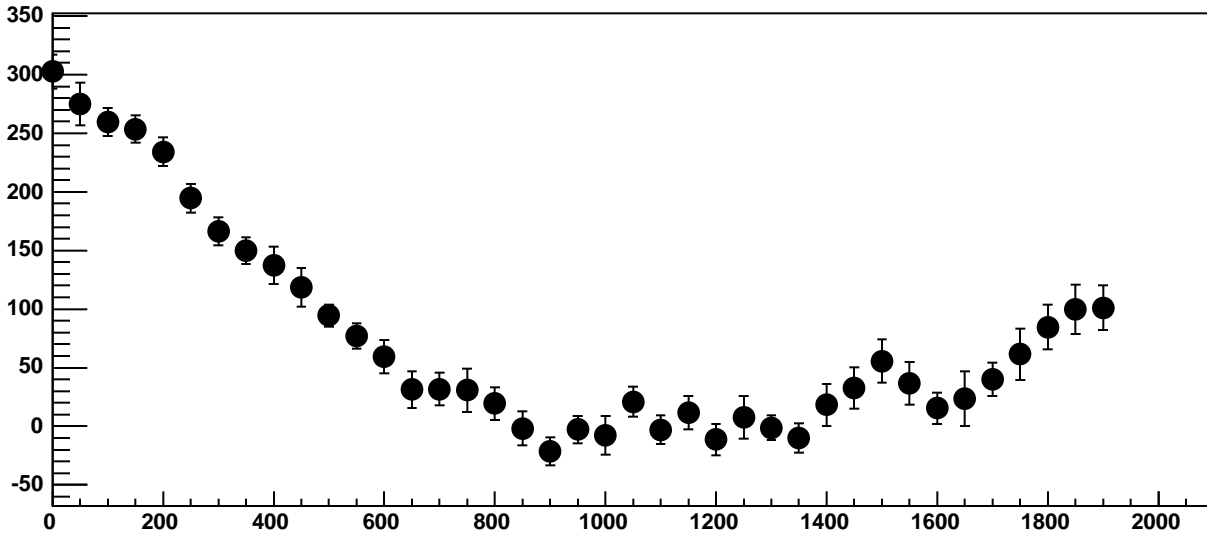
Chip 8, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC



Chip 8, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC

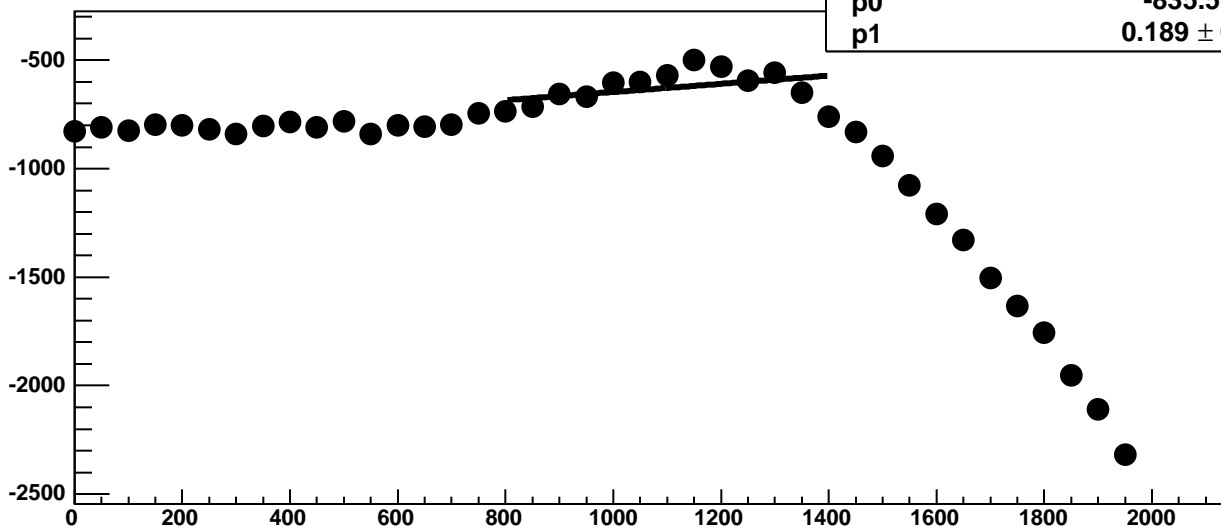


Chip 8, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC



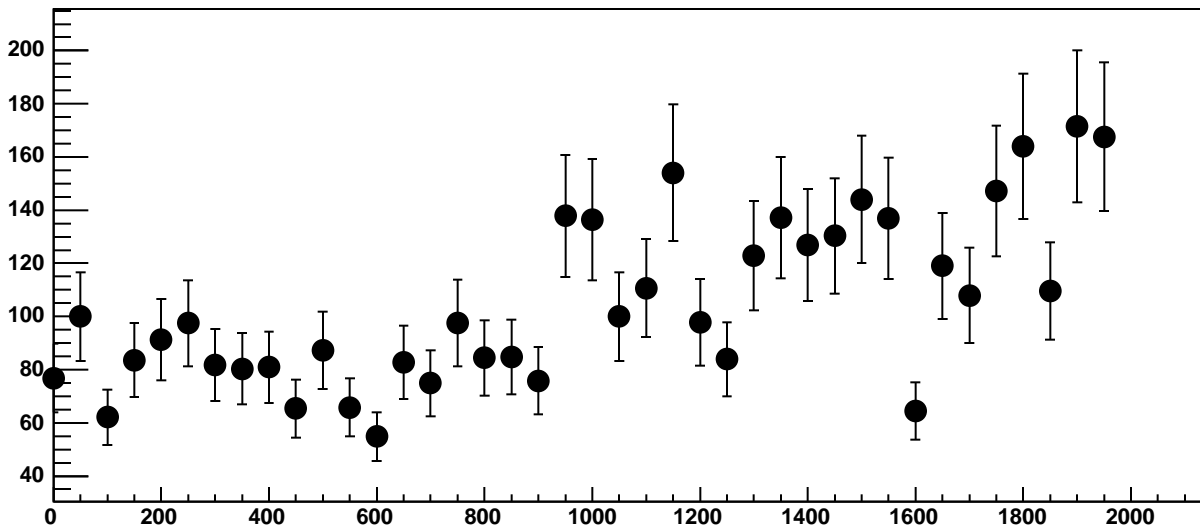


Chip 8, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC

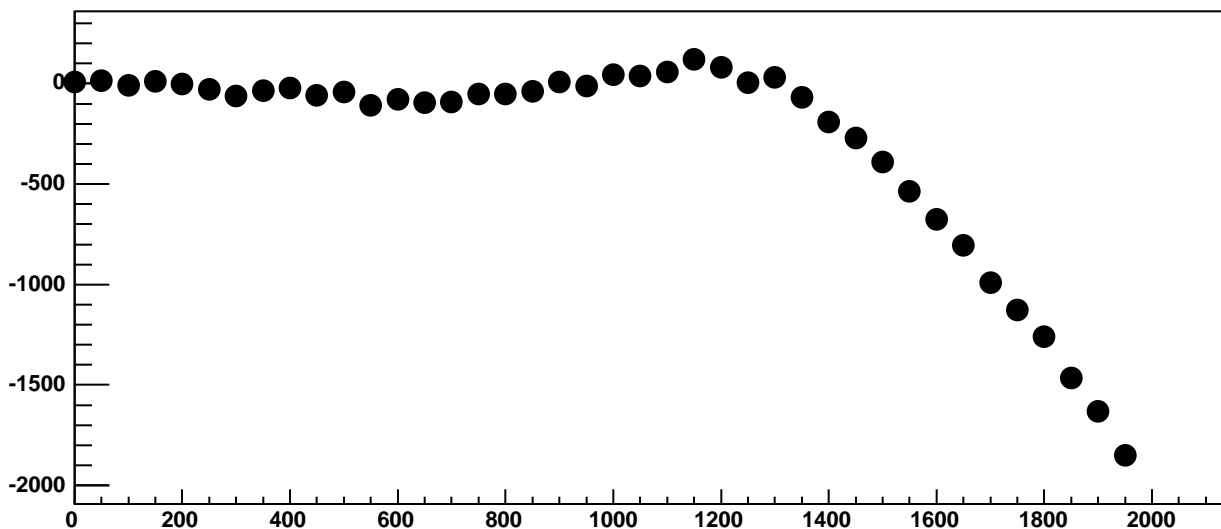


$\chi^2 / \text{ndf}$	94.03 / 11
p0	$-835.5 \pm 37.17$
p1	$0.189 \pm 0.03457$

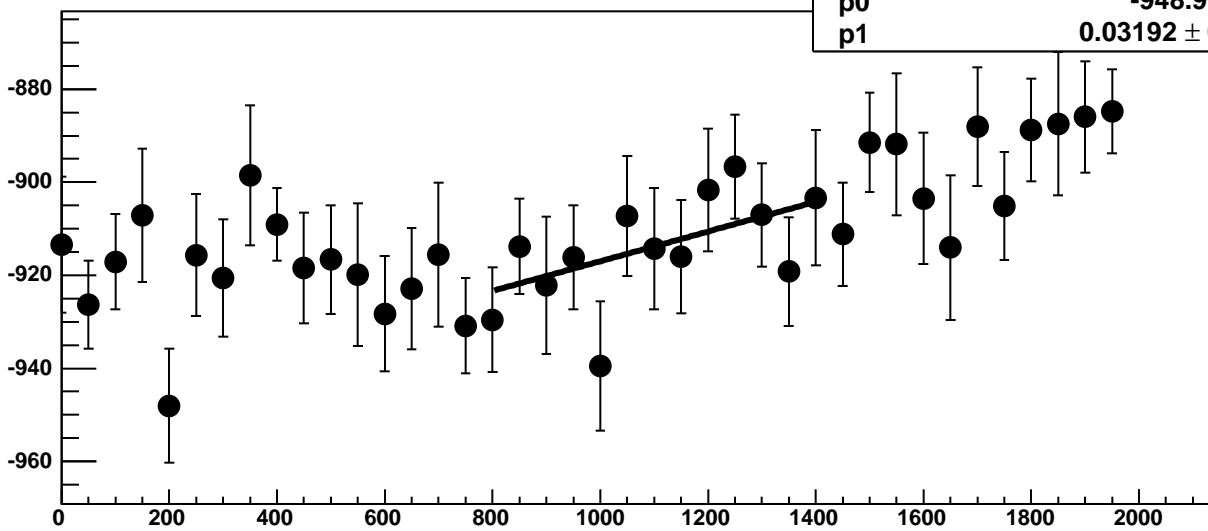
Chip 8, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC



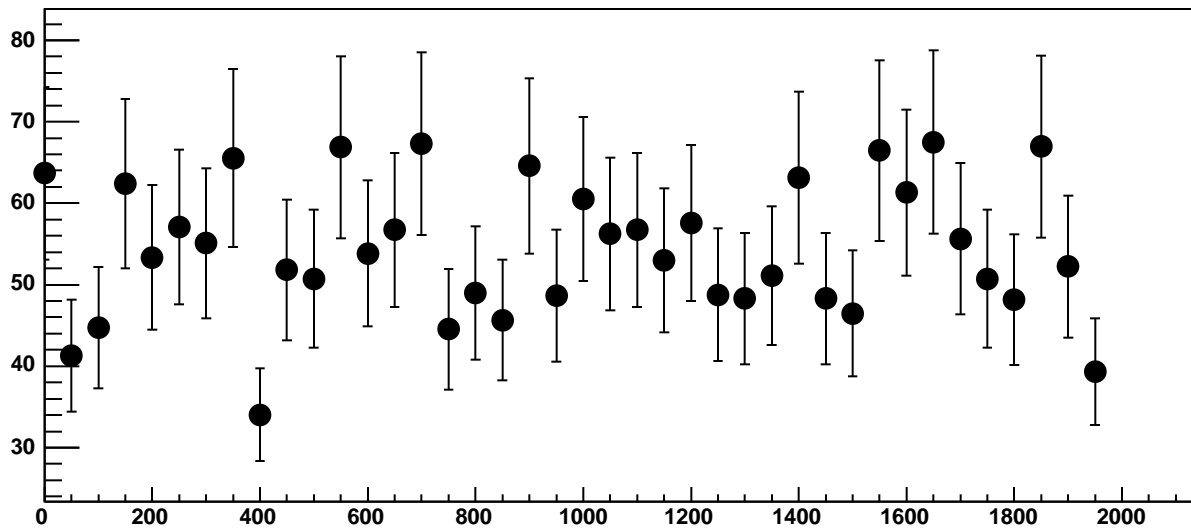
Chip 8, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC



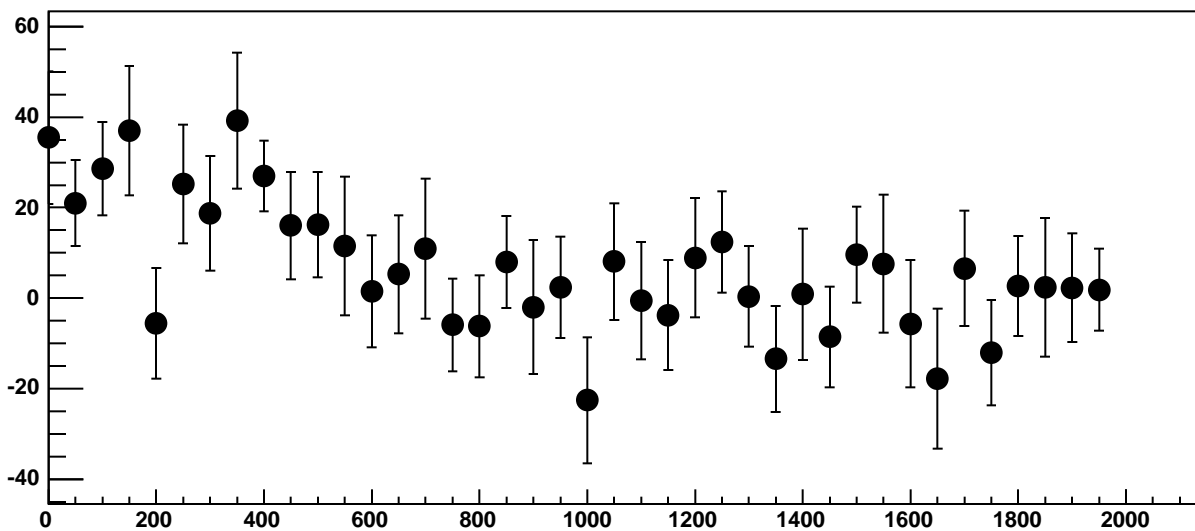
Chip 8, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC



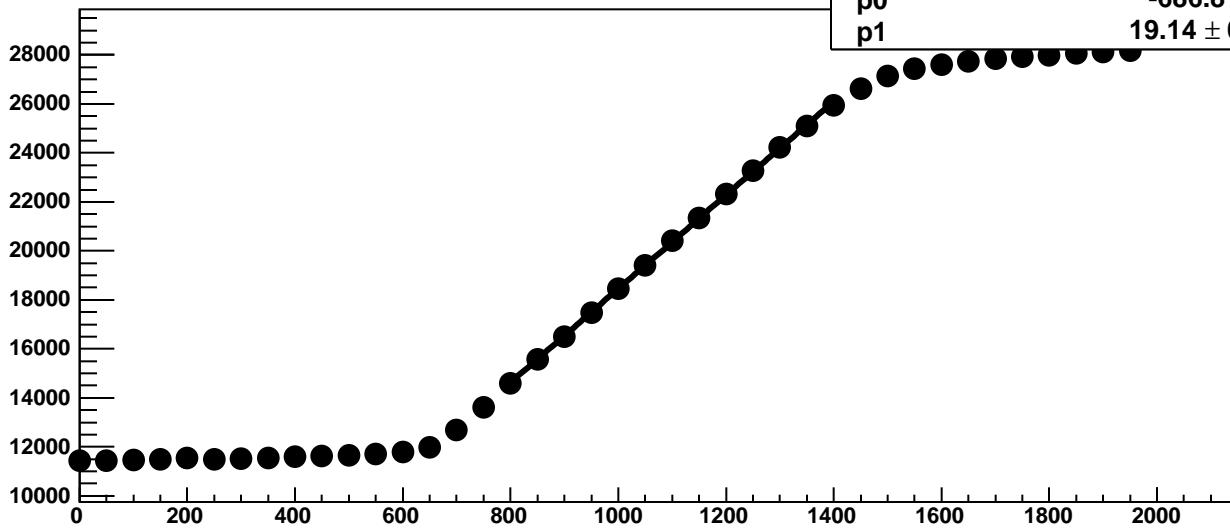
Chip 8, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC



Chip 8, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC

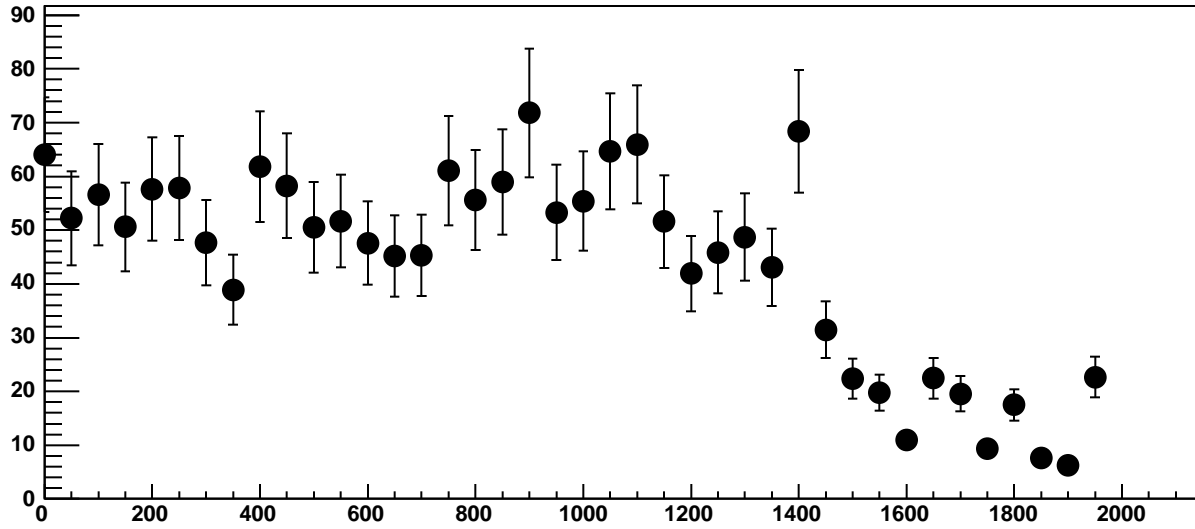


Chip 8, Channel 15, Enable 2!, Hold=30, ADC Mean vs DAC

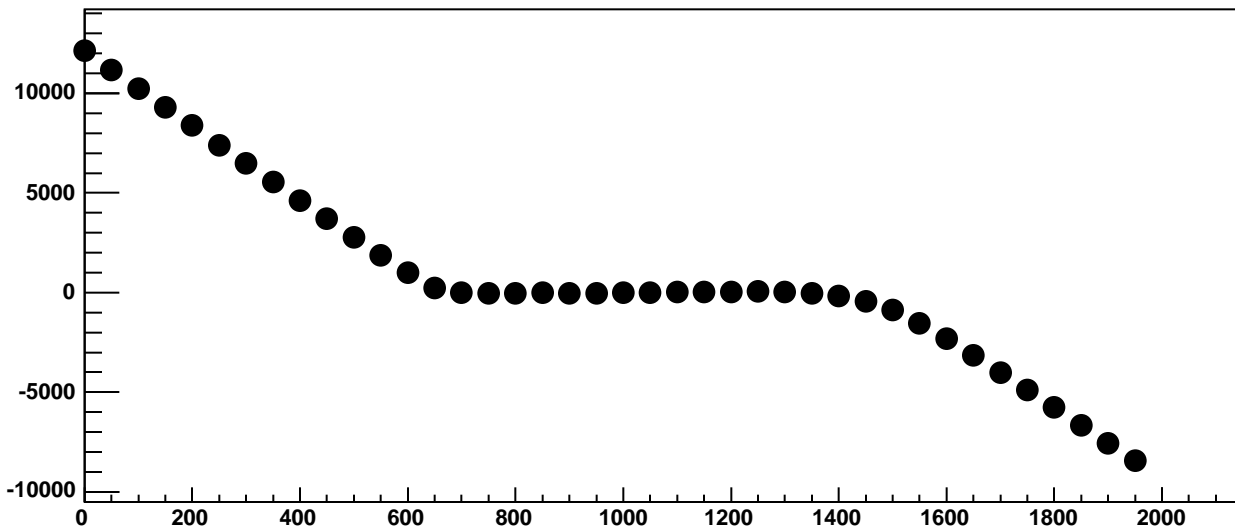


$\chi^2 / \text{ndf}$  202.6 / 11  
p0  $-686.8 \pm 21.37$   
p1  $19.14 \pm 0.01871$

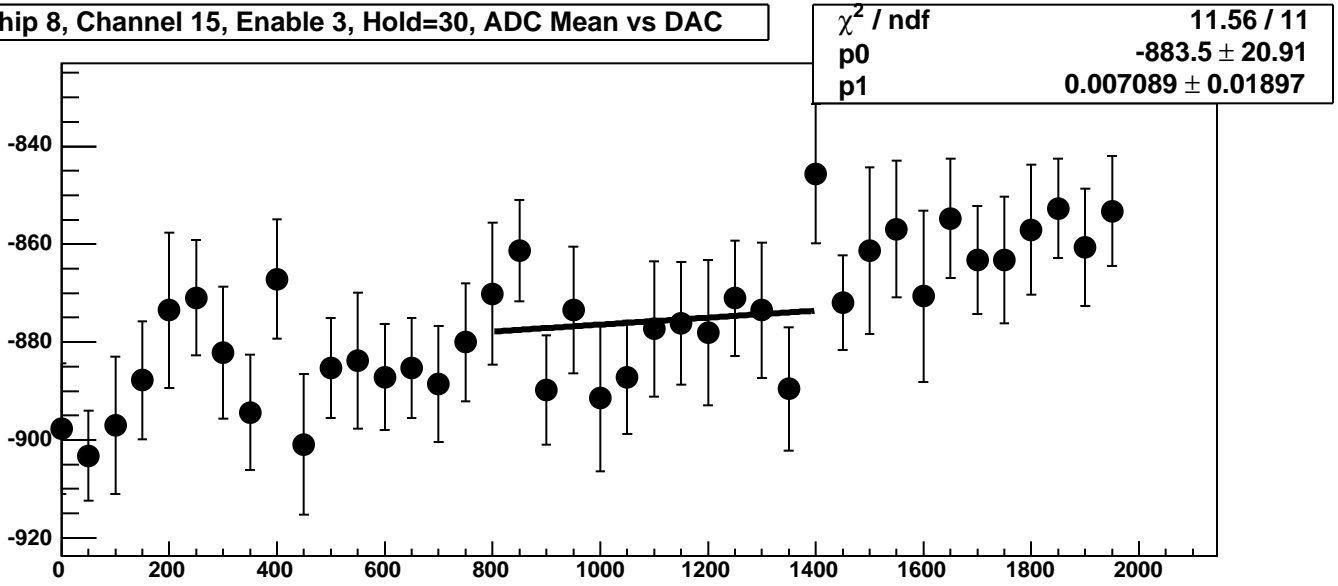
Chip 8, Channel 15, Enable 2!, Hold=30, ADC Noise vs DAC



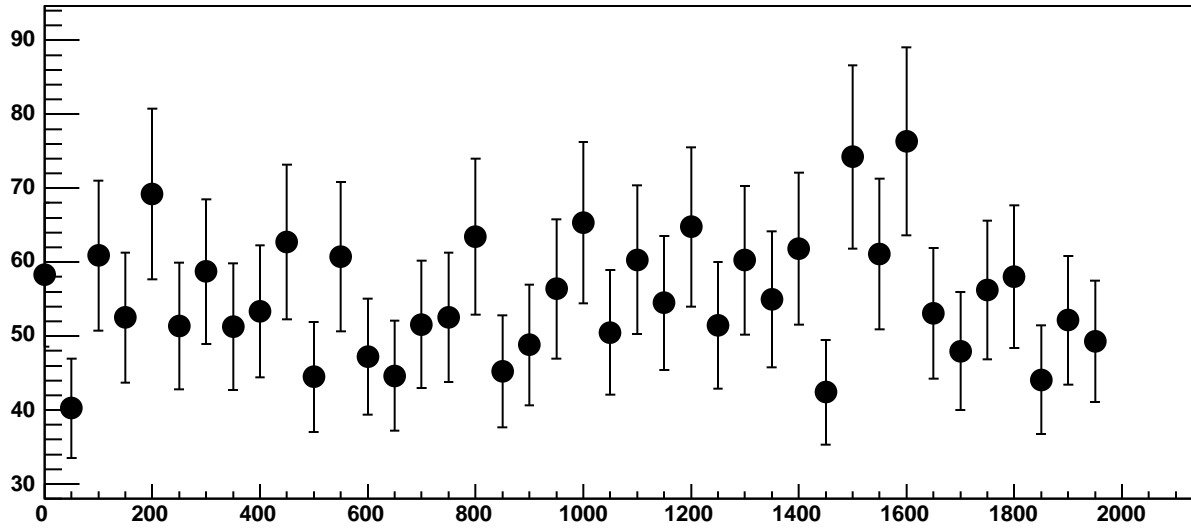
Chip 8, Channel 15, Enable 2!, Hold=30, ADC Residuals vs DAC



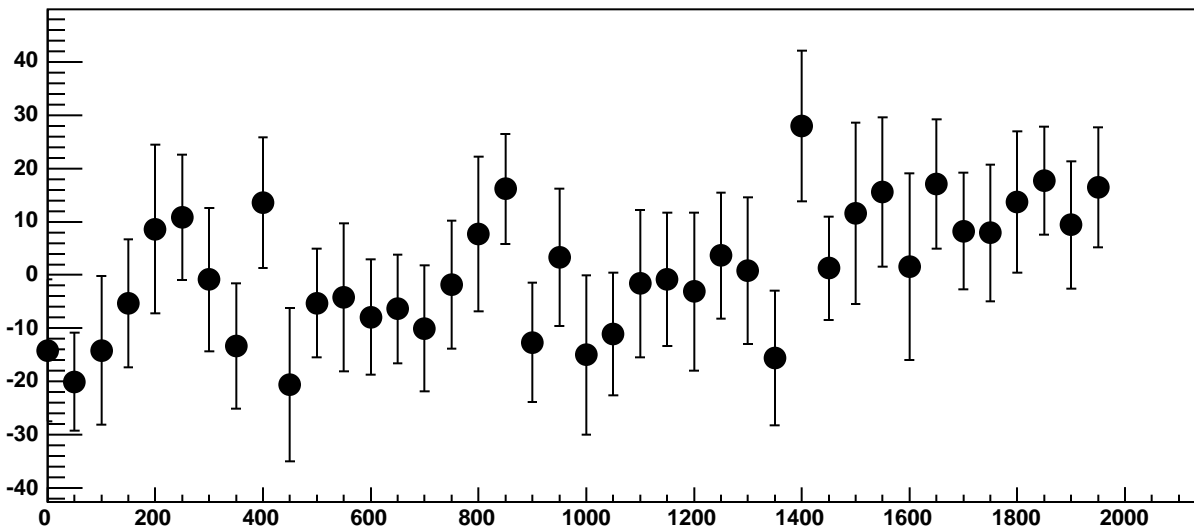
Chip 8, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC



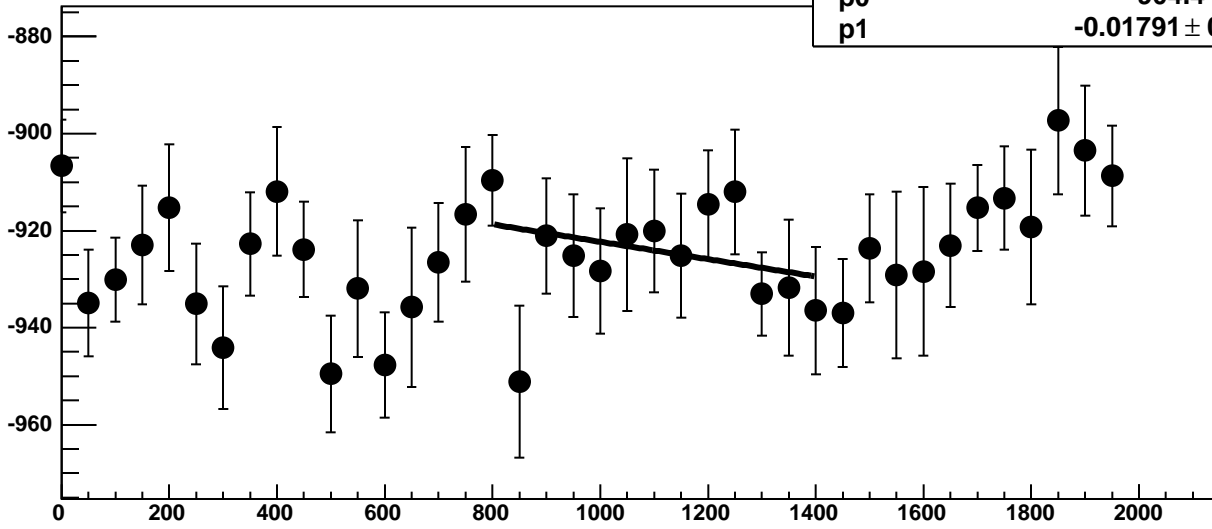
Chip 8, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC



Chip 8, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC



Chip 8, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

8.478 / 11

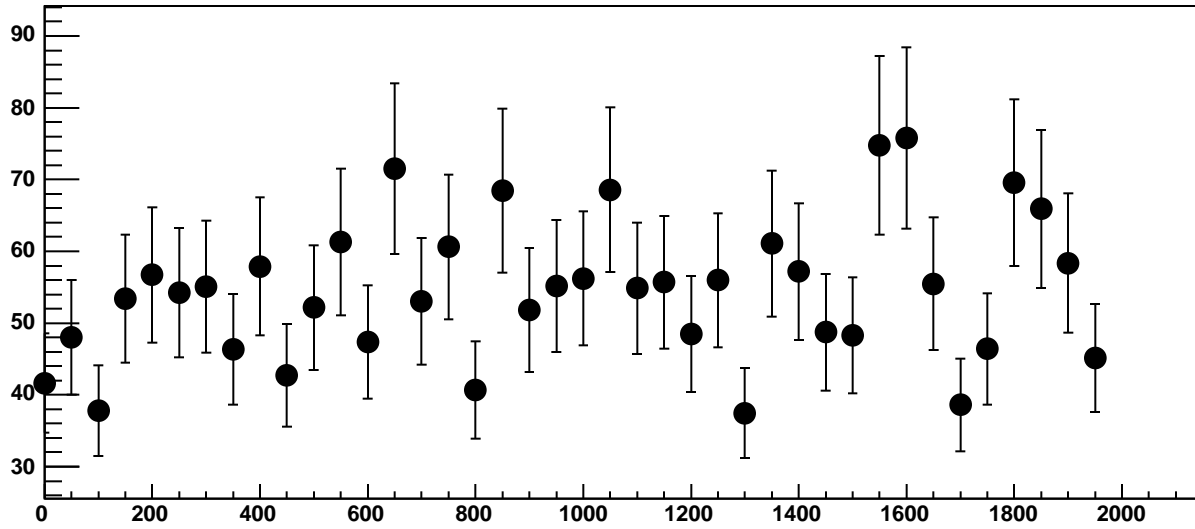
p0

$-904.4 \pm 19.19$

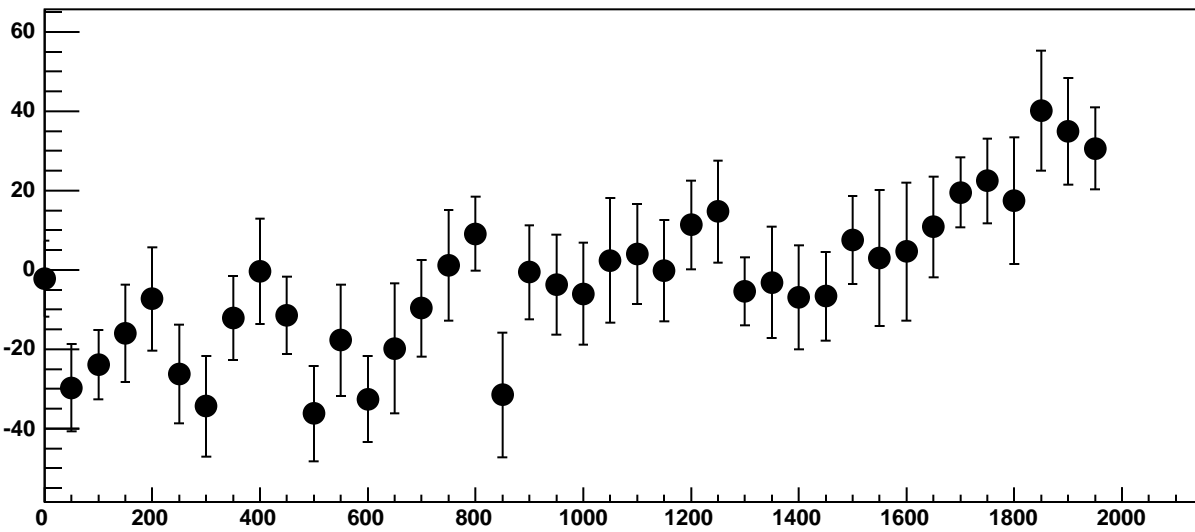
p1

$-0.01791 \pm 0.01716$

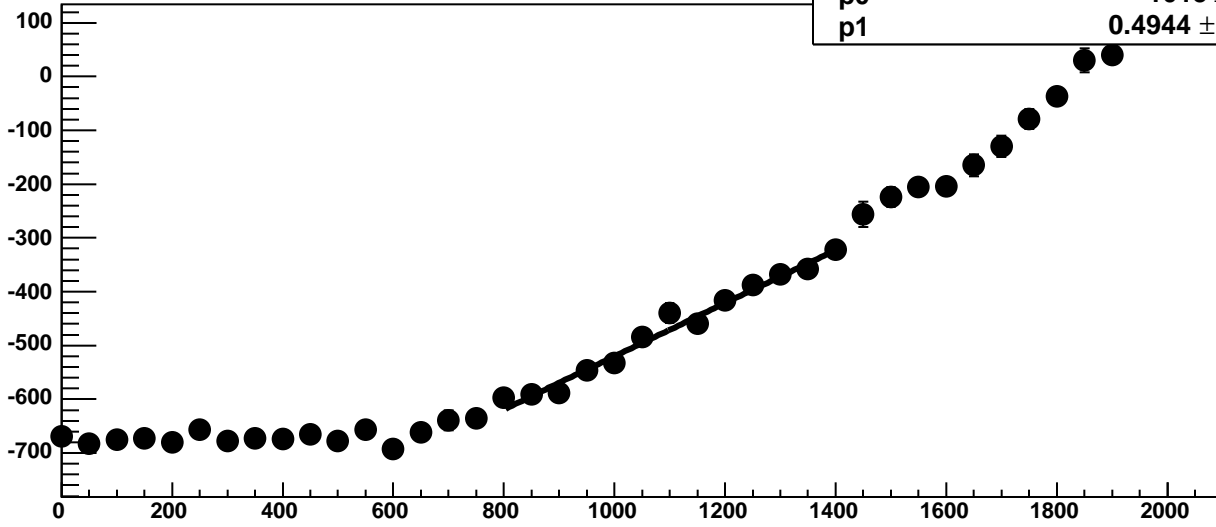
Chip 8, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC



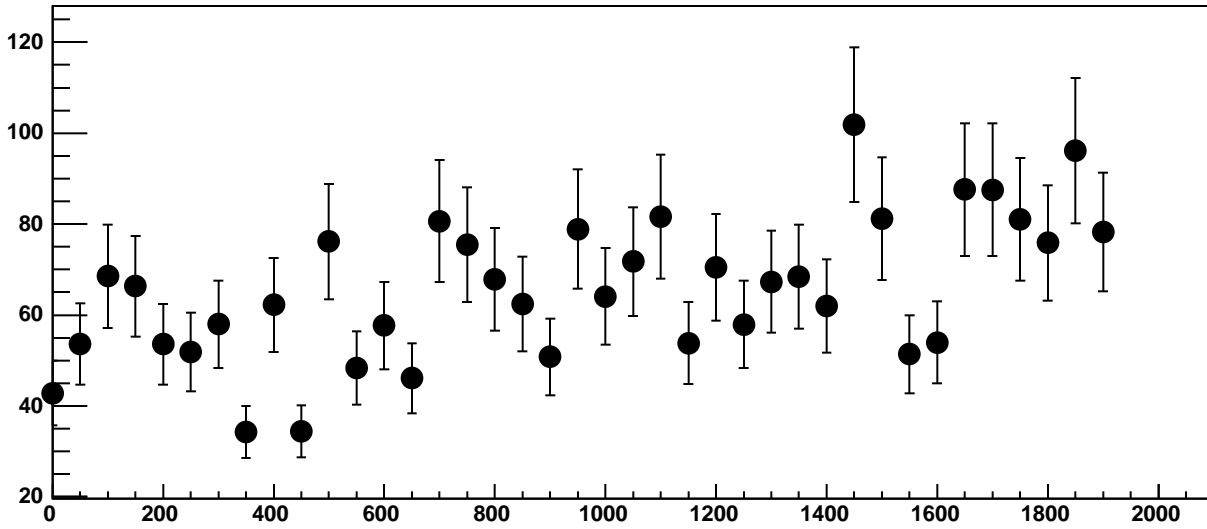
Chip 8, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC



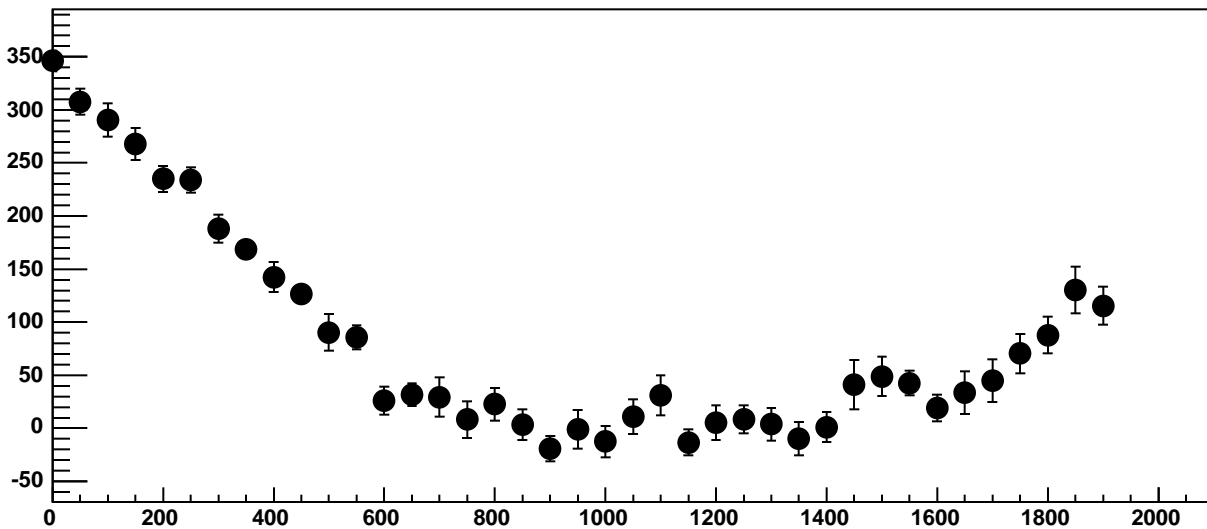
Chip 8, Channel 15, Enable 5, Hold=30, ADC Mean vs DAC



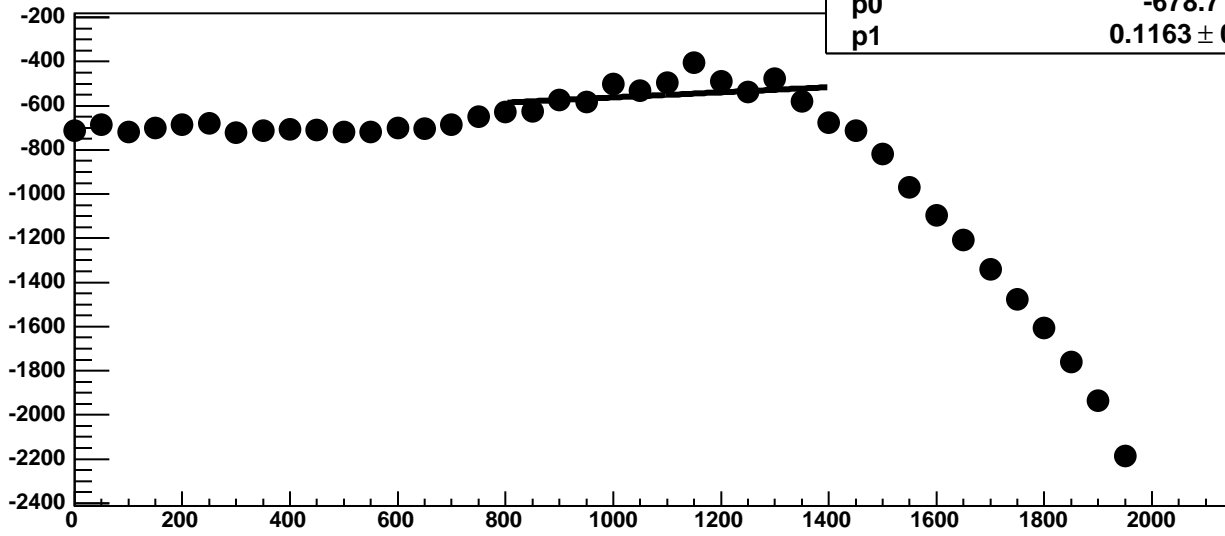
Chip 8, Channel 15, Enable 5, Hold=30, ADC Noise vs DAC



Chip 8, Channel 15, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 8, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

76.2 / 11

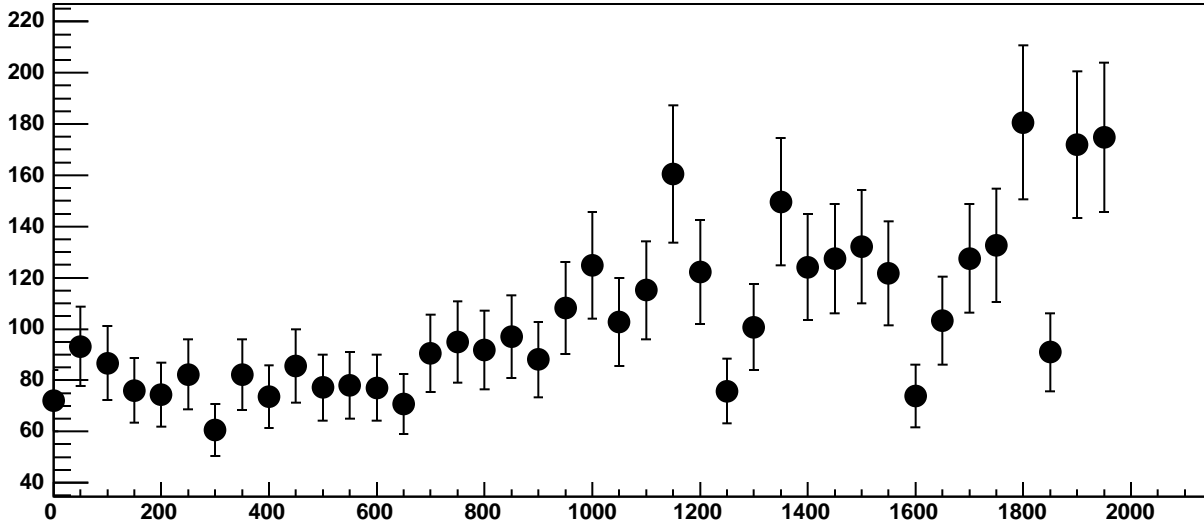
p0

$-678.7 \pm 38.77$

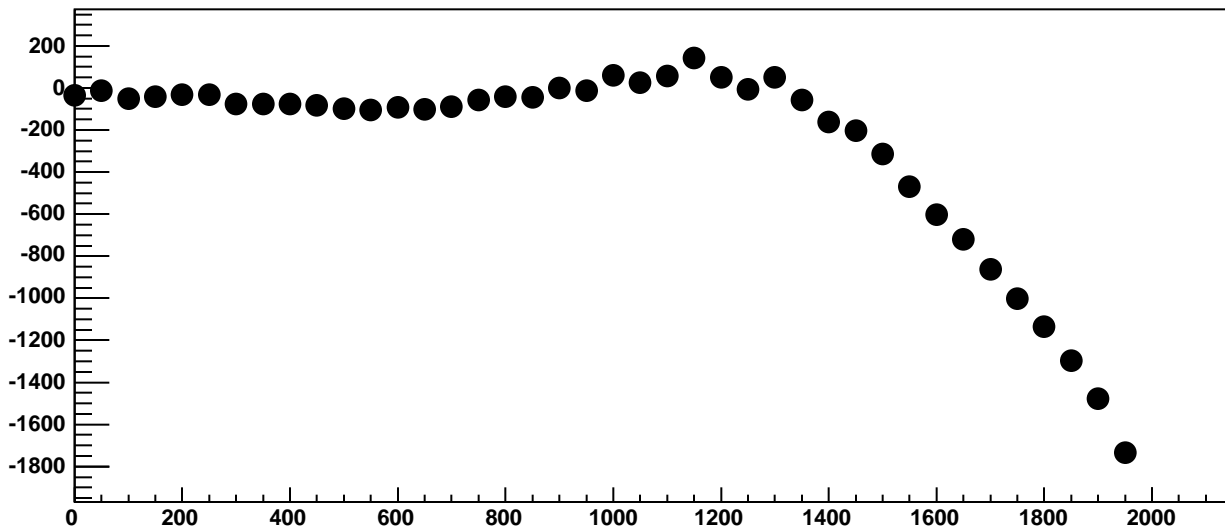
p1

$0.1163 \pm 0.03545$

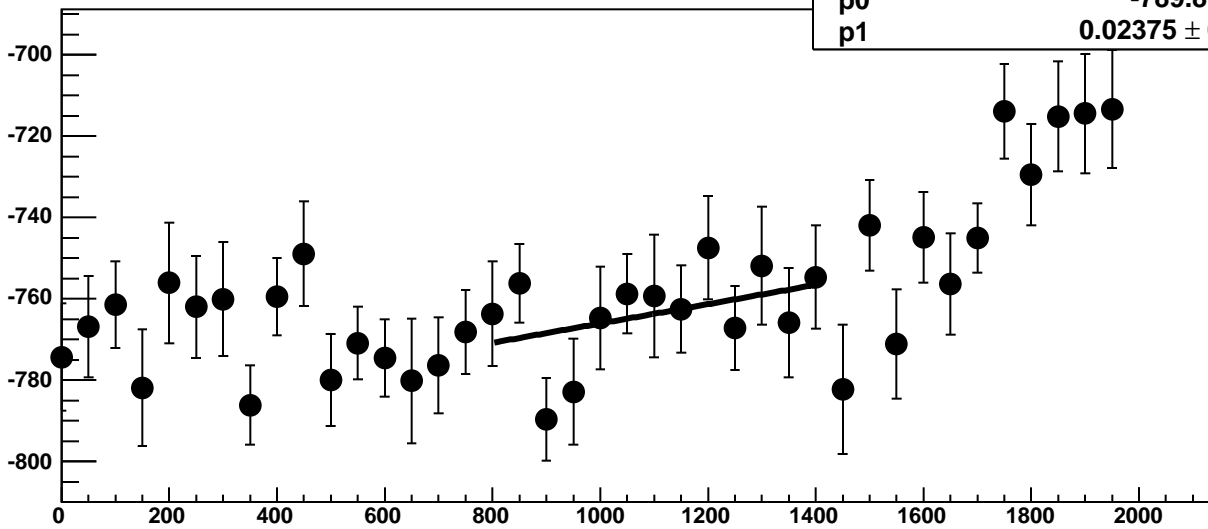
Chip 8, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC



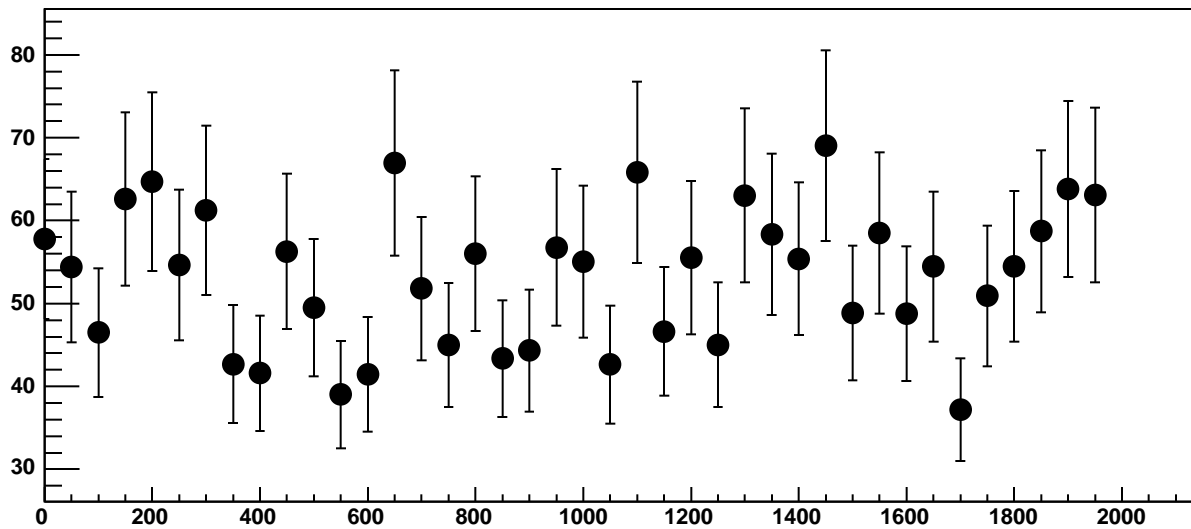
Chip 8, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC



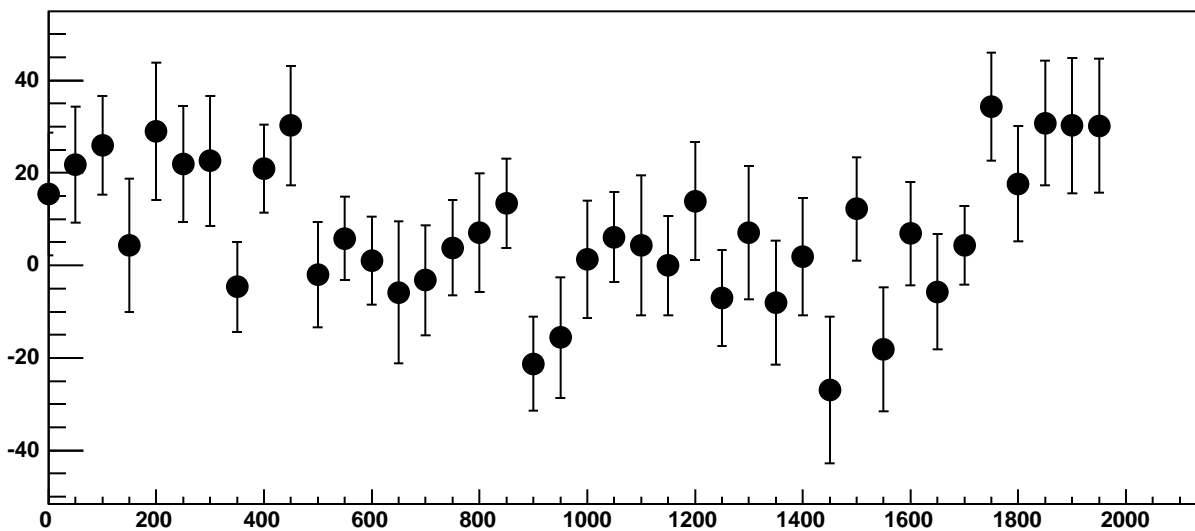
Chip 8, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC



Chip 8, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC

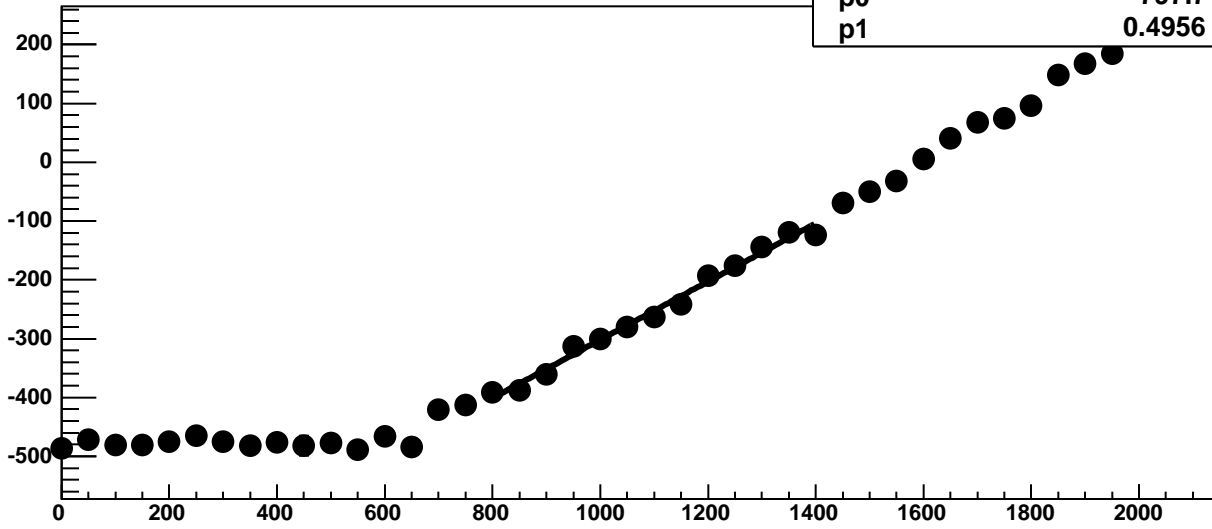


Chip 8, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC



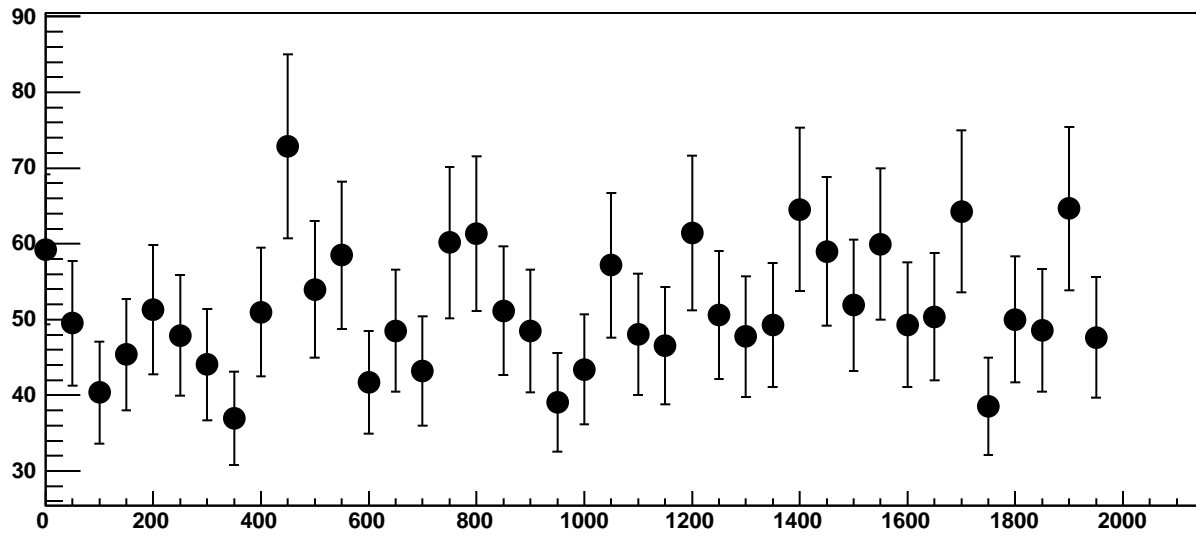


Chip 8, Channel 16, Enable 2, Hold=30, ADC Mean vs DAC

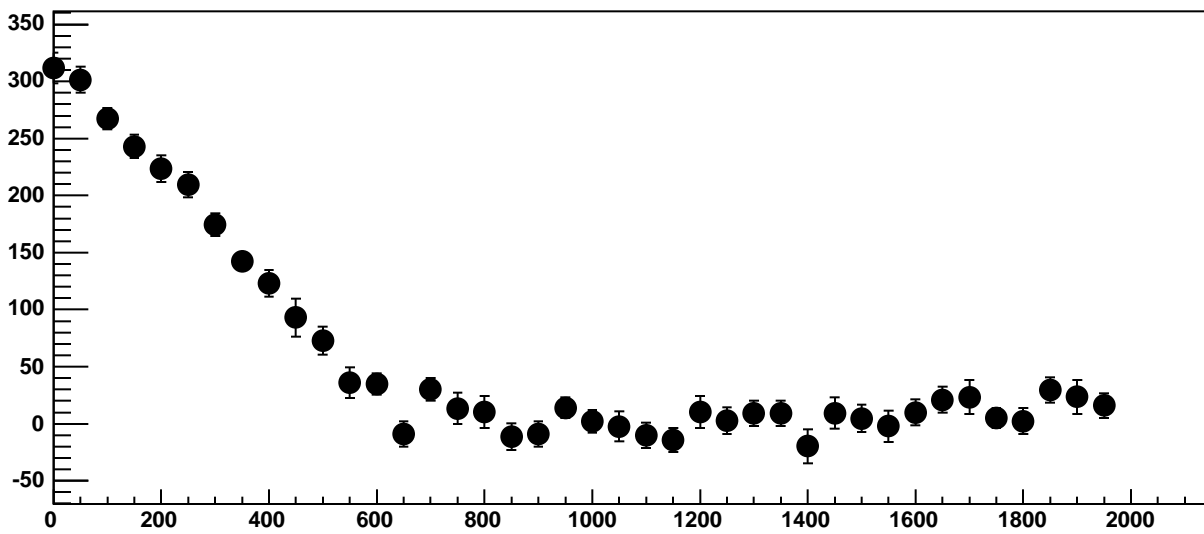


$\chi^2 / \text{ndf}$  10.93 / 11  
p0  $-797.7 \pm 19.86$   
p1  $0.4956 \pm 0.018$

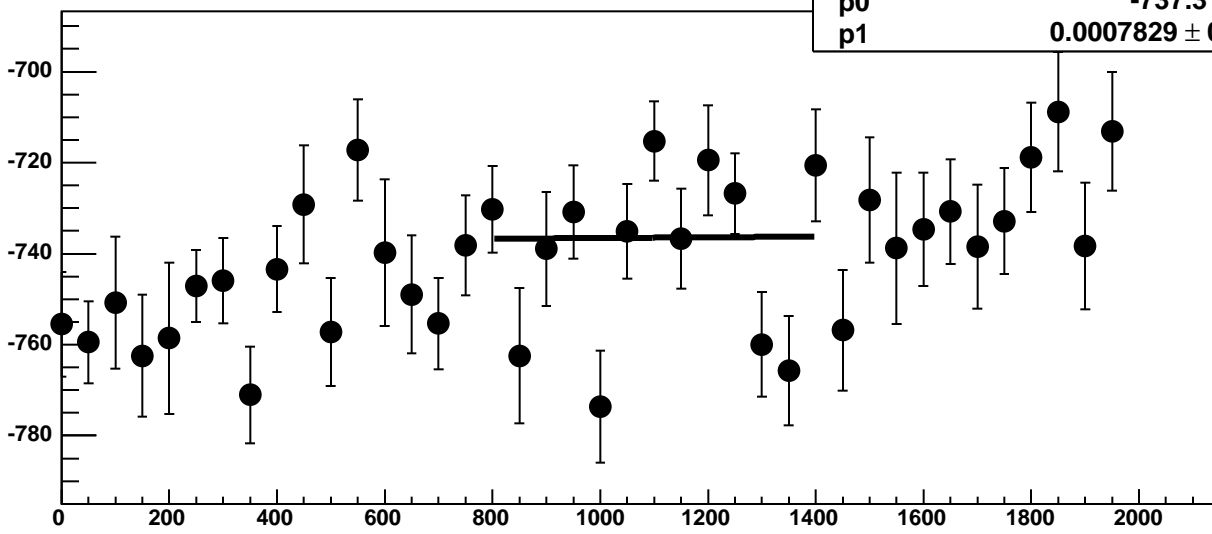
Chip 8, Channel 16, Enable 2, Hold=30, ADC Noise vs DAC



Chip 8, Channel 16, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 8, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

33.61 / 11

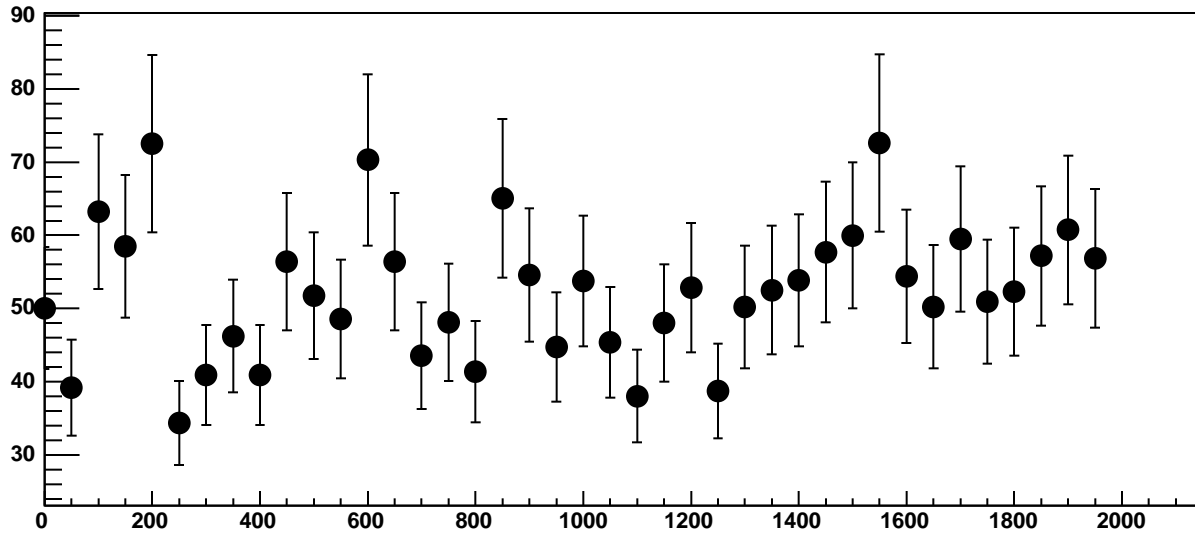
p0

$-737.3 \pm 18.75$

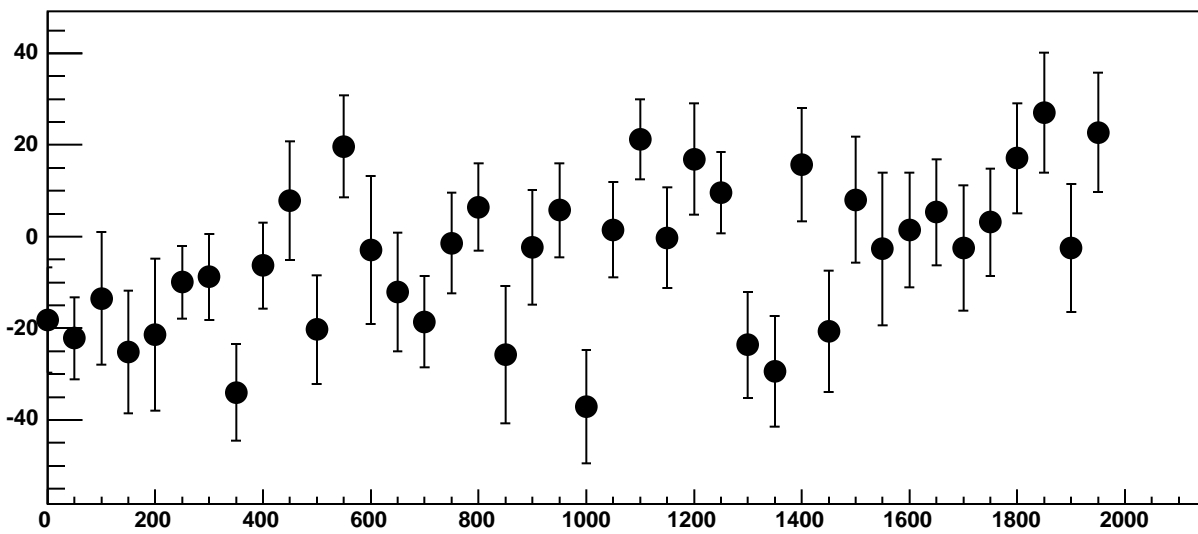
p1

$0.0007829 \pm 0.01683$

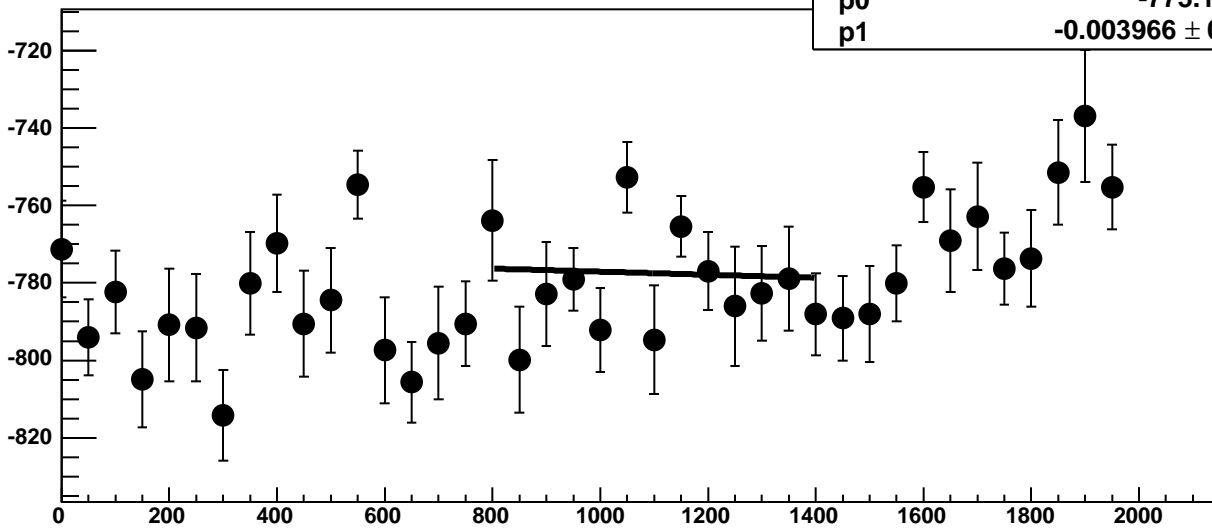
Chip 8, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC



Chip 8, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC

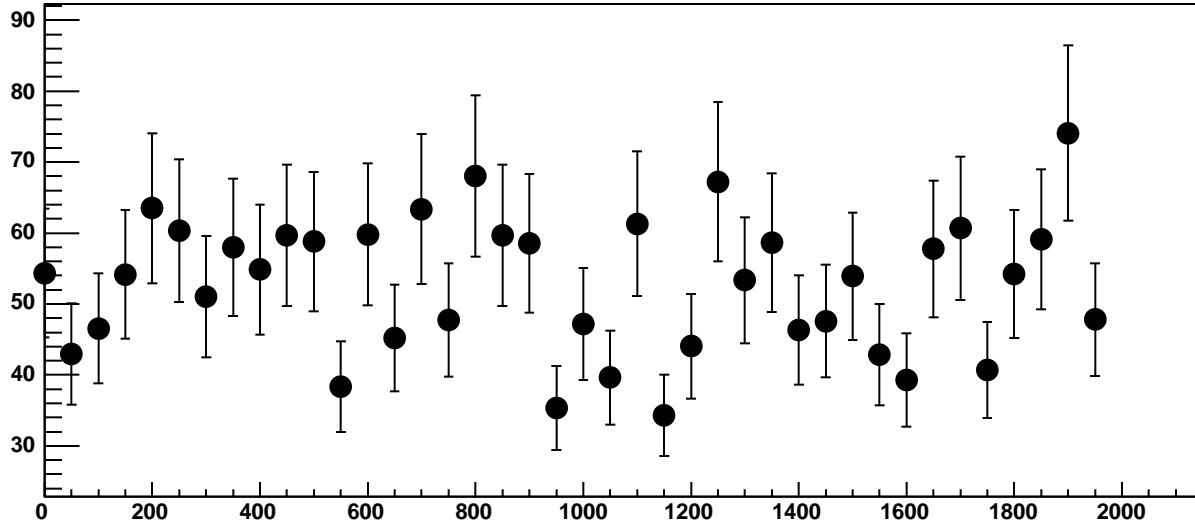


Chip 8, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC

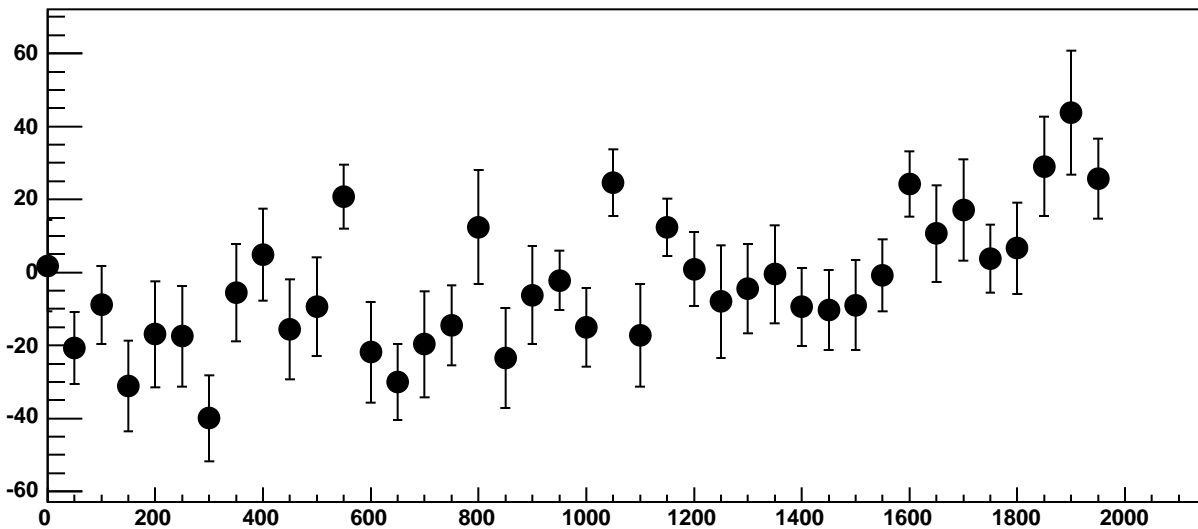


$\chi^2 / \text{ndf}$  18.22 / 11  
p0  $-773.1 \pm 20.31$   
p1  $-0.003966 \pm 0.01819$

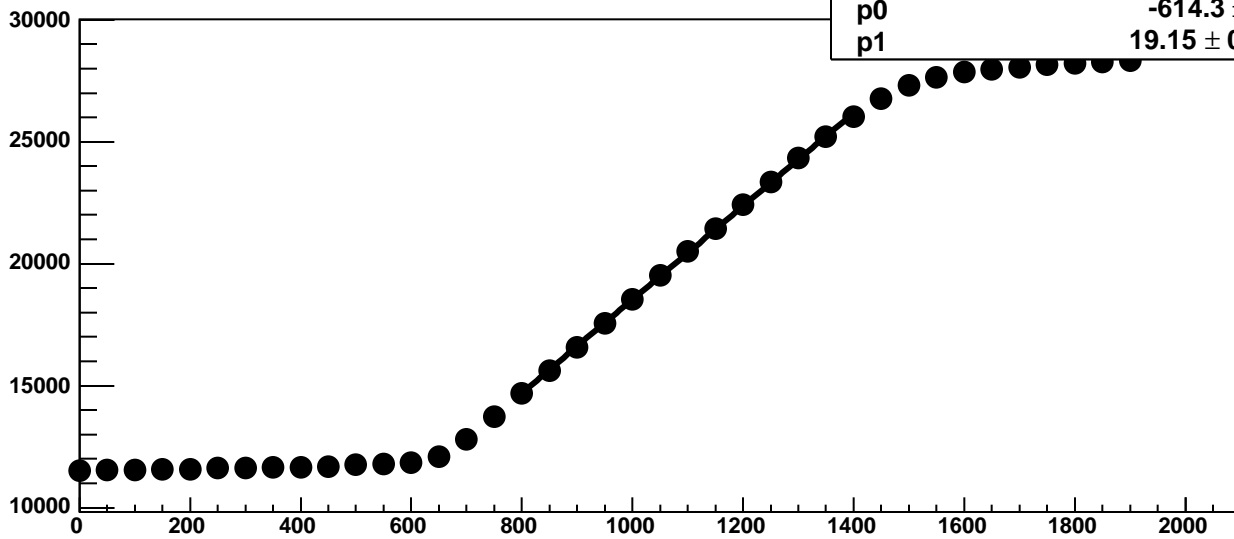
Chip 8, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC



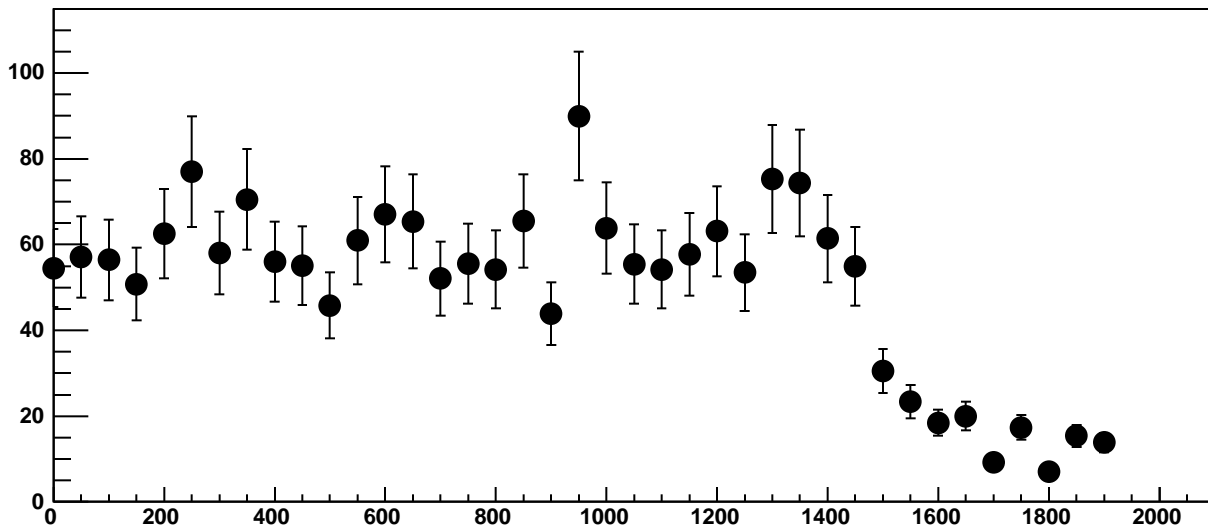
Chip 8, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC



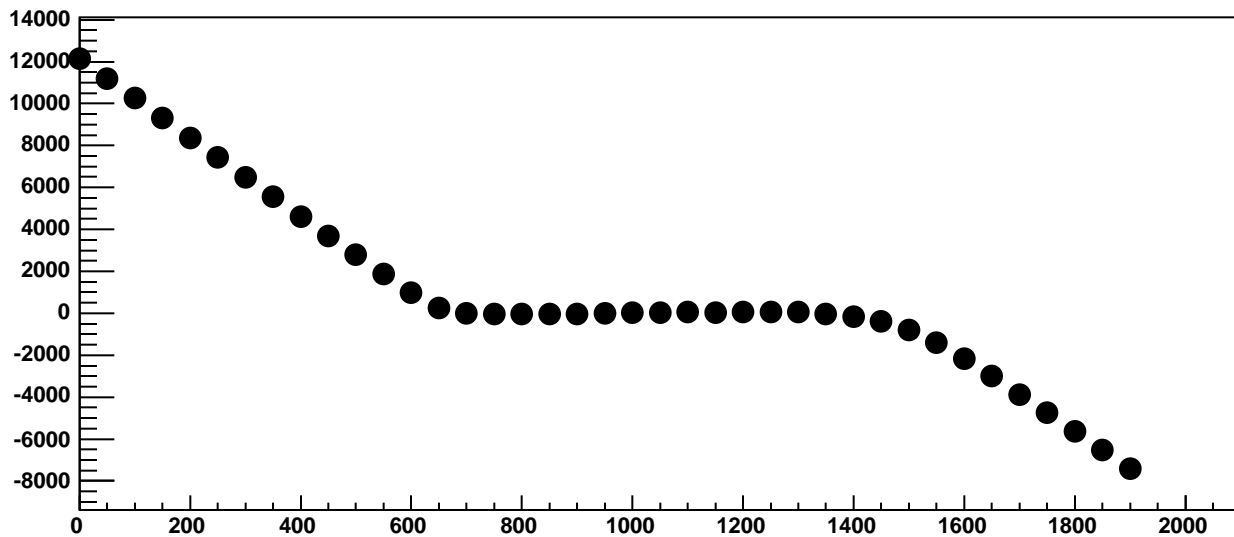
Chip 8, Channel 16, Enable 5!, Hold=30, ADC Mean vs DAC



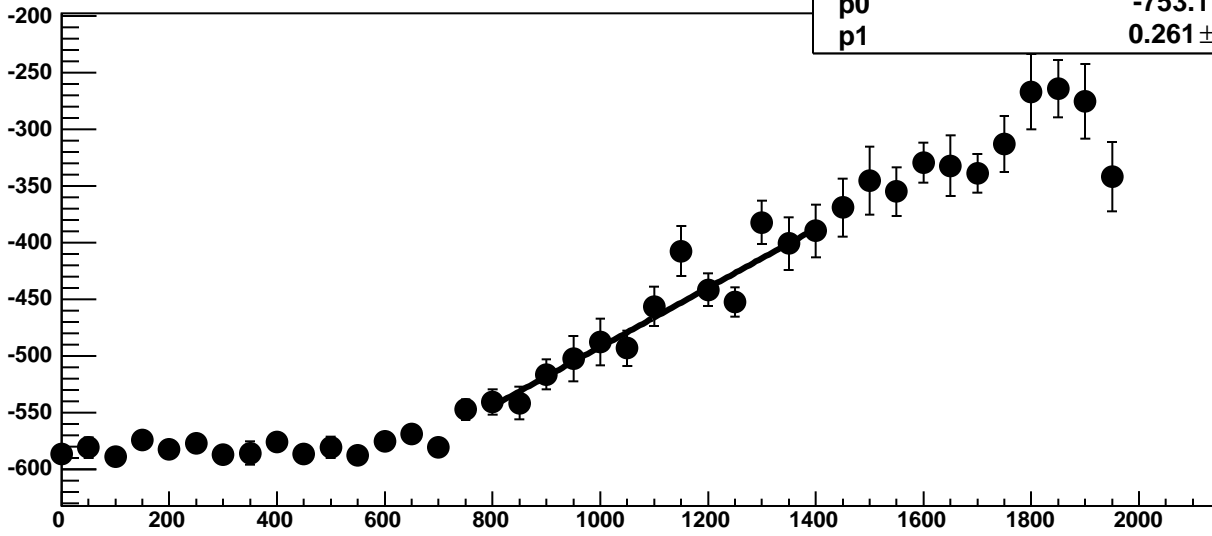
Chip 8, Channel 16, Enable 5!, Hold=30, ADC Noise vs DAC



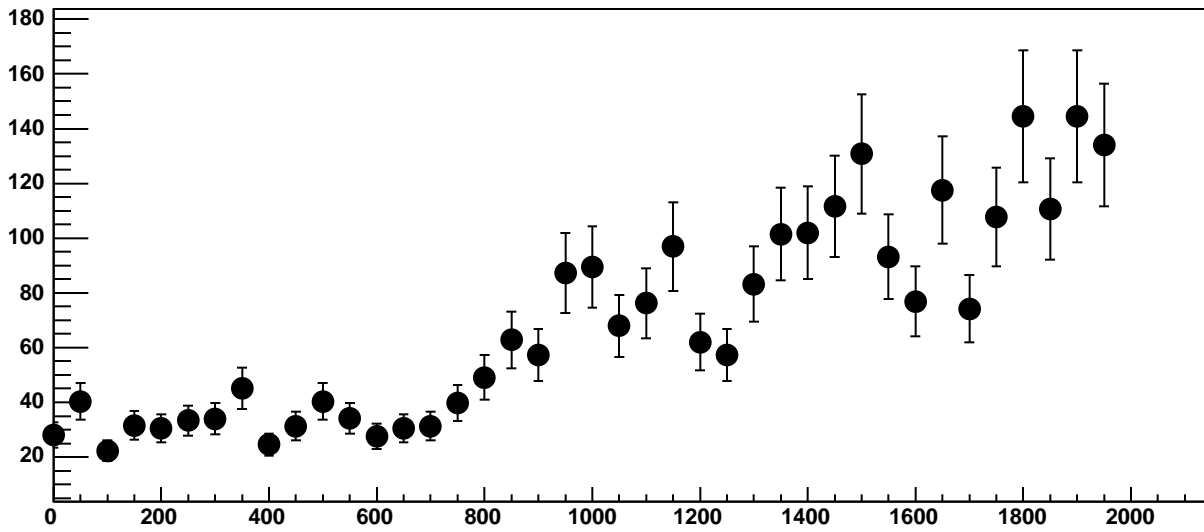
Chip 8, Channel 16, Enable 5!, Hold=30, ADC Residuals vs DAC



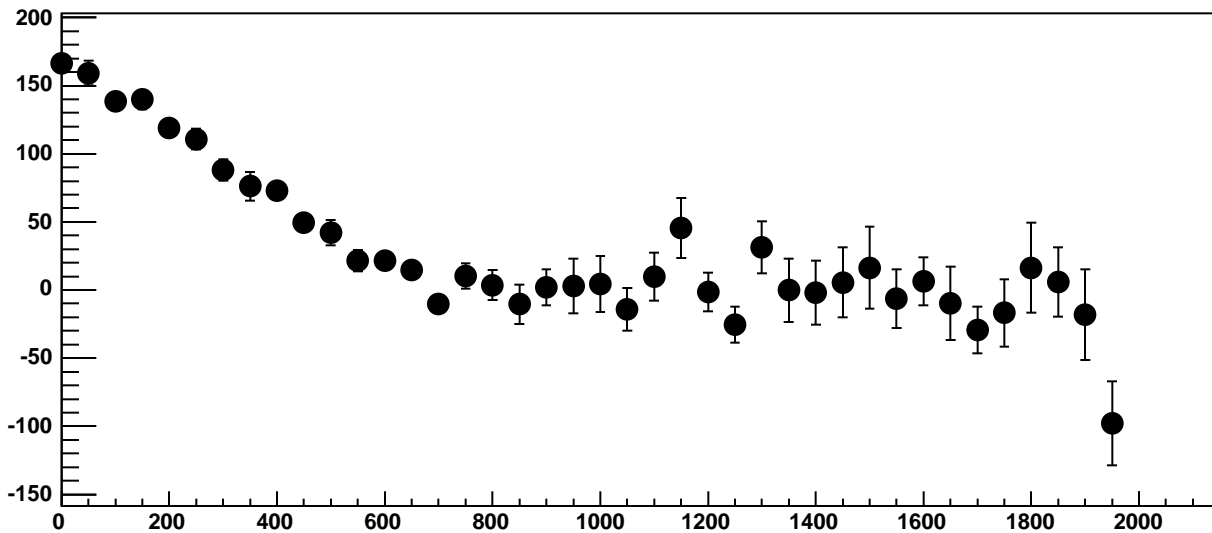
Chip 8, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC



Chip 8, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC

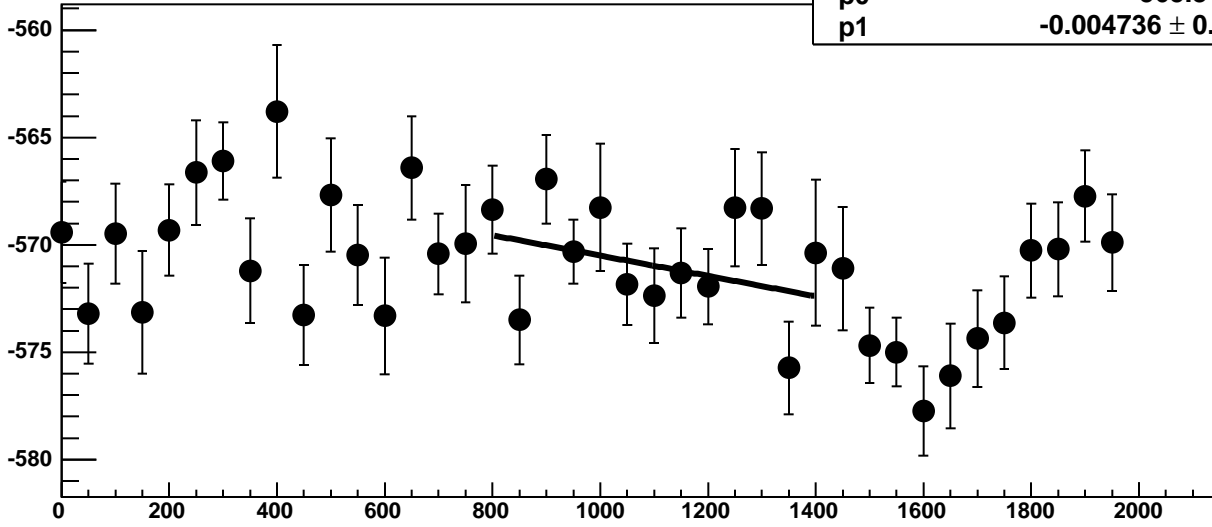


Chip 8, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC

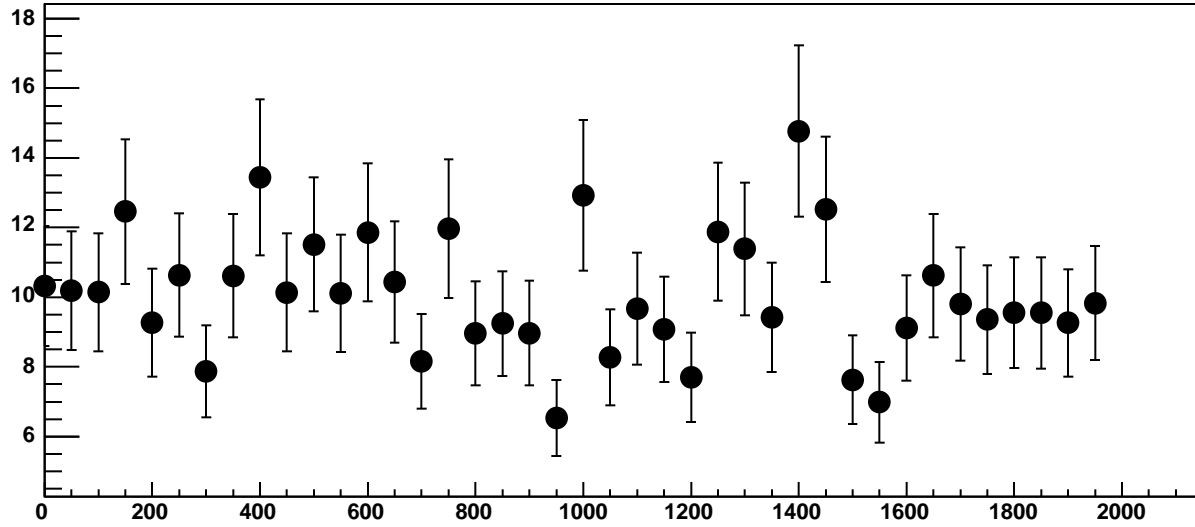


Chip 8, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC

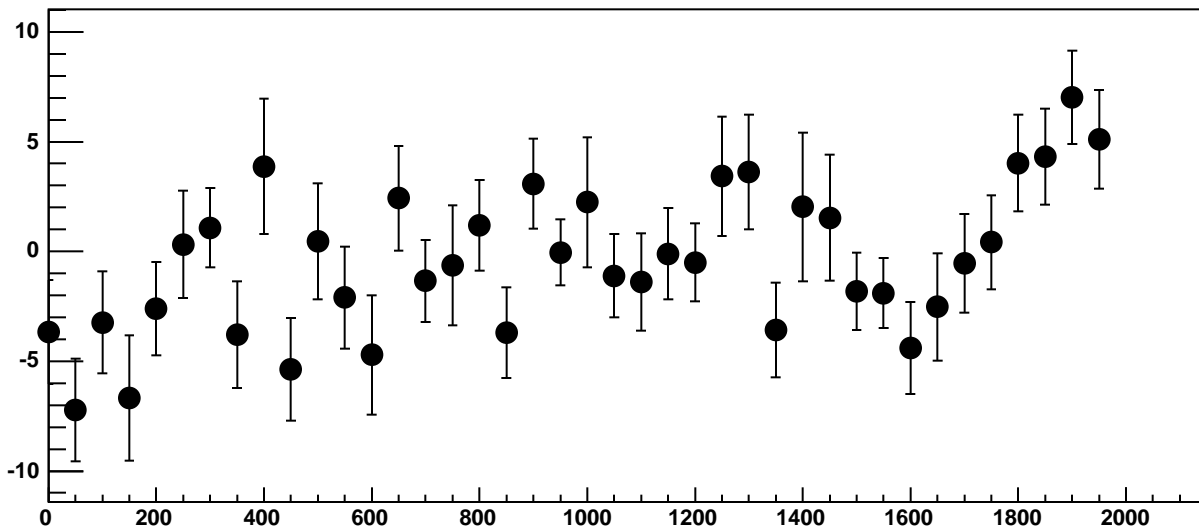
$\chi^2 / \text{ndf}$  13.76 / 11  
p0  $-565.8 \pm 3.646$   
p1  $-0.004736 \pm 0.003373$



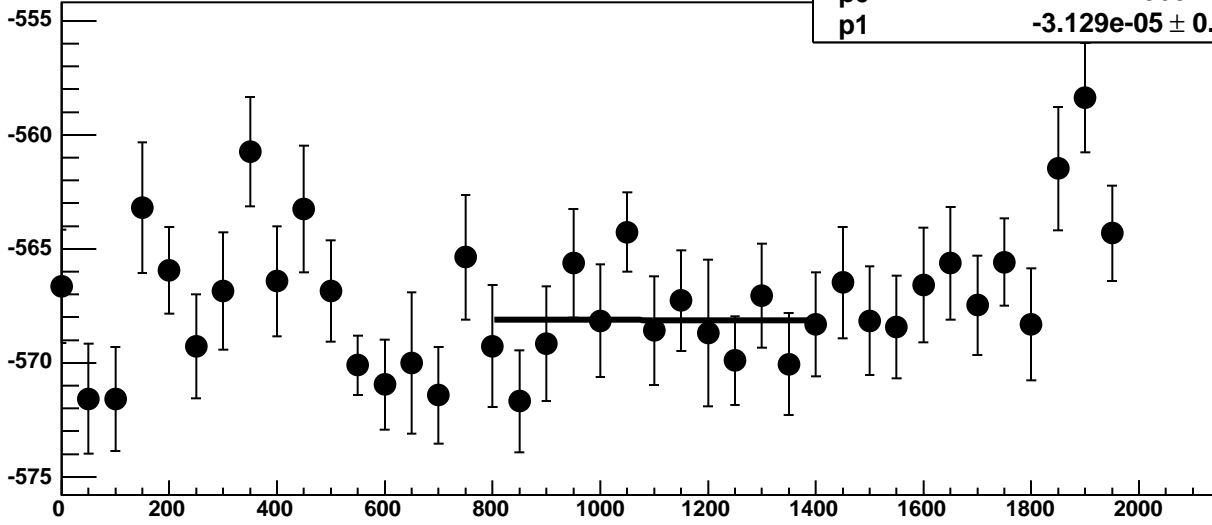
Chip 8, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC



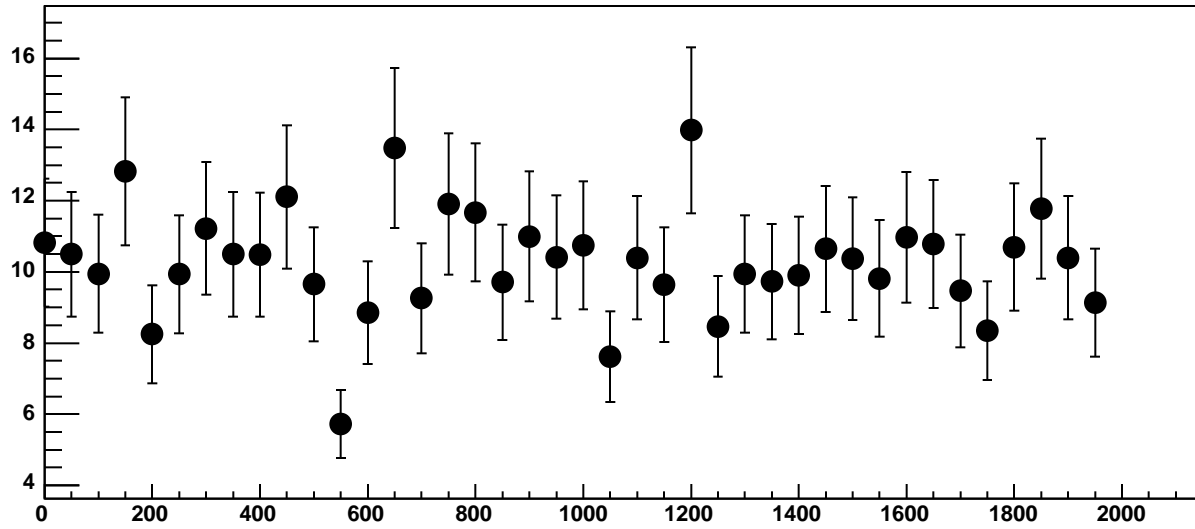
Chip 8, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC



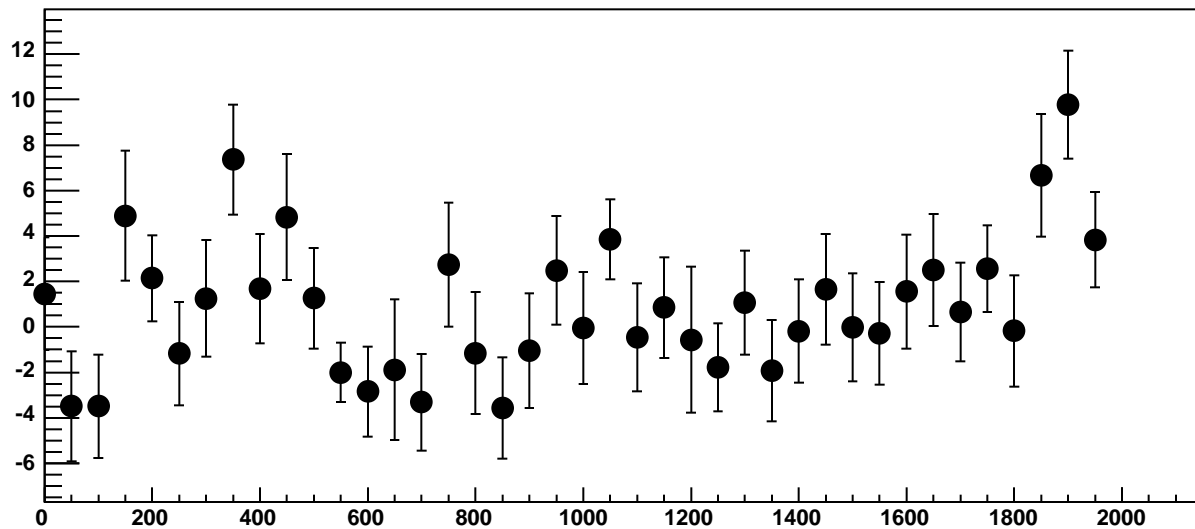
Chip 8, Channel 17, Enable 2!, Hold=30, ADC Mean vs DAC



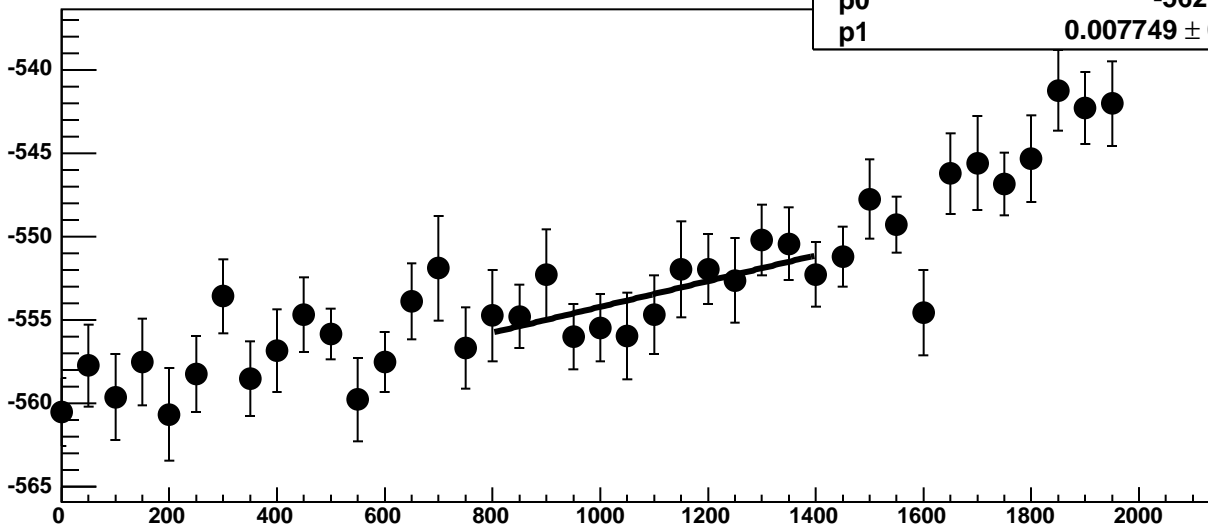
Chip 8, Channel 17, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 8, Channel 17, Enable 2!, Hold=30, ADC Residuals vs DAC



Chip 8, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

4.584 / 11

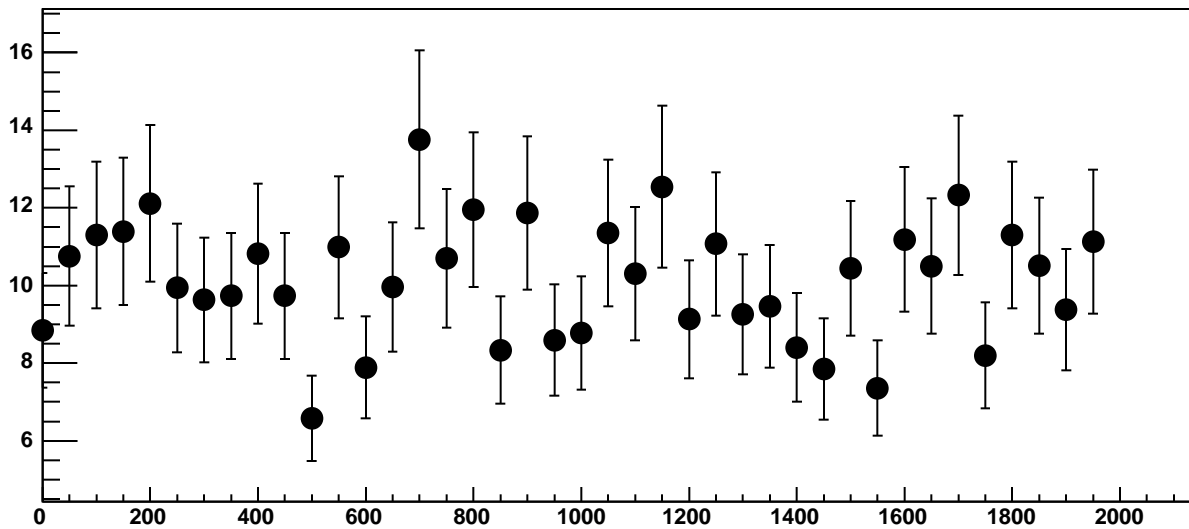
p0

$-562 \pm 3.648$

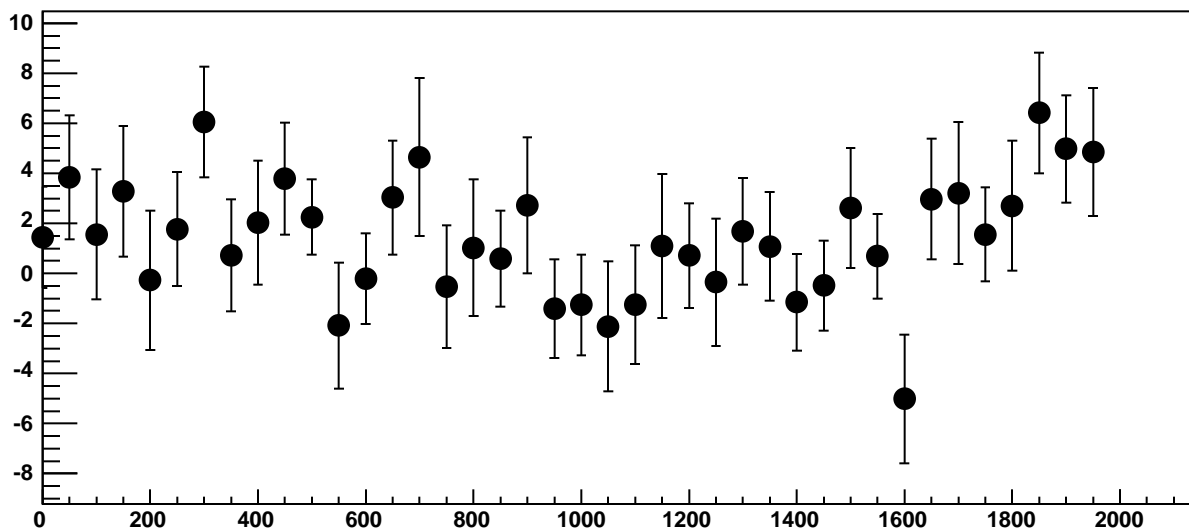
p1

$0.007749 \pm 0.00324$

Chip 8, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC

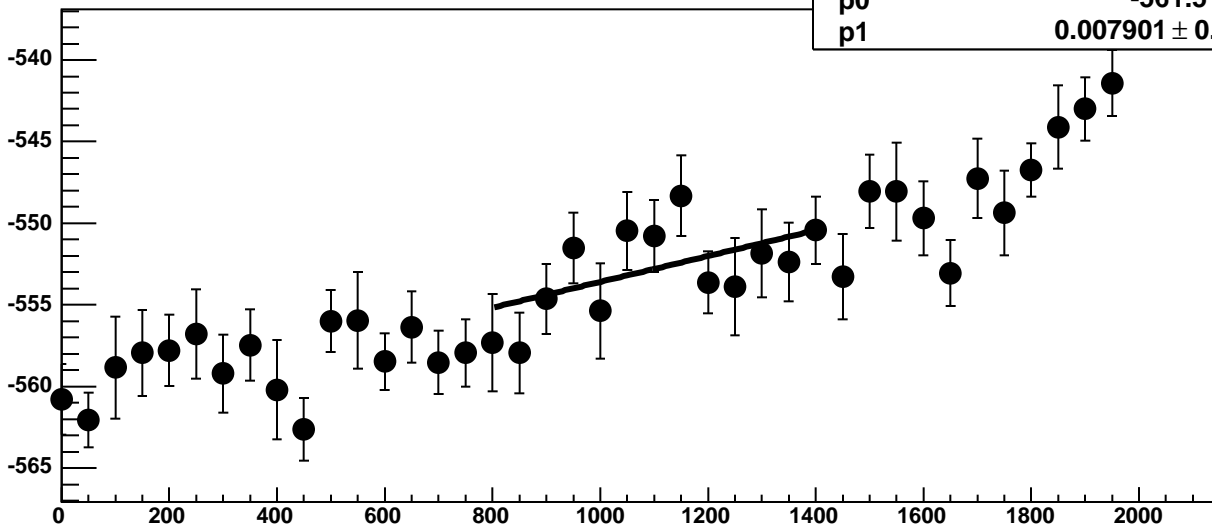


Chip 8, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC



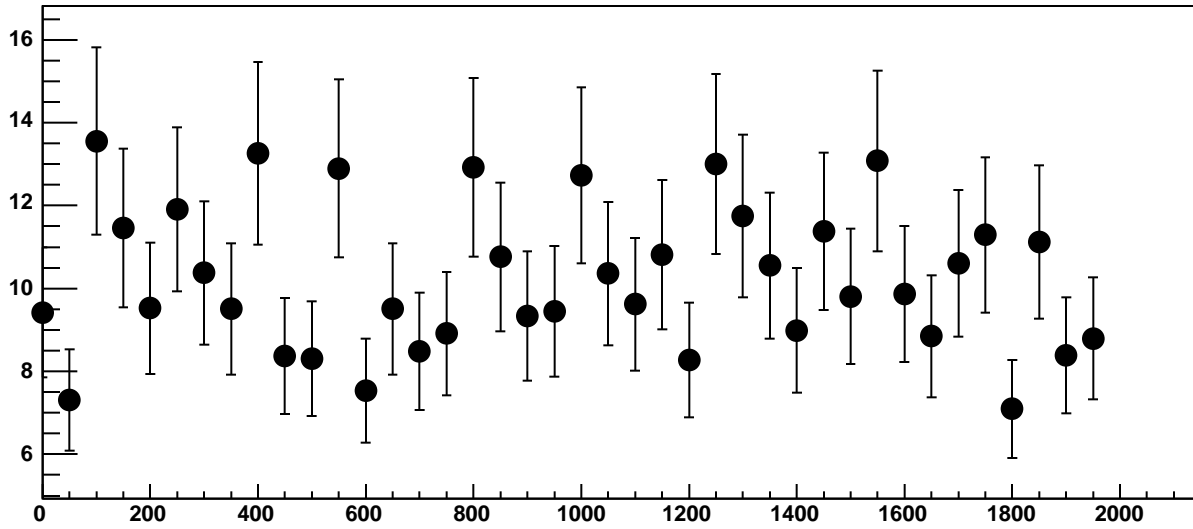


Chip 8, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC

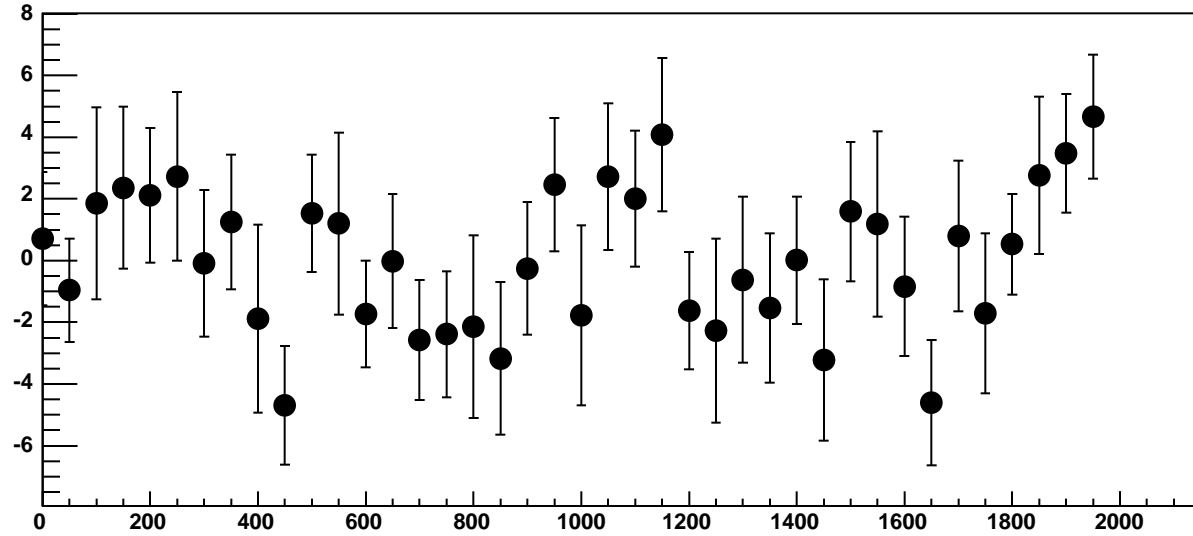


$\chi^2 / \text{ndf}$  10.47 / 11  
p0  $-561.5 \pm 4.017$   
p1  $0.007901 \pm 0.003571$

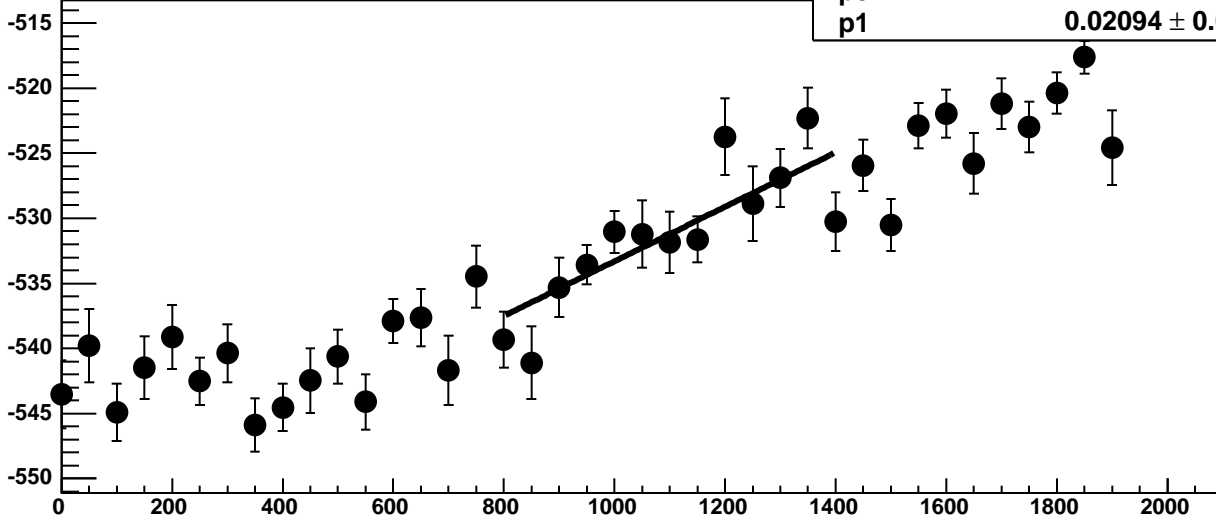
Chip 8, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC



Chip 8, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC

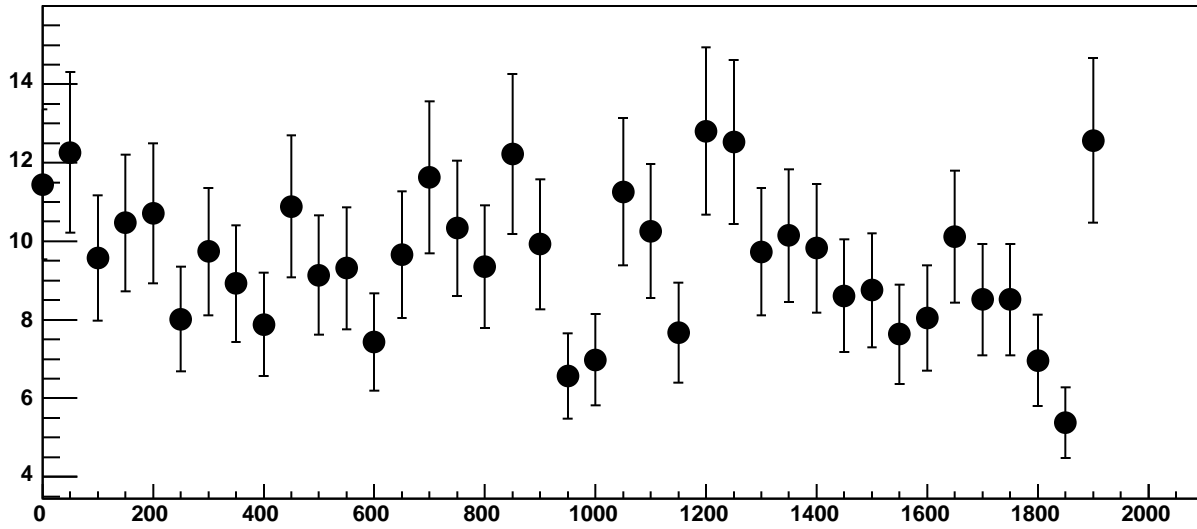


Chip 8, Channel 17, Enable 5, Hold=30, ADC Mean vs DAC

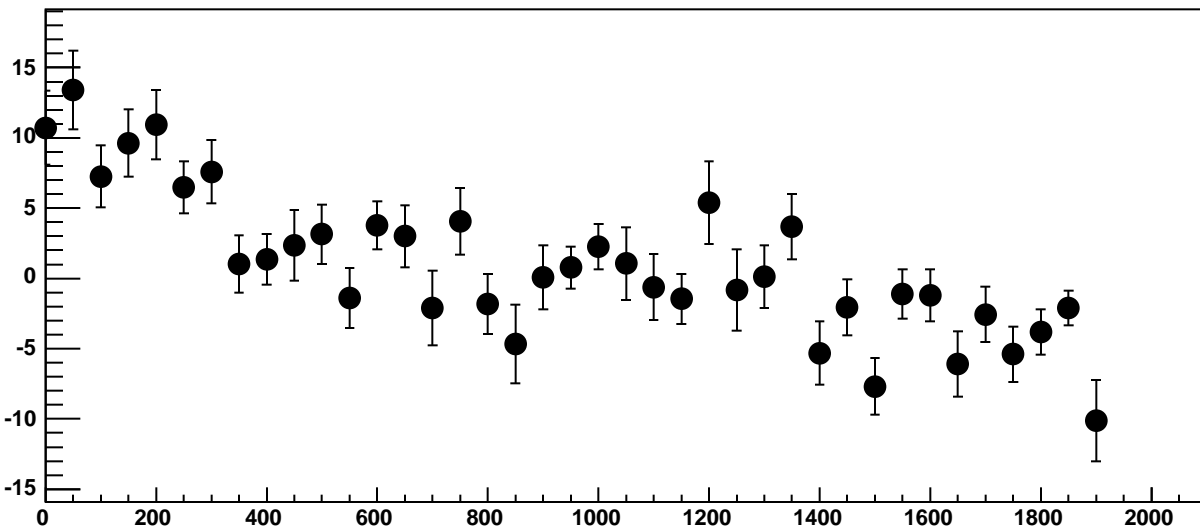


$\chi^2 / \text{ndf}$  18.17 / 11  
p0  $-554.2 \pm 3.644$   
p1  $0.02094 \pm 0.003329$

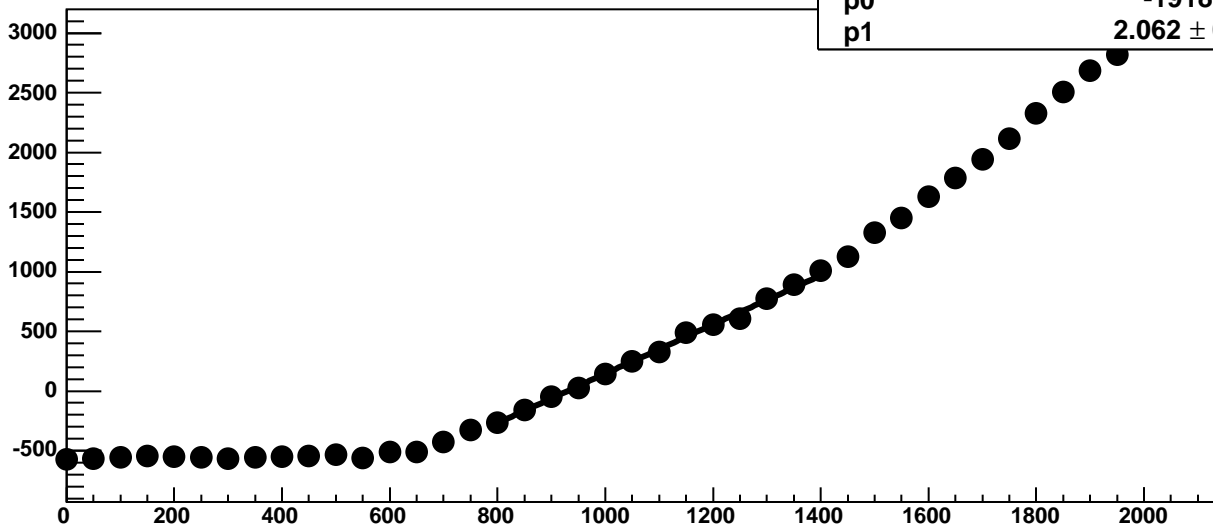
Chip 8, Channel 17, Enable 5, Hold=30, ADC Noise vs DAC



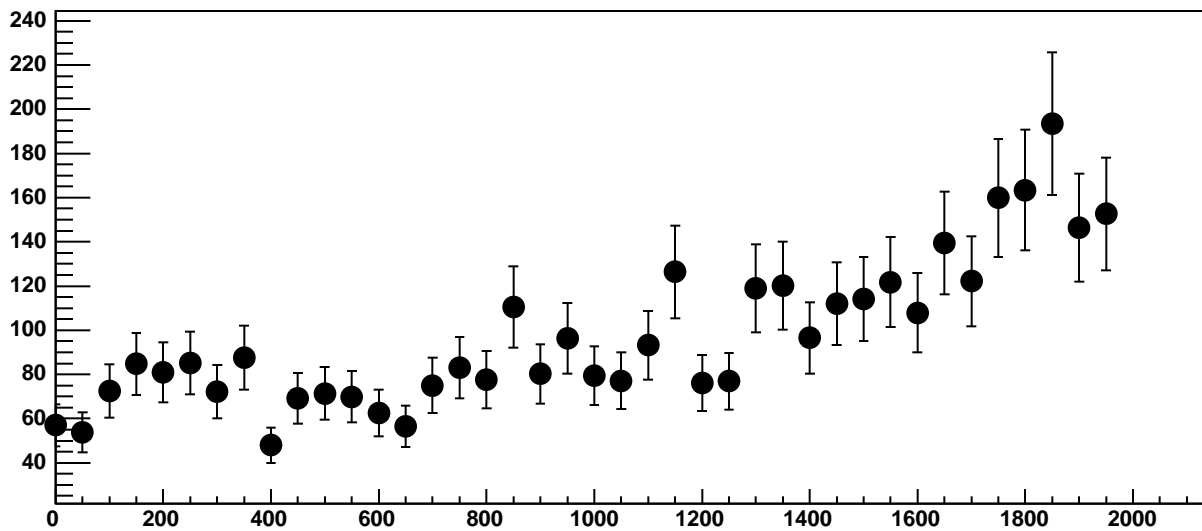
Chip 8, Channel 17, Enable 5, Hold=30, ADC Residuals vs DAC



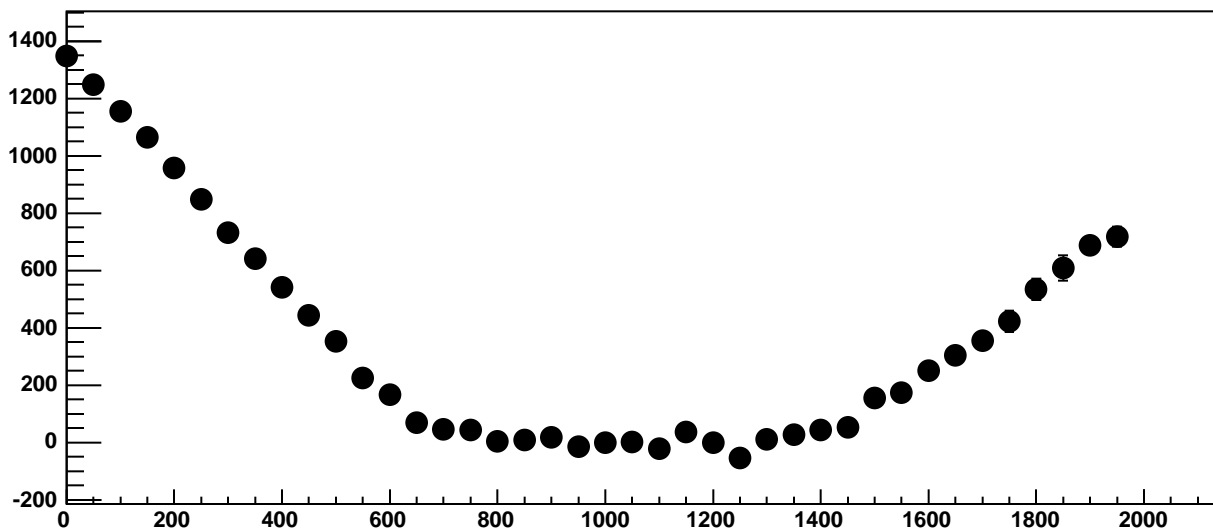
Chip 9, Channel 0, Enable 0, Hold=30, ADC Mean vs DAC



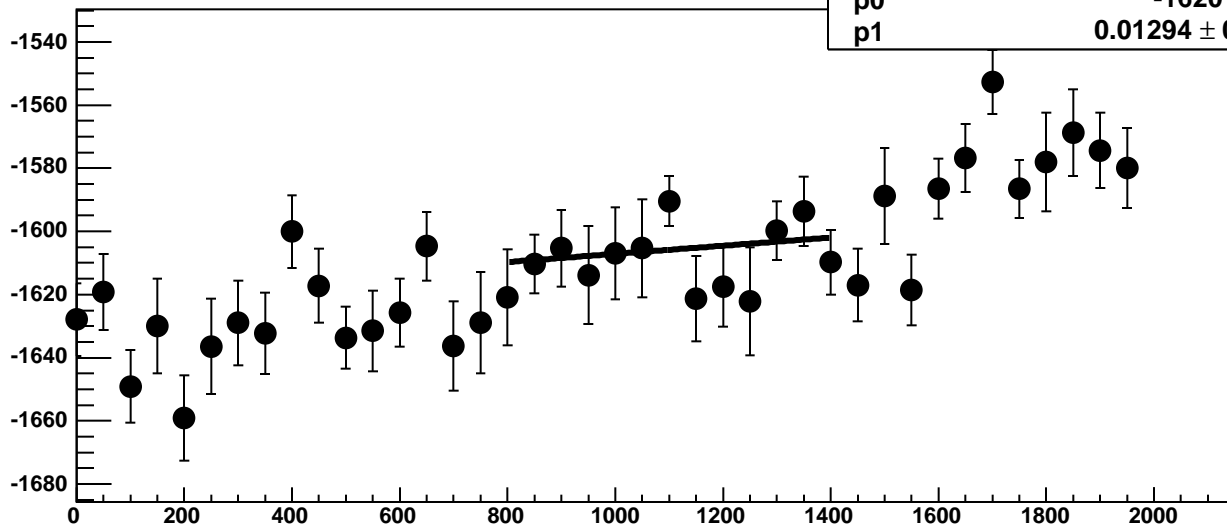
Chip 9, Channel 0, Enable 0, Hold=30, ADC Noise vs DAC



Chip 9, Channel 0, Enable 0, Hold=30, ADC Residuals vs DAC

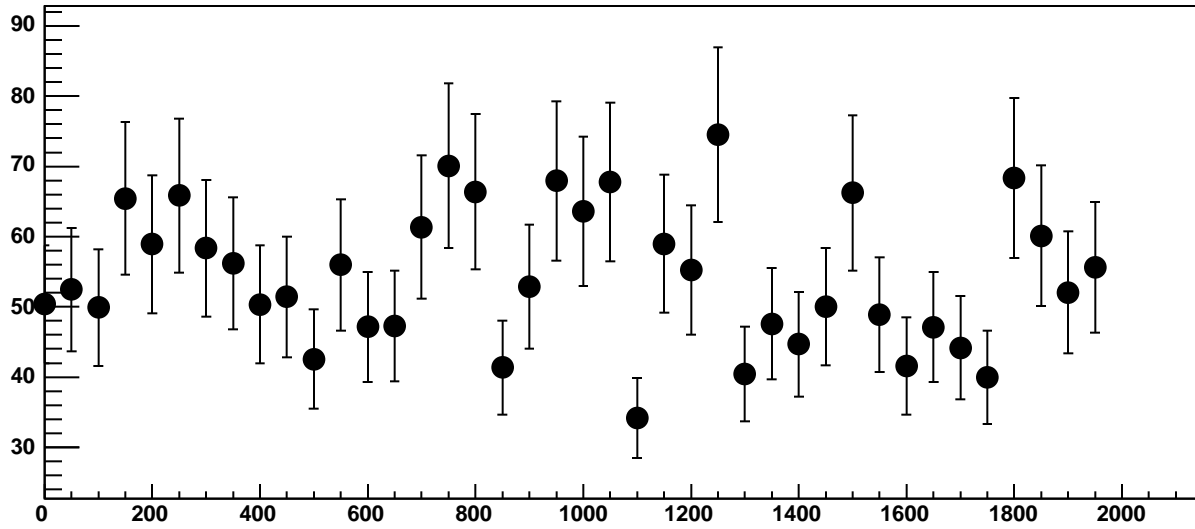


Chip 9, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC

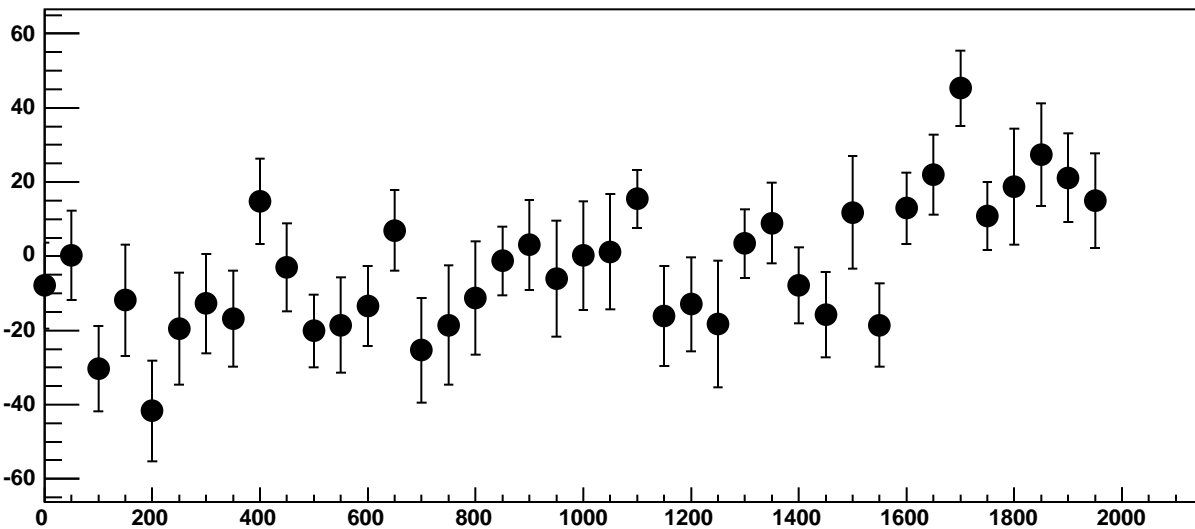


$\chi^2 / \text{ndf}$  9.661 / 11  
p0 -1620 ± 19.17  
p1 0.01294 ± 0.01689

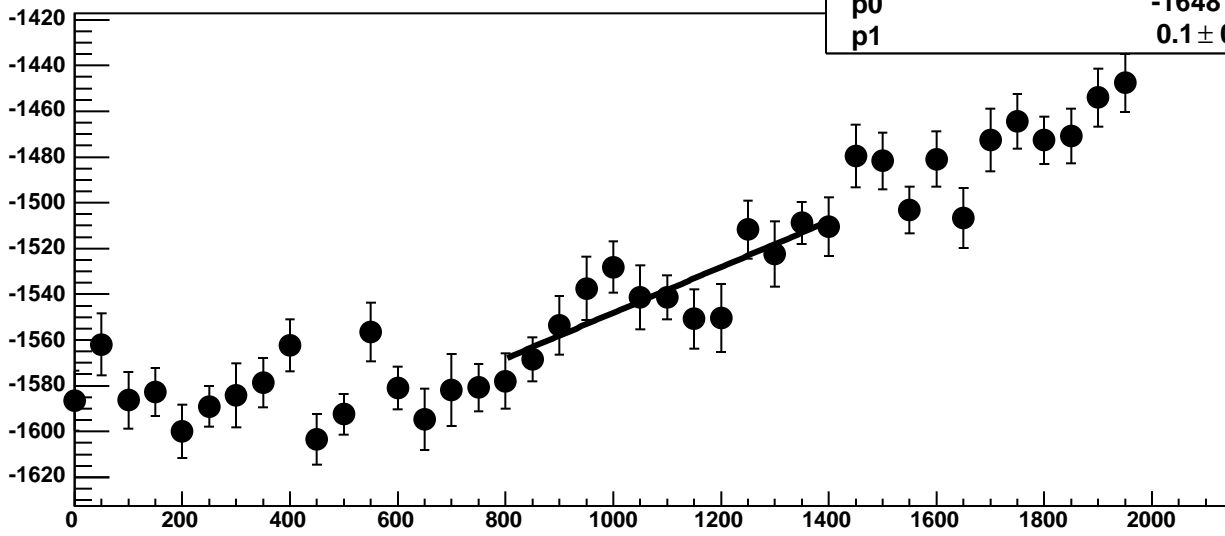
Chip 9, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC



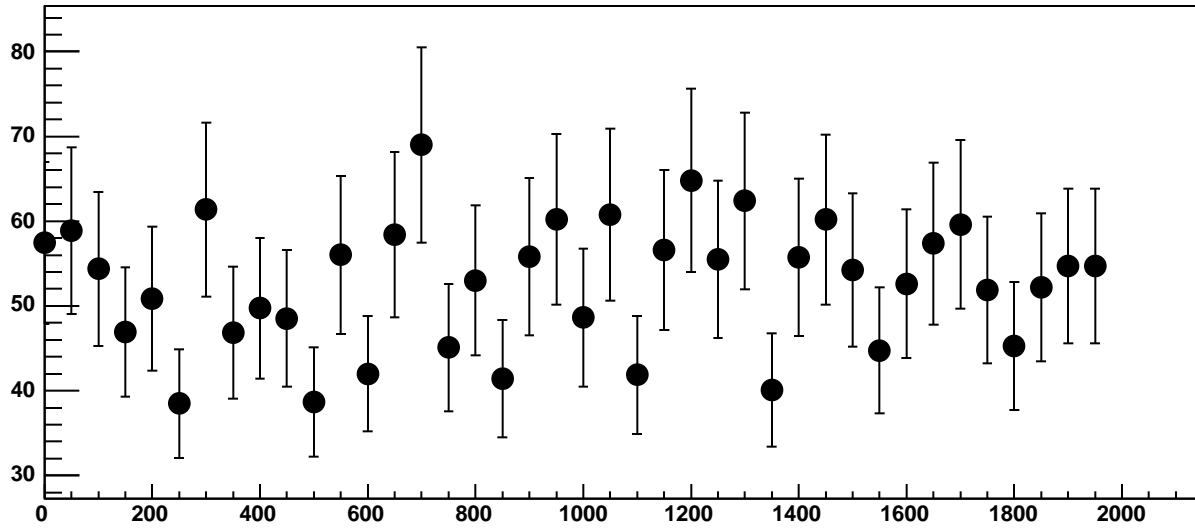
Chip 9, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC



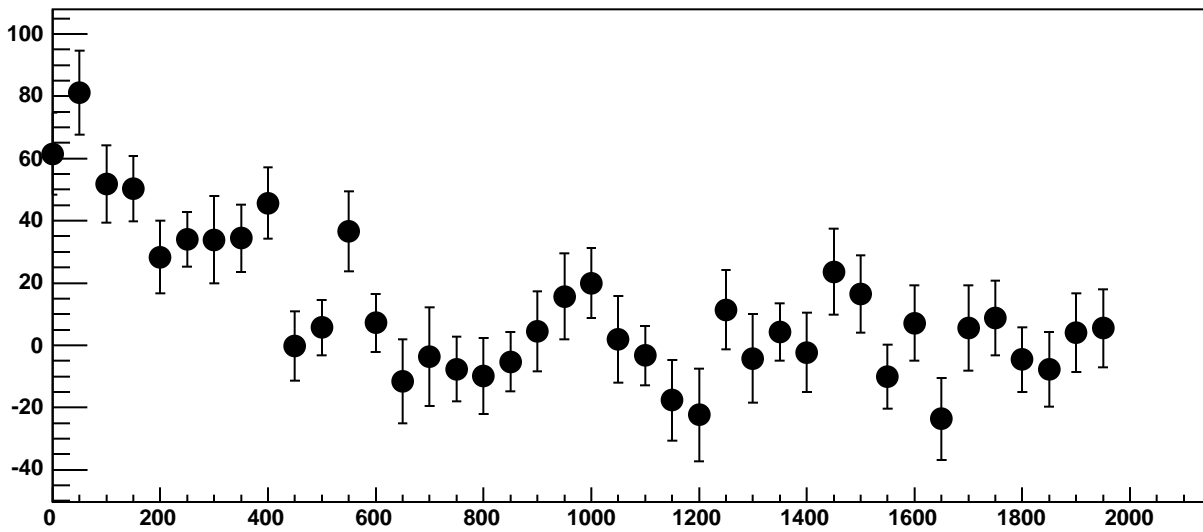
Chip 9, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC



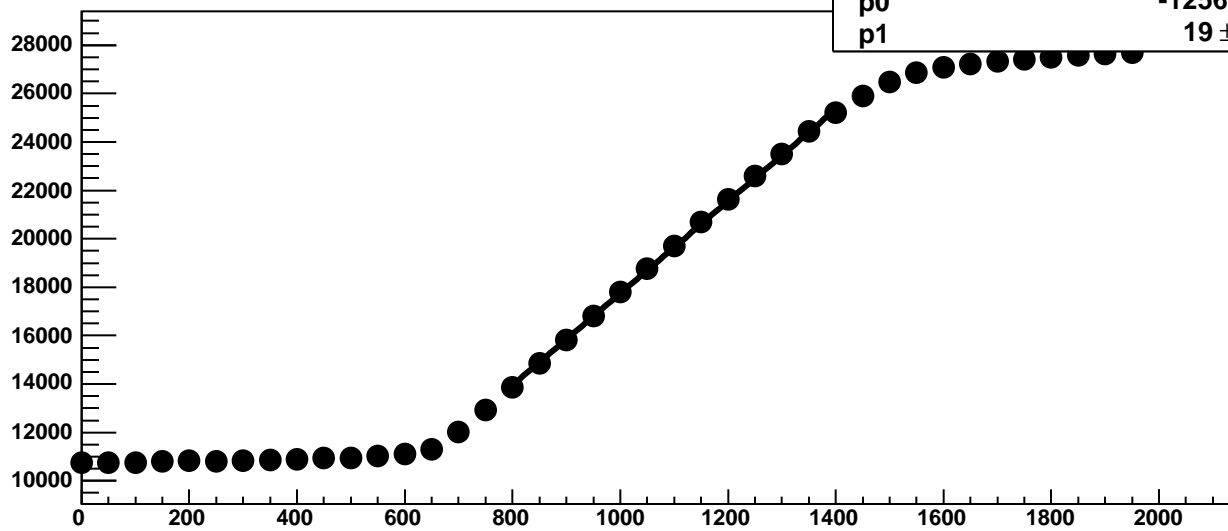
Chip 9, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC



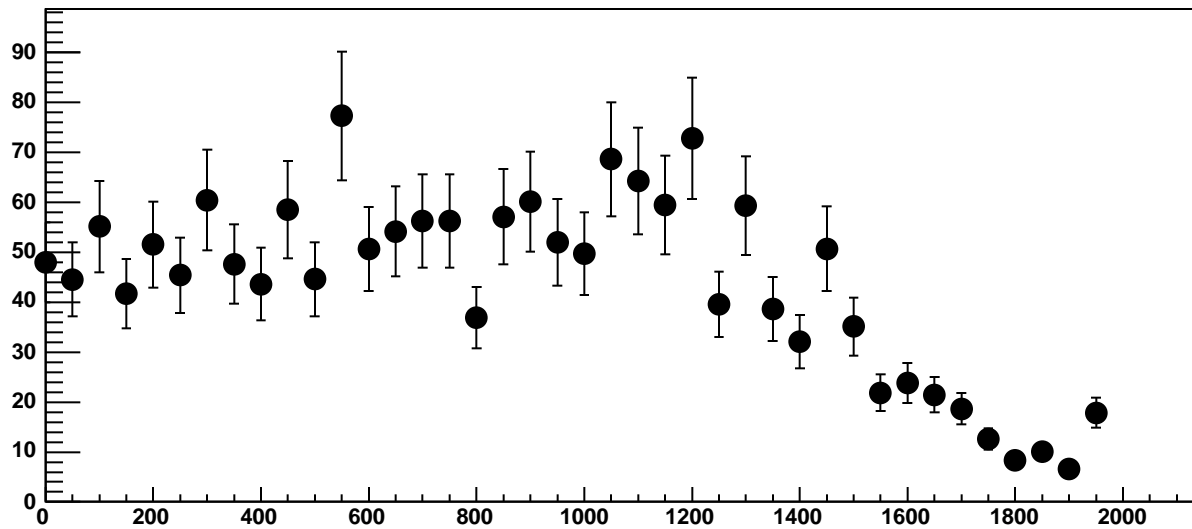
Chip 9, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC



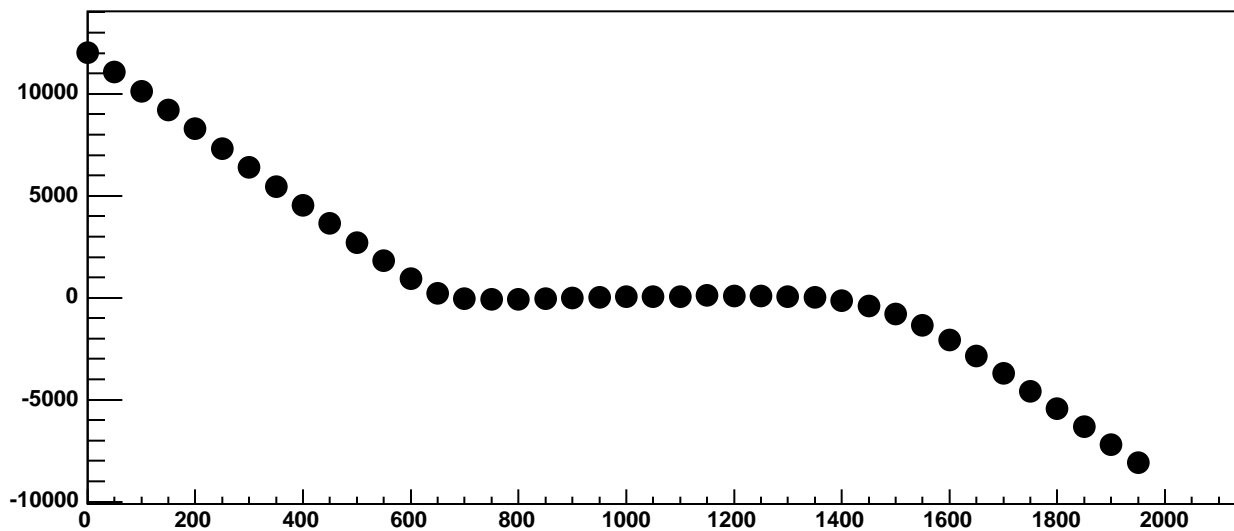
Chip 9, Channel 0, Enable 3!, Hold=30, ADC Mean vs DAC



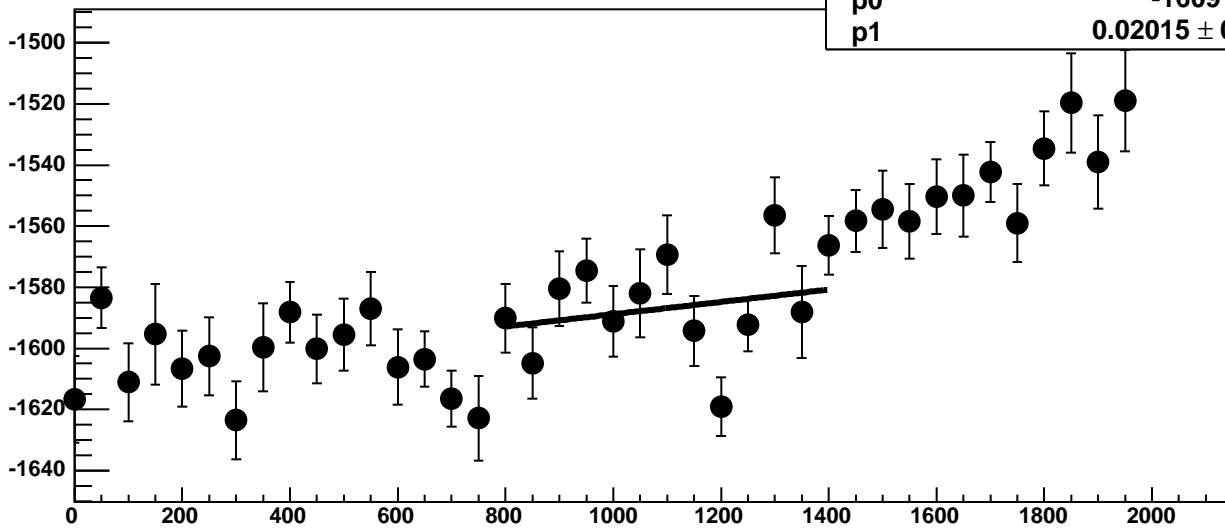
Chip 9, Channel 0, Enable 3!, Hold=30, ADC Noise vs DAC



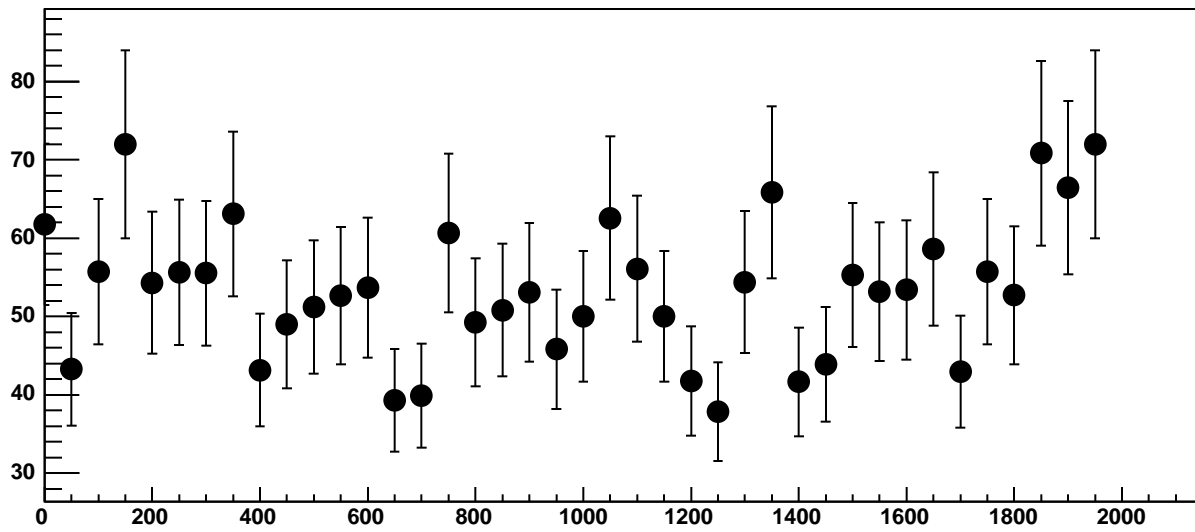
Chip 9, Channel 0, Enable 3!, Hold=30, ADC Residuals vs DAC



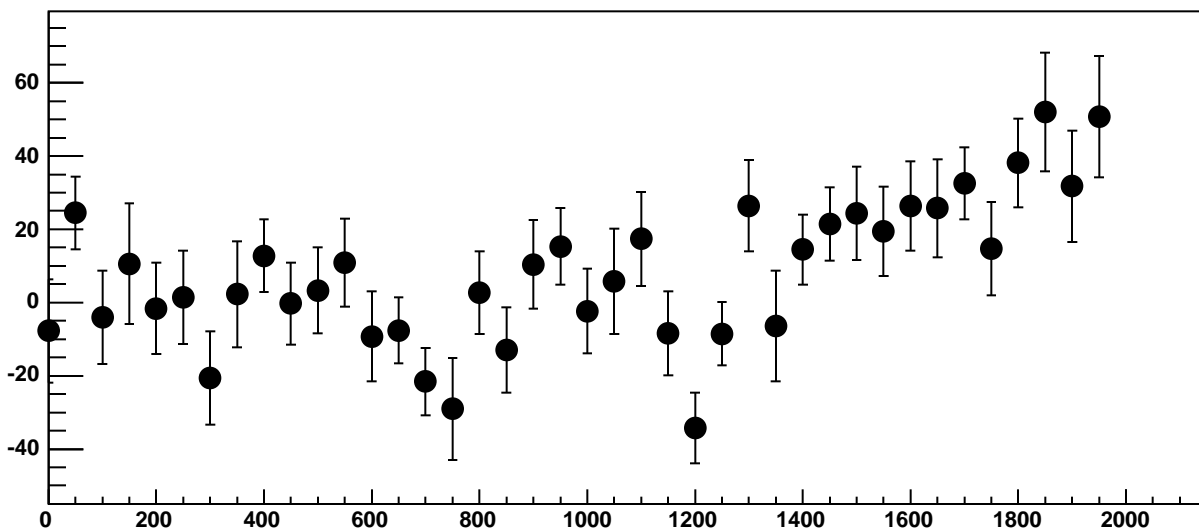
Chip 9, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC



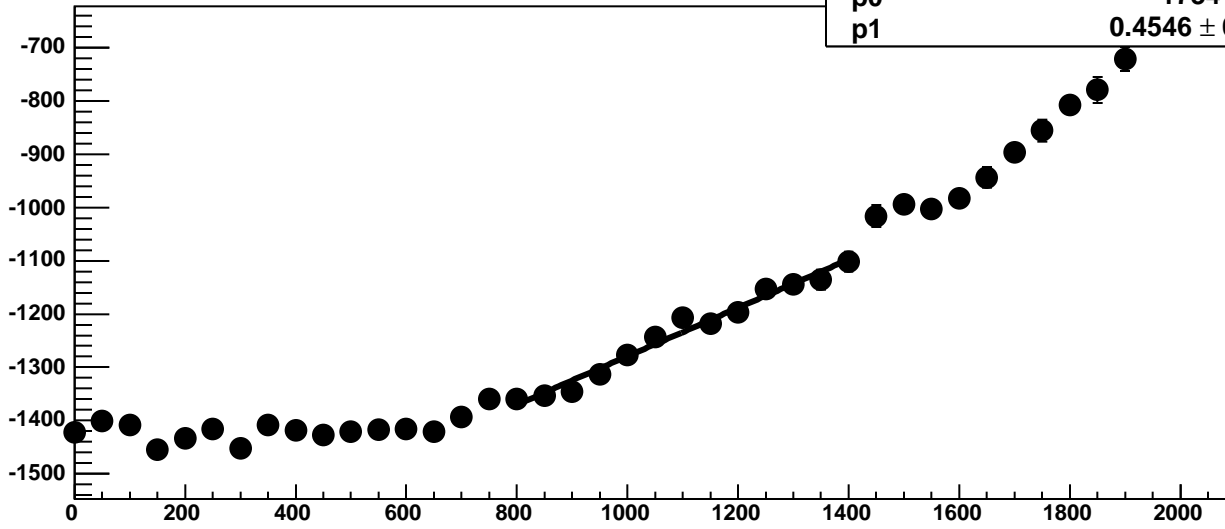
Chip 9, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC



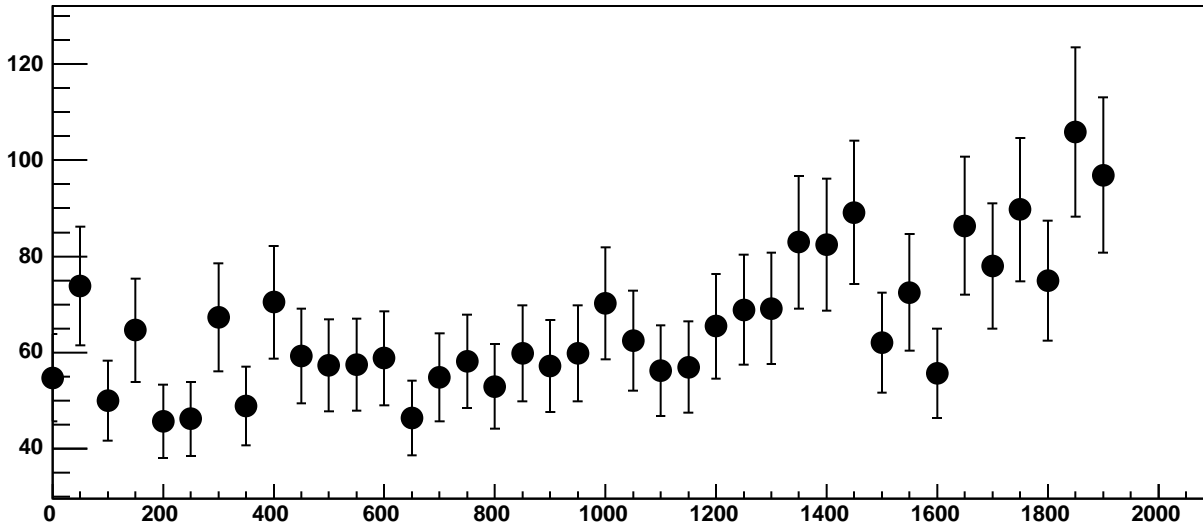
Chip 9, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC



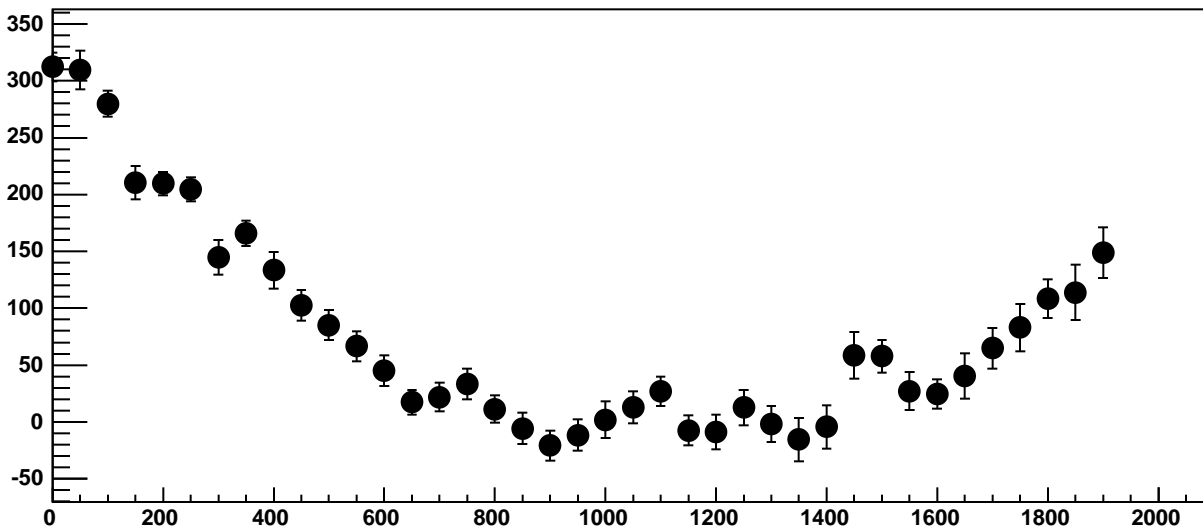
Chip 9, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC



Chip 9, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC

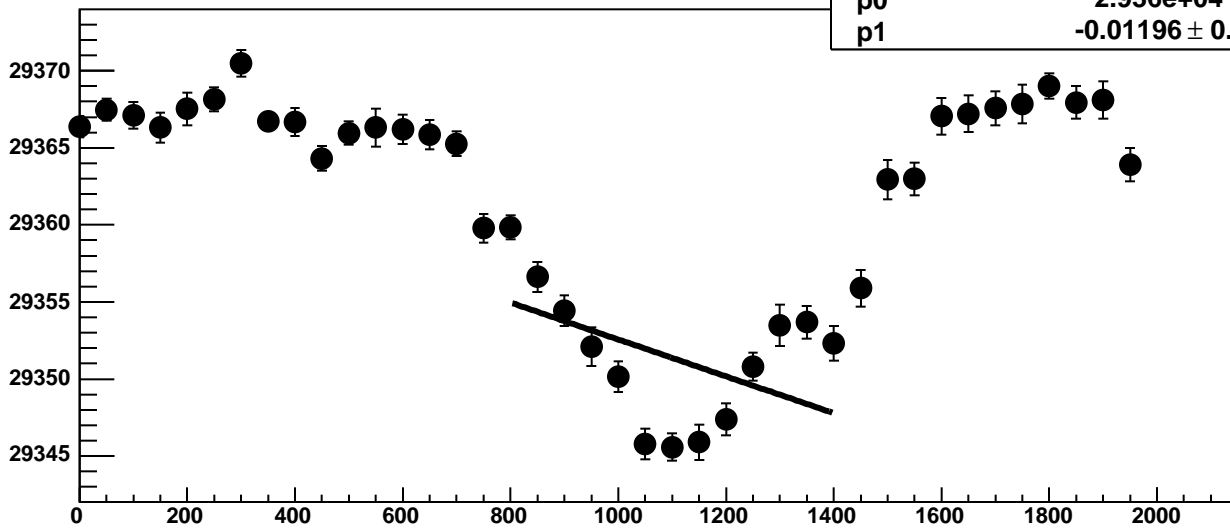


Chip 9, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC



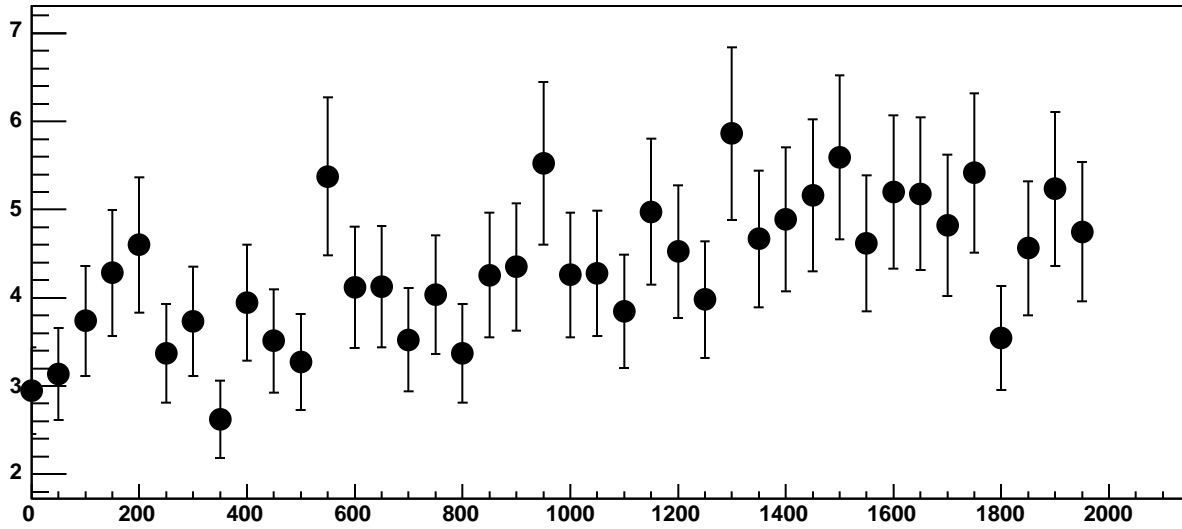


Chip 9, Channel 1, Enable 0!, Hold=30, ADC Mean vs DAC

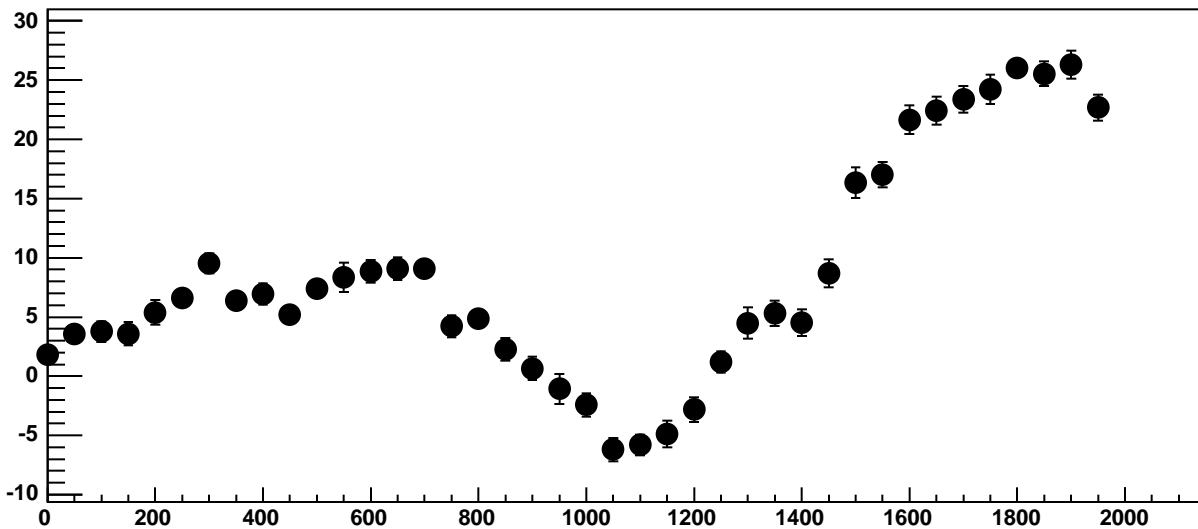


$\chi^2 / \text{ndf}$  214.6 / 11  
p0  $2.936e+04 \pm 1.606$   
p1  $-0.01196 \pm 0.001473$

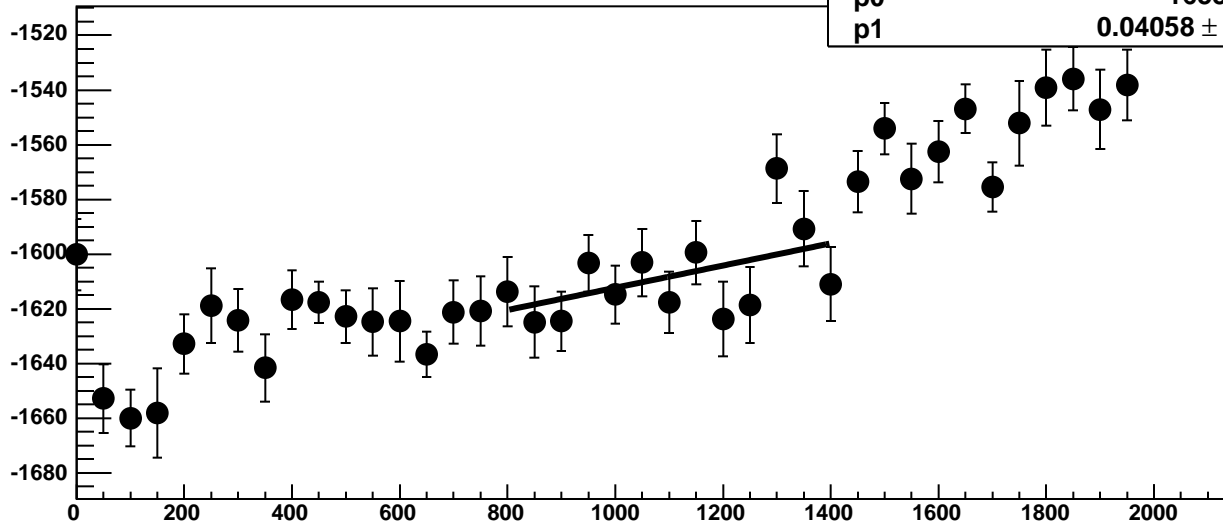
Chip 9, Channel 1, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 9, Channel 1, Enable 0!, Hold=30, ADC Residuals vs DAC

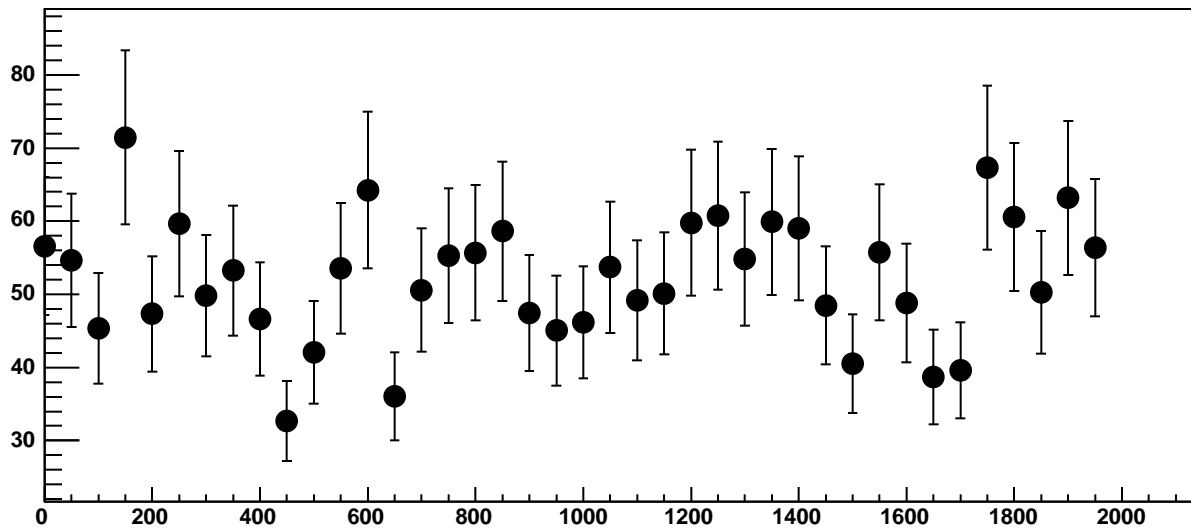


Chip 9, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC

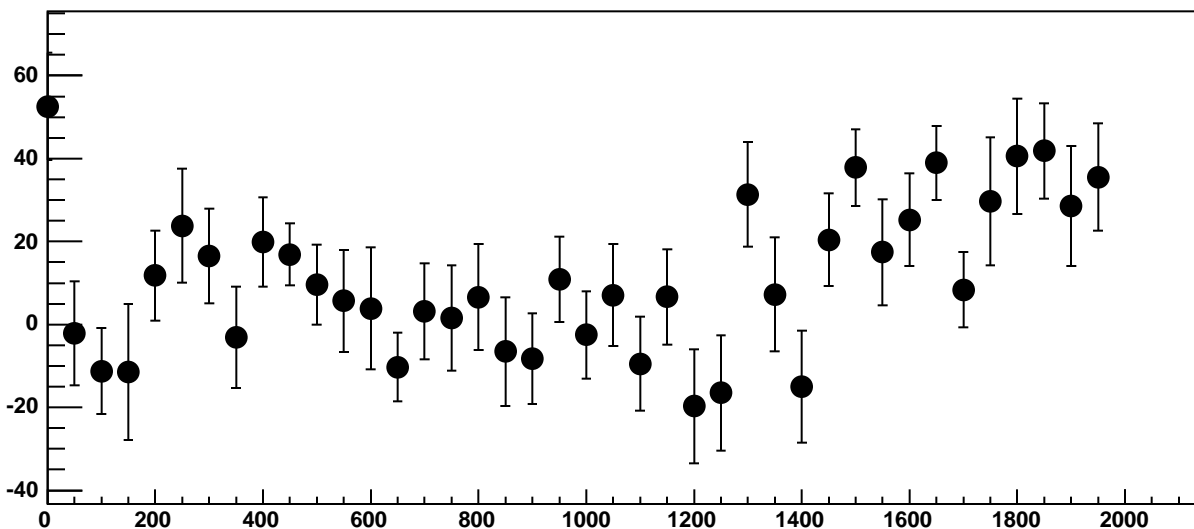


$\chi^2 / \text{ndf}$  14.8 / 11  
p0  $-1653 \pm 20.71$   
p1  $0.04058 \pm 0.01891$

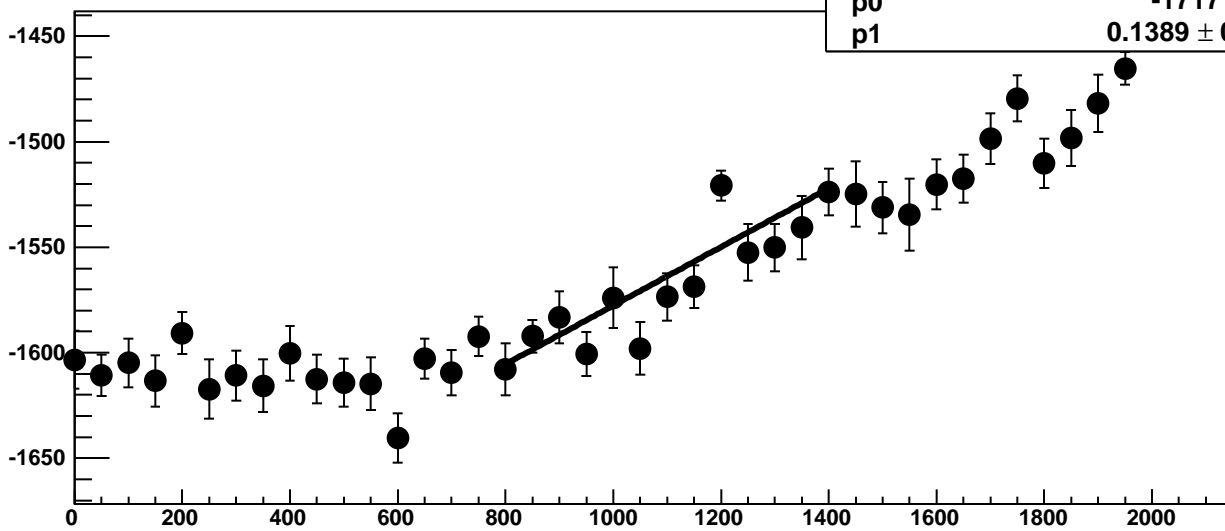
Chip 9, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC



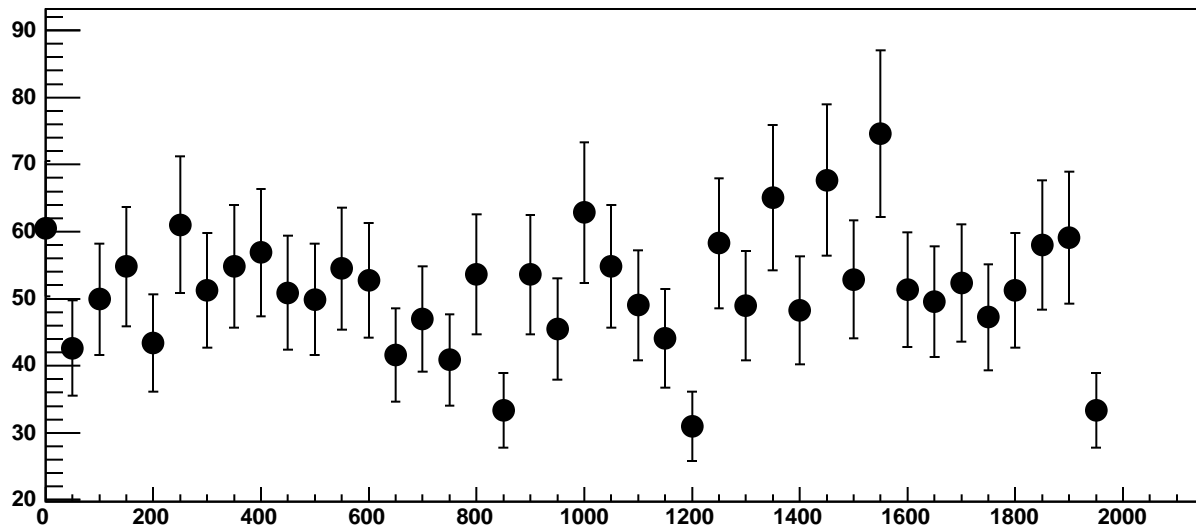
Chip 9, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC



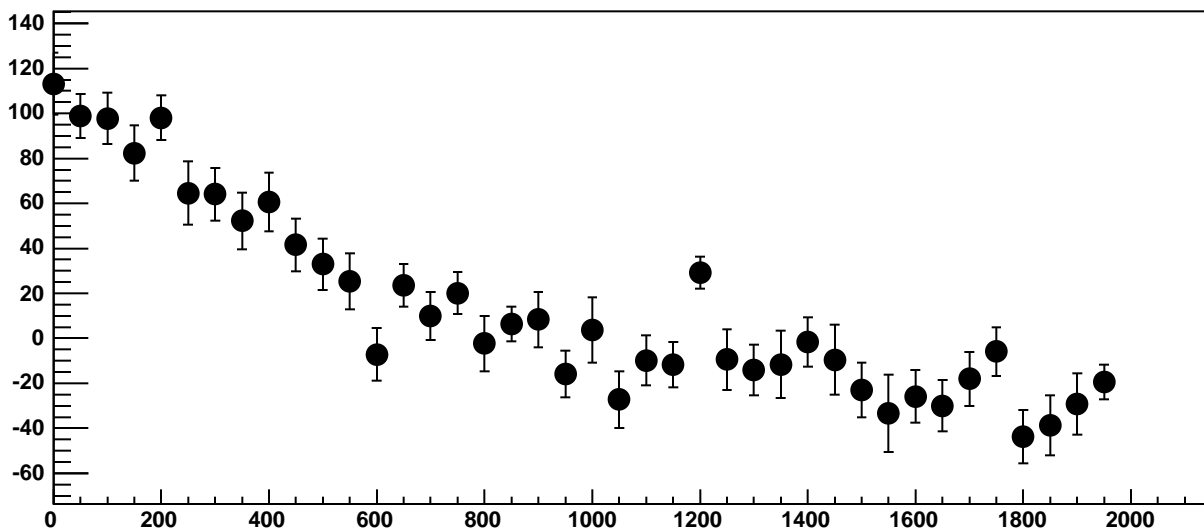
Chip 9, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC



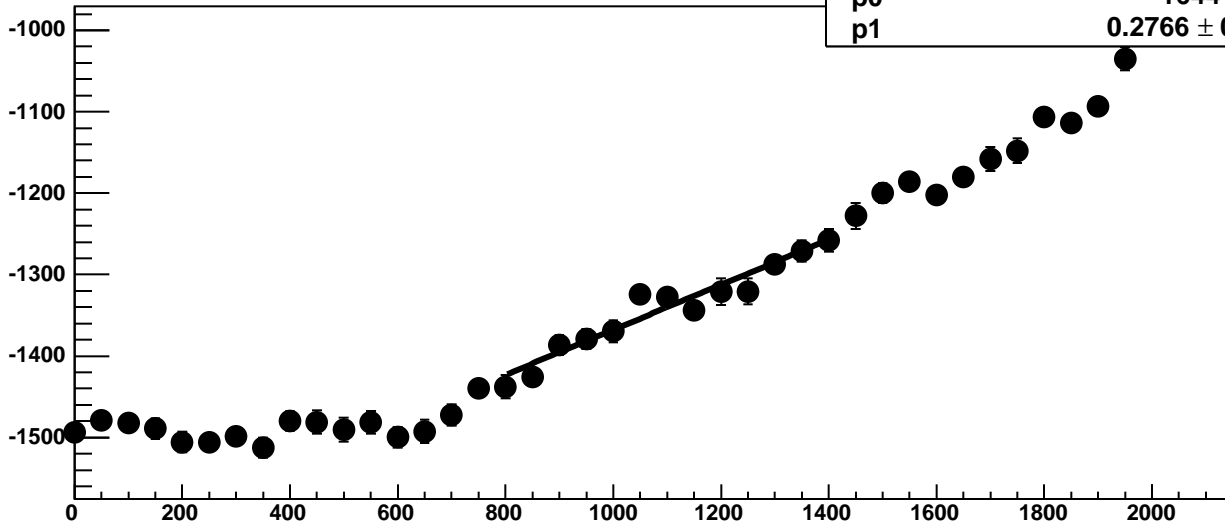
Chip 9, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC



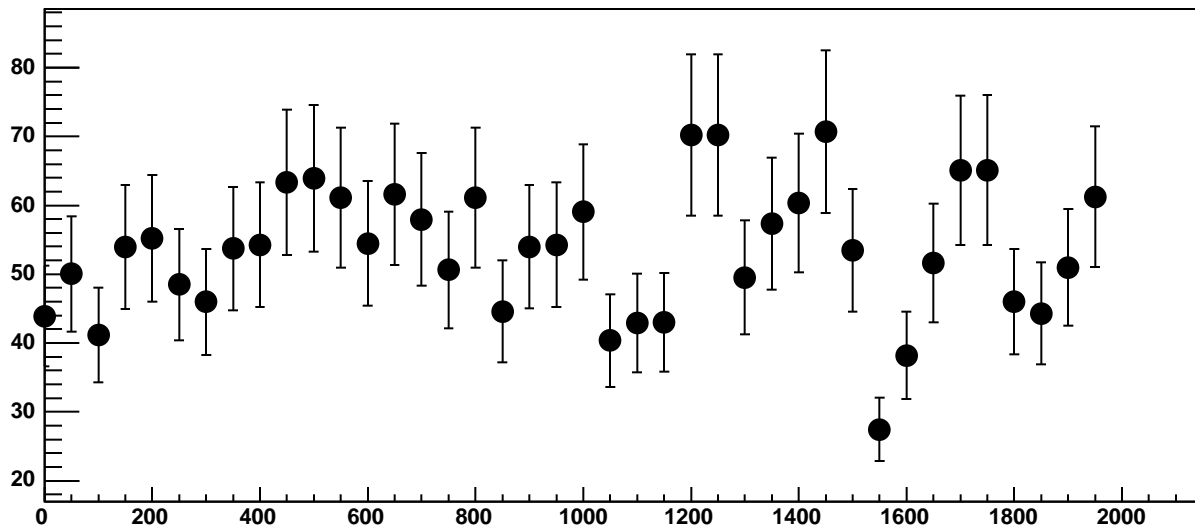
Chip 9, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC



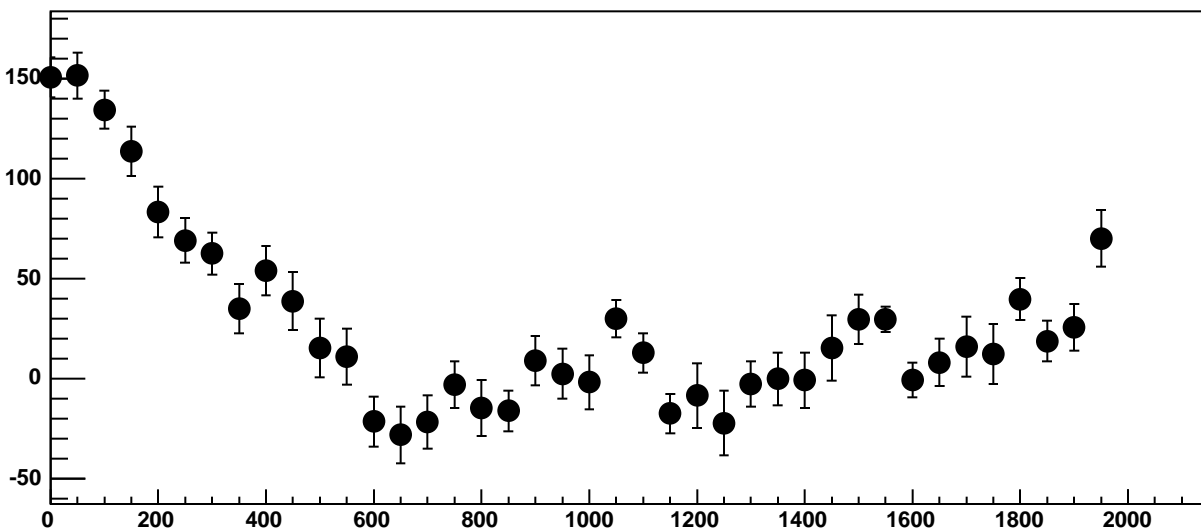
Chip 9, Channel 1, Enable 3, Hold=30, ADC Mean vs DAC



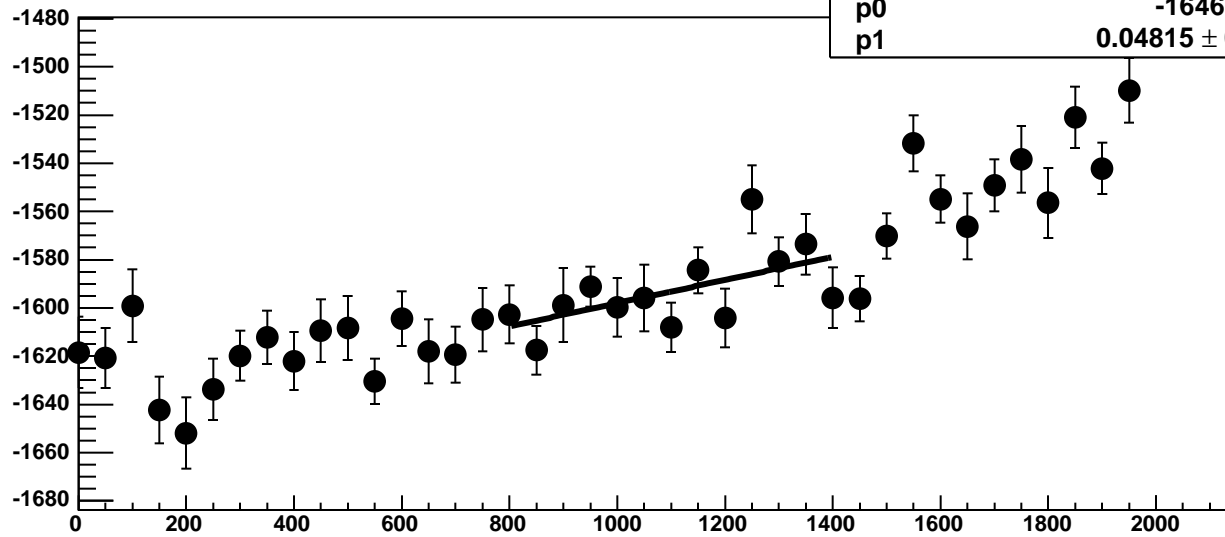
Chip 9, Channel 1, Enable 3, Hold=30, ADC Noise vs DAC



Chip 9, Channel 1, Enable 3, Hold=30, ADC Residuals vs DAC



Chip 9, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

14.3 / 11

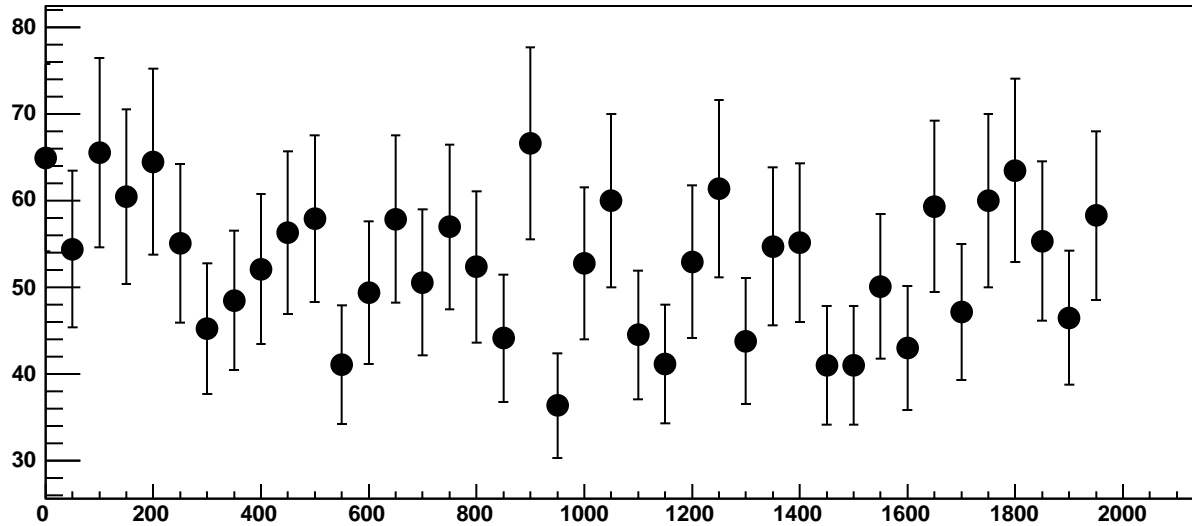
p0

$-1646 \pm 19.05$

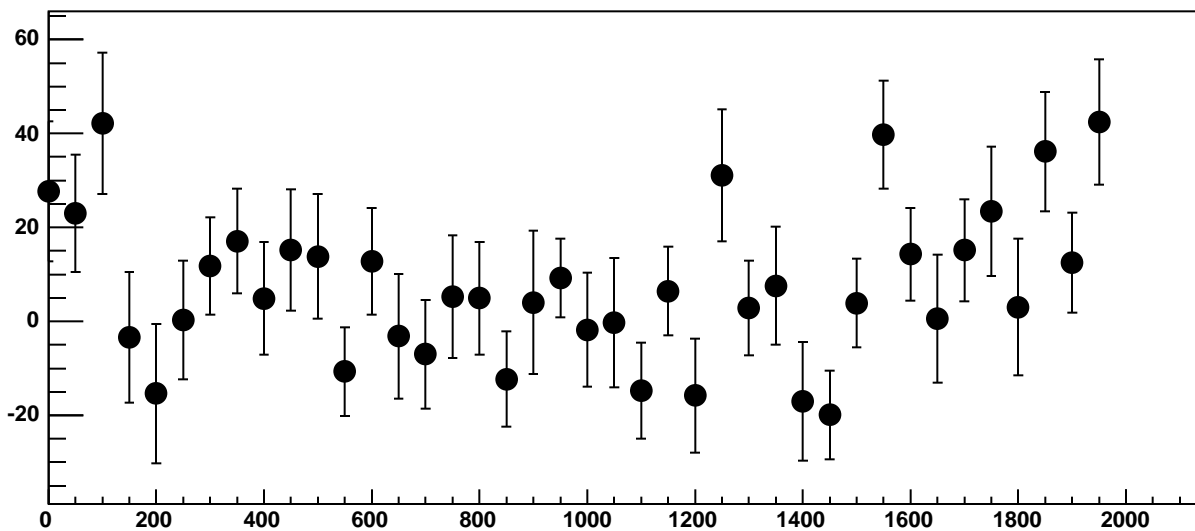
p1

$0.04815 \pm 0.01724$

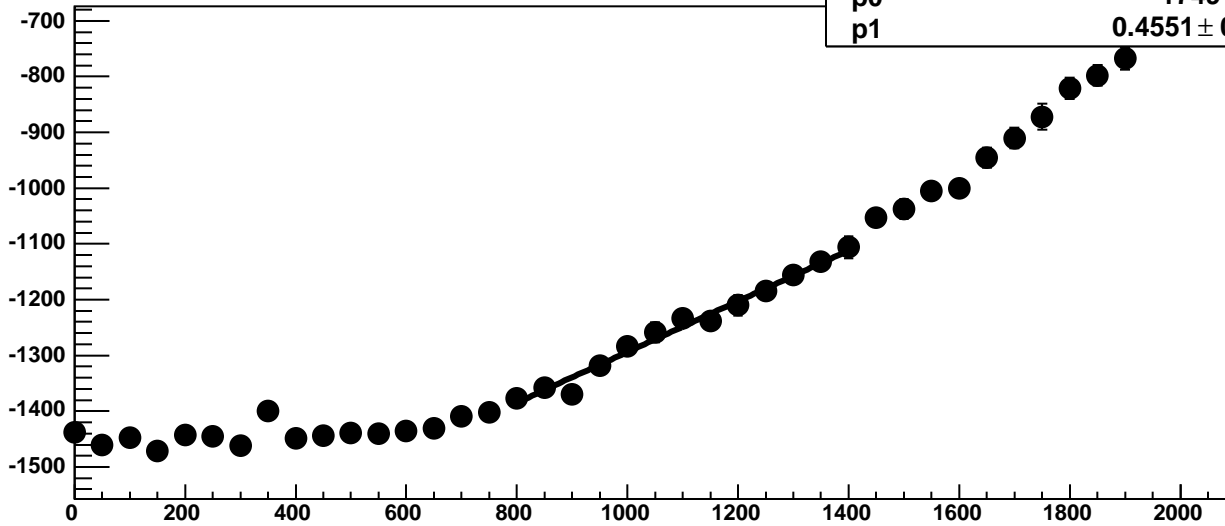
Chip 9, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC



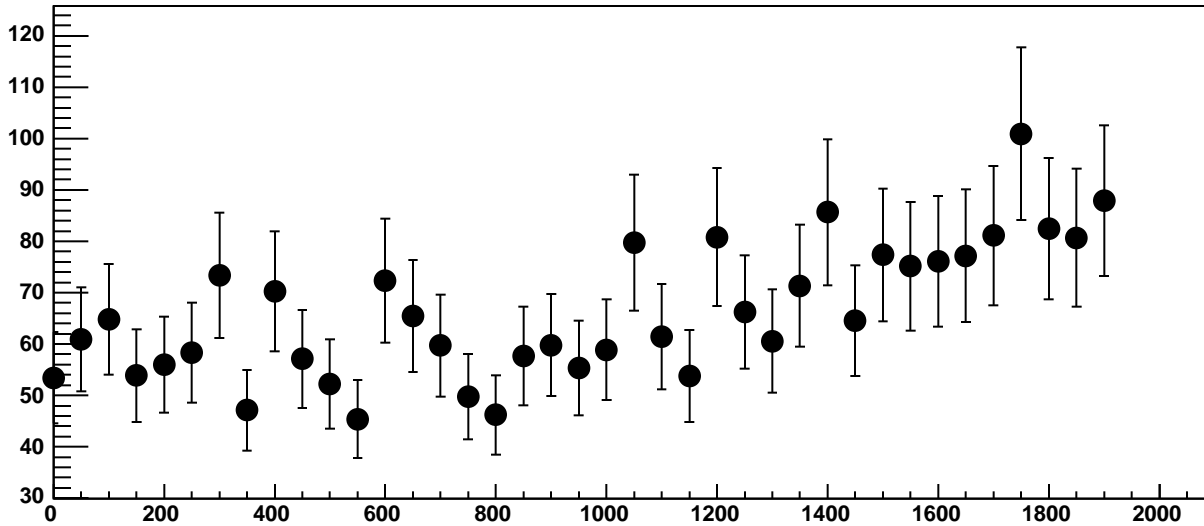
Chip 9, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC



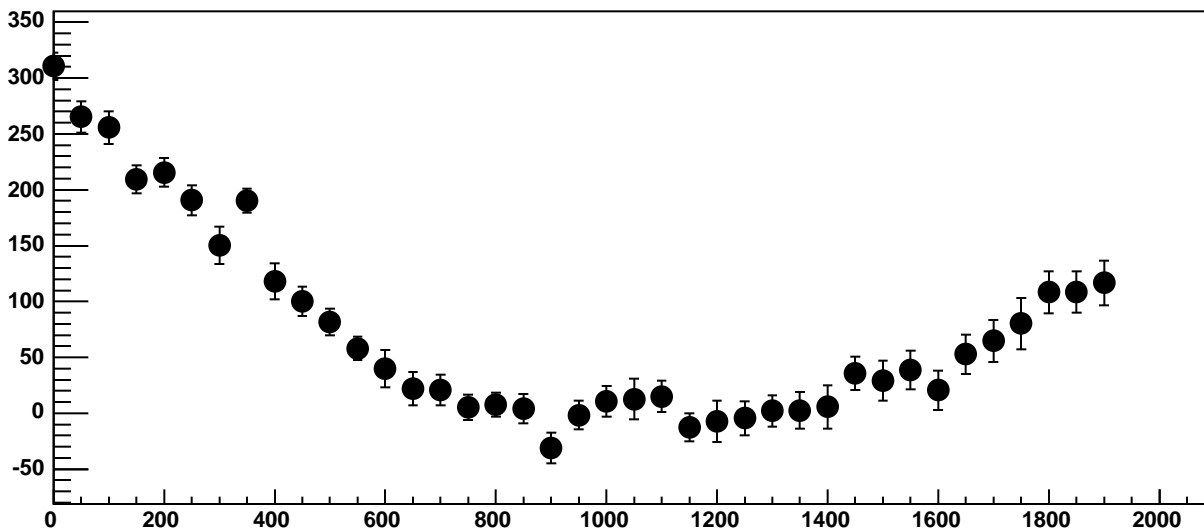
Chip 9, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC



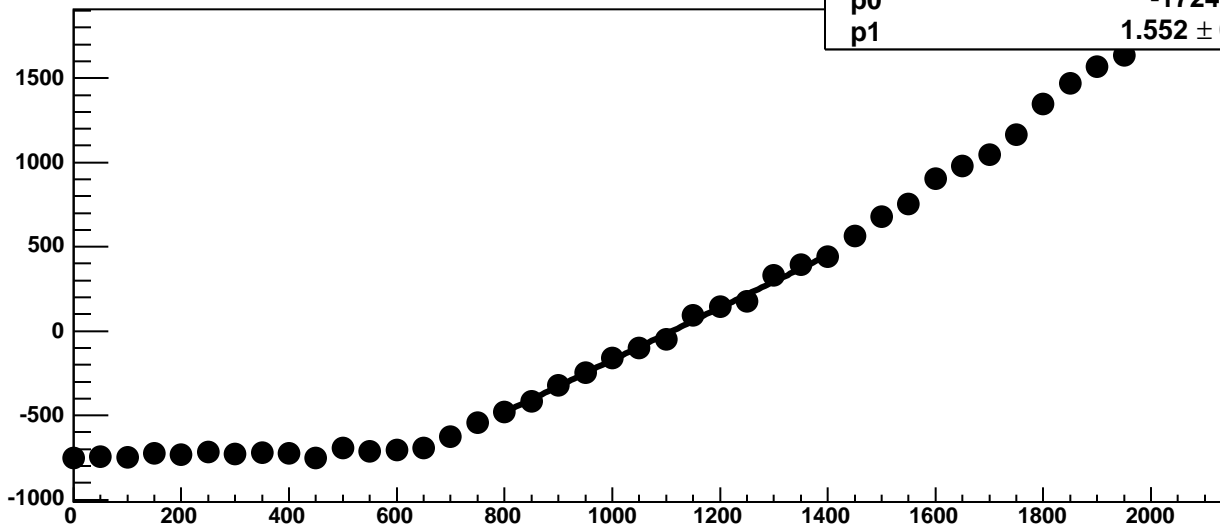
Chip 9, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC



Chip 9, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC

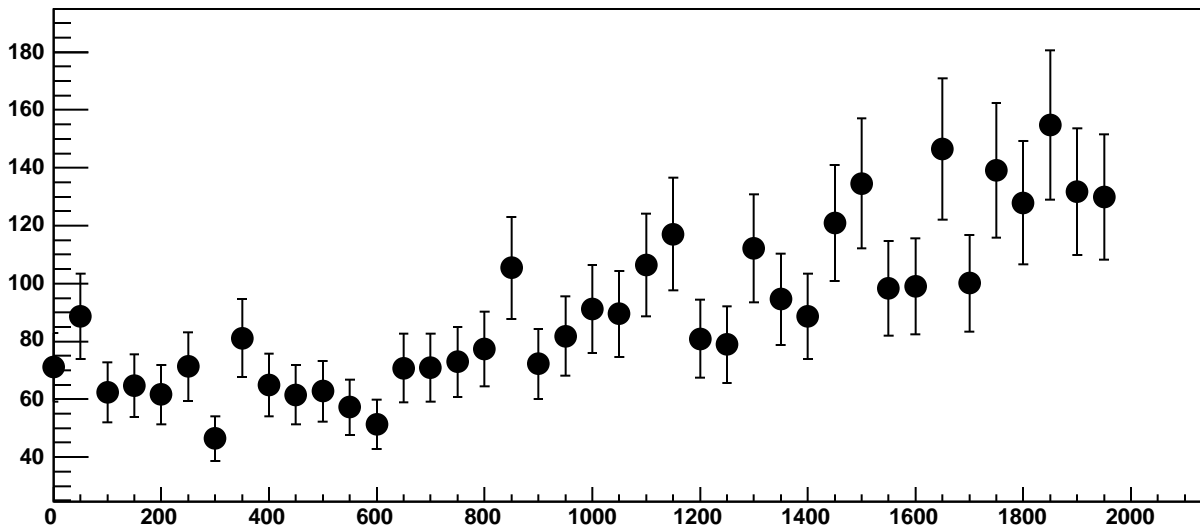


Chip 9, Channel 2, Enable 0, Hold=30, ADC Mean vs DAC

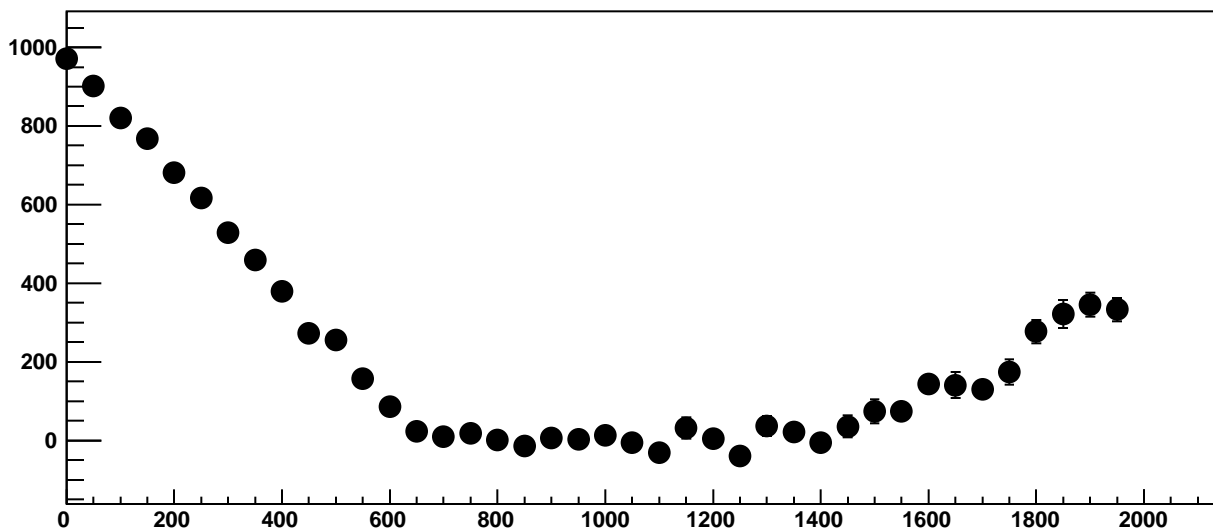


$\chi^2 / \text{ndf}$  11.97 / 11  
p0  $-1724 \pm 32.67$   
p1  $1.552 \pm 0.02969$

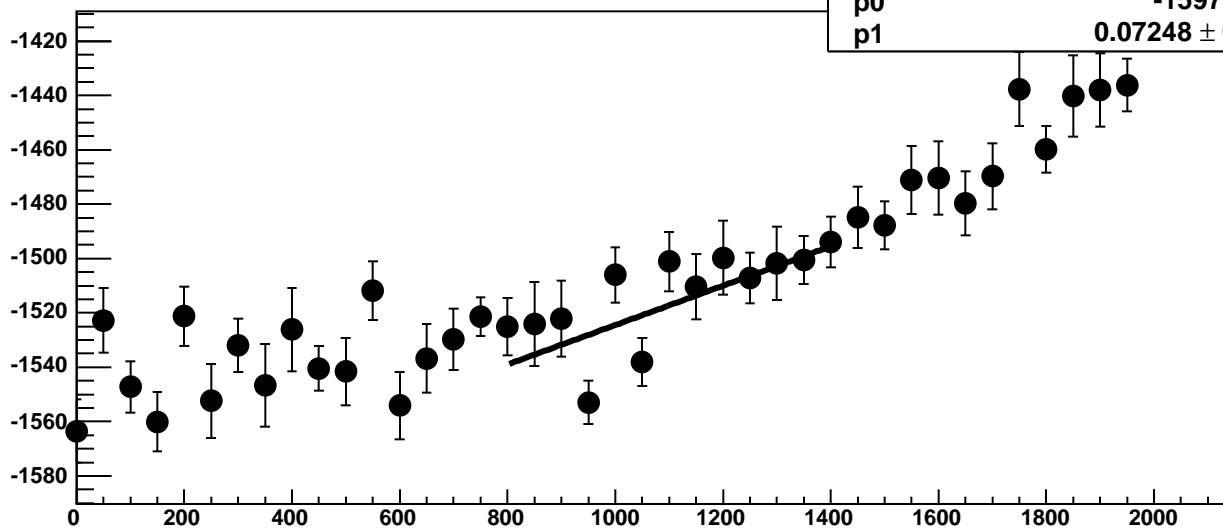
Chip 9, Channel 2, Enable 0, Hold=30, ADC Noise vs DAC



Chip 9, Channel 2, Enable 0, Hold=30, ADC Residuals vs DAC

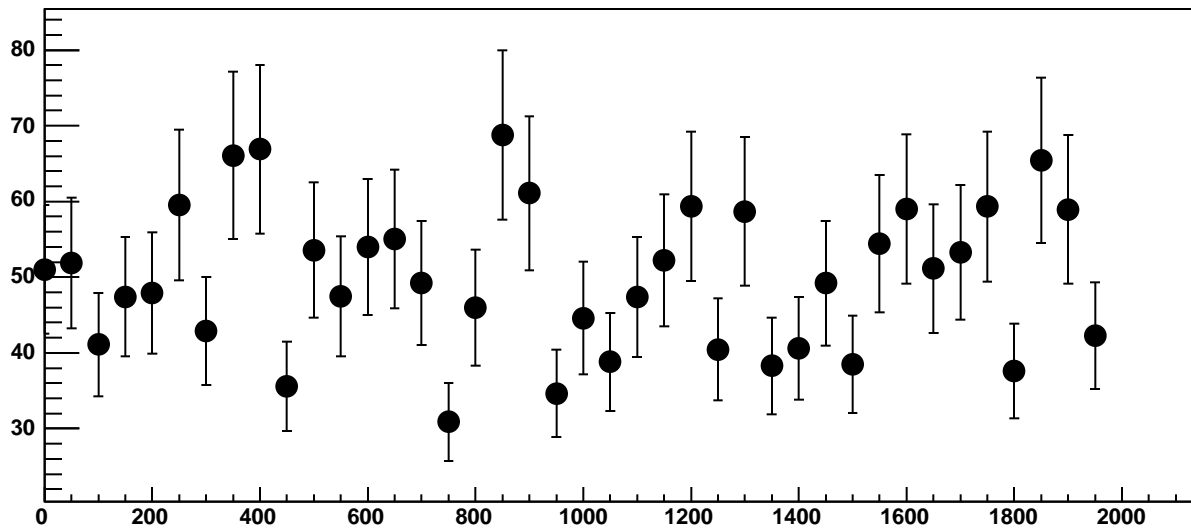


Chip 9, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC

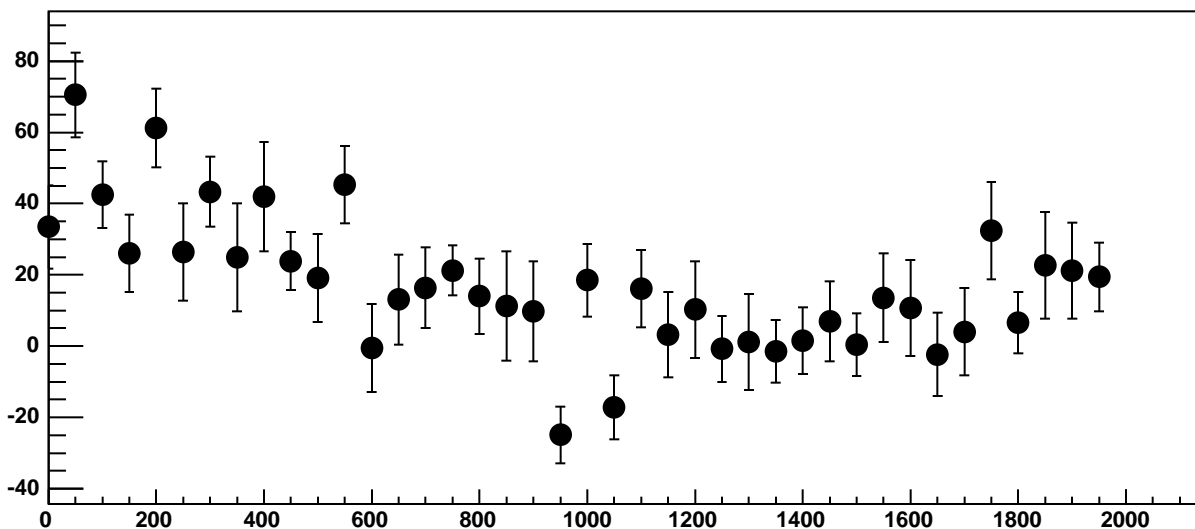


$\chi^2 / \text{ndf}$  22.48 / 11  
p0  $-1597 \pm 17.57$   
p1  $0.07248 \pm 0.01555$

Chip 9, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC

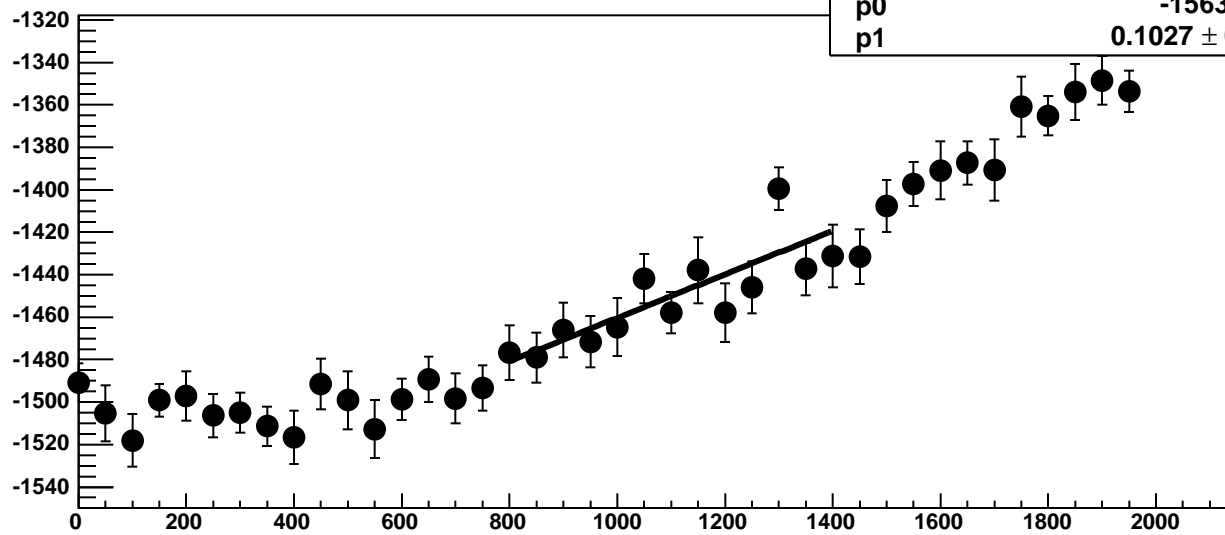


Chip 9, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC





Chip 9, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

16.16 / 11

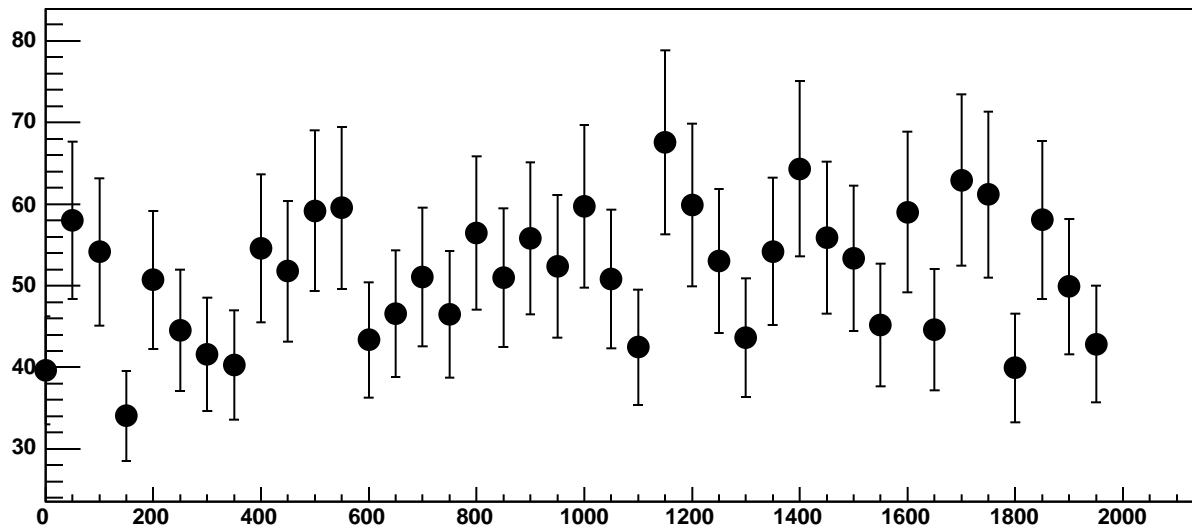
p0

$-1563 \pm 20.71$

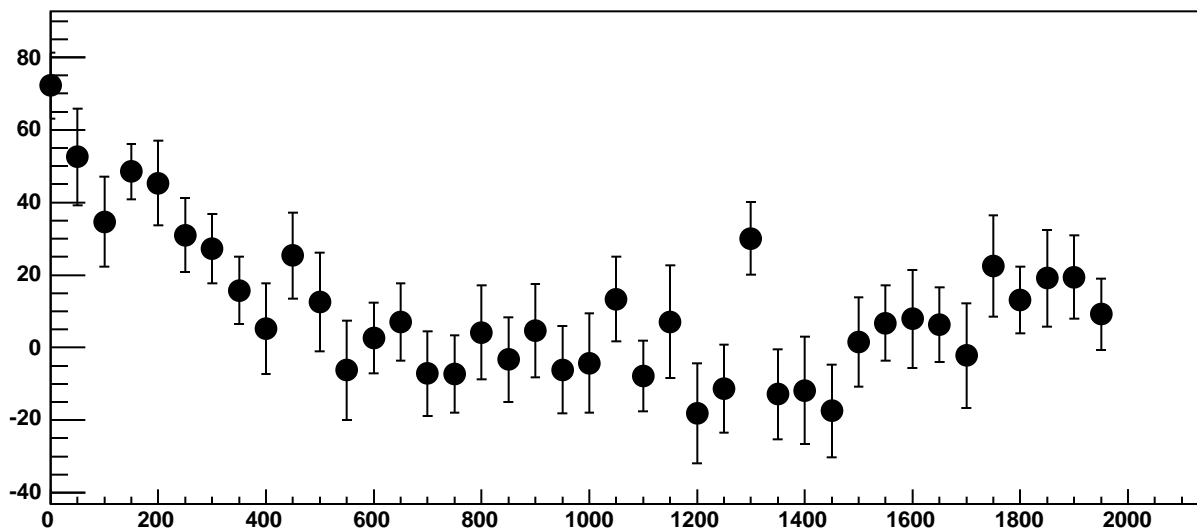
p1

$0.1027 \pm 0.01858$

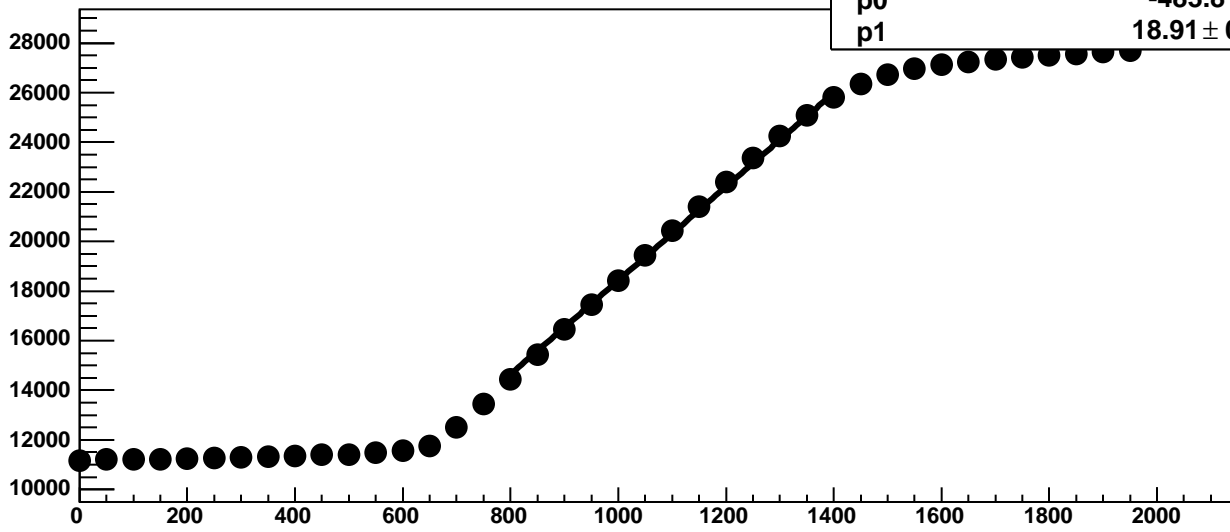
Chip 9, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC



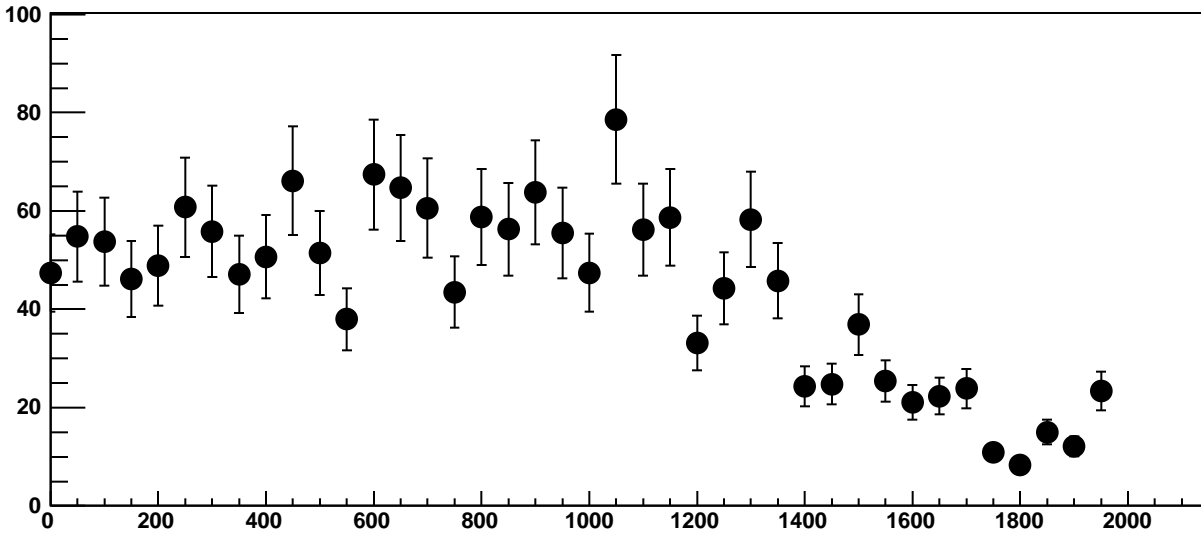
Chip 9, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC



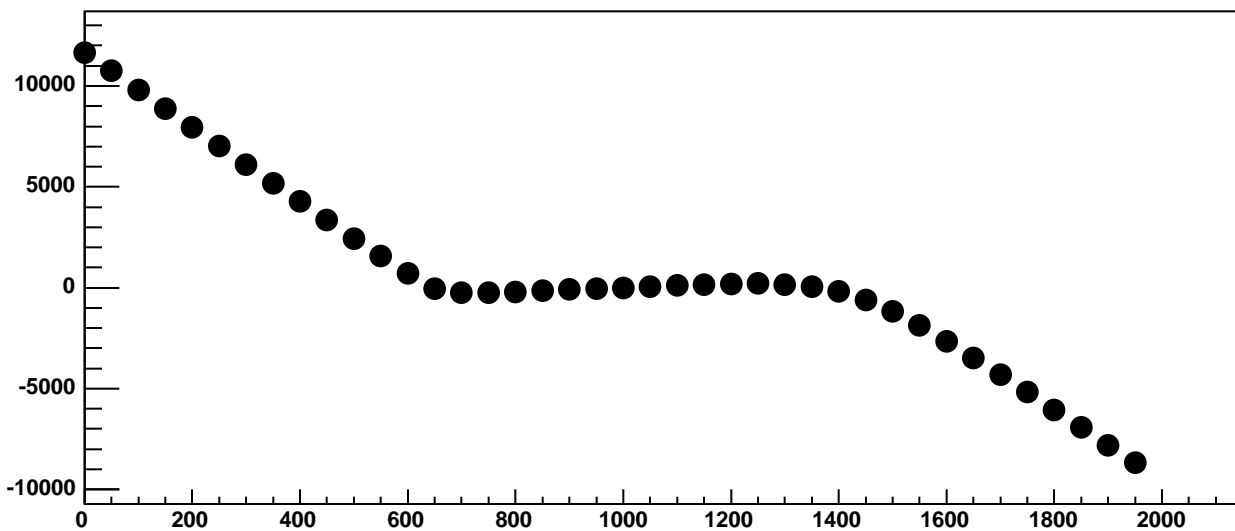
Chip 9, Channel 2, Enable 3!, Hold=30, ADC Mean vs DAC



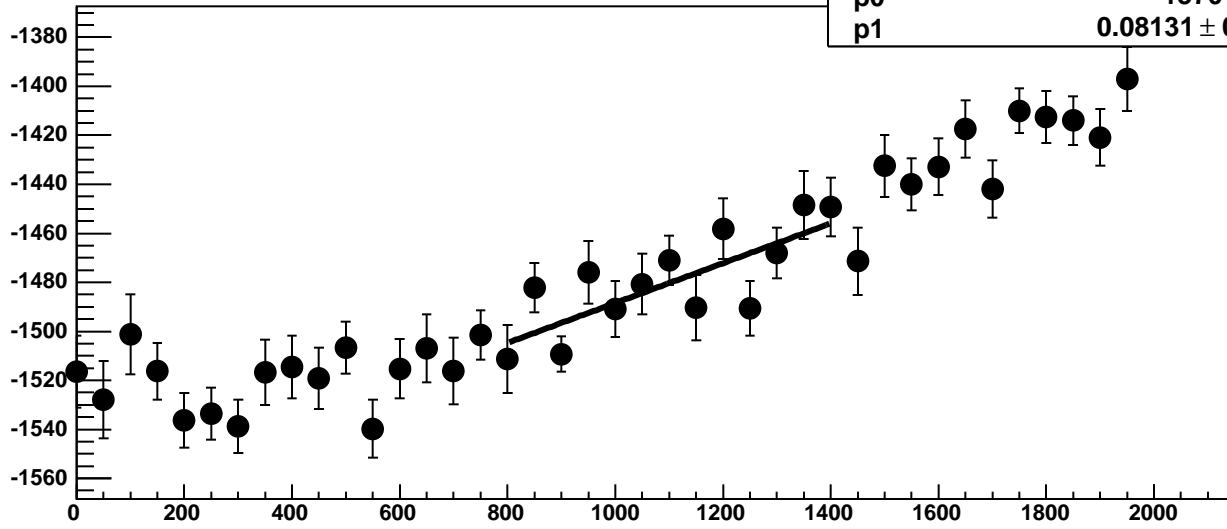
Chip 9, Channel 2, Enable 3!, Hold=30, ADC Noise vs DAC



Chip 9, Channel 2, Enable 3!, Hold=30, ADC Residuals vs DAC

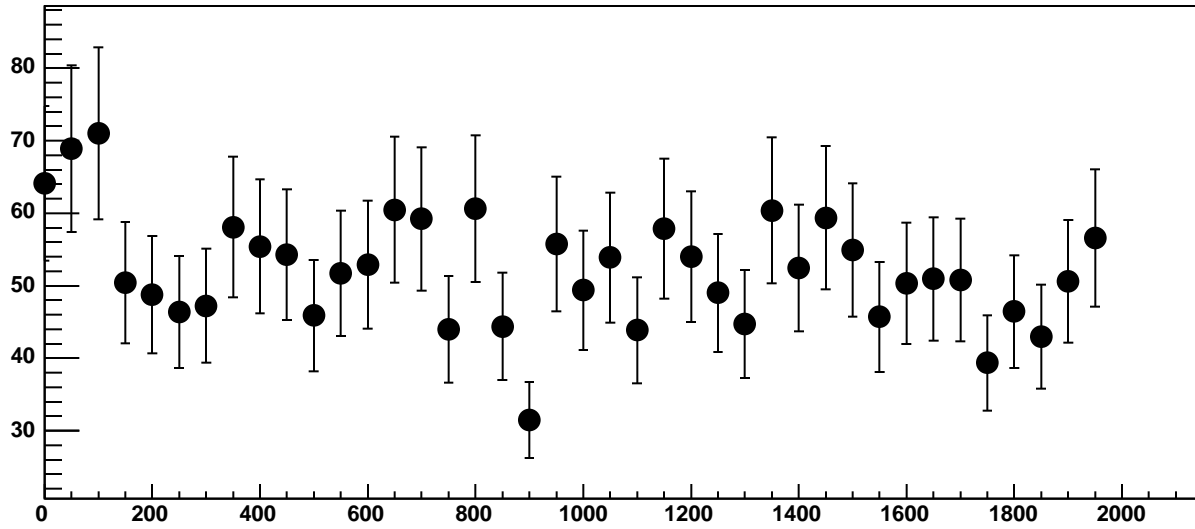


Chip 9, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC

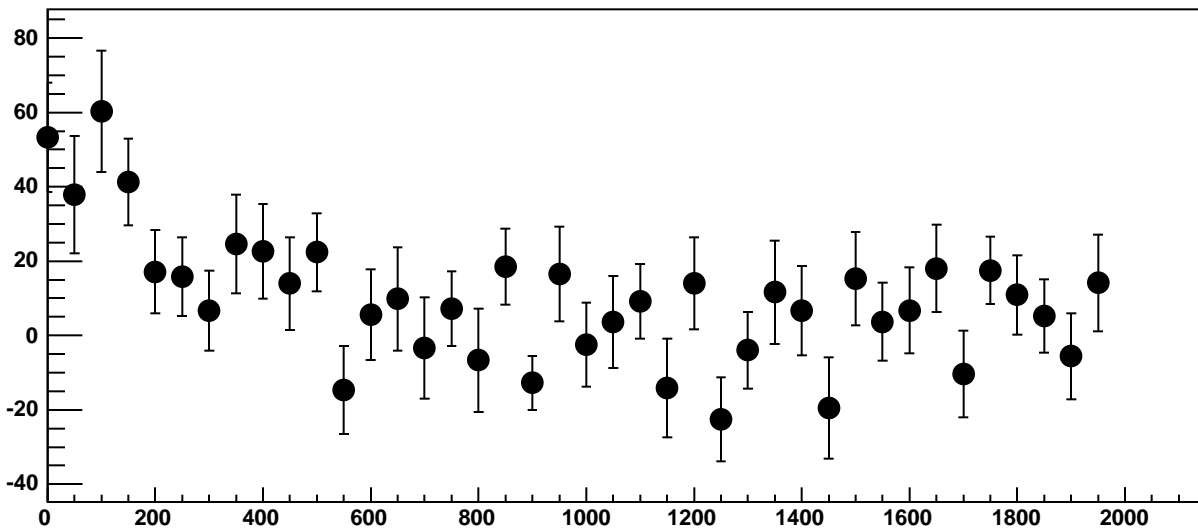


$\chi^2 / \text{ndf}$  16.87 / 11  
p0  $-1570 \pm 18.22$   
p1  $0.08131 \pm 0.01668$

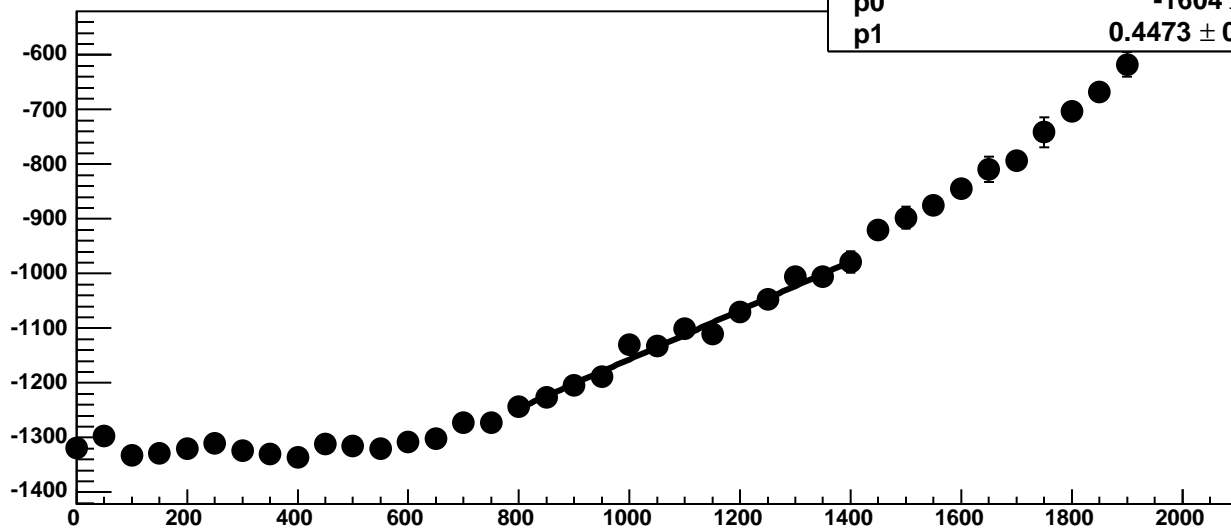
Chip 9, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC



Chip 9, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC

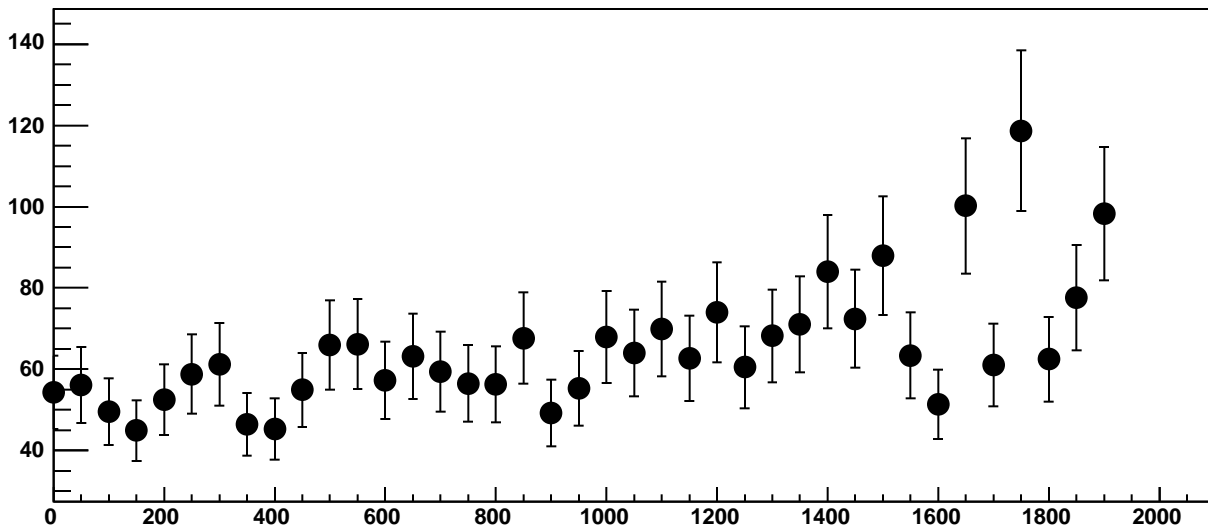


Chip 9, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC

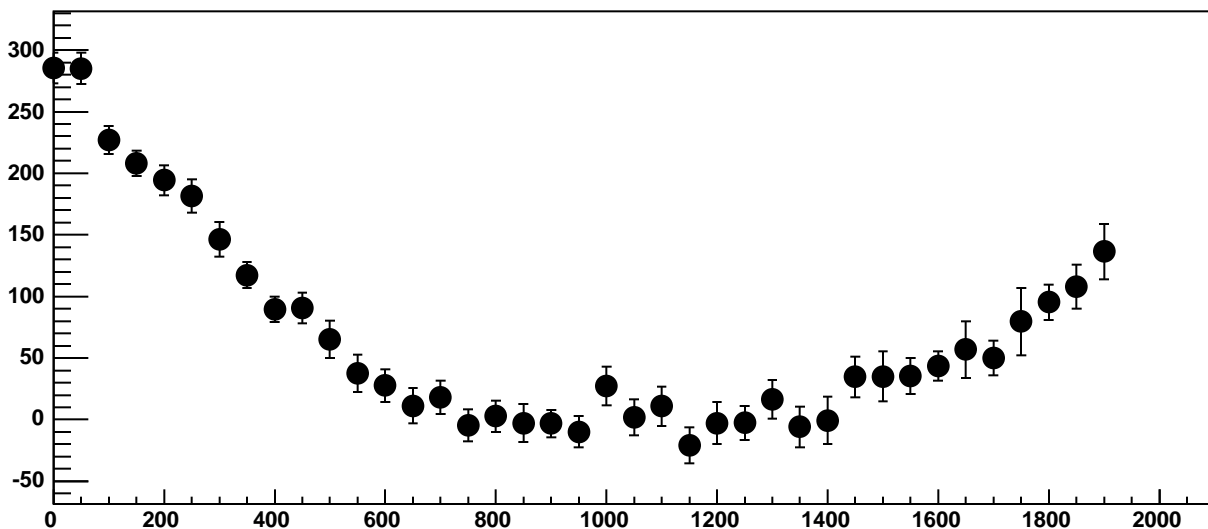


$\chi^2 / \text{ndf}$  7.694 / 11  
p0  $-1604 \pm 23.94$   
p1  $0.4473 \pm 0.02212$

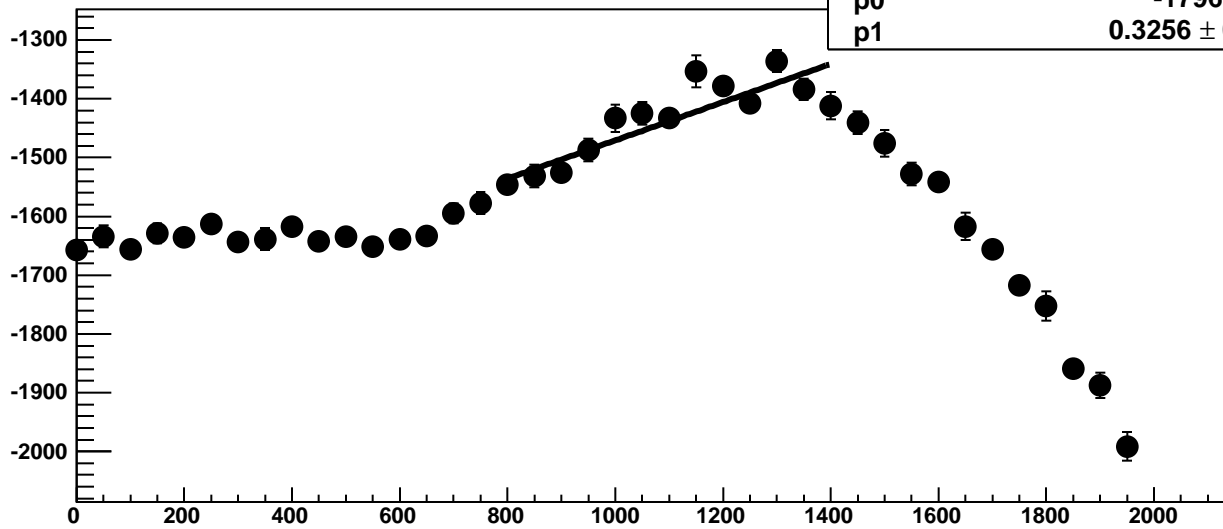
Chip 9, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC



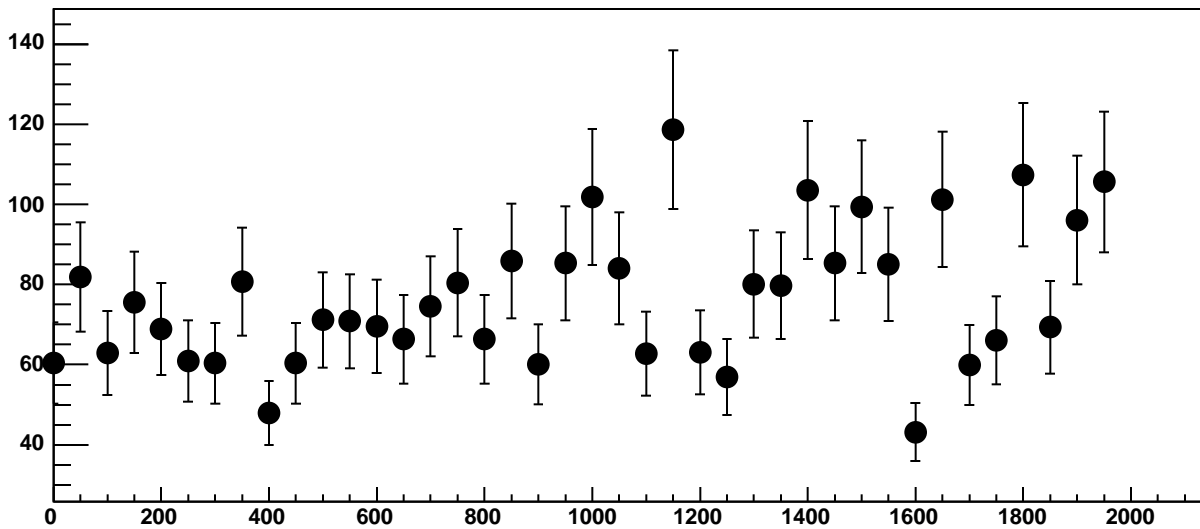
Chip 9, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC



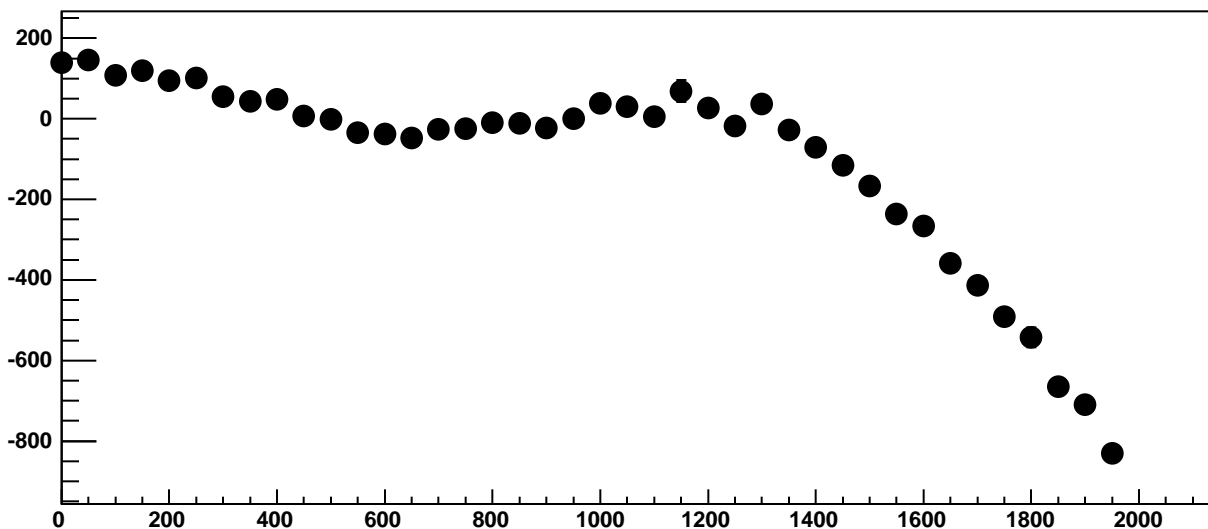
Chip 9, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC



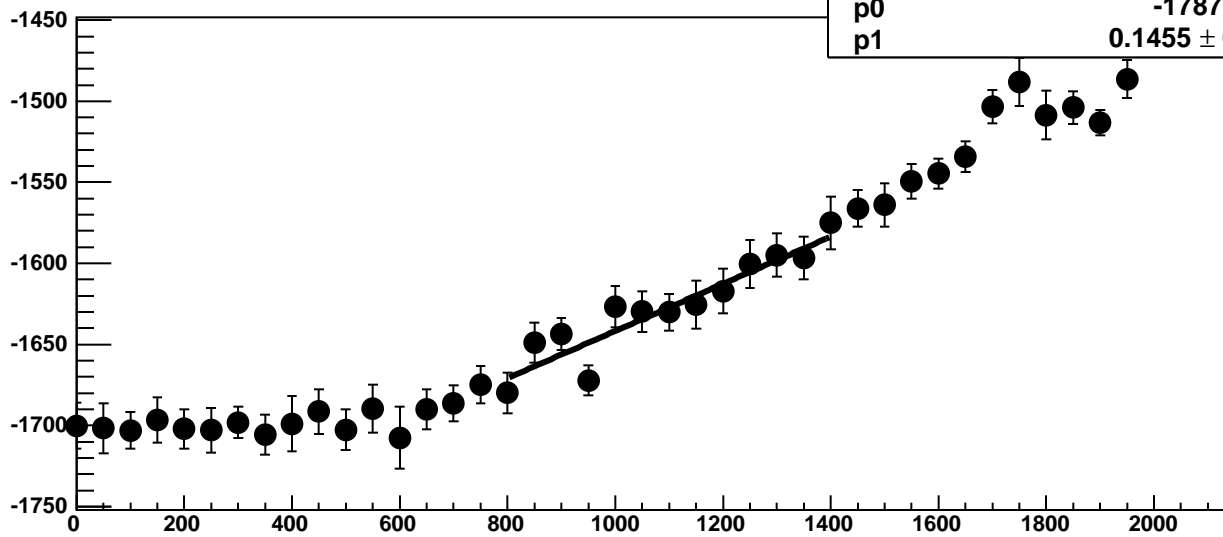
Chip 9, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC



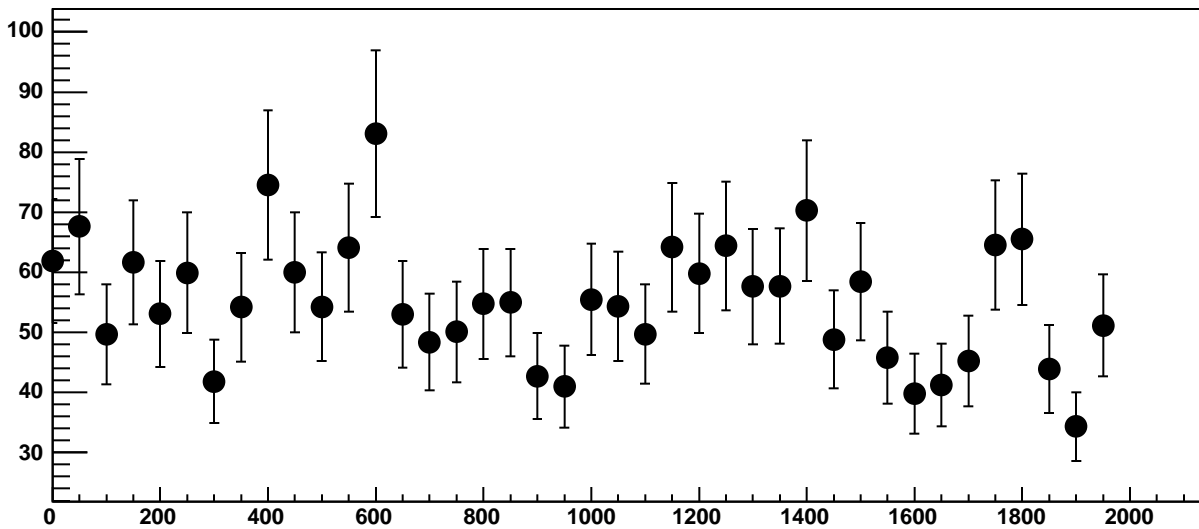
Chip 9, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC



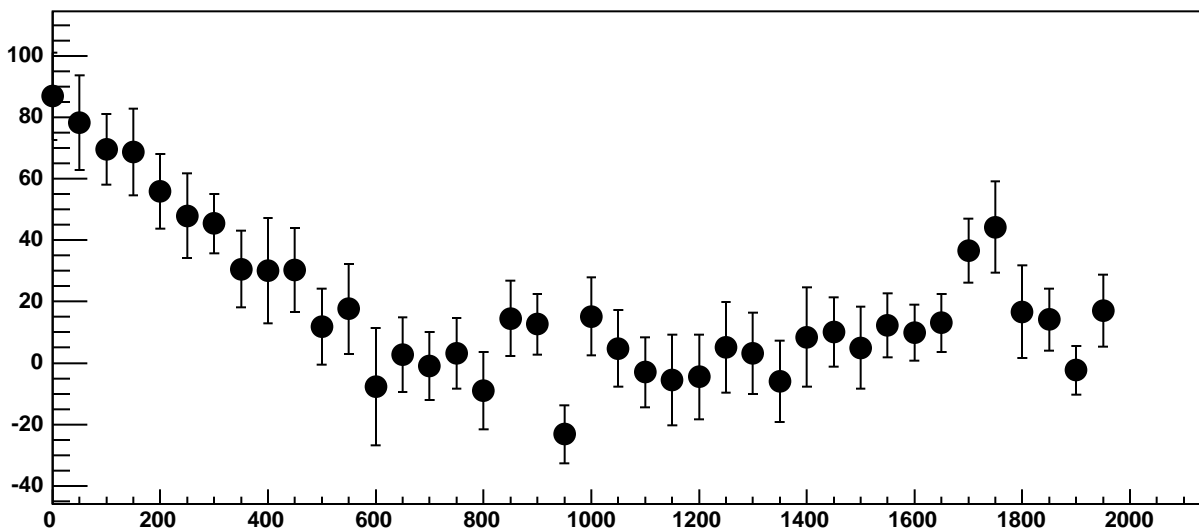
Chip 9, Channel 3, Enable 1, Hold=30, ADC Mean vs DAC



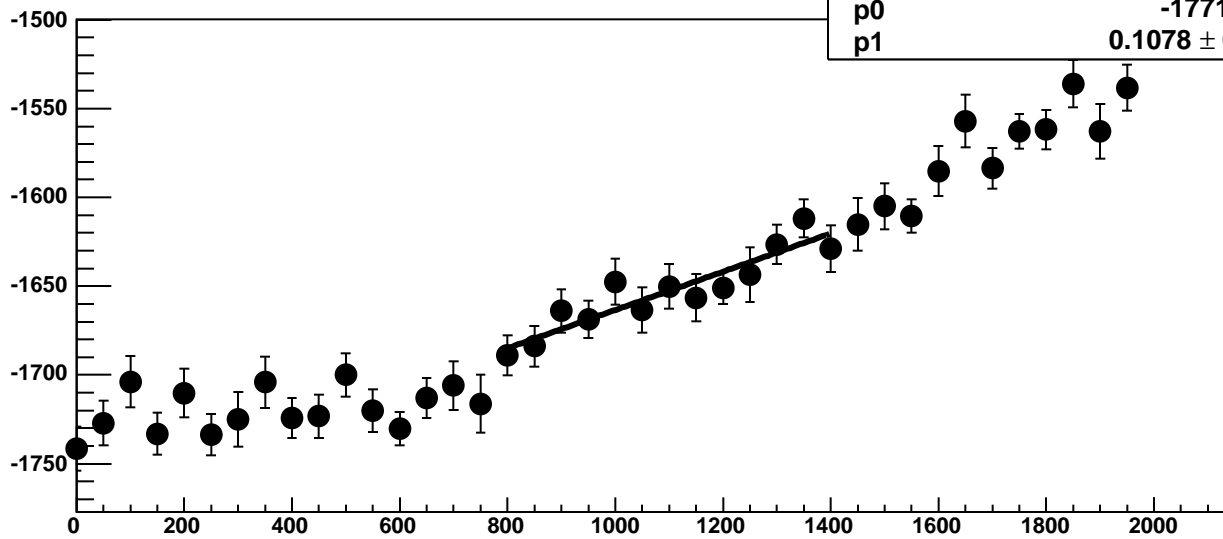
Chip 9, Channel 3, Enable 1, Hold=30, ADC Noise vs DAC



Chip 9, Channel 3, Enable 1, Hold=30, ADC Residuals vs DAC



Chip 9, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

6.729 / 11

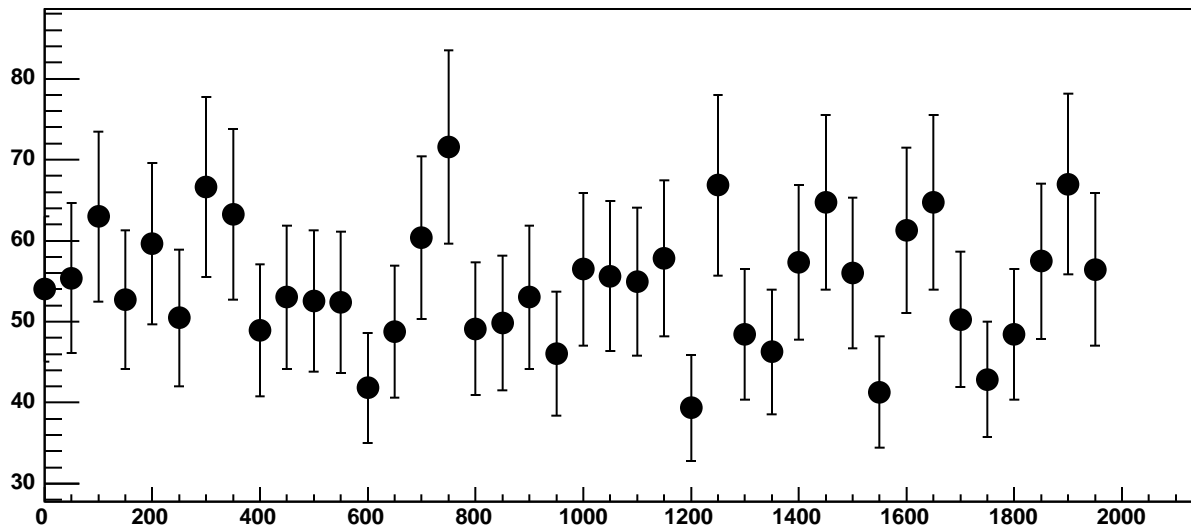
p0

$-1771 \pm 19.21$

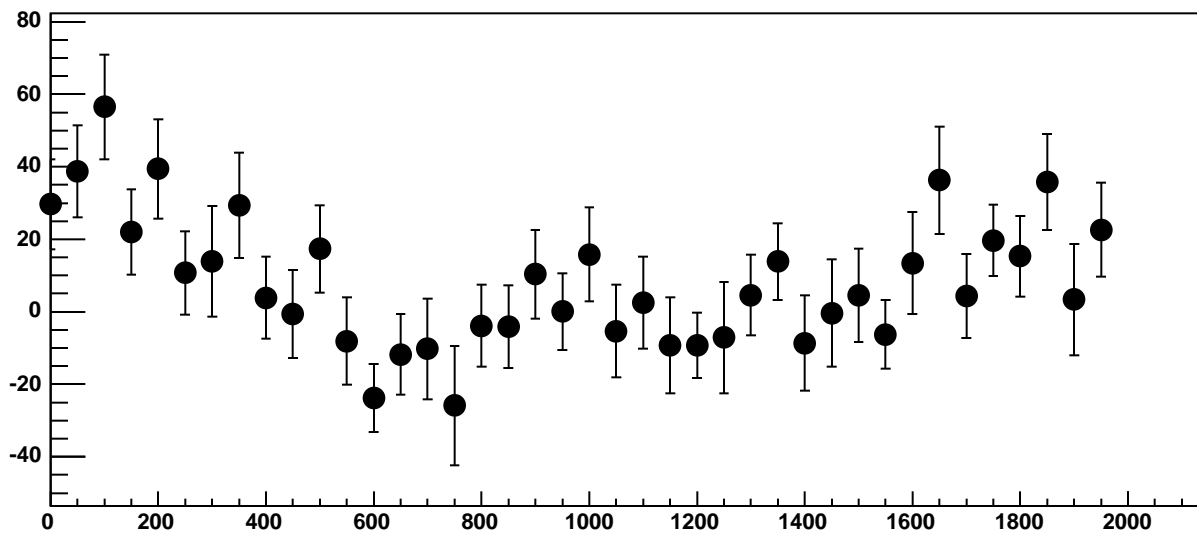
p1

$0.1078 \pm 0.01724$

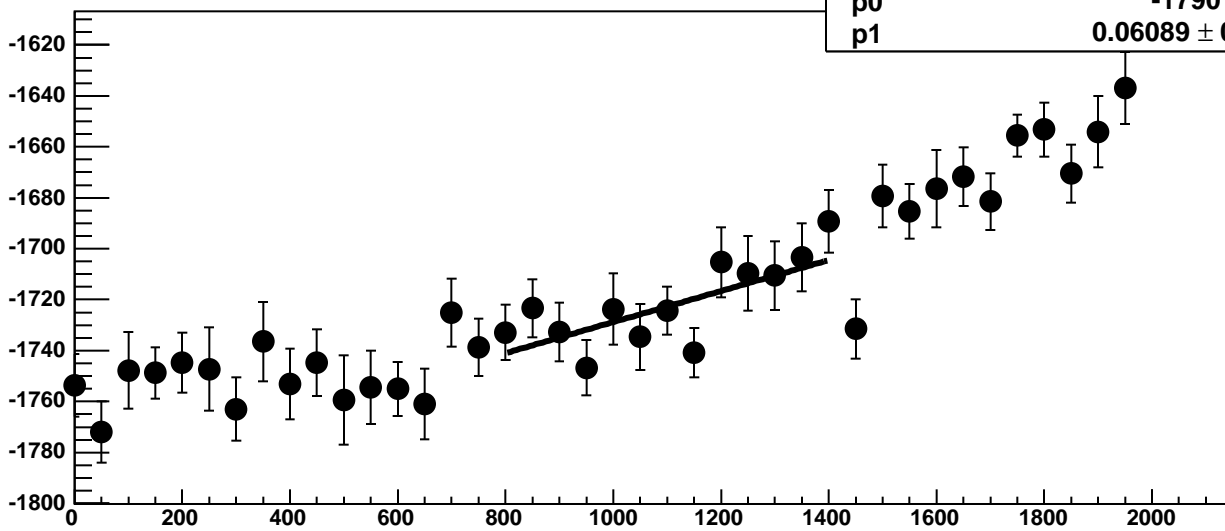
Chip 9, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC



Chip 9, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC

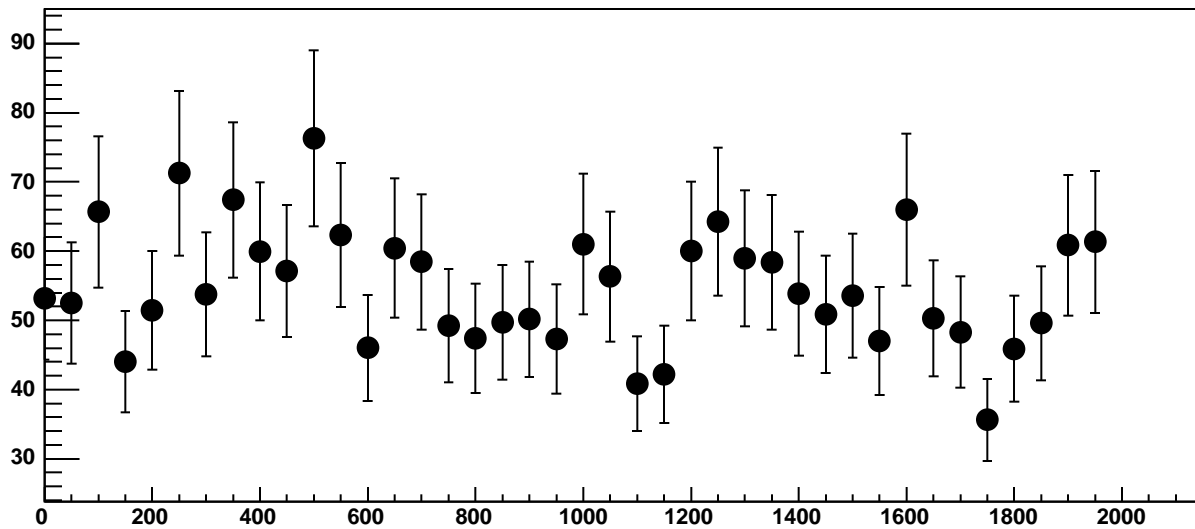


Chip 9, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC

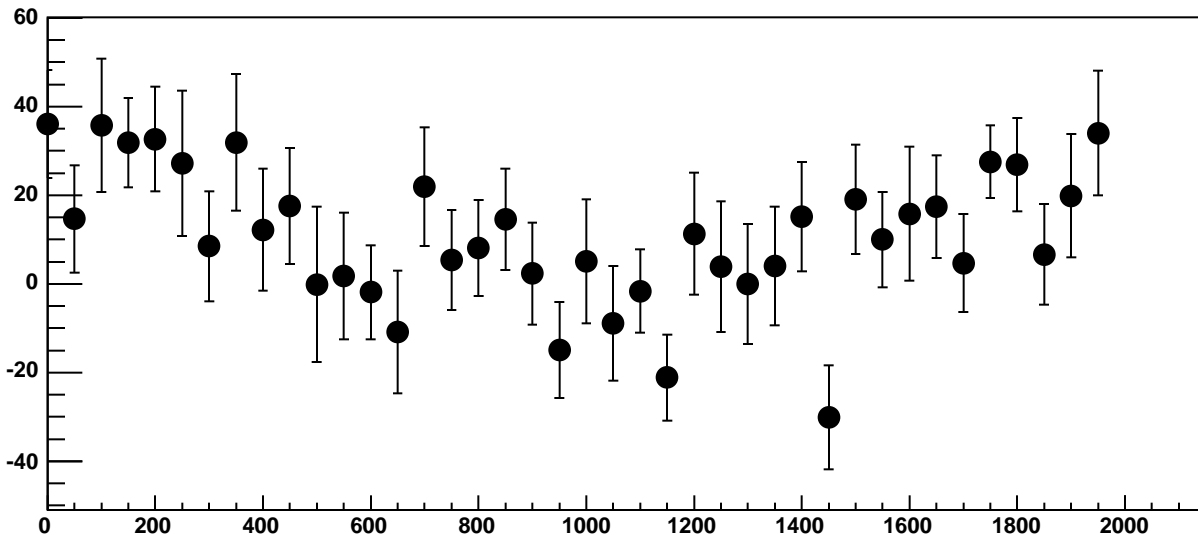


$\chi^2 / \text{ndf}$  11.87 / 11  
p0  $-1790 \pm 19.67$   
p1  $0.06089 \pm 0.01795$

Chip 9, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC

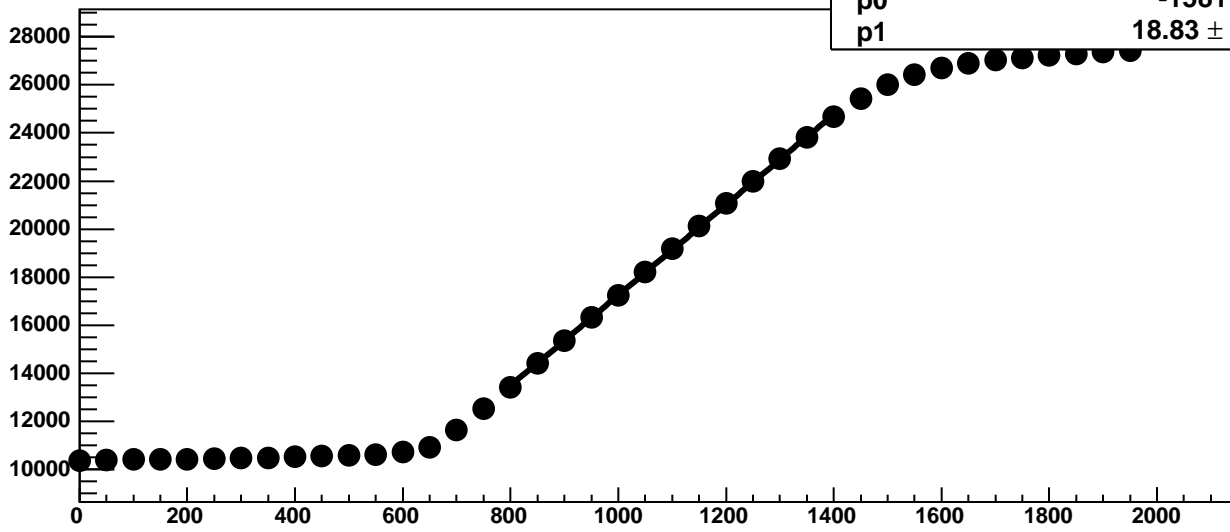


Chip 9, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC

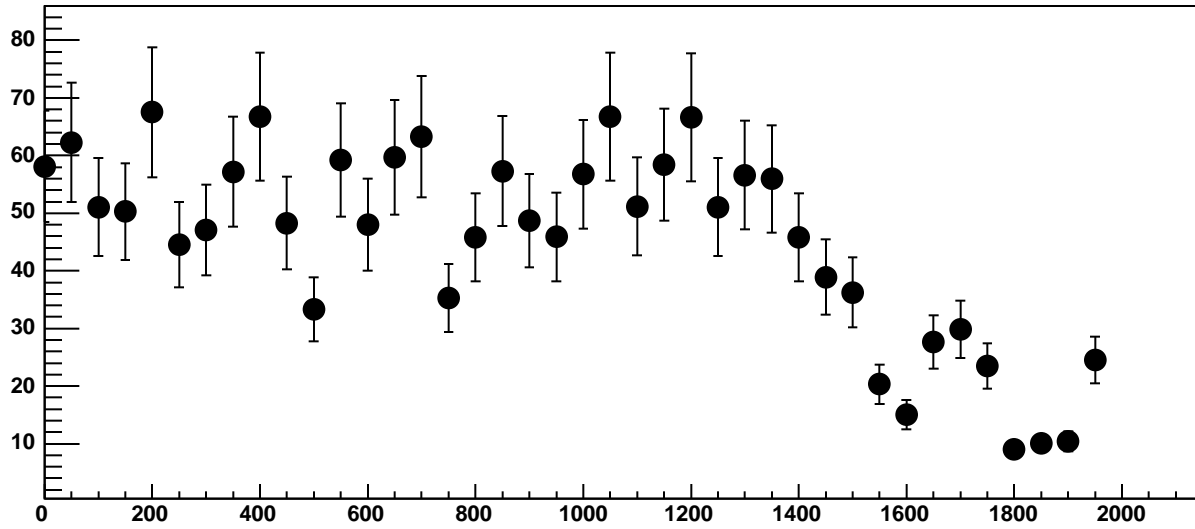




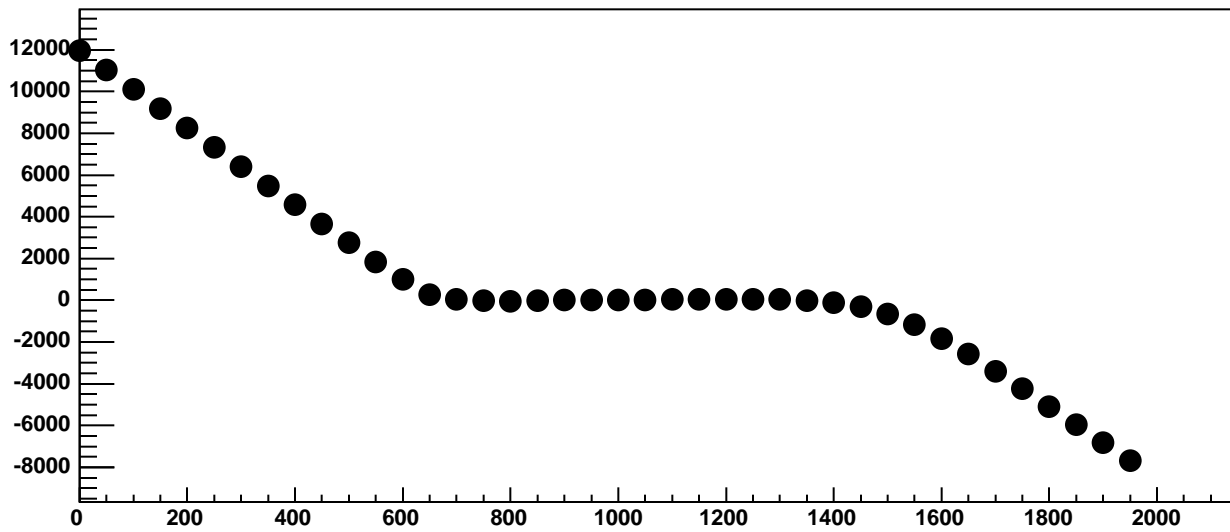
Chip 9, Channel 3, Enable 4!, Hold=30, ADC Mean vs DAC



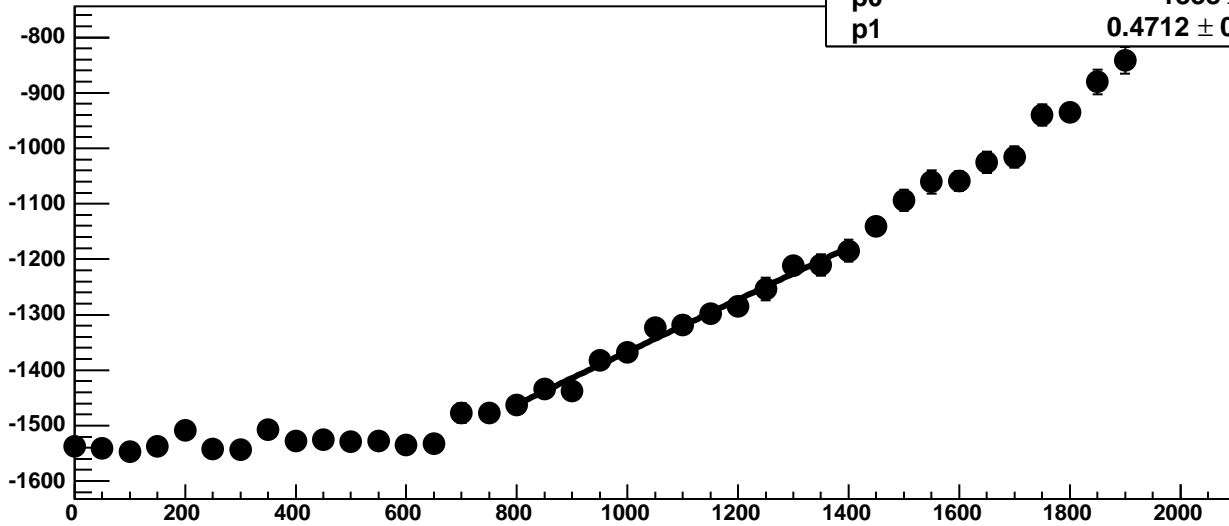
Chip 9, Channel 3, Enable 4!, Hold=30, ADC Noise vs DAC



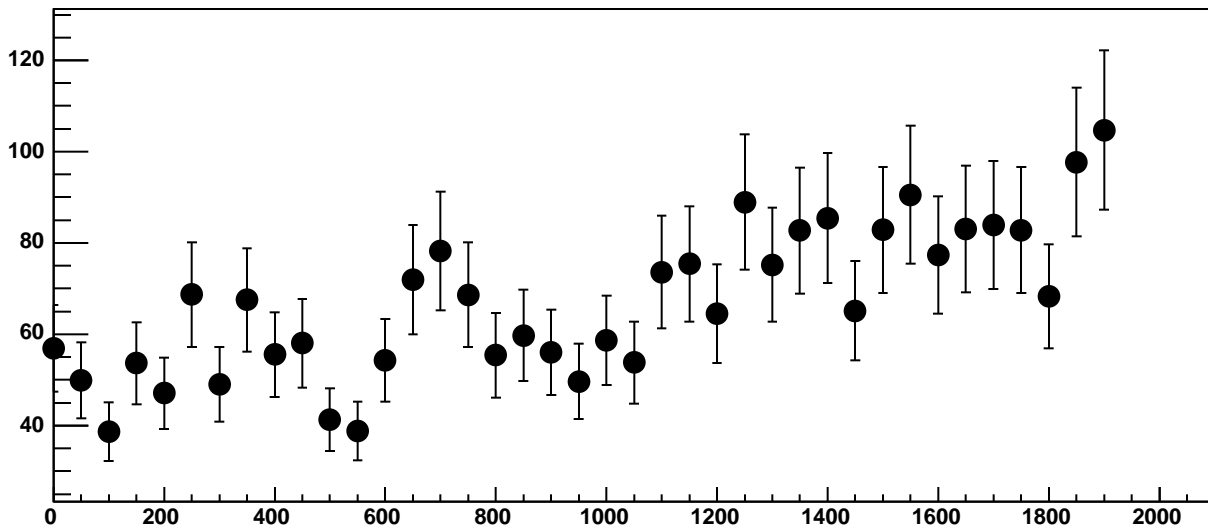
Chip 9, Channel 3, Enable 4!, Hold=30, ADC Residuals vs DAC



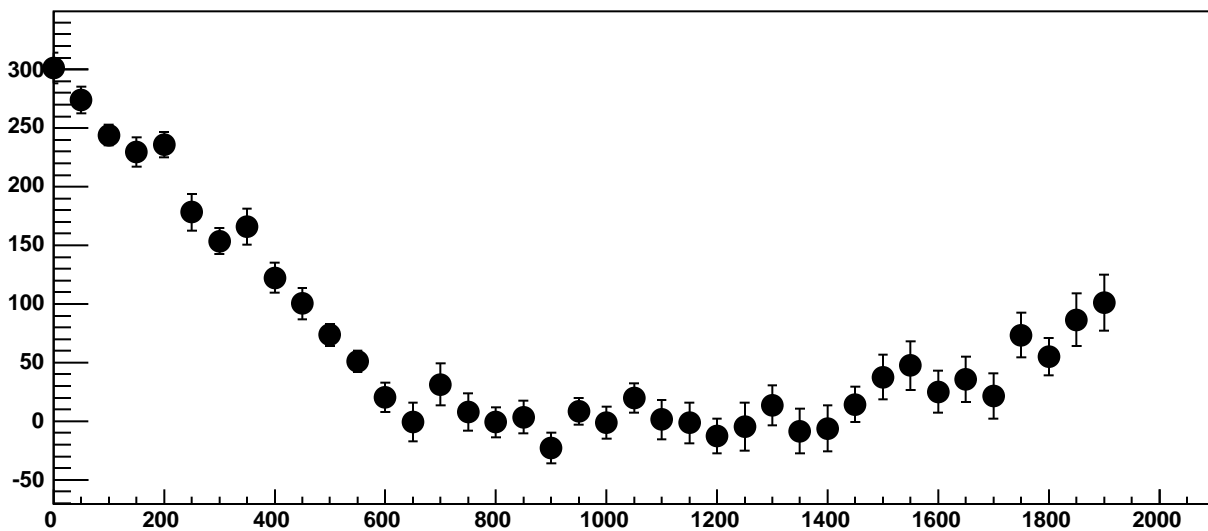
Chip 9, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC



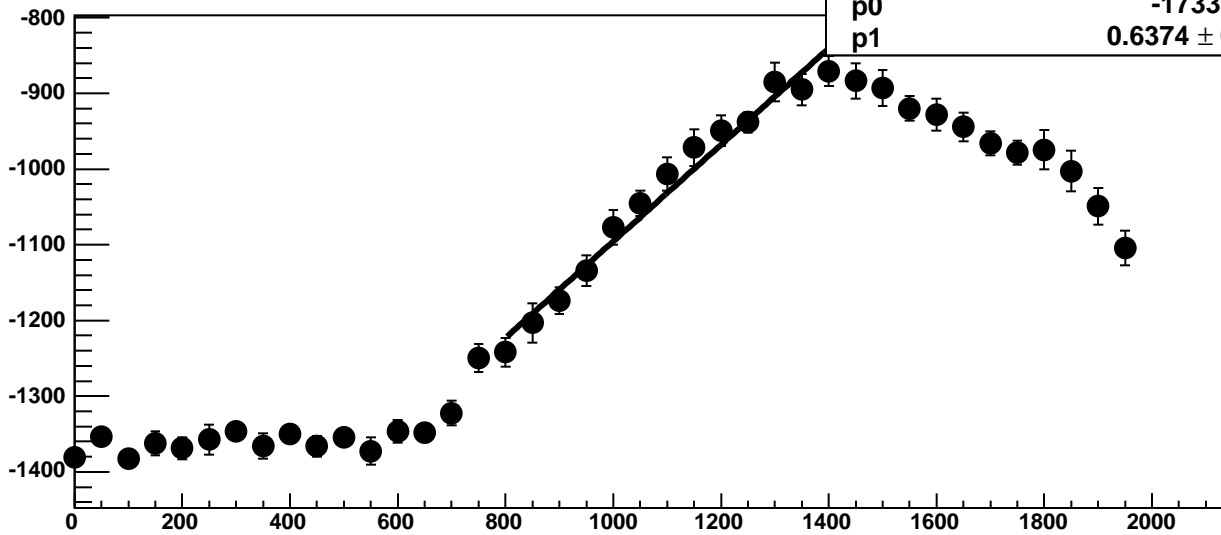
Chip 9, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC



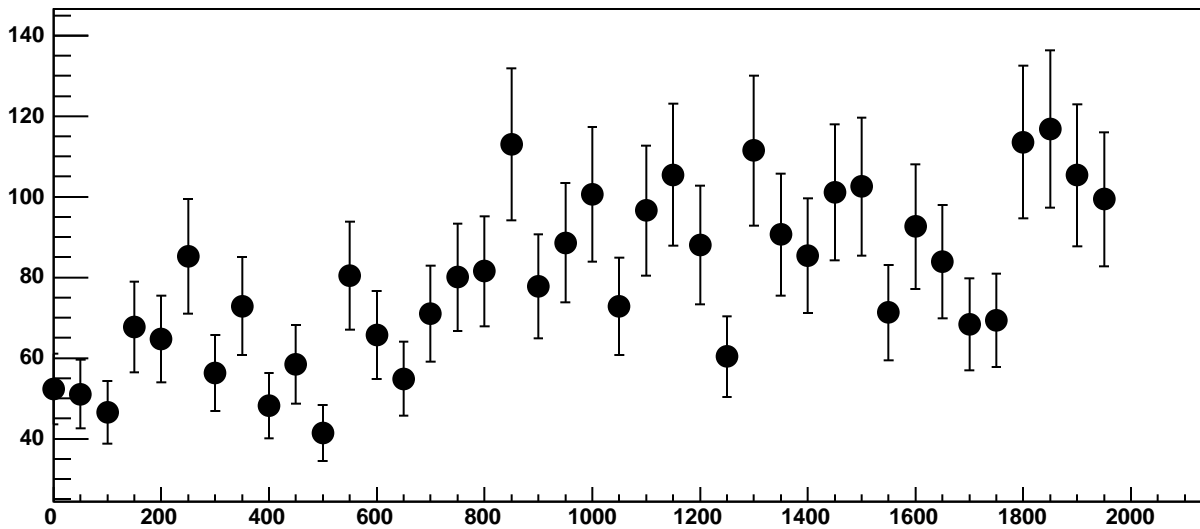
Chip 9, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC



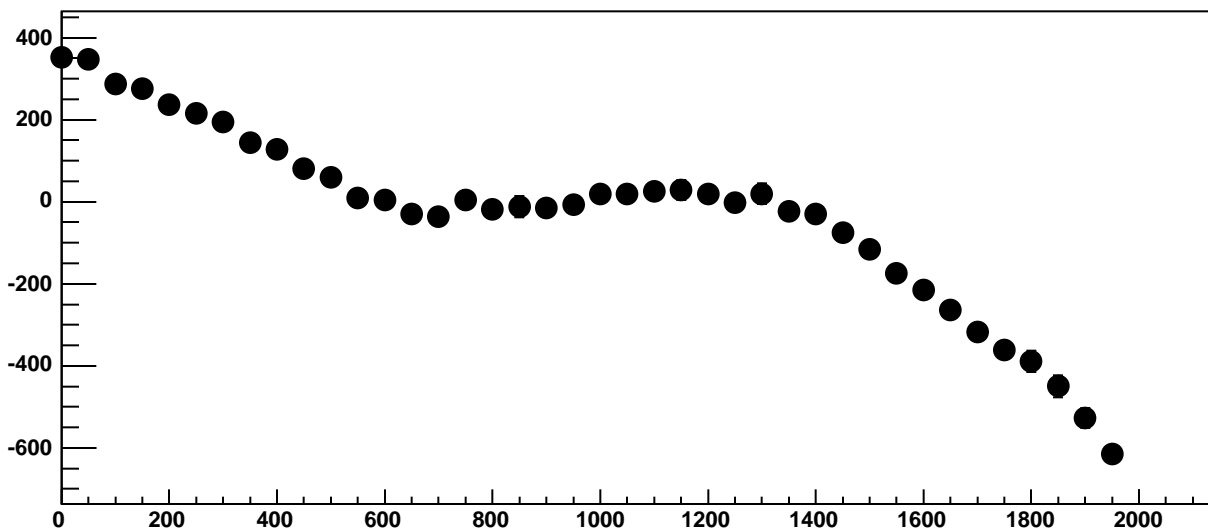
Chip 9, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC



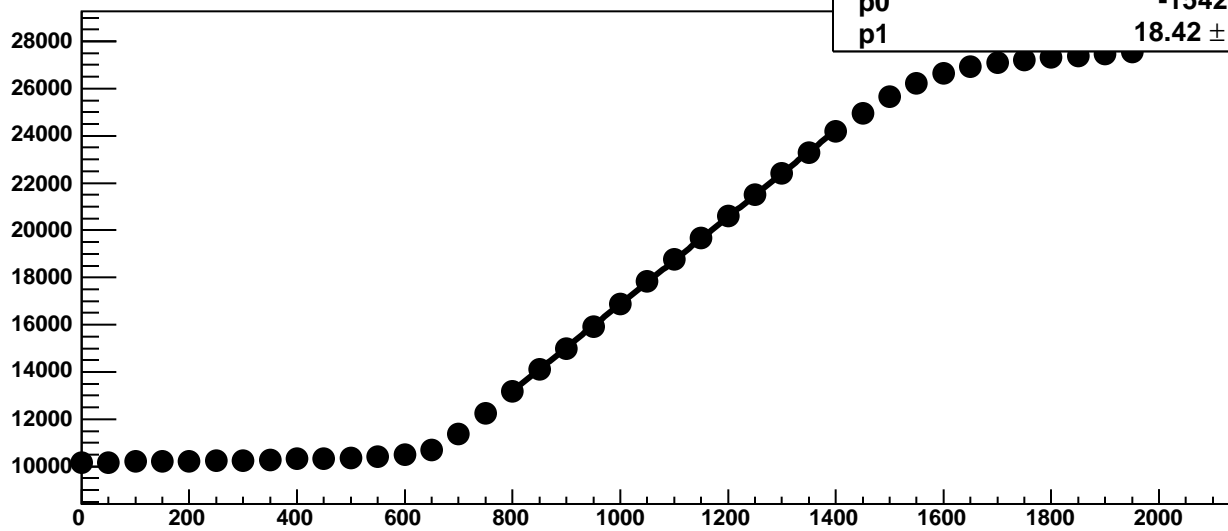
Chip 9, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC



Chip 9, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC

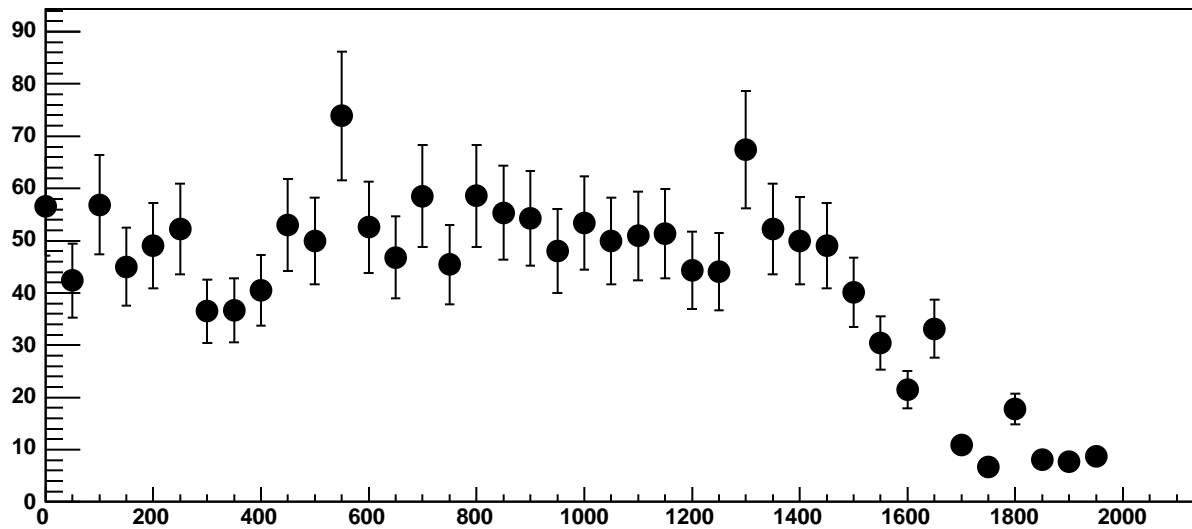


Chip 9, Channel 4, Enable 1!, Hold=30, ADC Mean vs DAC

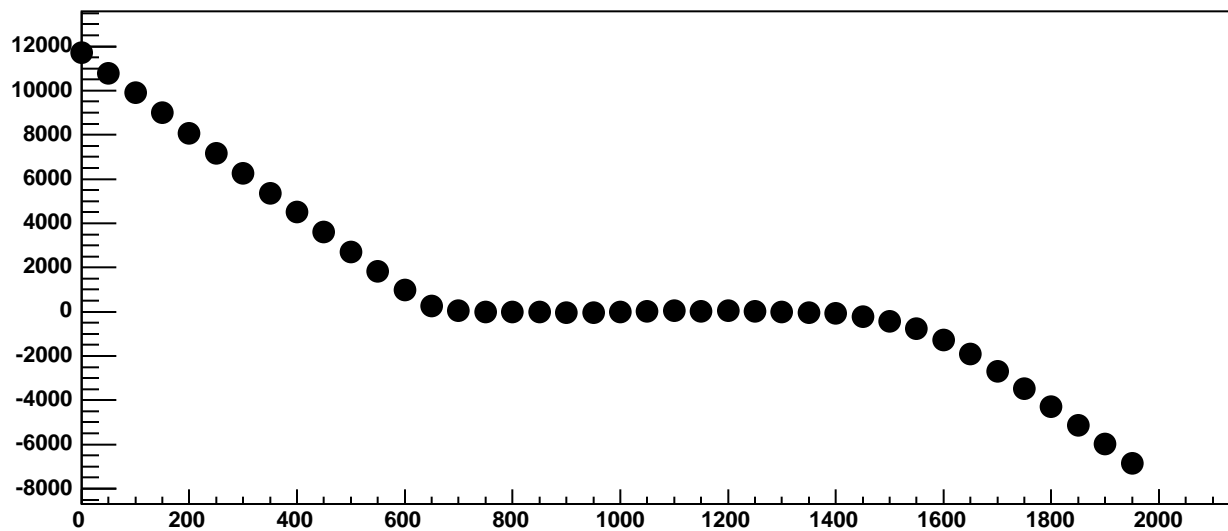


$\chi^2 / \text{ndf}$  107.4 / 11  
p0  $-1542 \pm 20.34$   
p1  $18.42 \pm 0.01811$

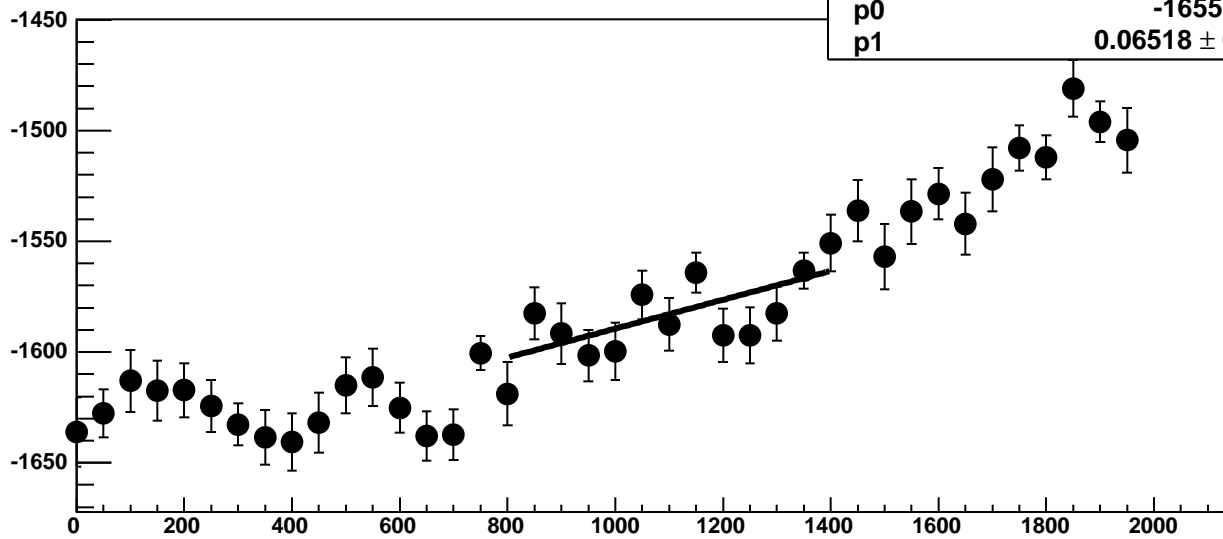
Chip 9, Channel 4, Enable 1!, Hold=30, ADC Noise vs DAC



Chip 9, Channel 4, Enable 1!, Hold=30, ADC Residuals vs DAC

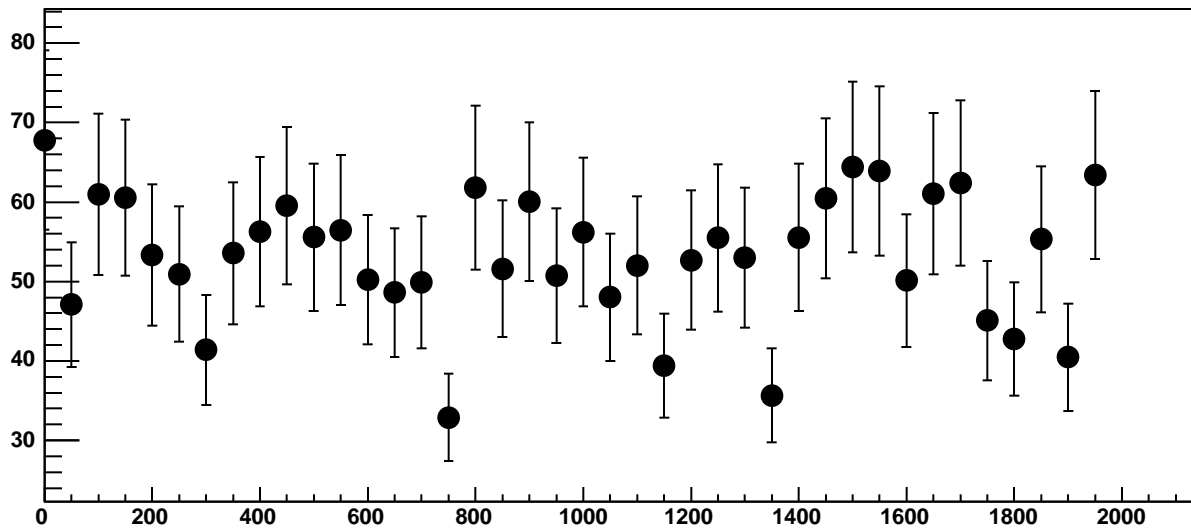


Chip 9, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC

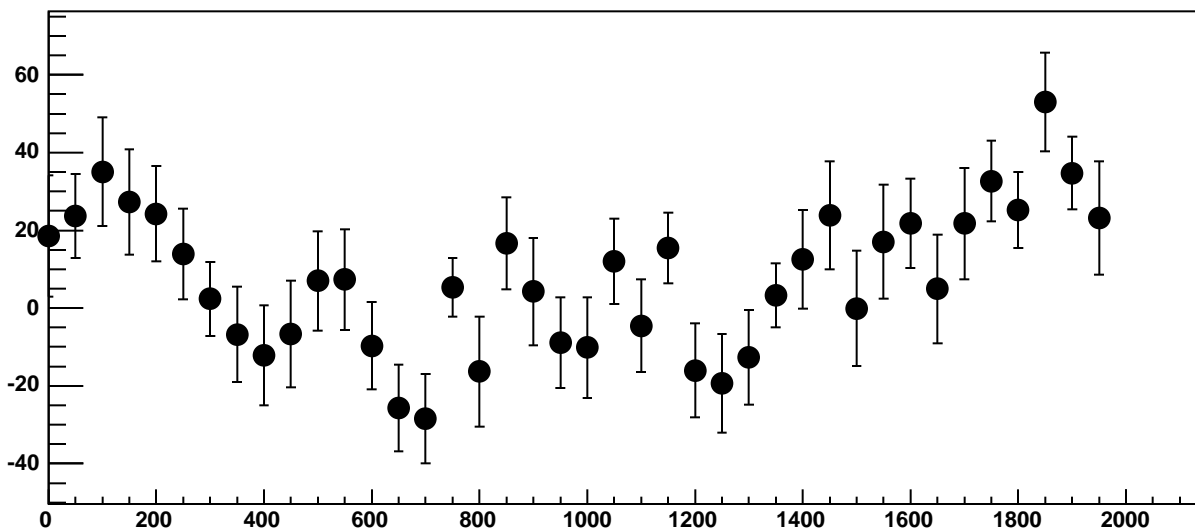


$\chi^2 / \text{ndf}$  15.17 / 11  
p0  $-1655 \pm 19.96$   
p1  $0.06518 \pm 0.01748$

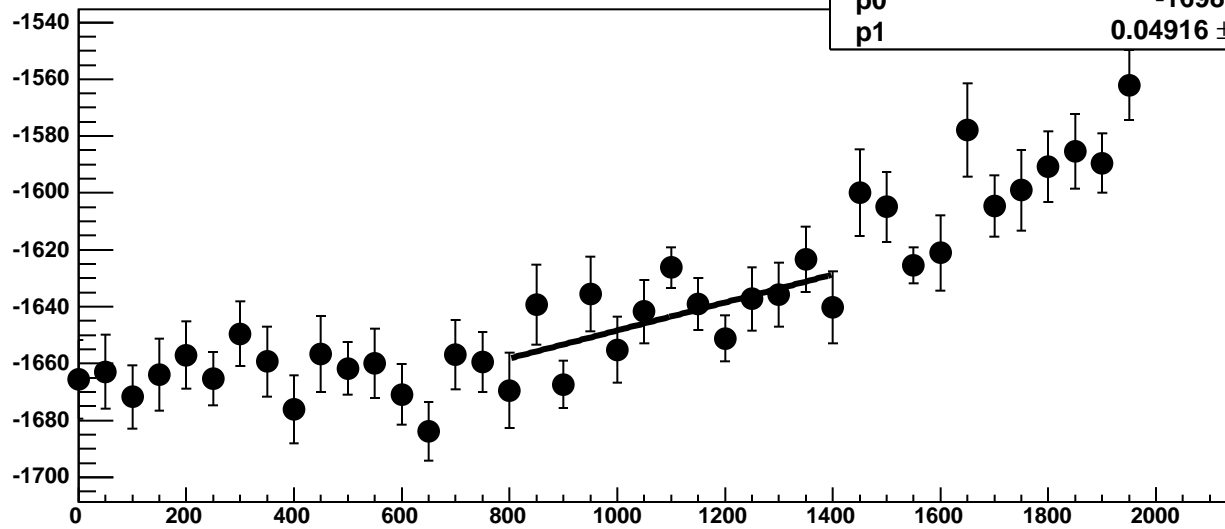
Chip 9, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC



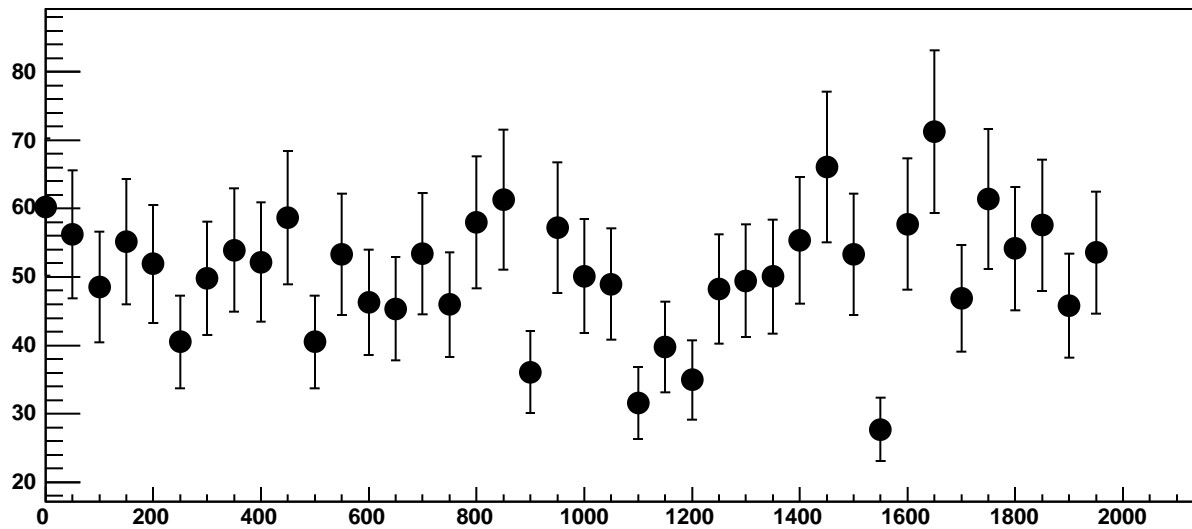
Chip 9, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC



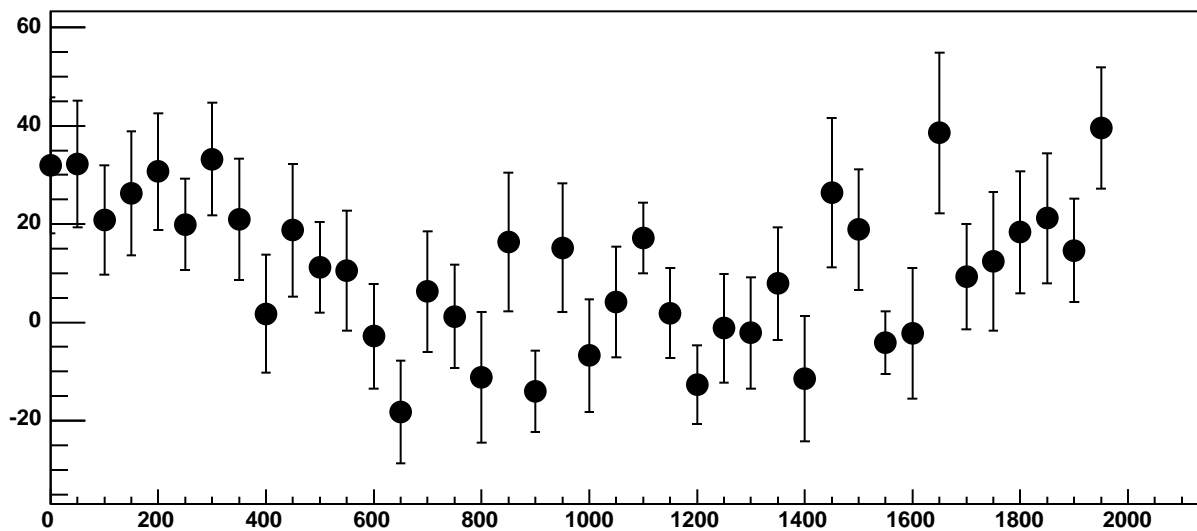
Chip 9, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC



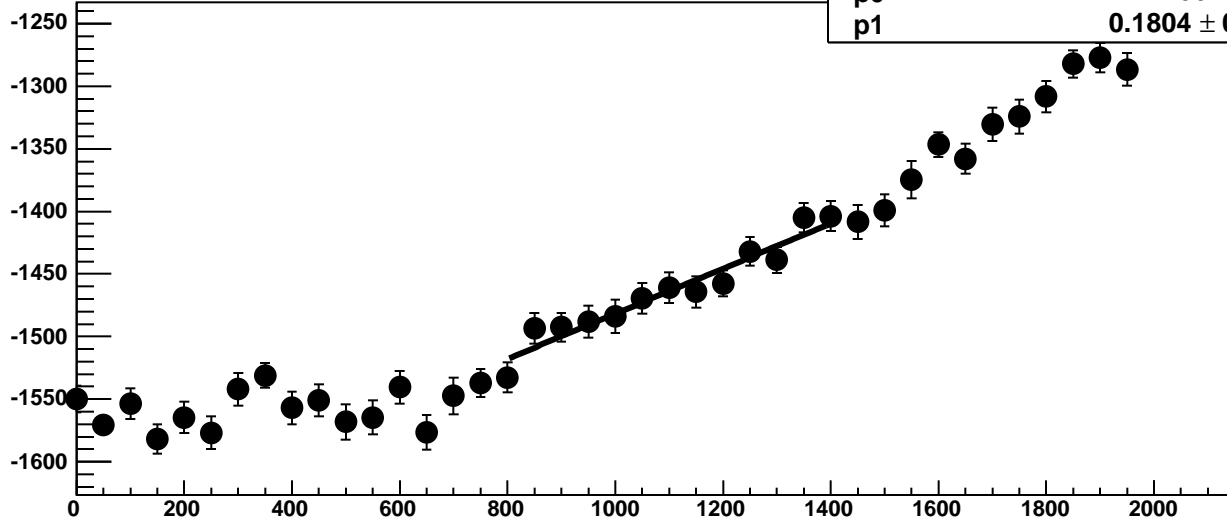
Chip 9, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC



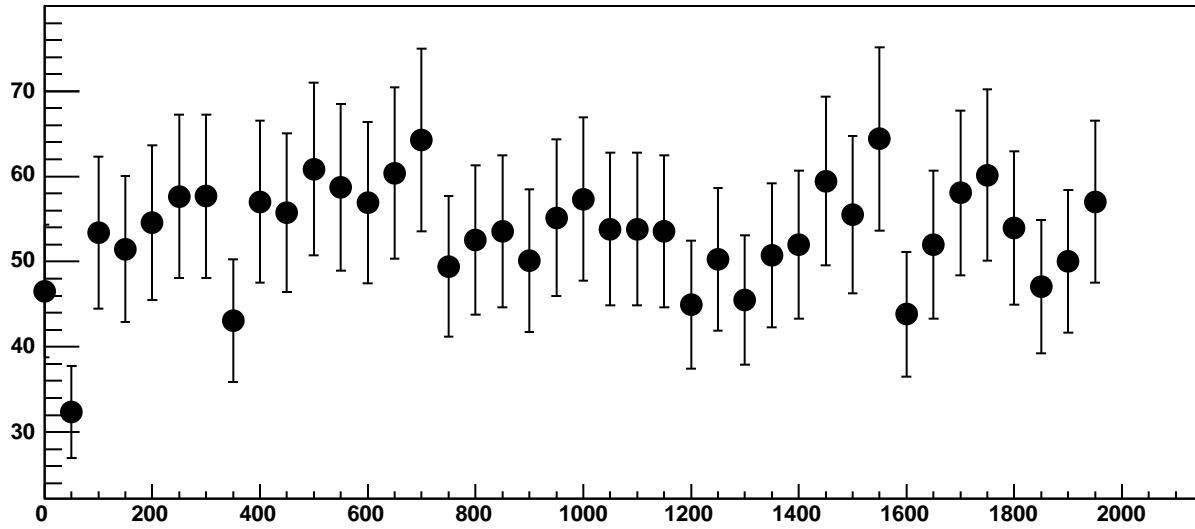
Chip 9, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC



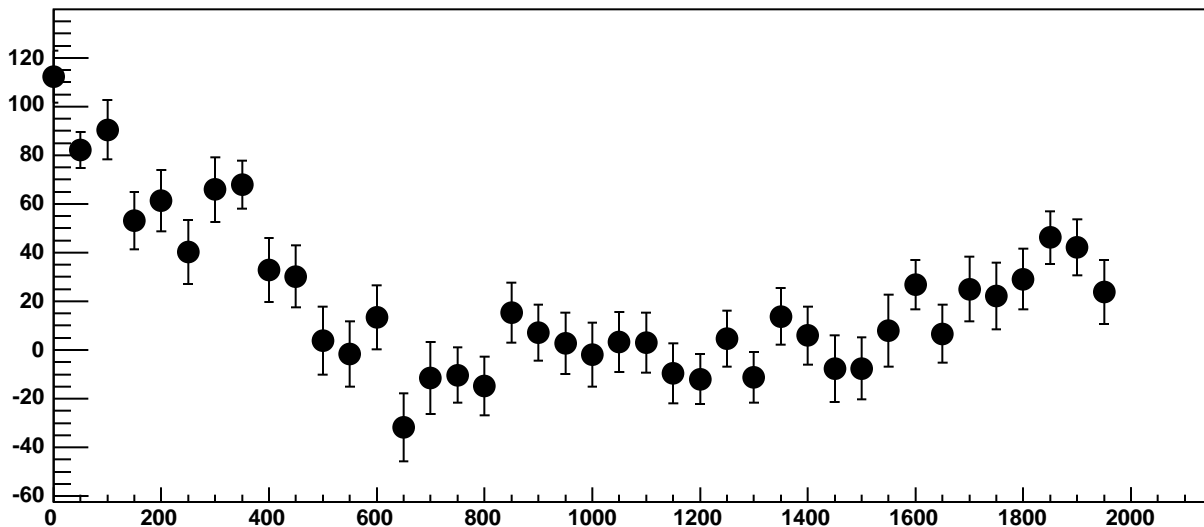
Chip 9, Channel 4, Enable 4, Hold=30, ADC Mean vs DAC



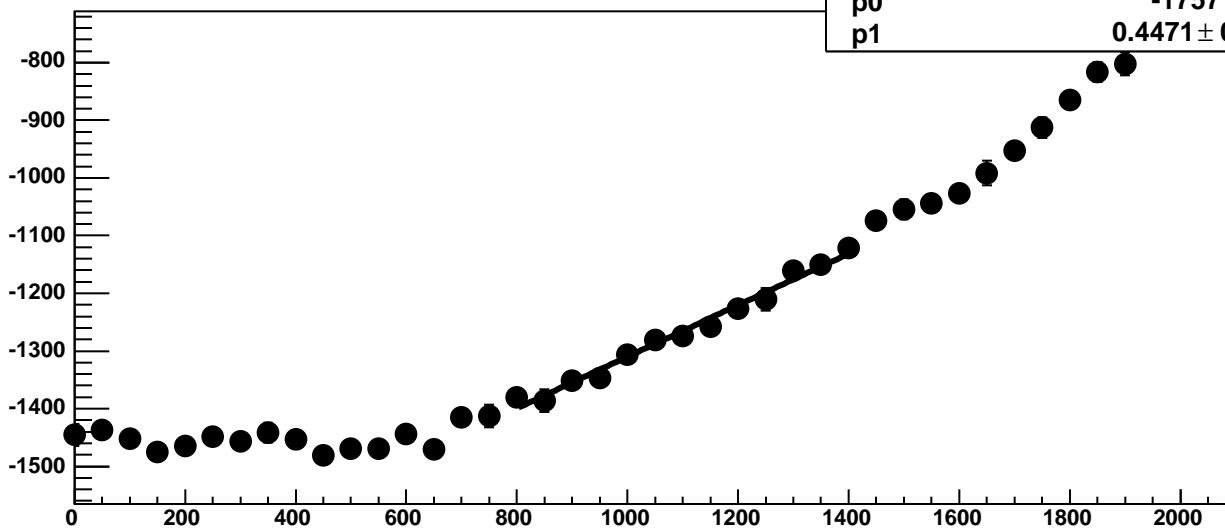
Chip 9, Channel 4, Enable 4, Hold=30, ADC Noise vs DAC



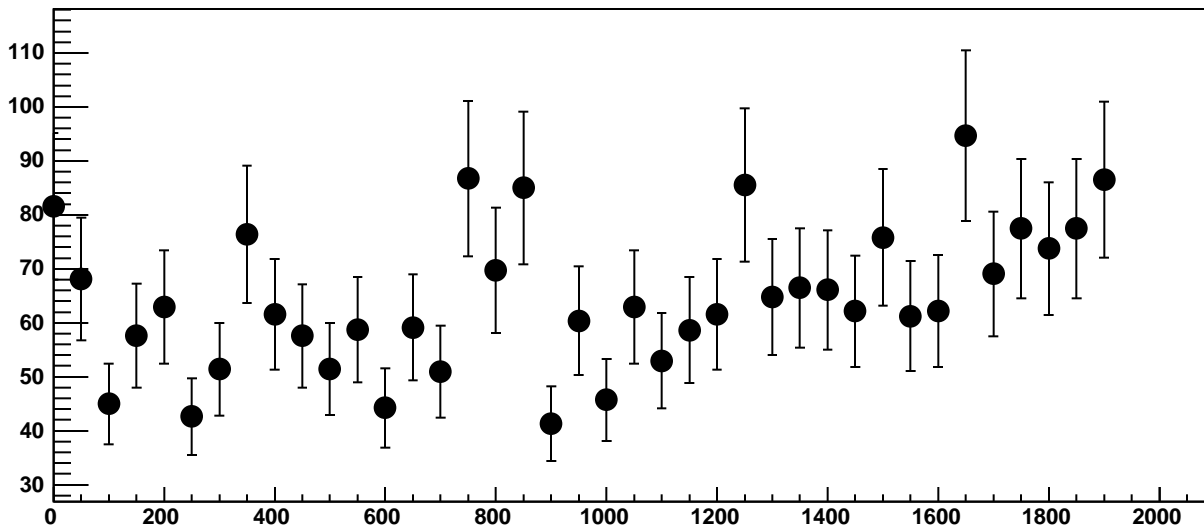
Chip 9, Channel 4, Enable 4, Hold=30, ADC Residuals vs DAC



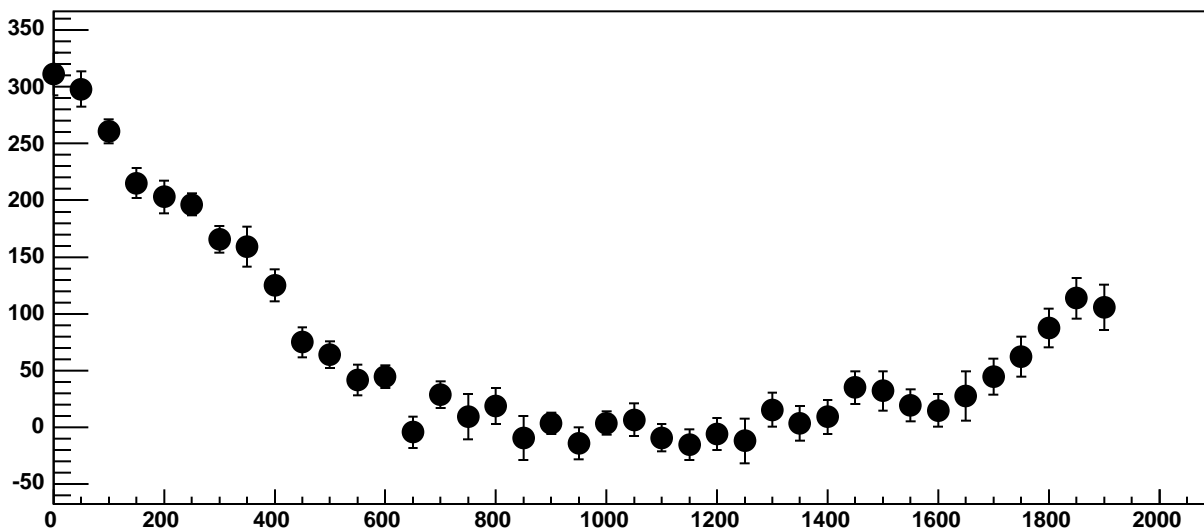
Chip 9, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC



Chip 9, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC

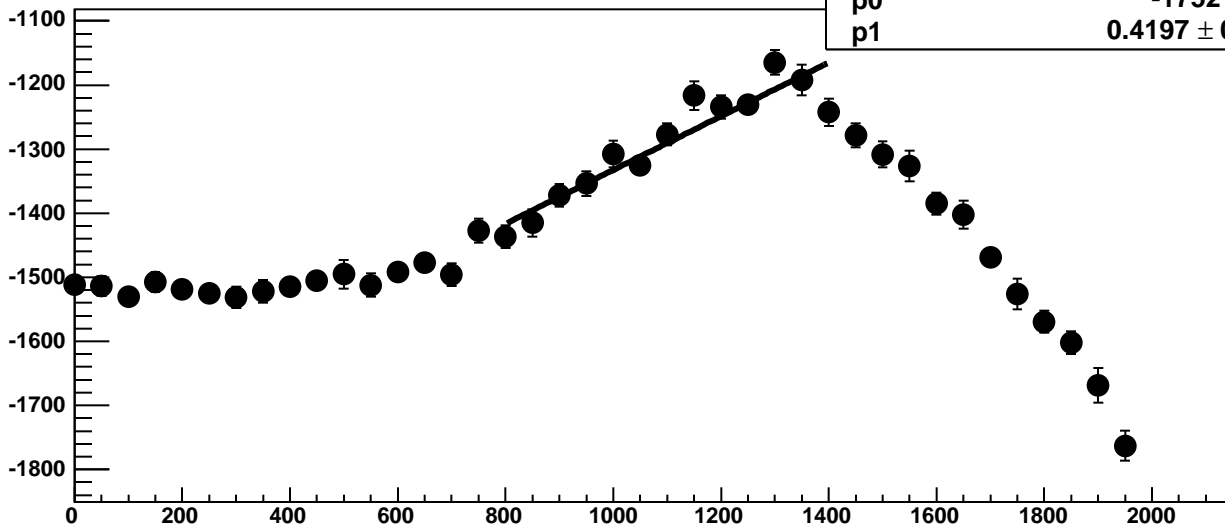


Chip 9, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC

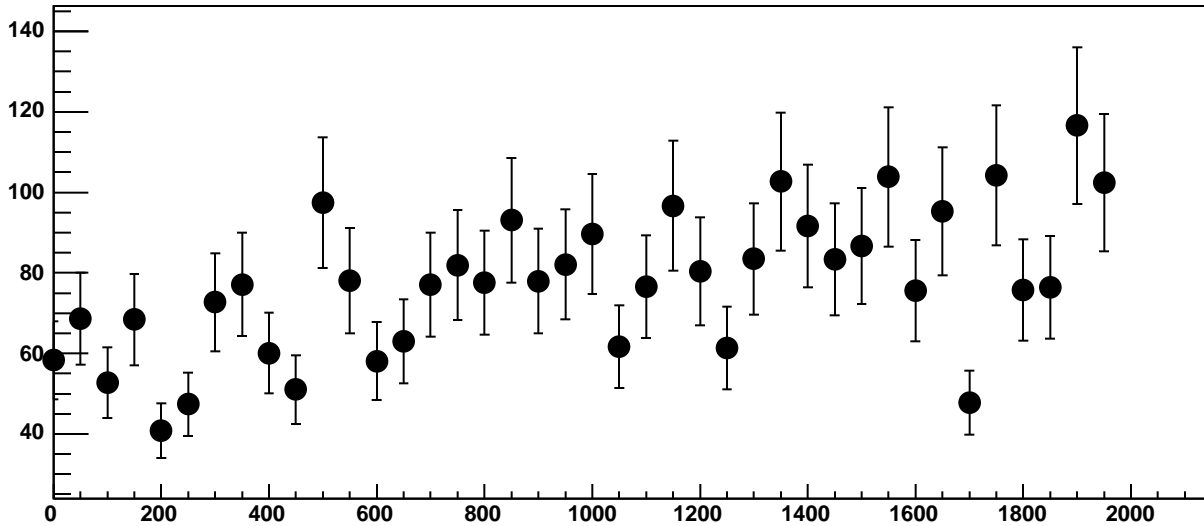




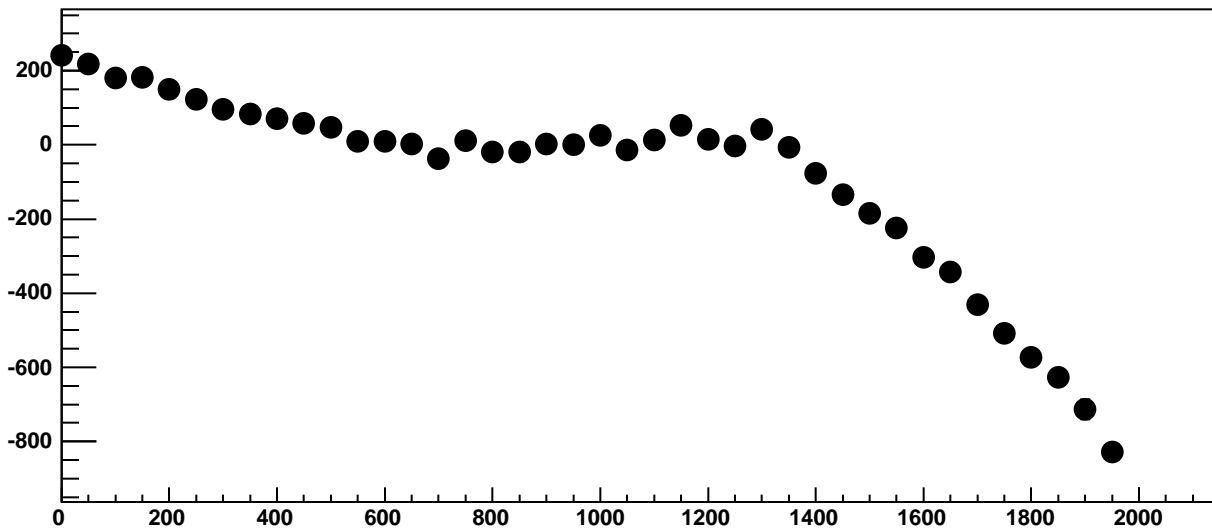
Chip 9, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC



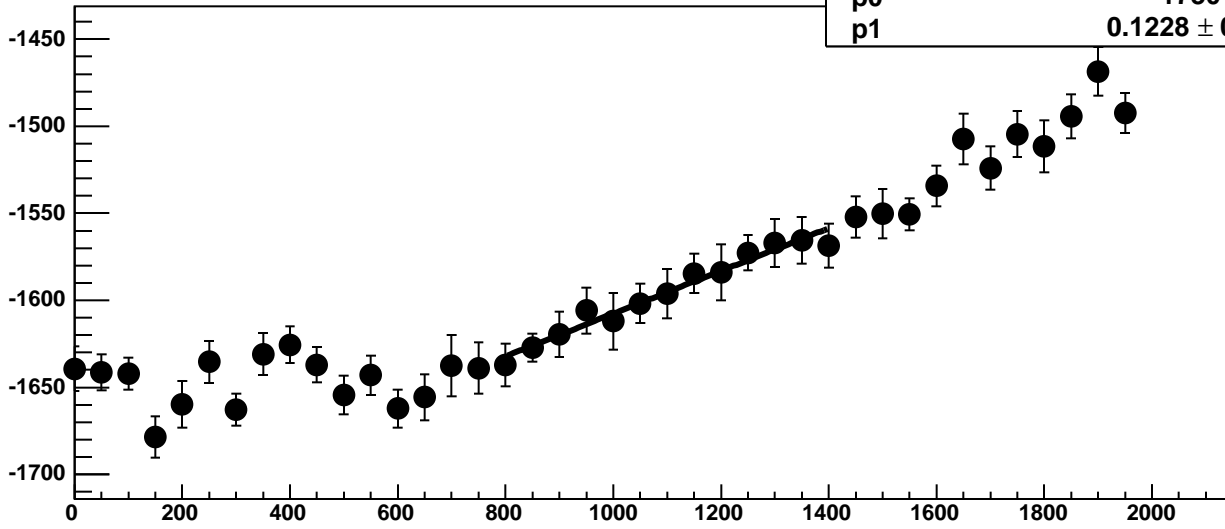
Chip 9, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC



Chip 9, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC

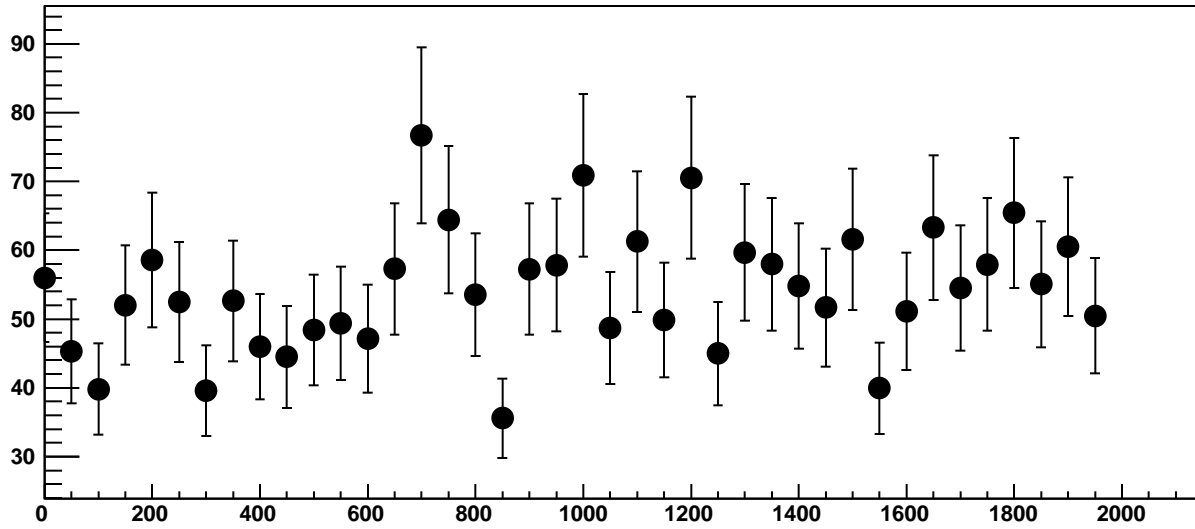


Chip 9, Channel 5, Enable 1, Hold=30, ADC Mean vs DAC

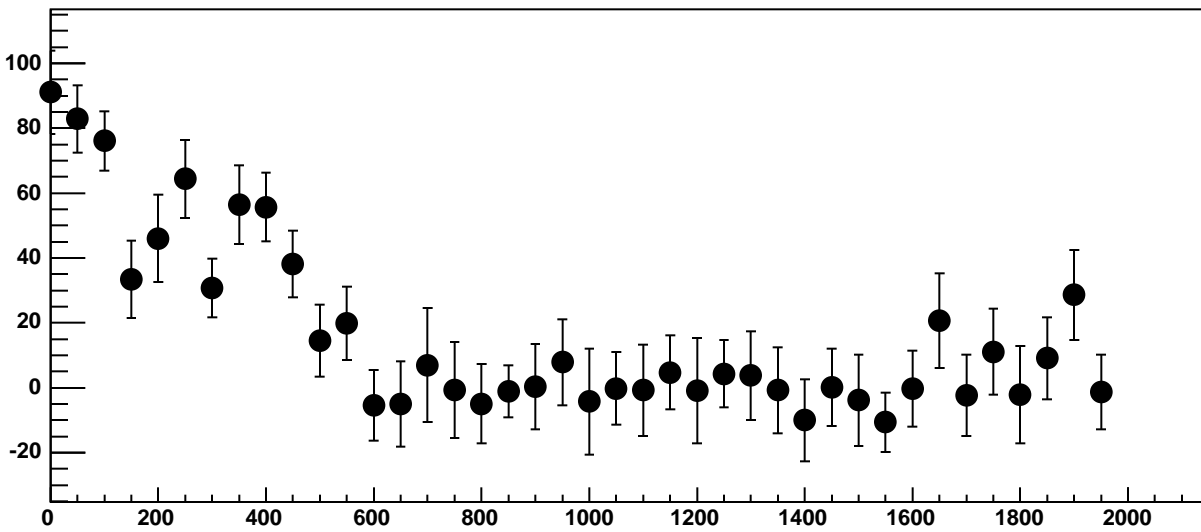


$\chi^2 / \text{ndf}$  1.672 / 11  
p0  $-1730 \pm 18.87$   
p1  $0.1228 \pm 0.01727$

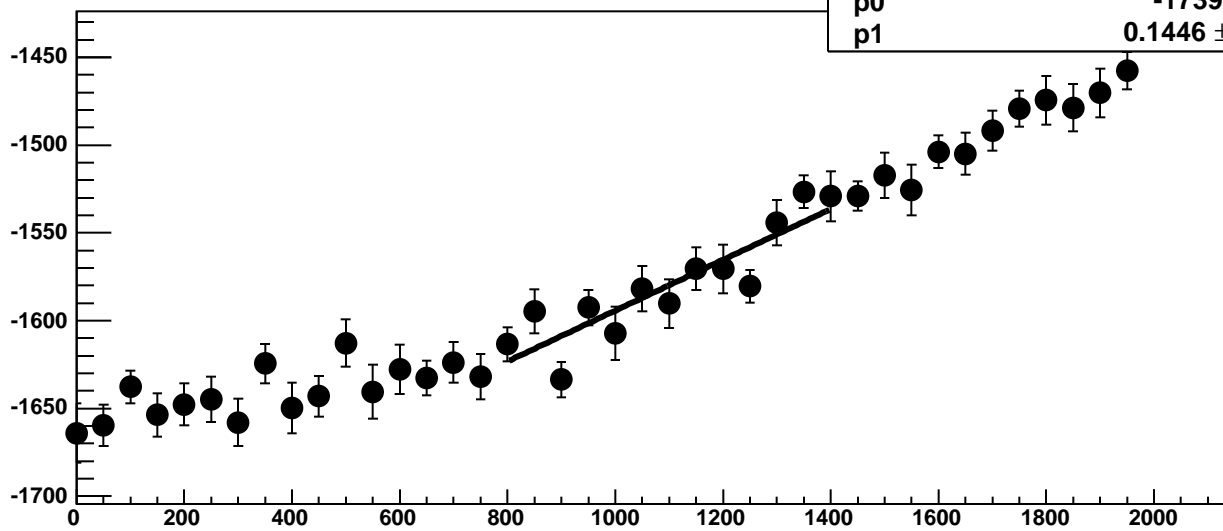
Chip 9, Channel 5, Enable 1, Hold=30, ADC Noise vs DAC



Chip 9, Channel 5, Enable 1, Hold=30, ADC Residuals vs DAC



Chip 9, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

22.14 / 11

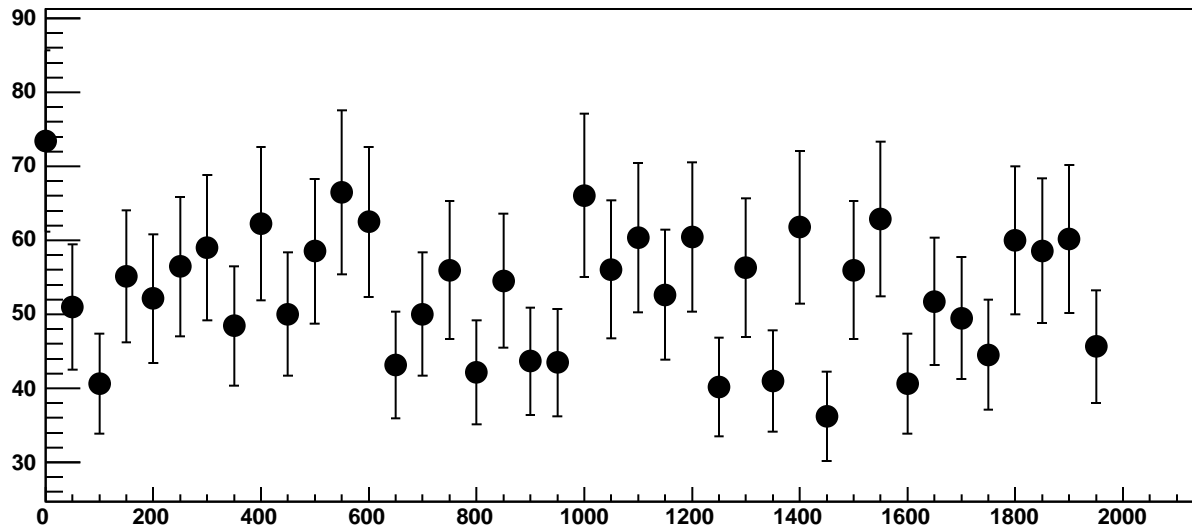
p0

$-1739 \pm 18.07$

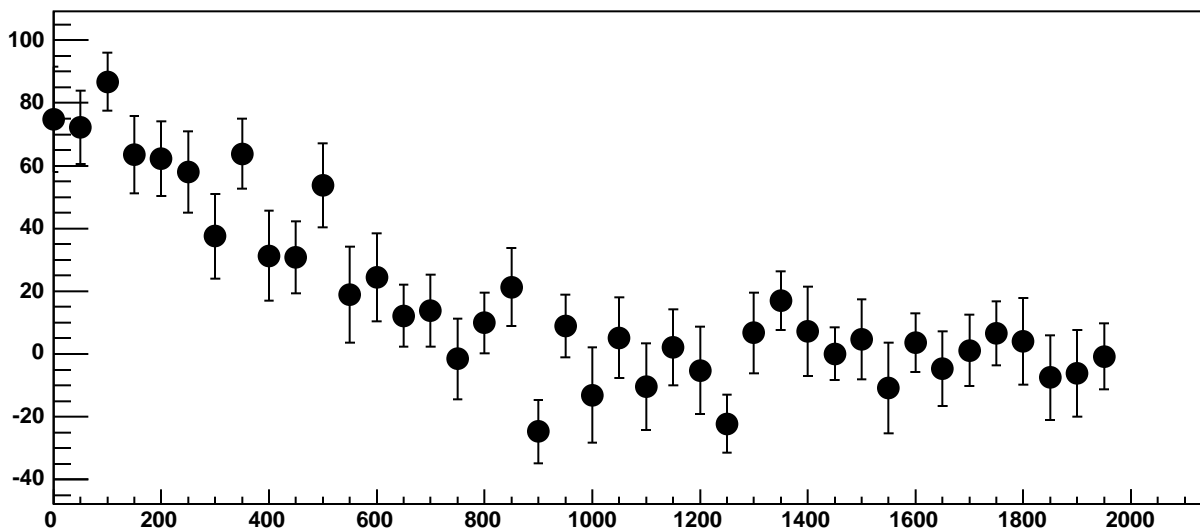
p1

$0.1446 \pm 0.0163$

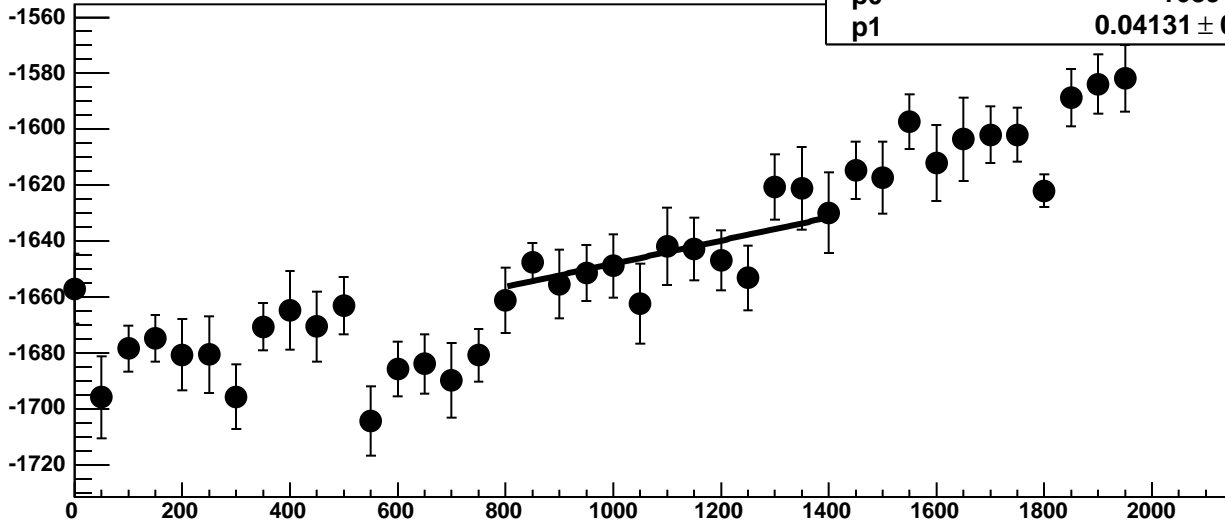
Chip 9, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC



Chip 9, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC

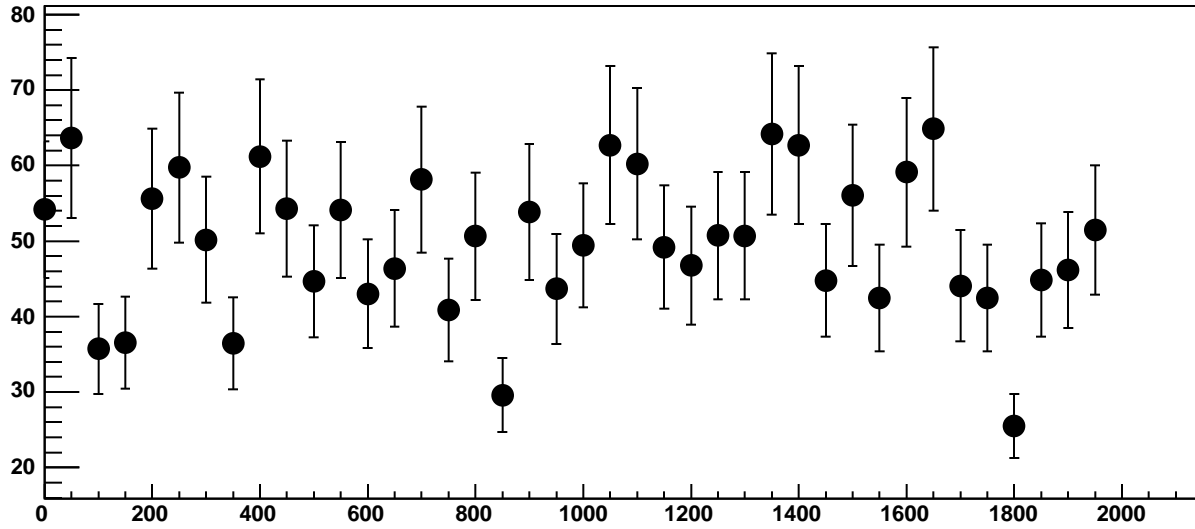


Chip 9, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC

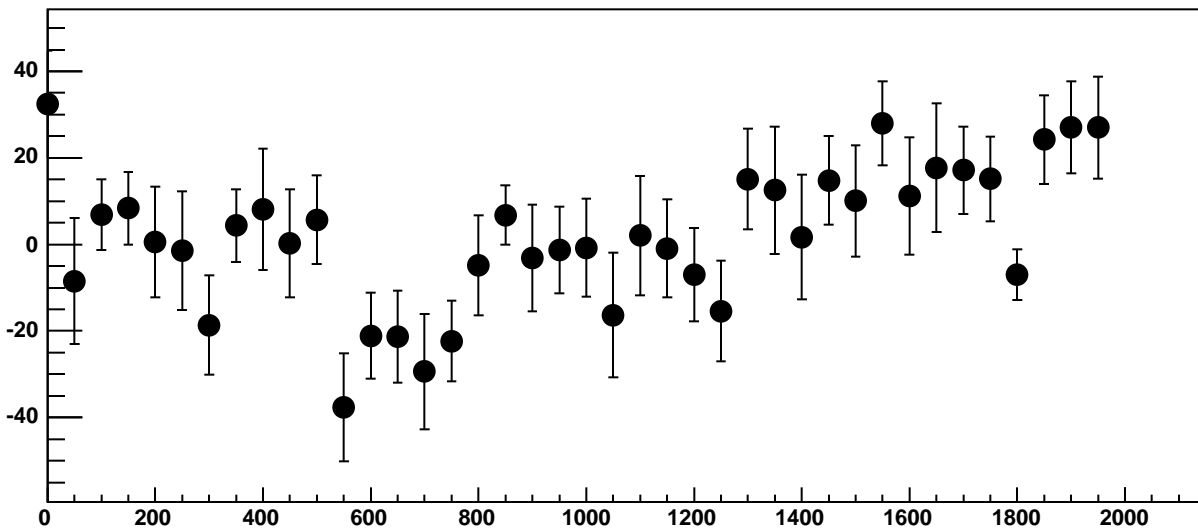


$\chi^2 / \text{ndf}$  7.164 / 11  
p0  $-1689 \pm 17.69$   
p1  $0.04131 \pm 0.01656$

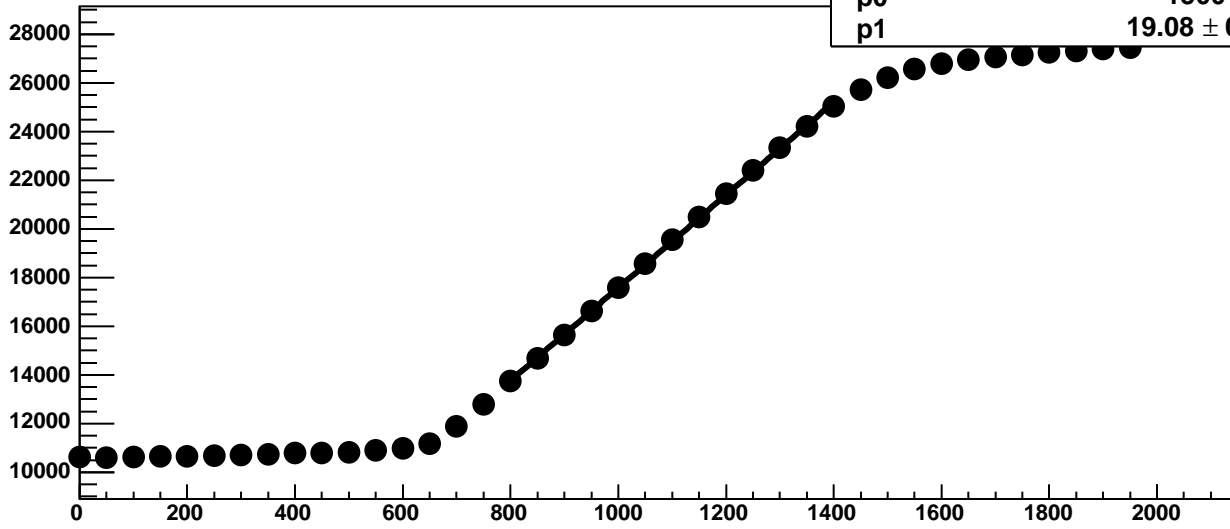
Chip 9, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC



Chip 9, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC

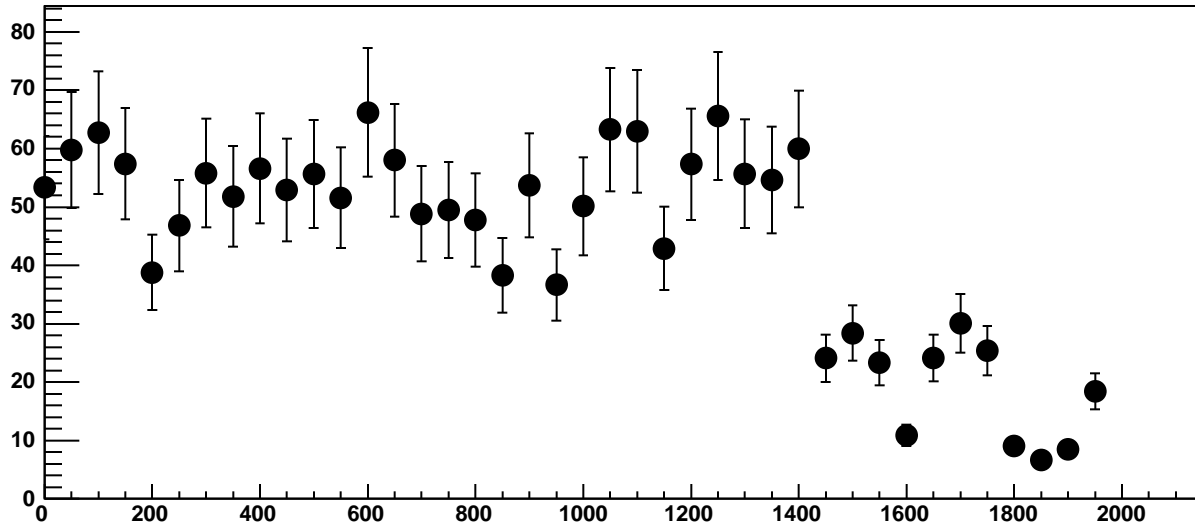


Chip 9, Channel 5, Enable 4!, Hold=30, ADC Mean vs DAC

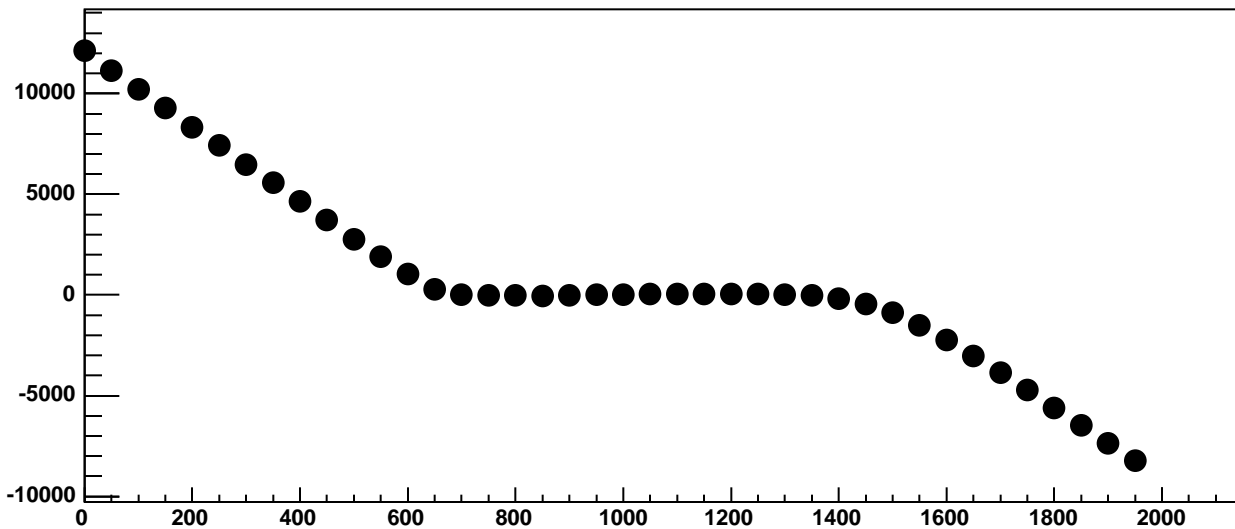


$\chi^2 / \text{ndf}$  264.7 / 11  
p0  $-1500 \pm 18.53$   
p1  $19.08 \pm 0.01724$

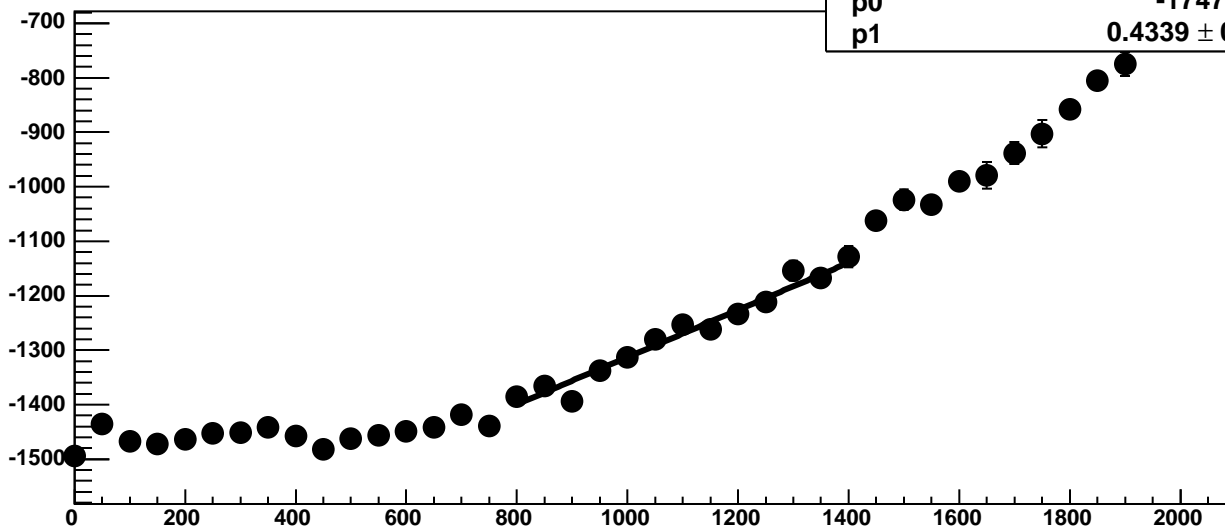
Chip 9, Channel 5, Enable 4!, Hold=30, ADC Noise vs DAC



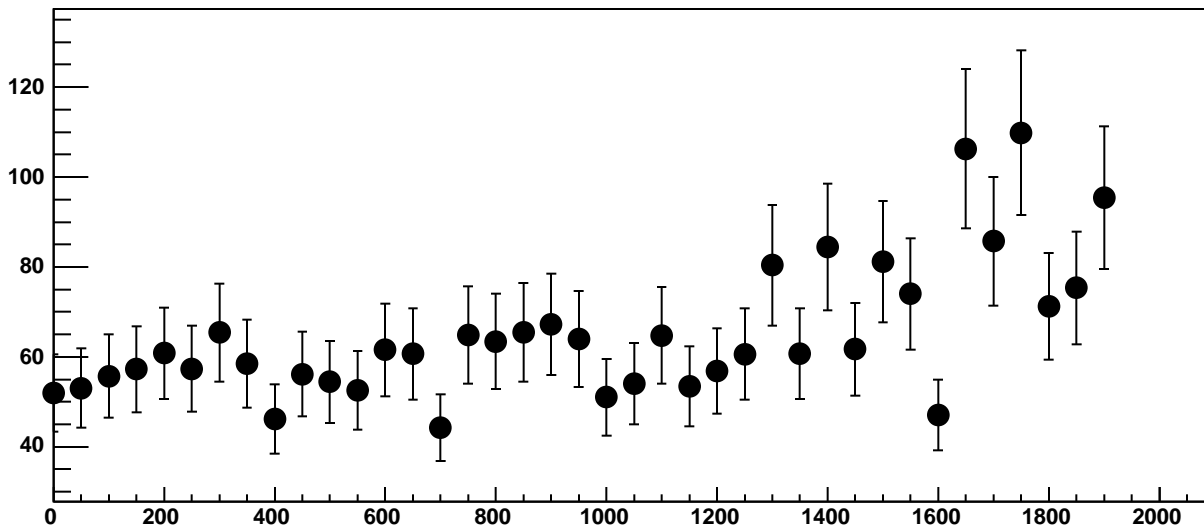
Chip 9, Channel 5, Enable 4!, Hold=30, ADC Residuals vs DAC



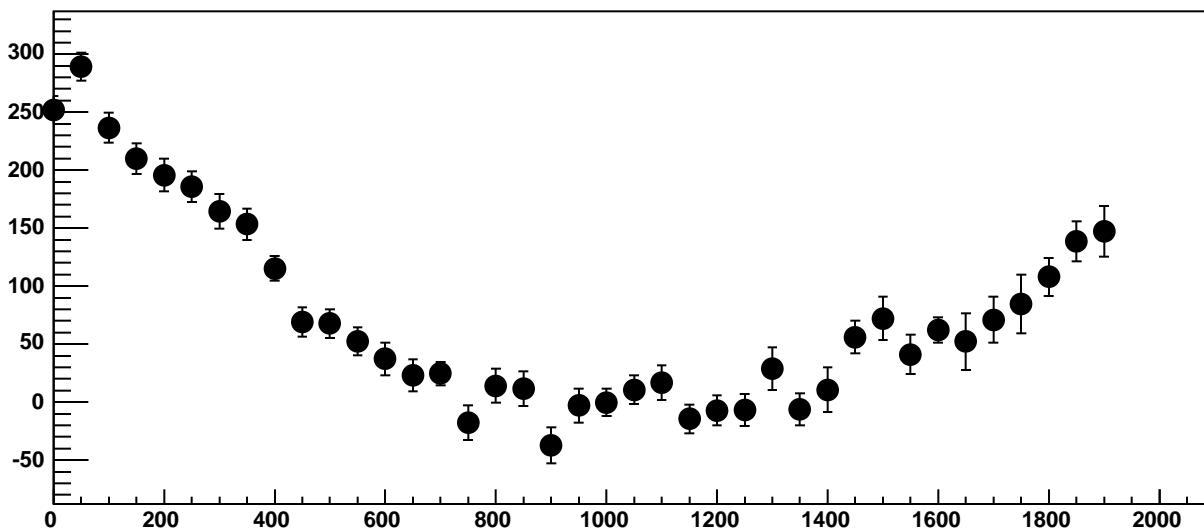
Chip 9, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC



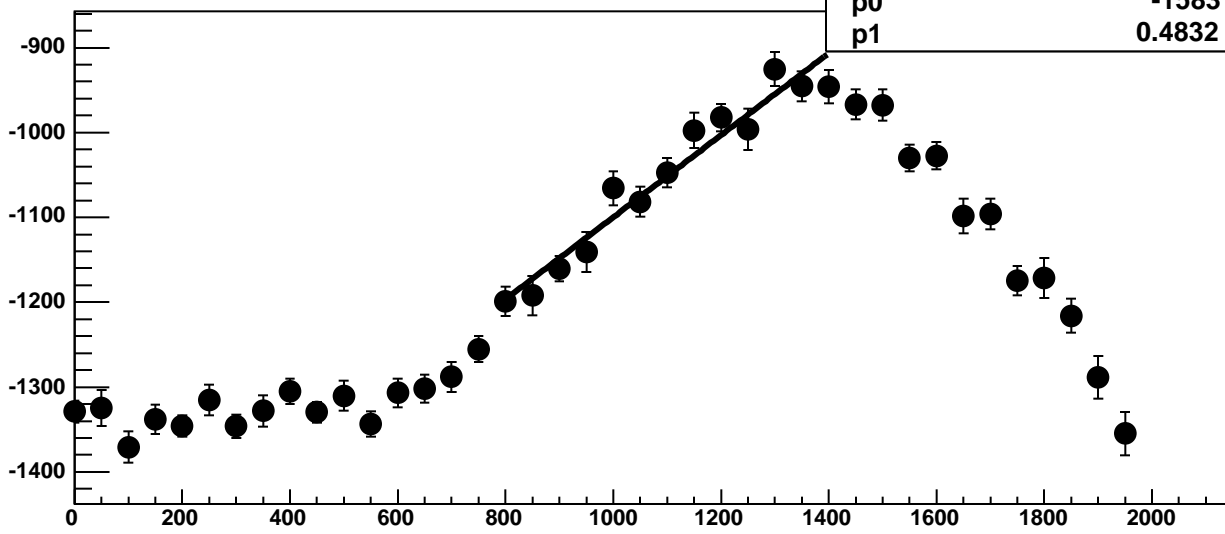
Chip 9, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC



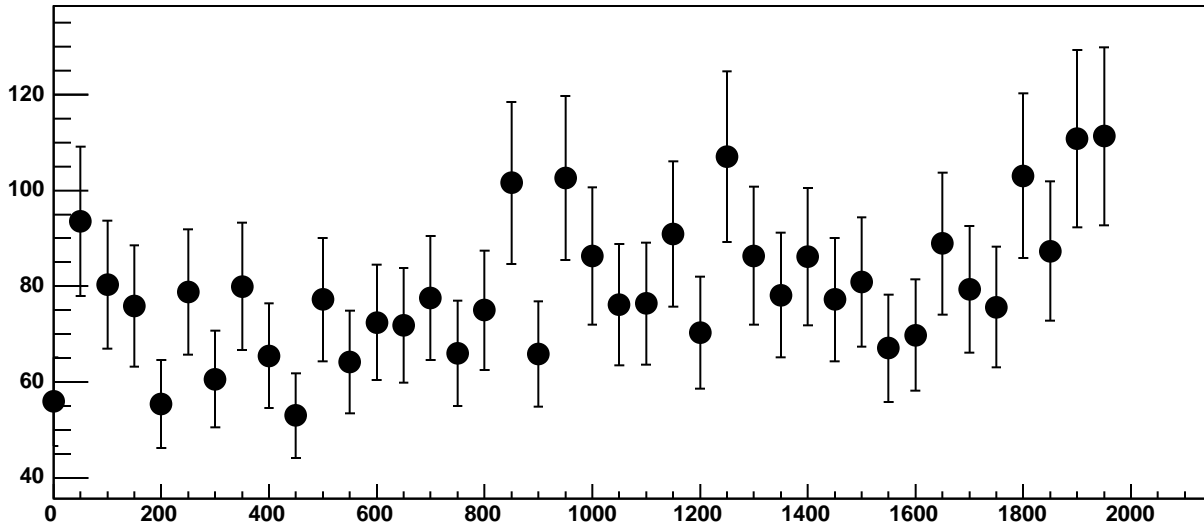
Chip 9, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC



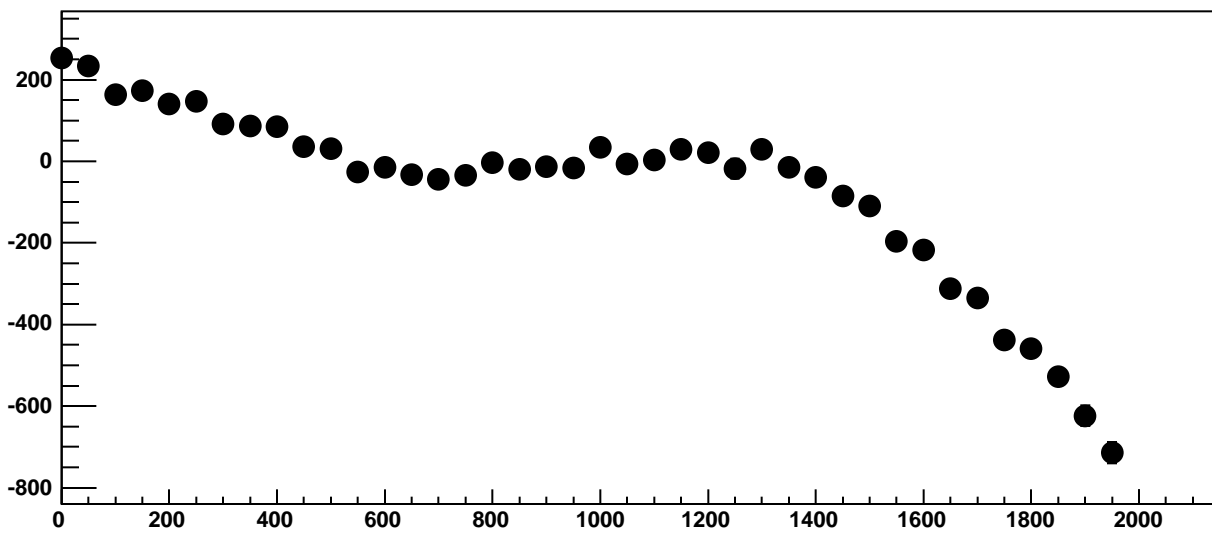
Chip 9, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC



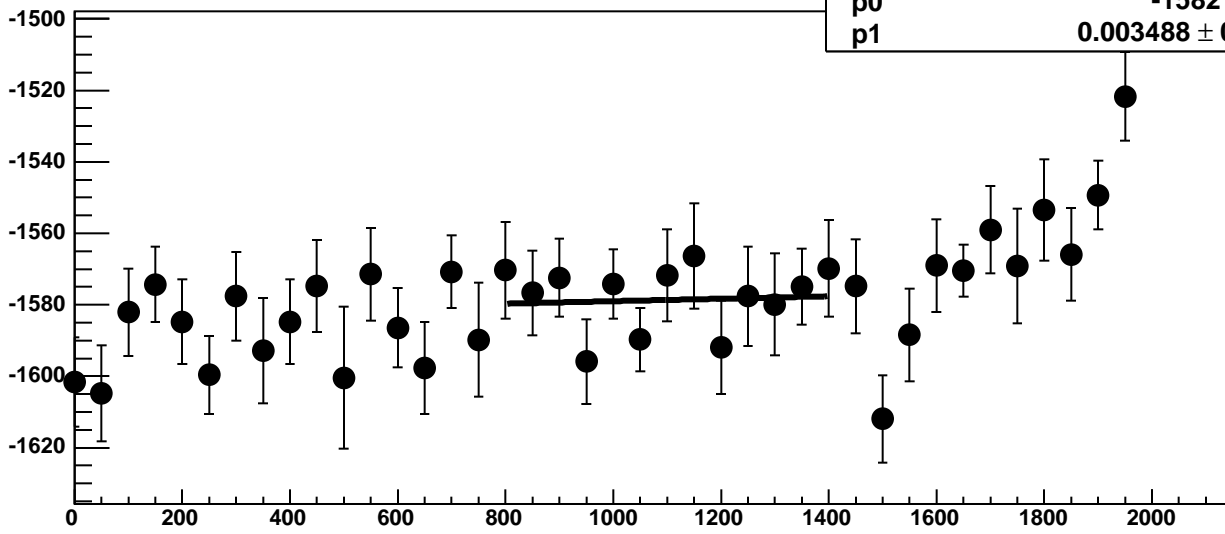
Chip 9, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC



Chip 9, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC

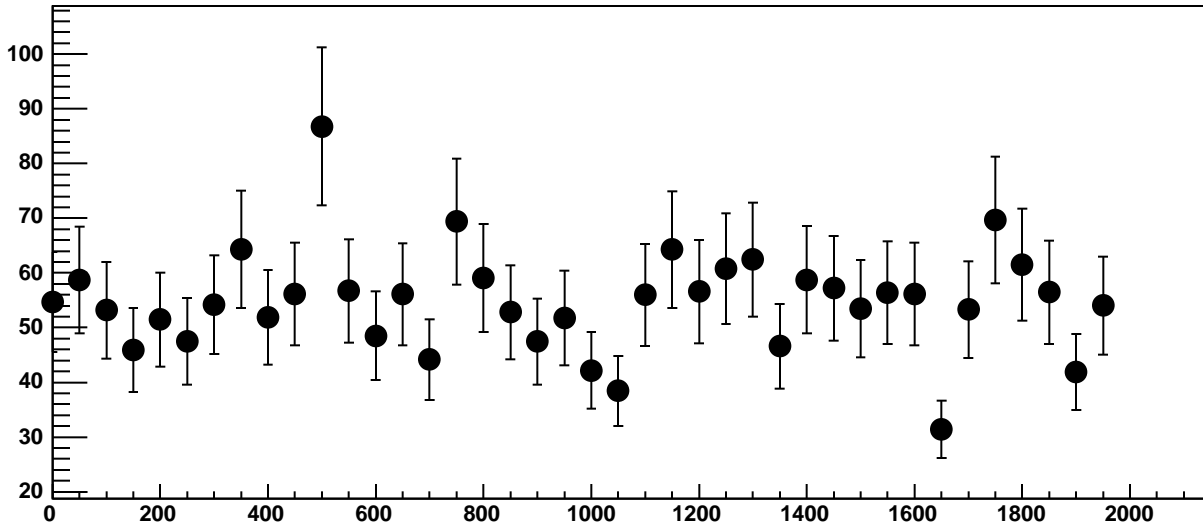


Chip 9, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC

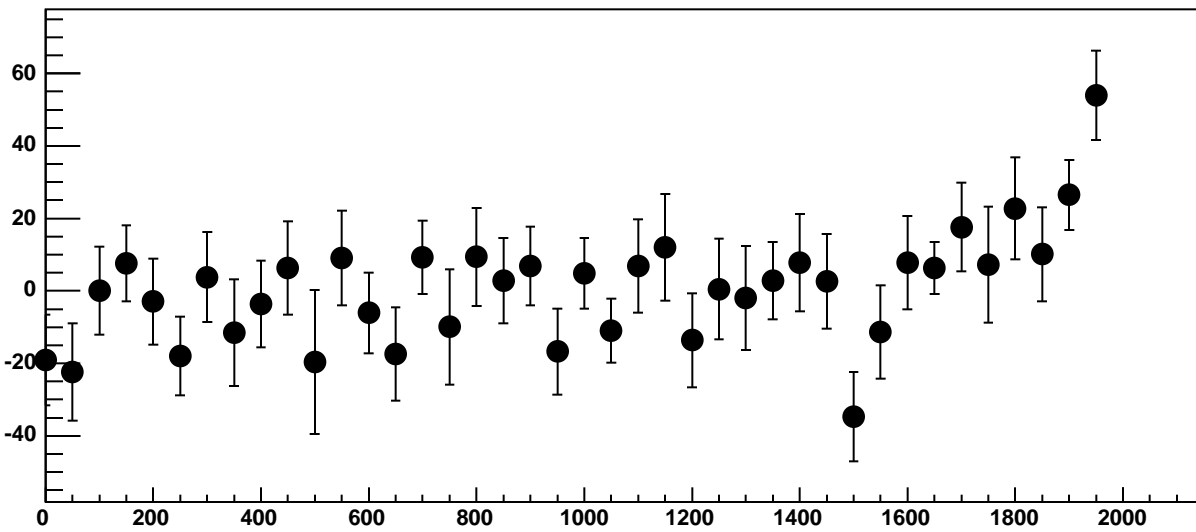


$\chi^2 / \text{ndf}$  7.174 / 11  
p0  $-1582 \pm 20.15$   
p1  $0.003488 \pm 0.01834$

Chip 9, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC

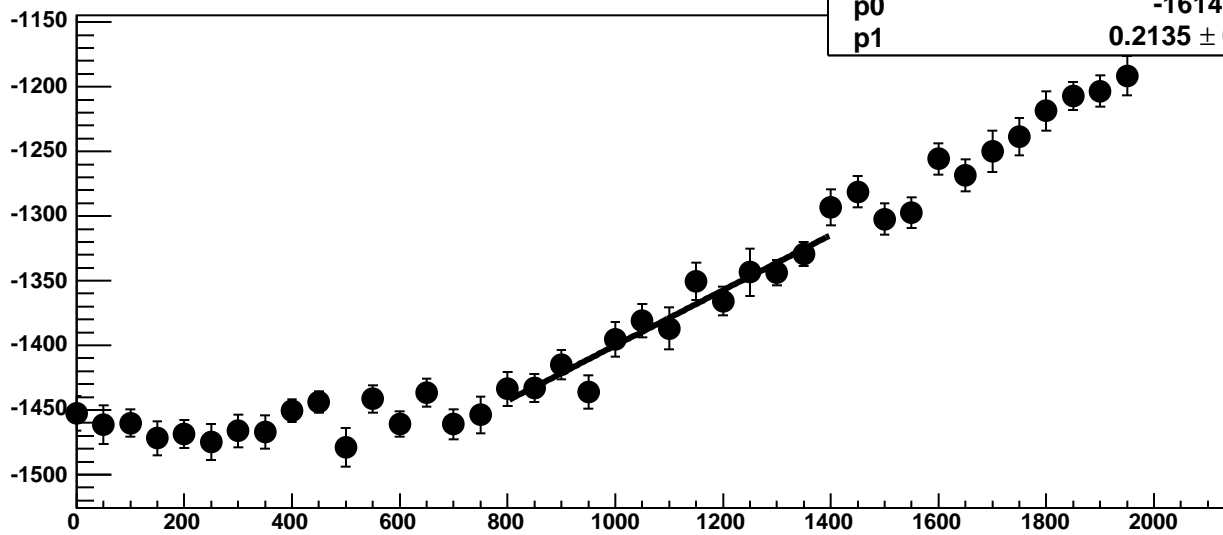


Chip 9, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC

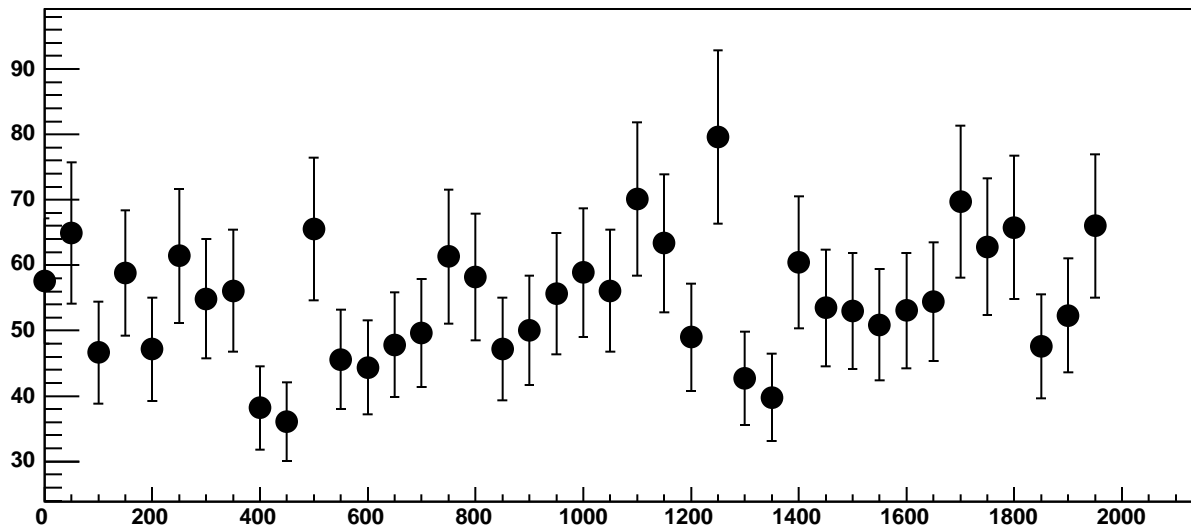




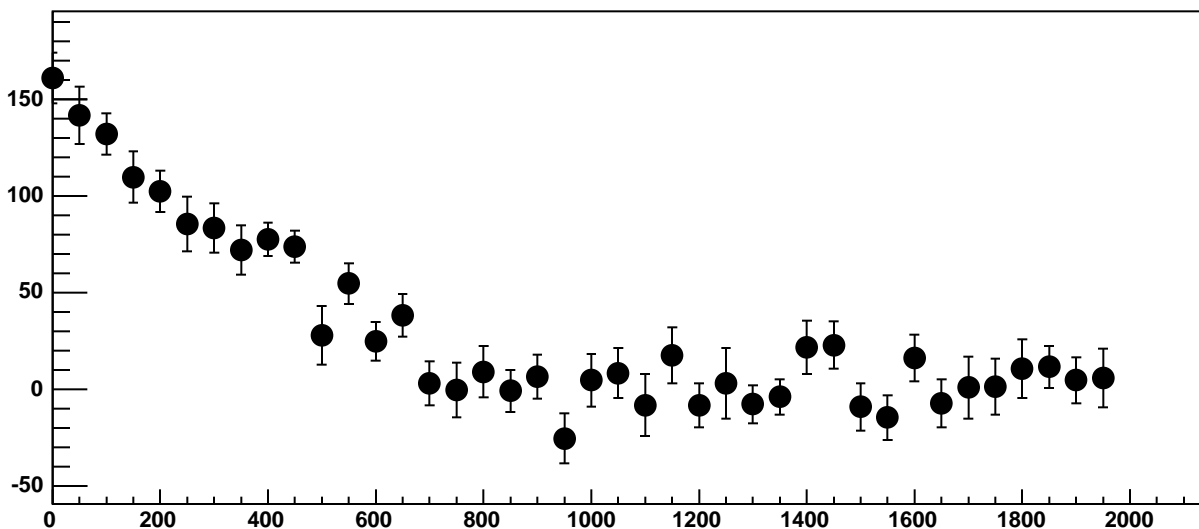
Chip 9, Channel 6, Enable 2, Hold=30, ADC Mean vs DAC



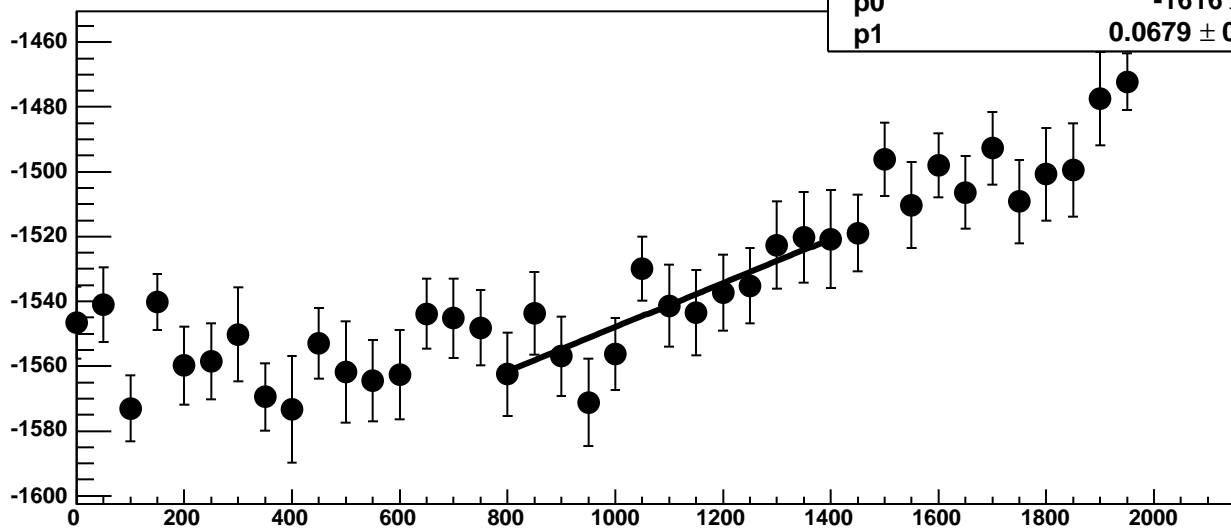
Chip 9, Channel 6, Enable 2, Hold=30, ADC Noise vs DAC



Chip 9, Channel 6, Enable 2, Hold=30, ADC Residuals vs DAC

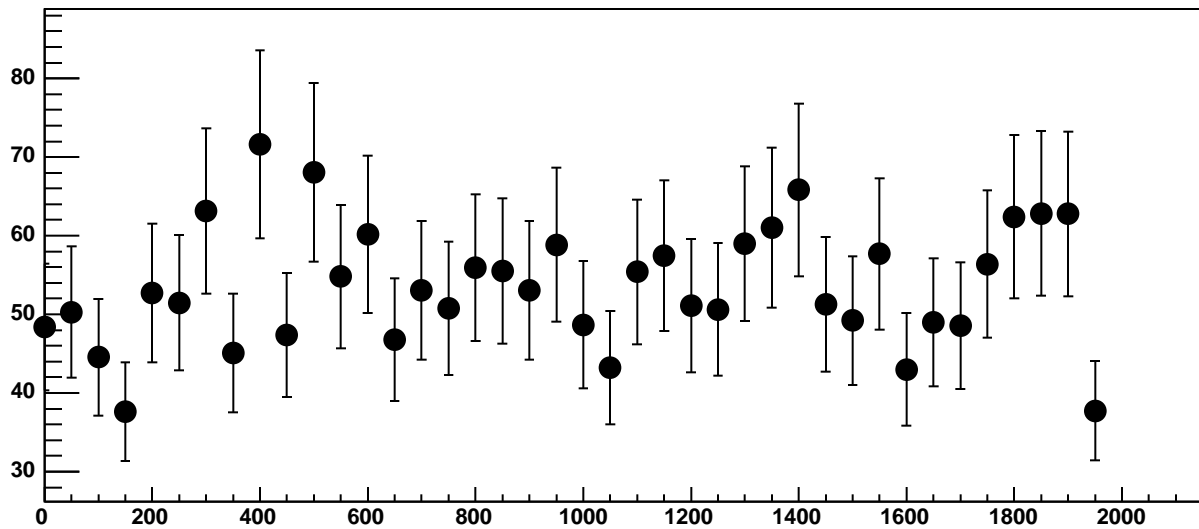


Chip 9, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC

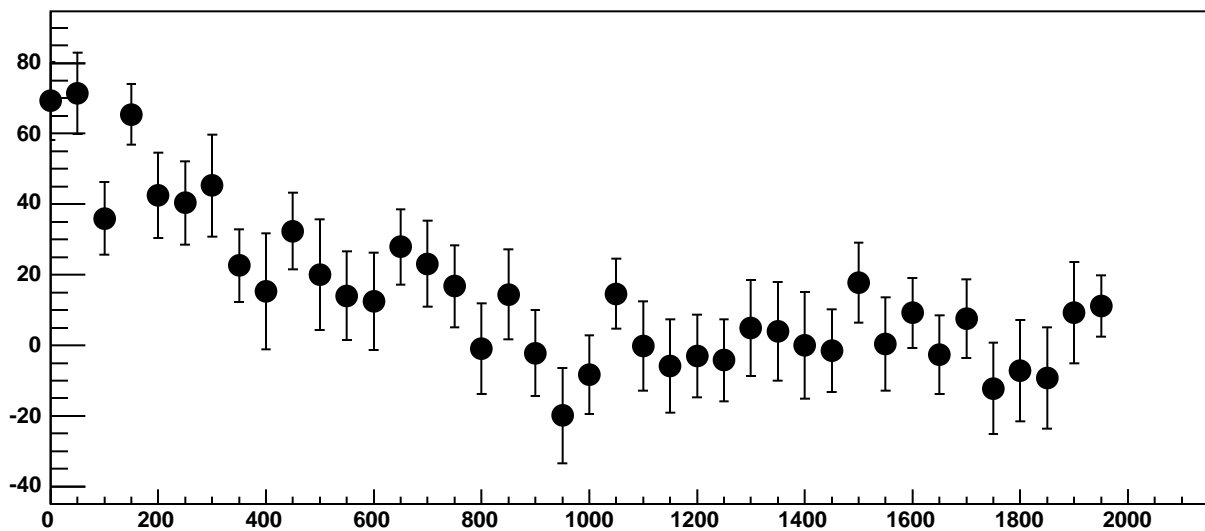


$\chi^2 / \text{ndf}$  6.802 / 11  
p0  $-1616 \pm 21.59$   
p1  $0.0679 \pm 0.01959$

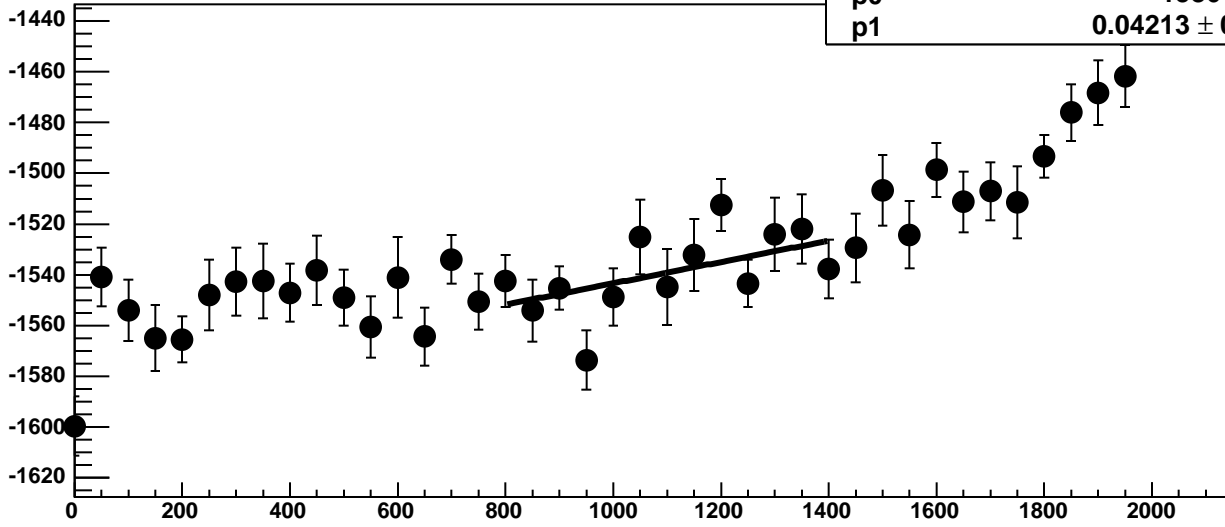
Chip 9, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC



Chip 9, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC



Chip 9, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

16.12 / 11

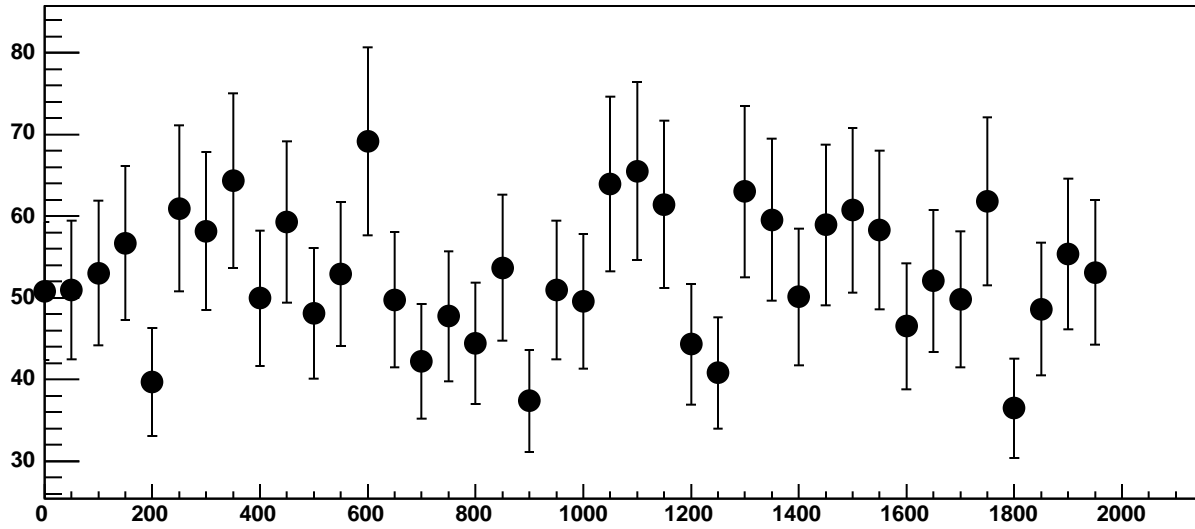
p0

$-1586 \pm 18.27$

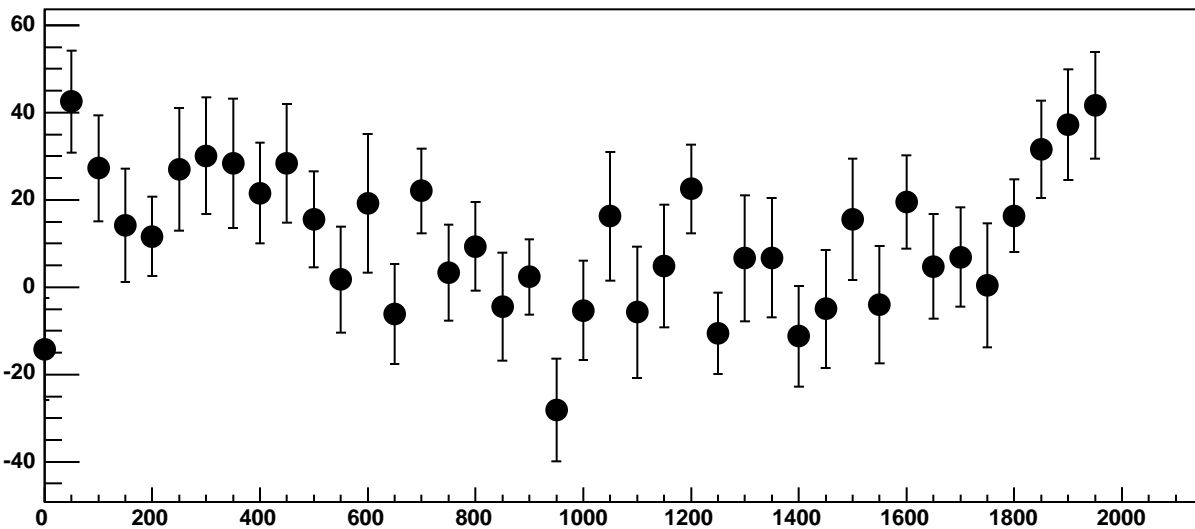
p1

$0.04213 \pm 0.01664$

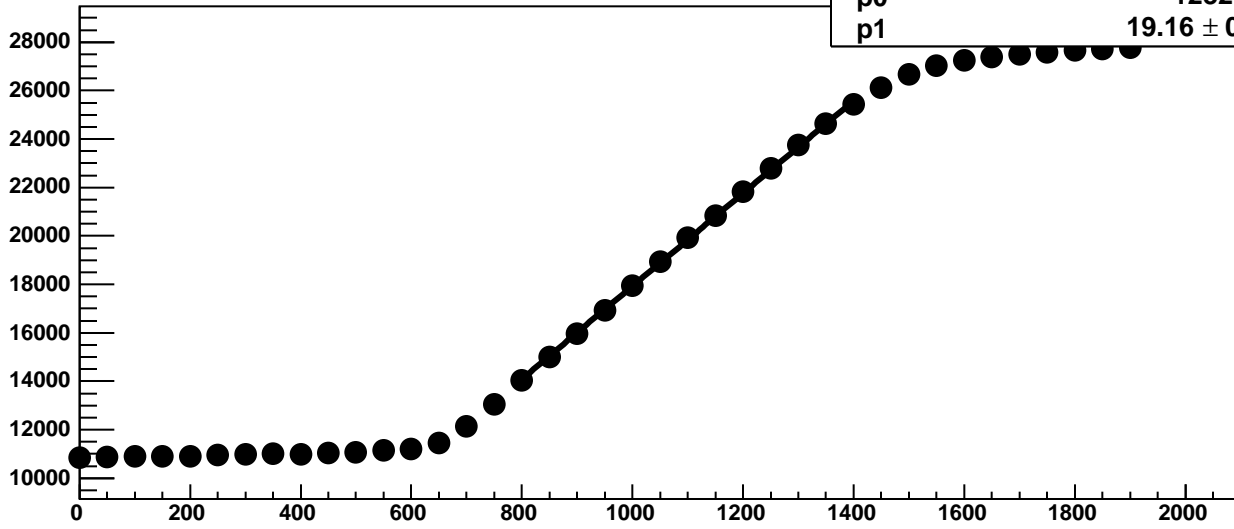
Chip 9, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC



Chip 9, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC

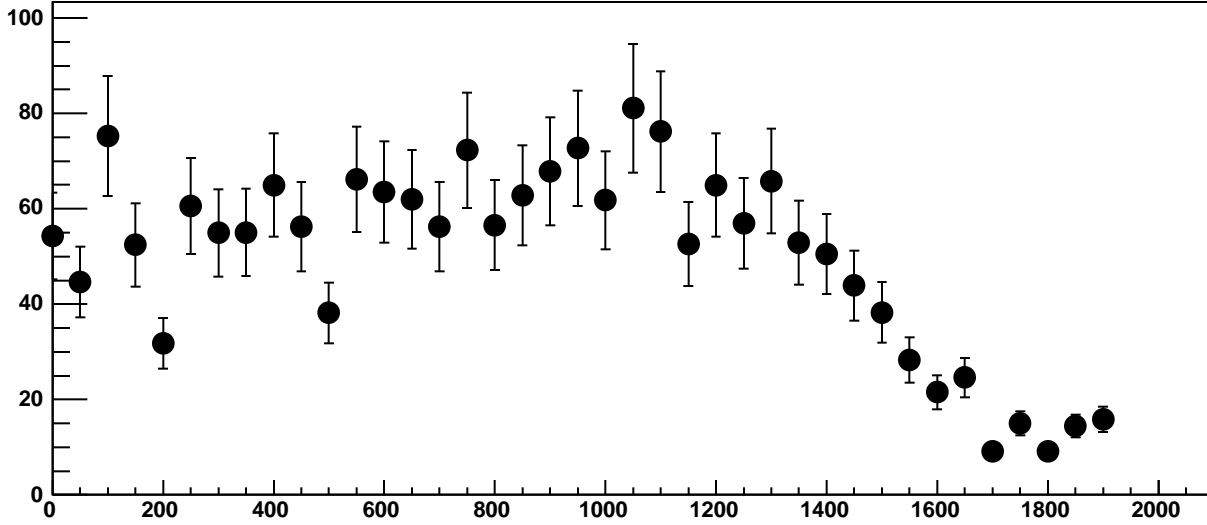


Chip 9, Channel 6, Enable 5!, Hold=30, ADC Mean vs DAC

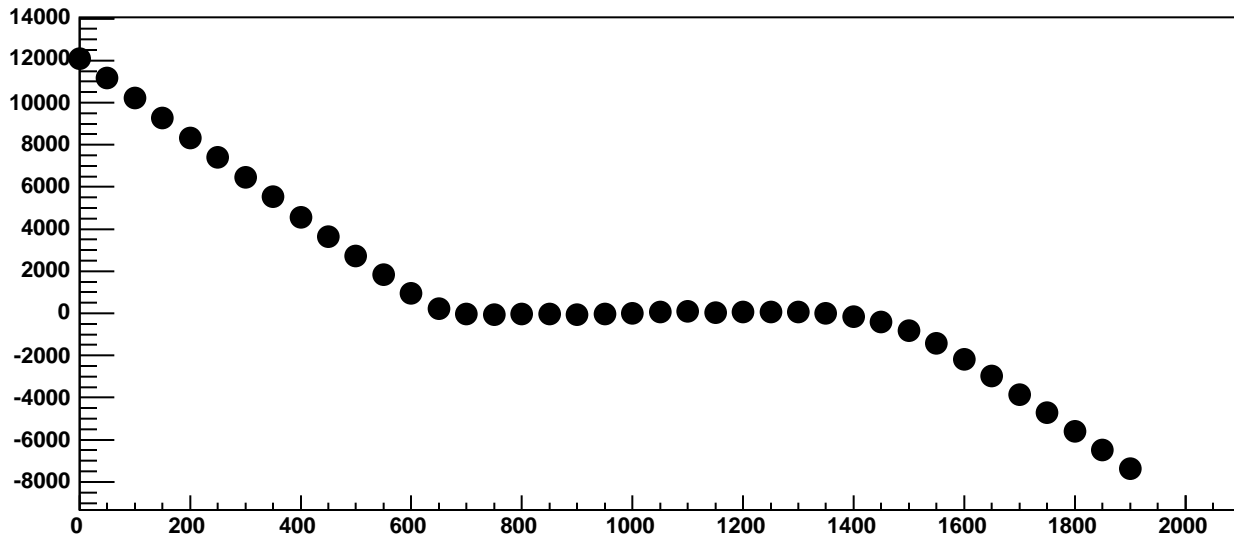


$\chi^2 / \text{ndf}$  341.7 / 11  
p0  $-1232 \pm 22.5$   
p1  $19.16 \pm 0.01973$

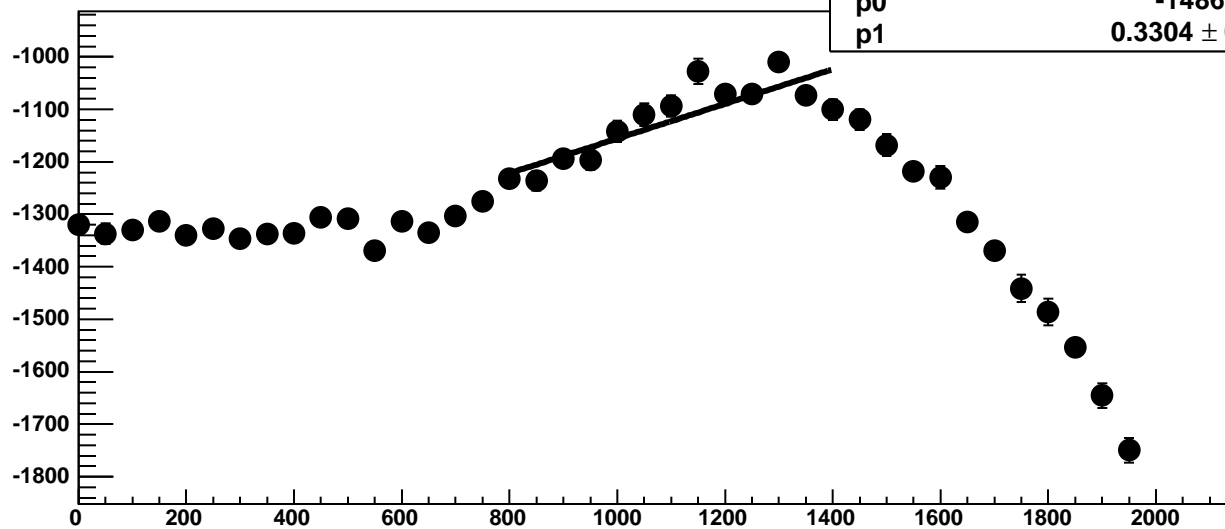
Chip 9, Channel 6, Enable 5!, Hold=30, ADC Noise vs DAC



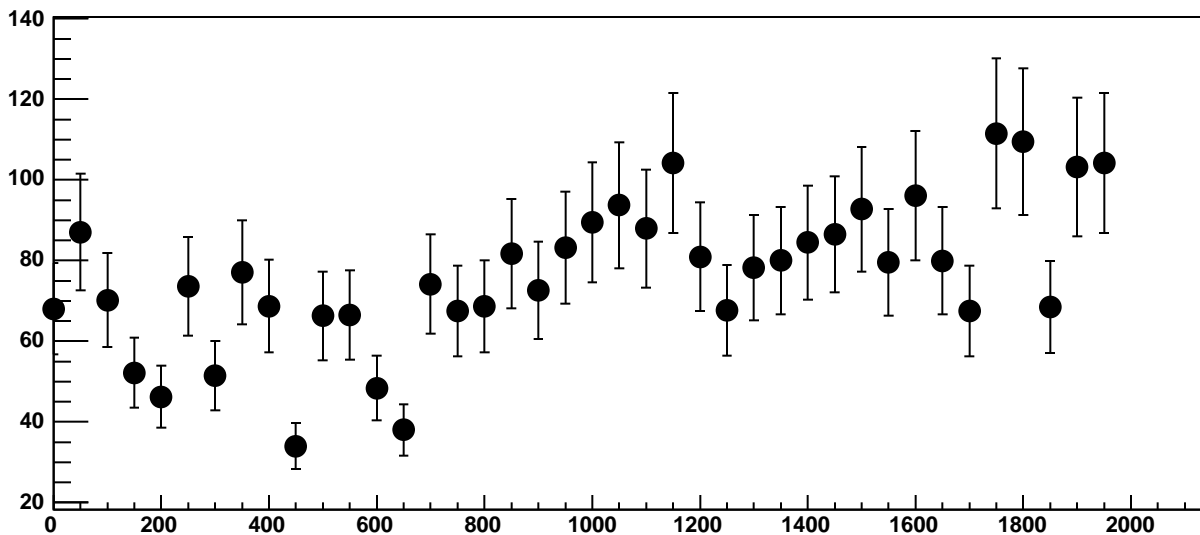
Chip 9, Channel 6, Enable 5!, Hold=30, ADC Residuals vs DAC



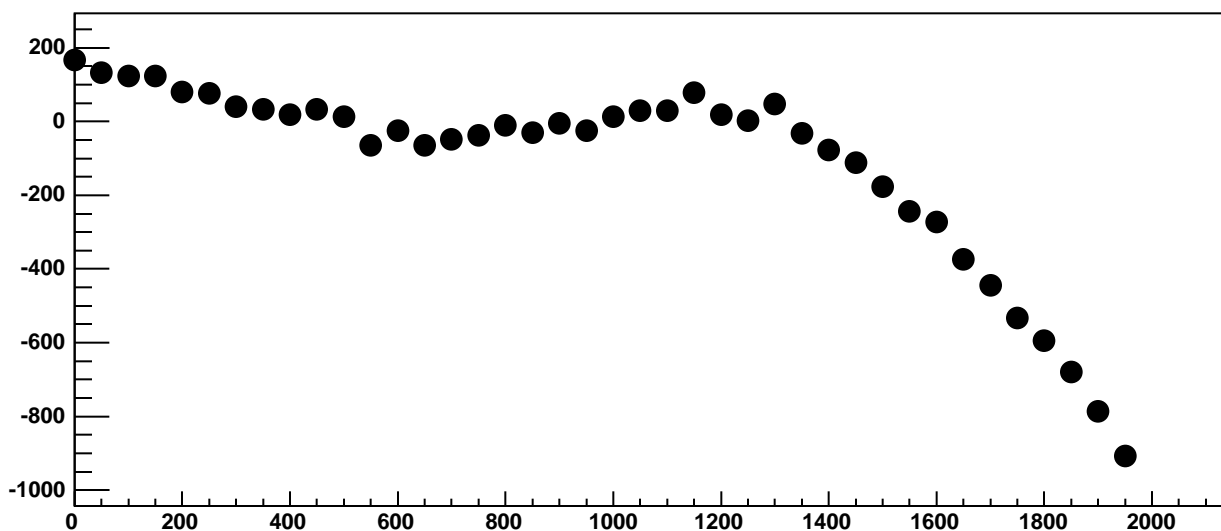
Chip 9, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC



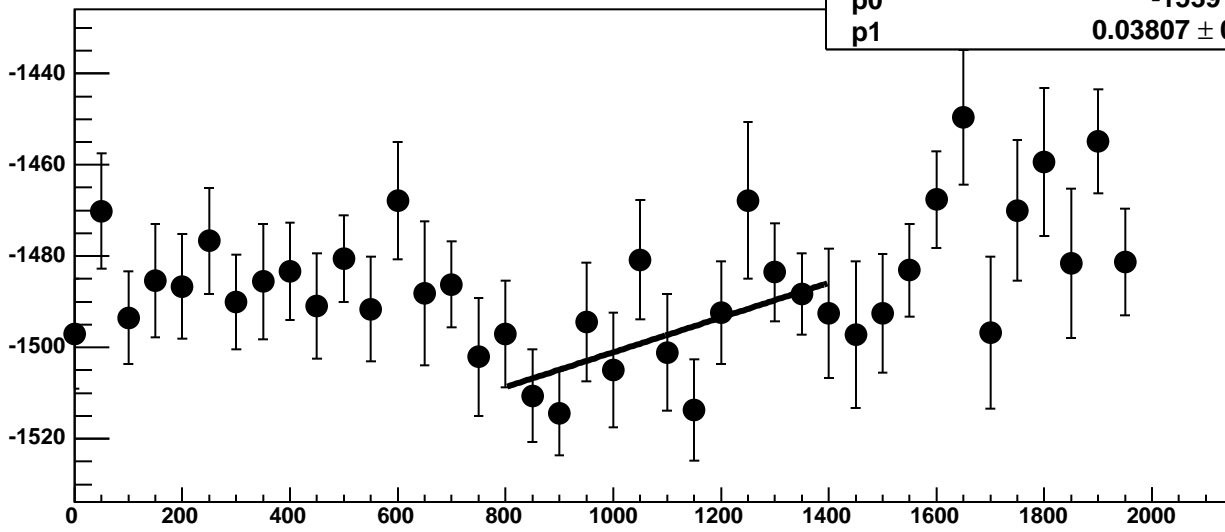
Chip 9, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC



Chip 9, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC

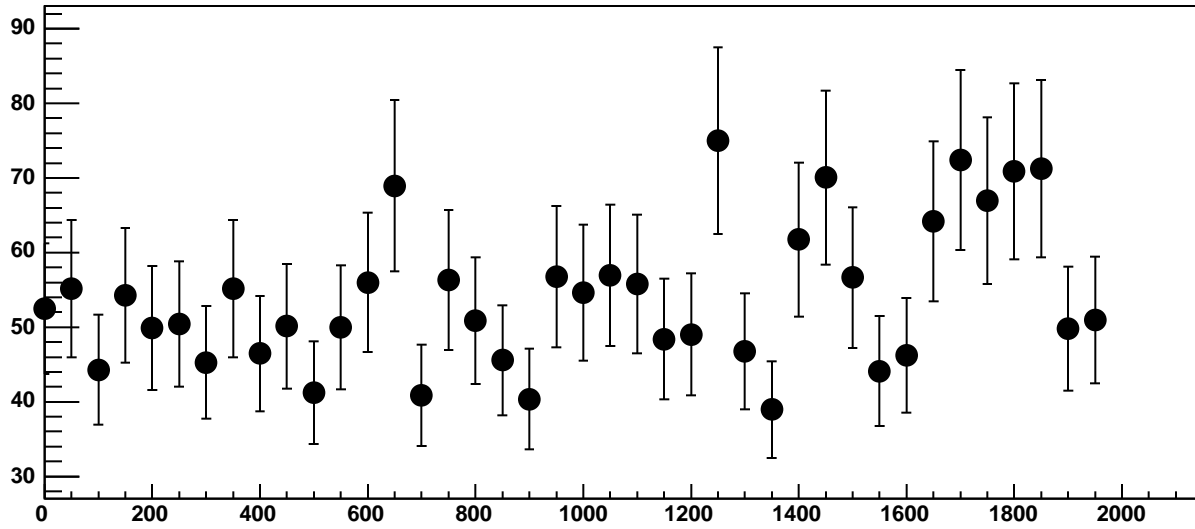


Chip 9, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC

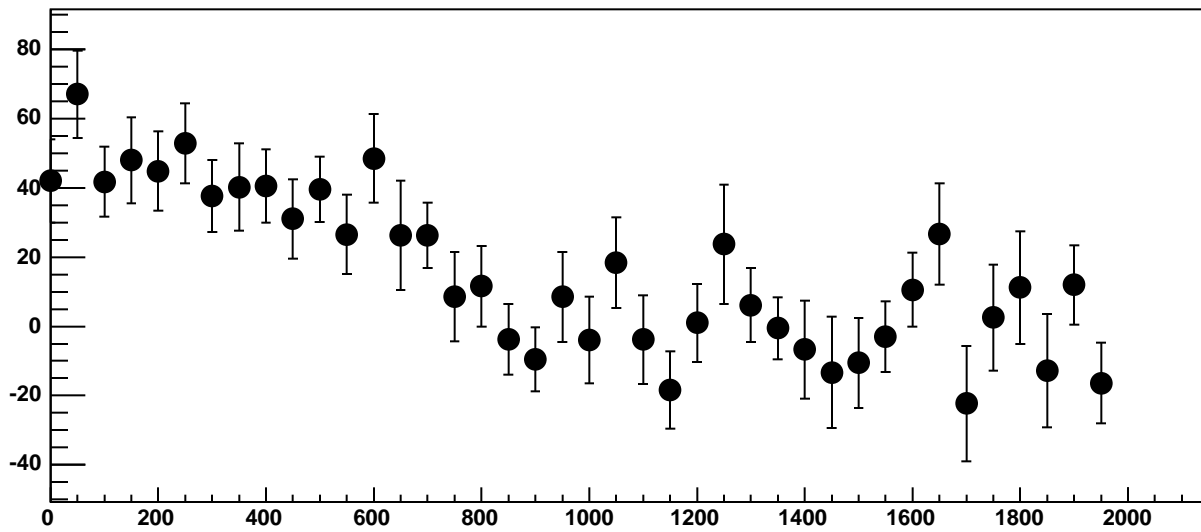


$\chi^2 / \text{ndf}$  9.988 / 11  
p0  $-1539 \pm 18.28$   
p1  $0.03807 \pm 0.01647$

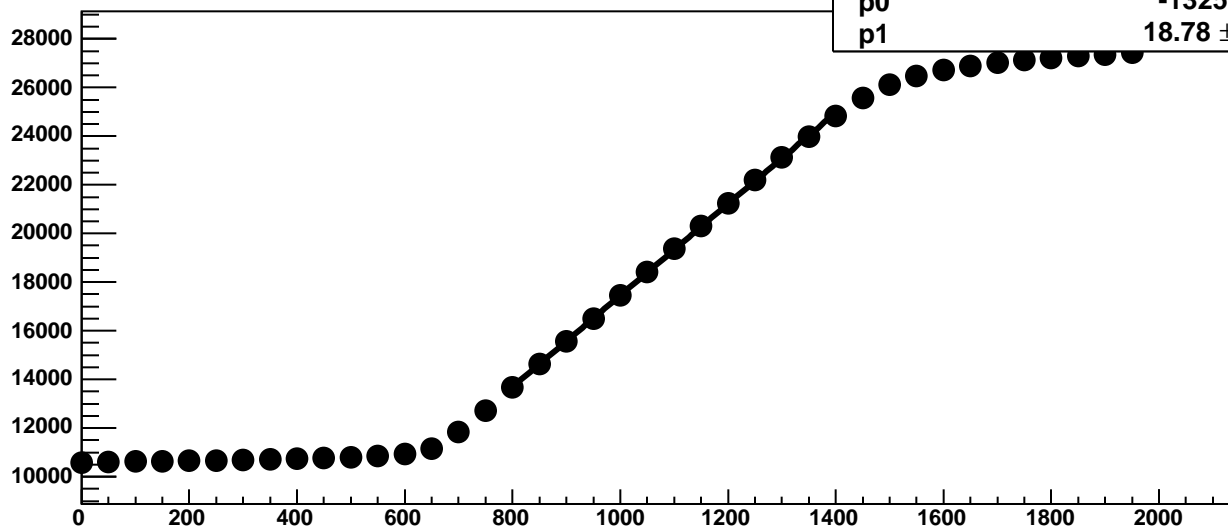
Chip 9, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC



Chip 9, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC

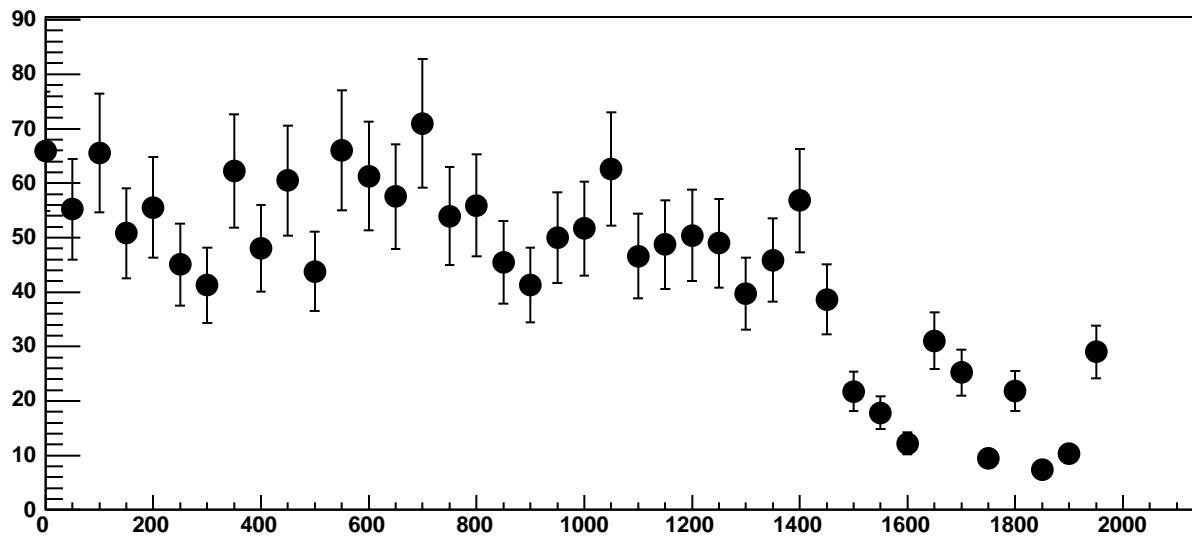


Chip 9, Channel 7, Enable 2!, Hold=30, ADC Mean vs DAC

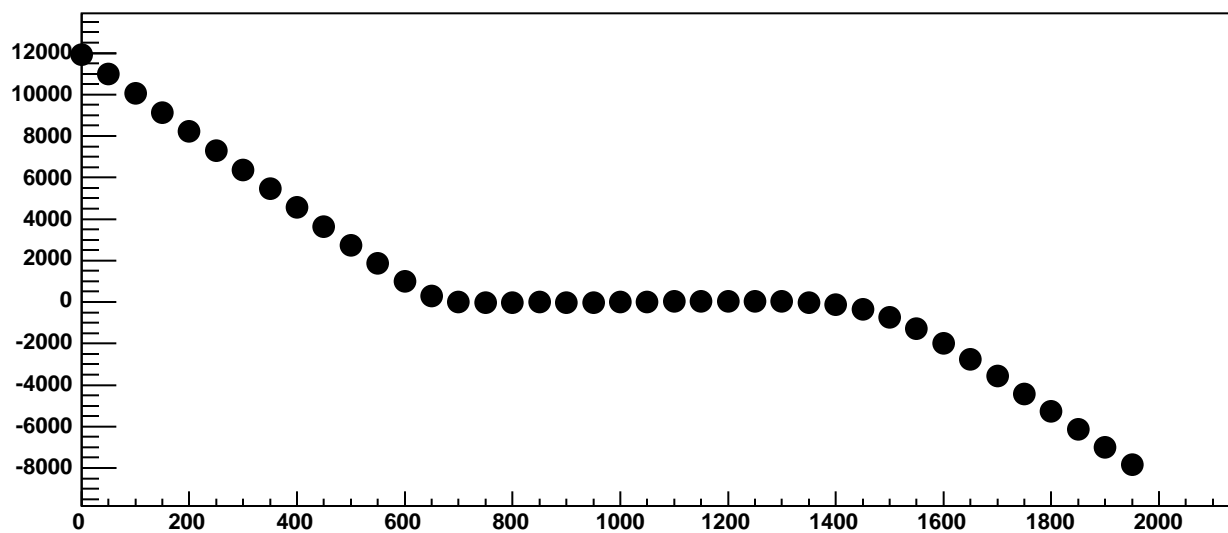


$\chi^2 / \text{ndf}$  208.4 / 11  
p0  $-1325 \pm 18.47$   
p1  $18.78 \pm 0.0165$

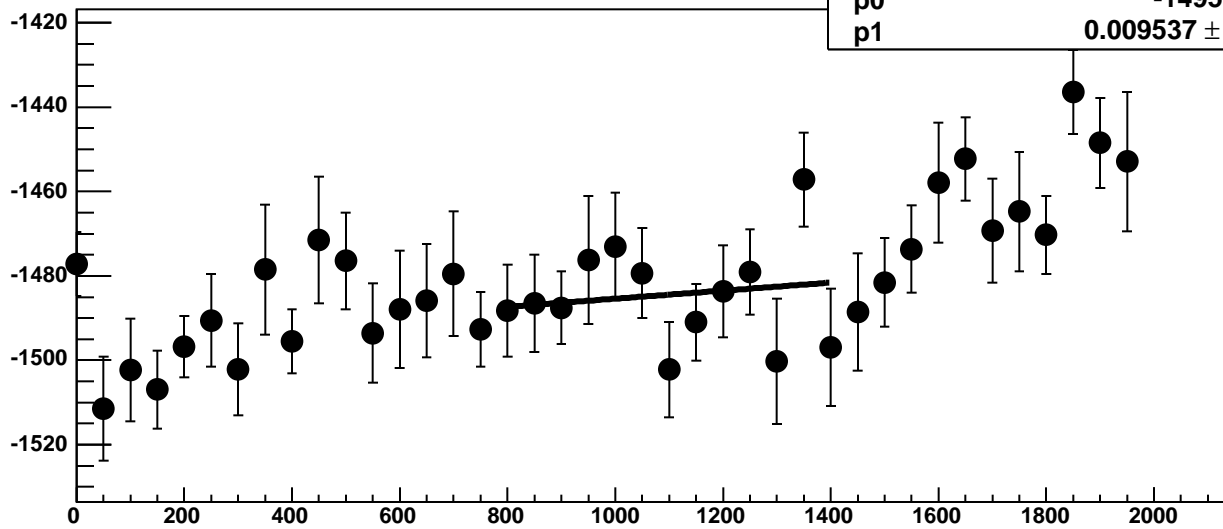
Chip 9, Channel 7, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 9, Channel 7, Enable 2!, Hold=30, ADC Residuals vs DAC

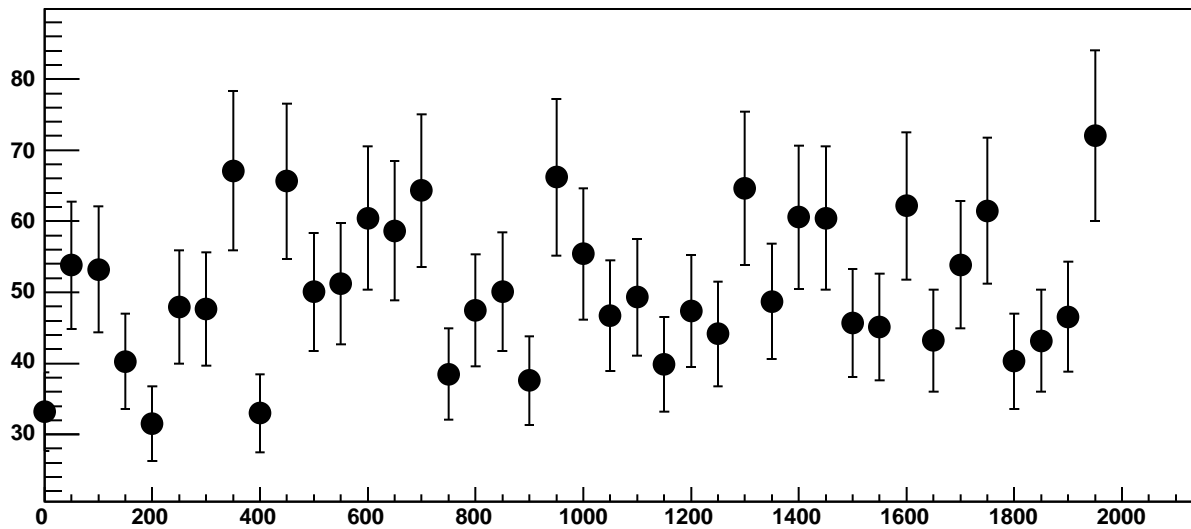


Chip 9, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC

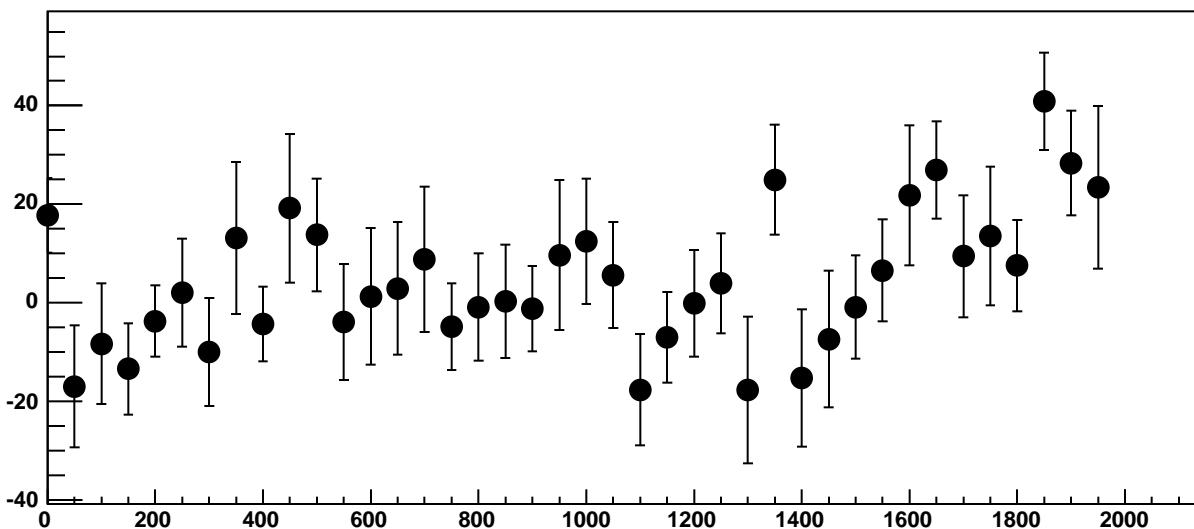


$\chi^2 / \text{ndf}$  12.46 / 11  
p0  $-1495 \pm 18.84$   
p1  $0.009537 \pm 0.01711$

Chip 9, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC

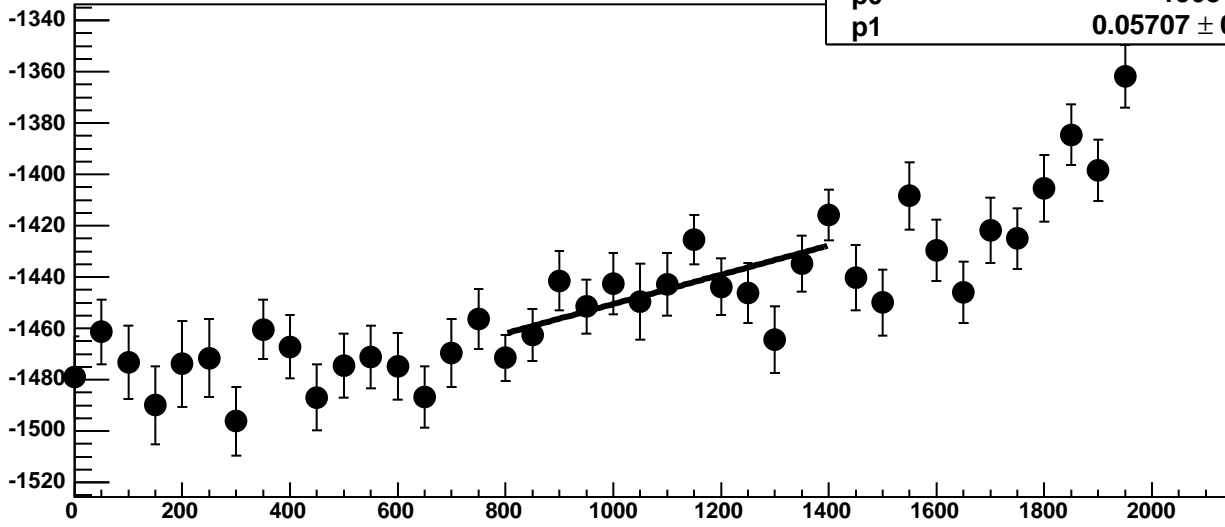


Chip 9, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC

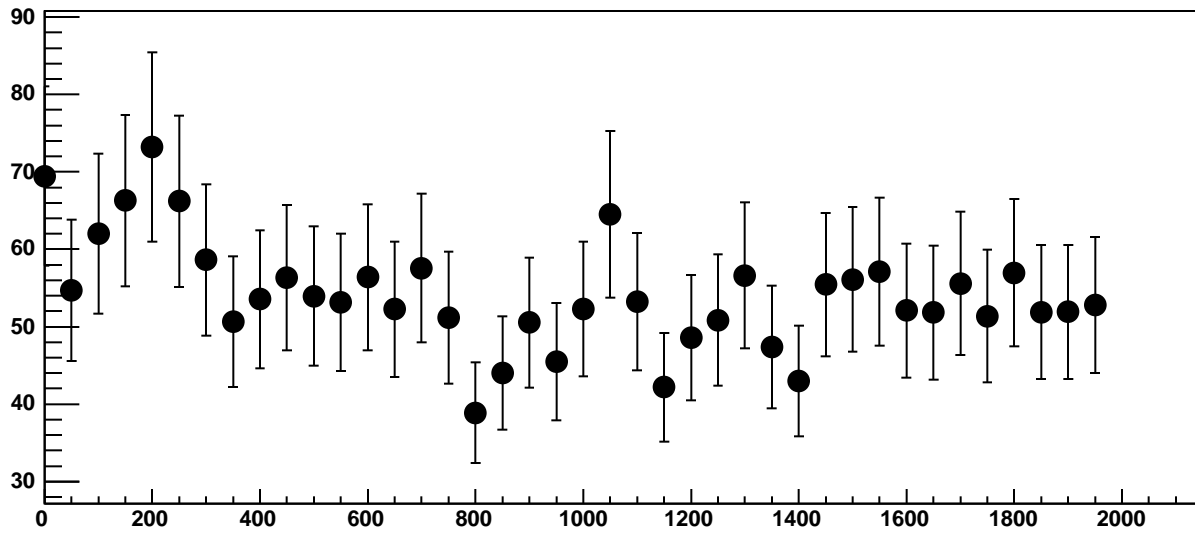




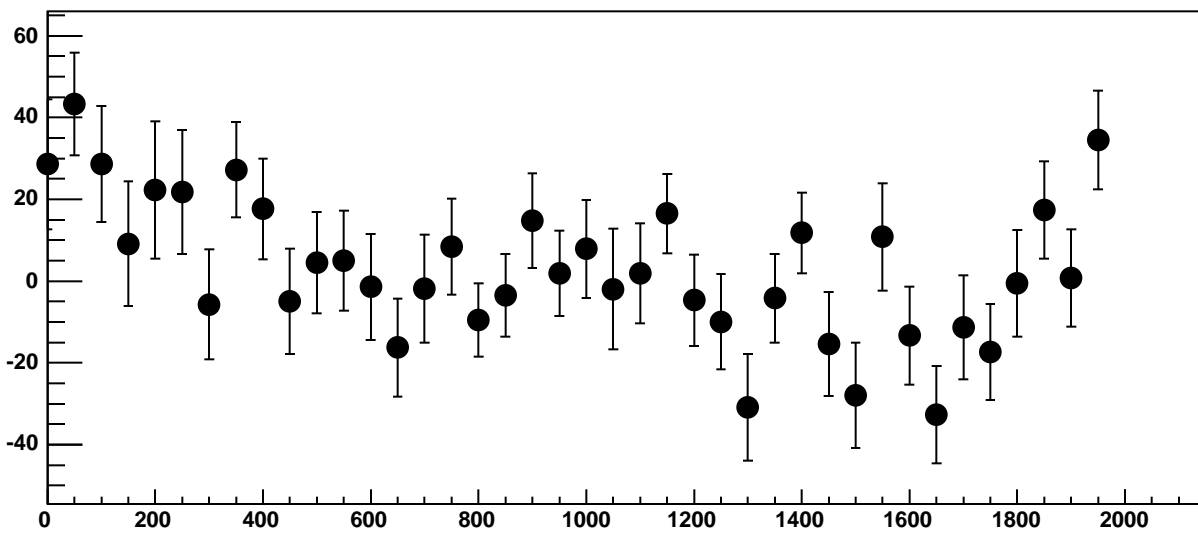
Chip 9, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC



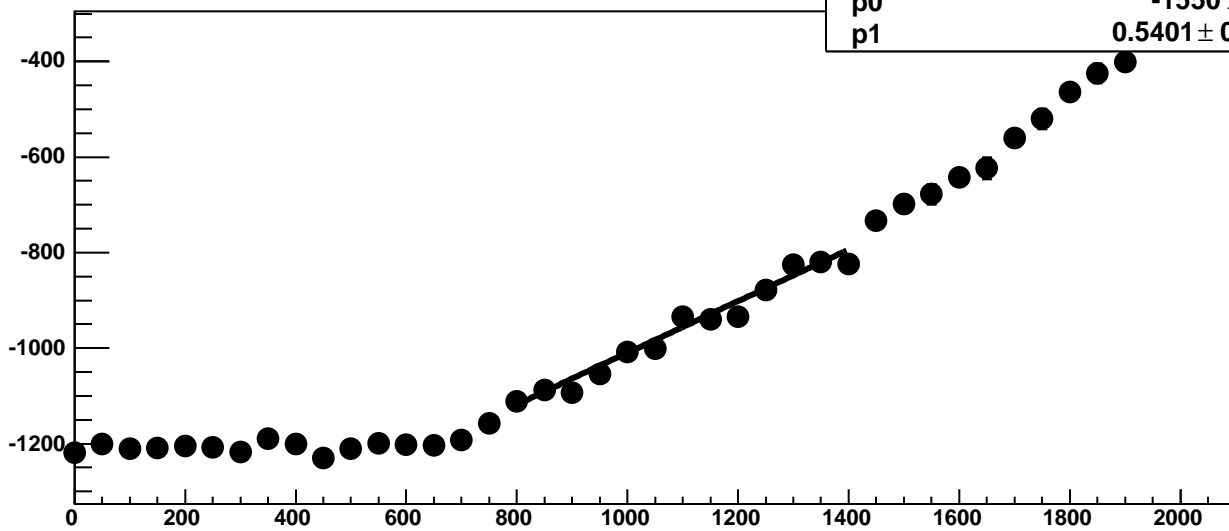
Chip 9, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC



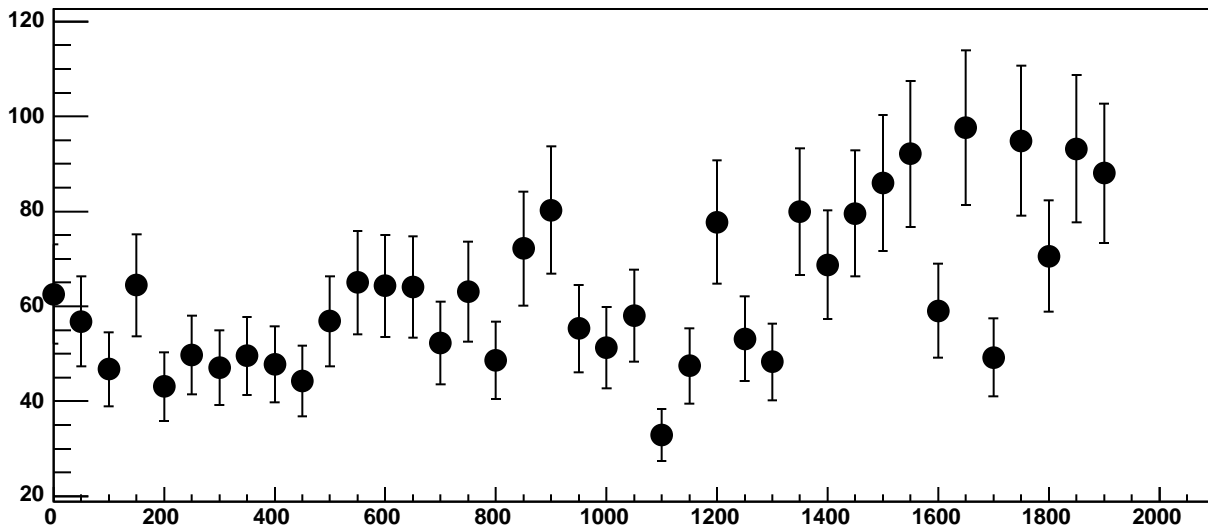
Chip 9, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC



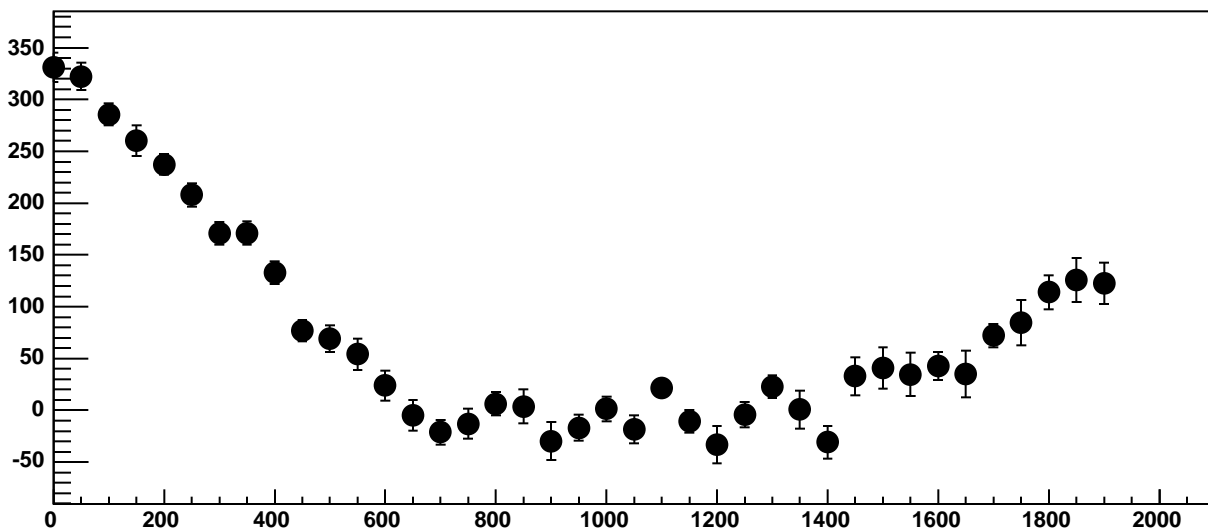
Chip 9, Channel 7, Enable 5, Hold=30, ADC Mean vs DAC



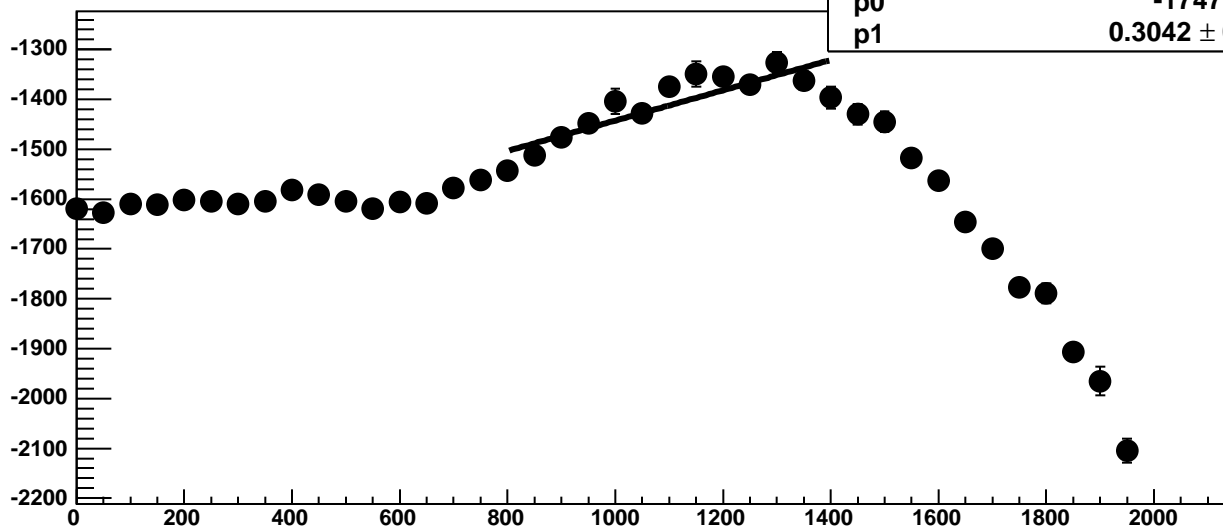
Chip 9, Channel 7, Enable 5, Hold=30, ADC Noise vs DAC



Chip 9, Channel 7, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 9, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

33.1 / 11

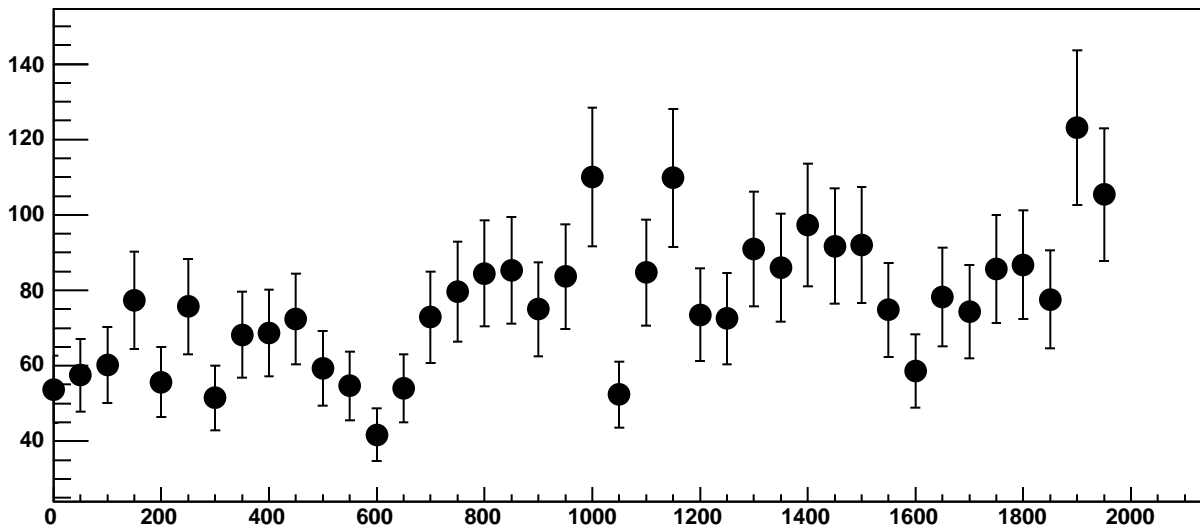
p0

$-1747 \pm 32.13$

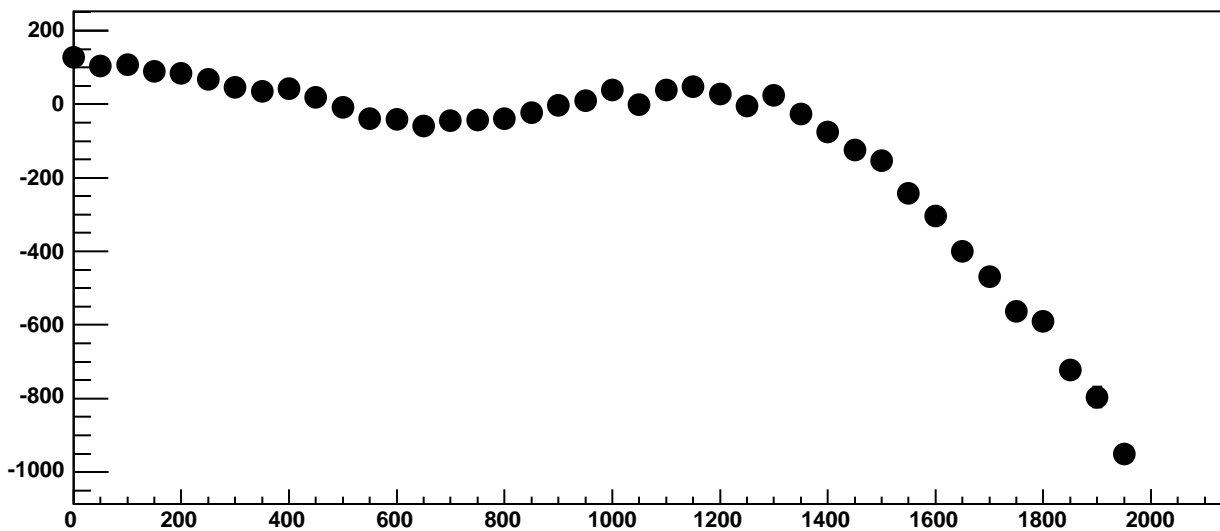
p1

$0.3042 \pm 0.02908$

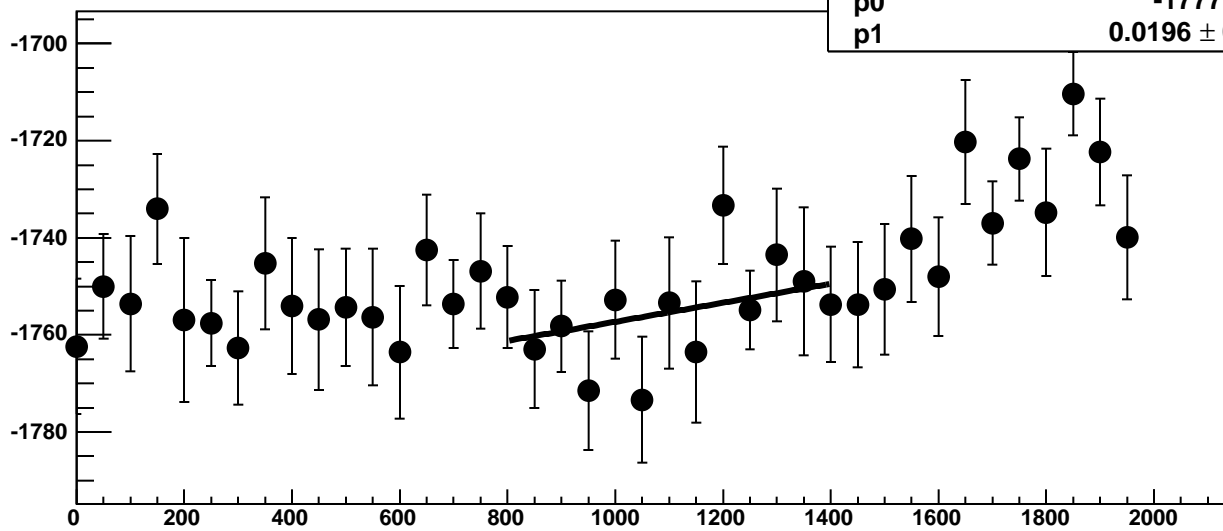
Chip 9, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC



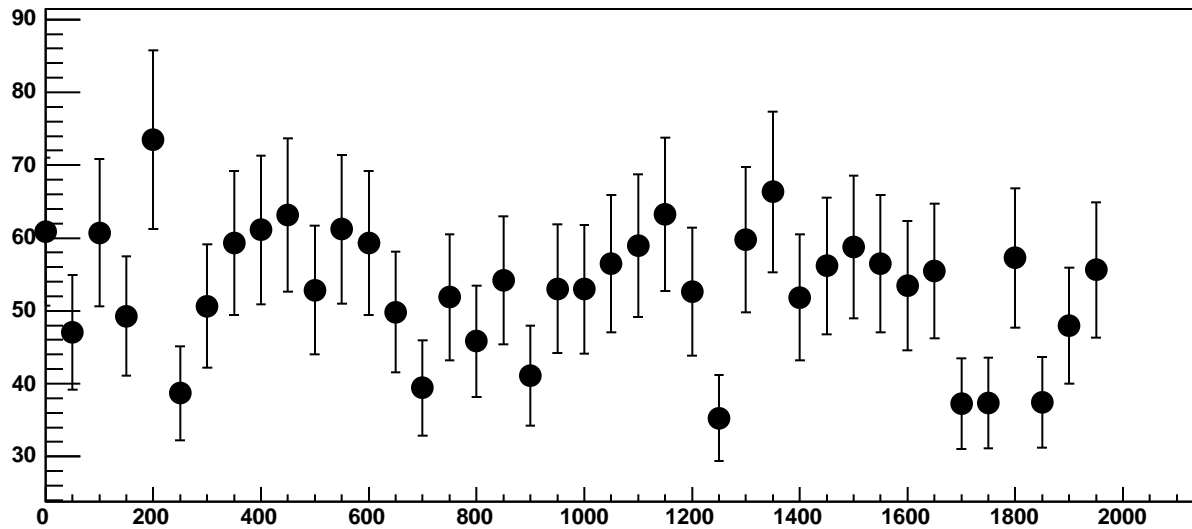
Chip 9, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC



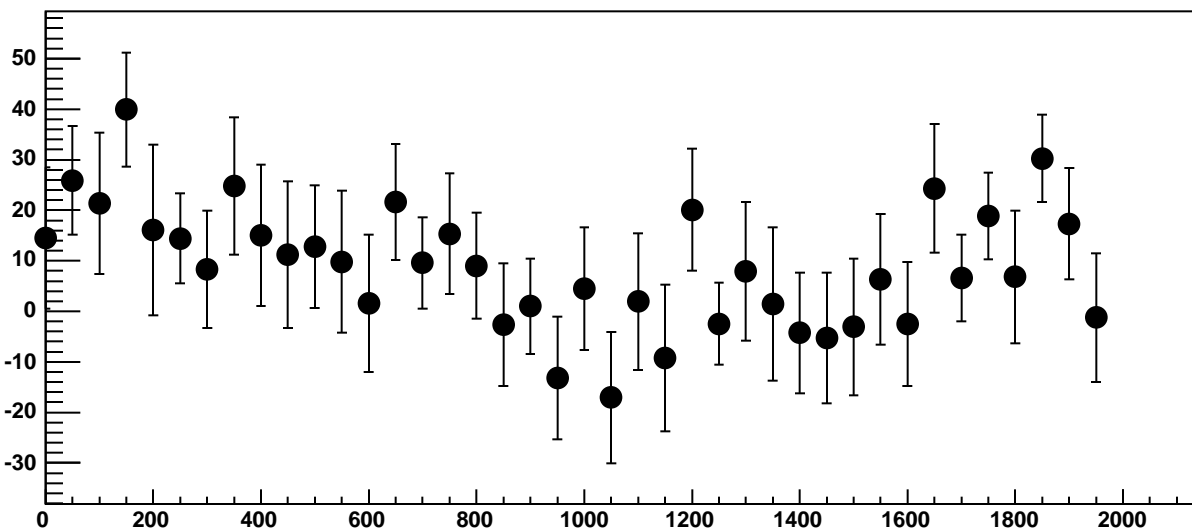
Chip 9, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC



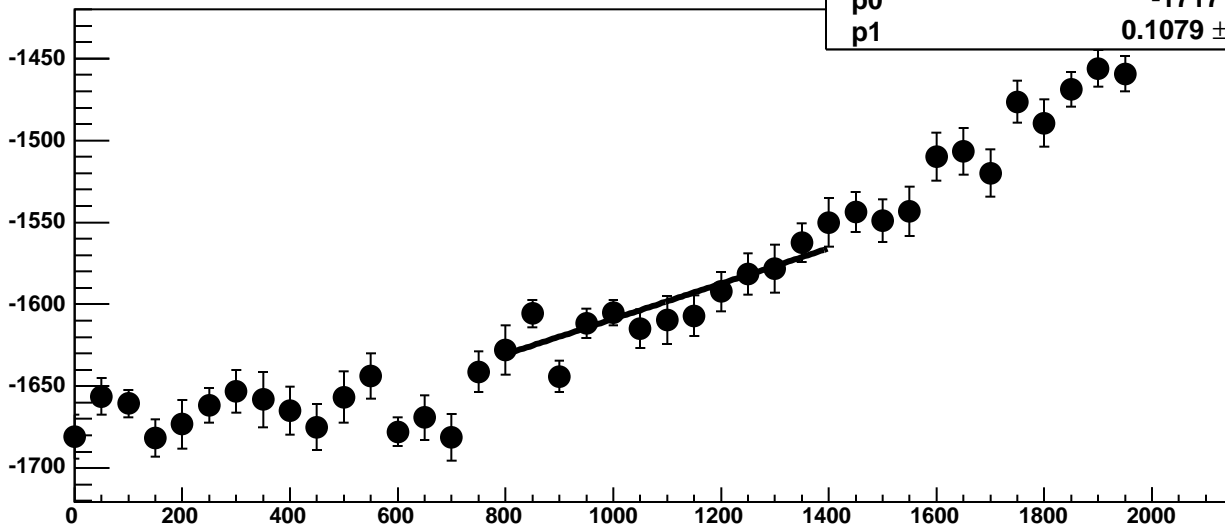
Chip 9, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC



Chip 9, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC

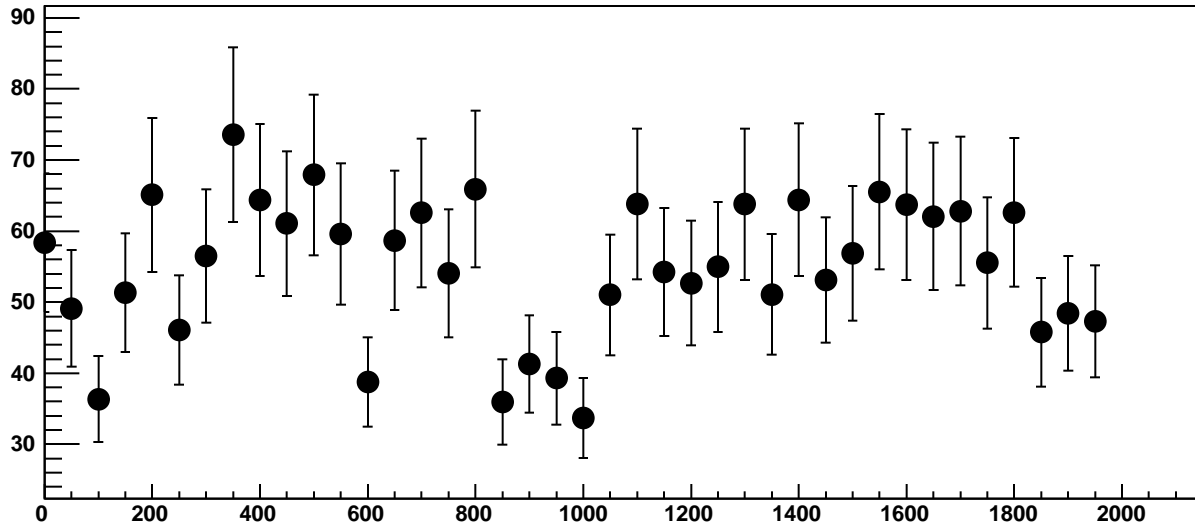


Chip 9, Channel 8, Enable 2, Hold=30, ADC Mean vs DAC

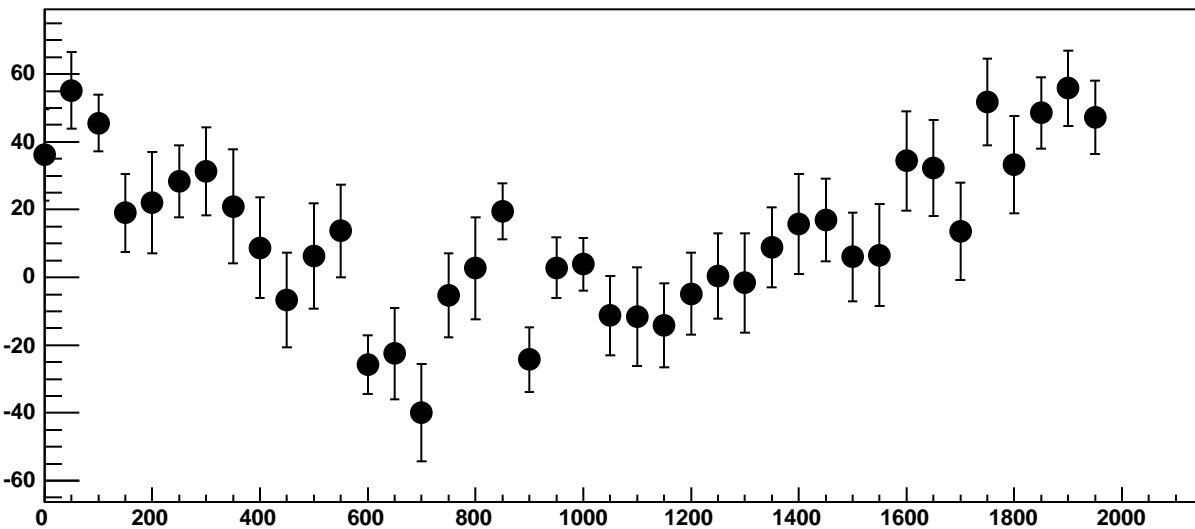


$\chi^2 / \text{ndf}$  17.26 / 11  
p0  $-1717 \pm 18.68$   
p1  $0.1079 \pm 0.0175$

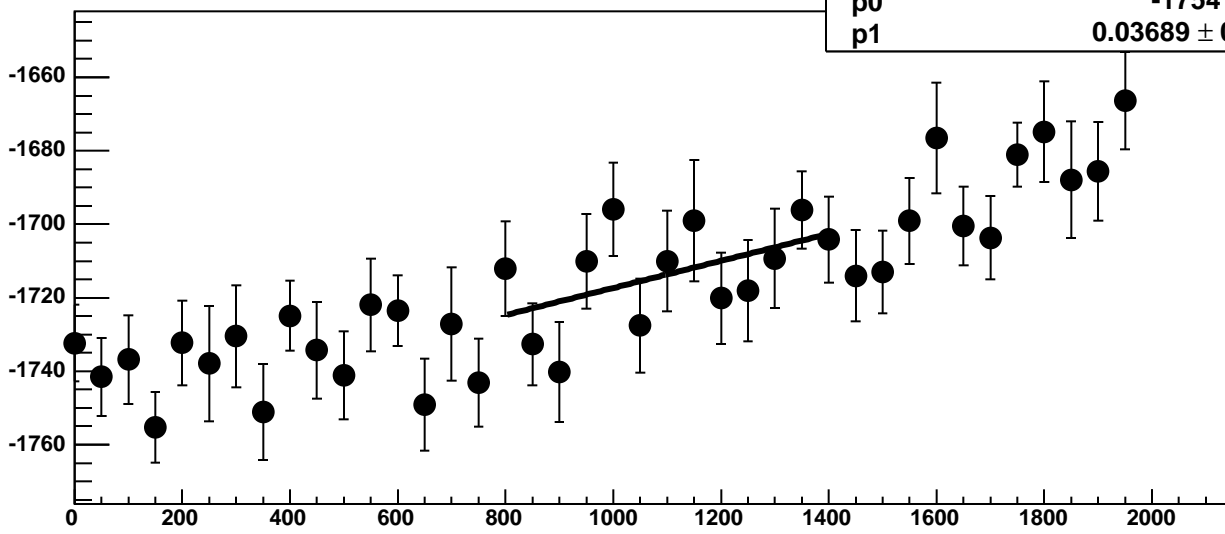
Chip 9, Channel 8, Enable 2, Hold=30, ADC Noise vs DAC



Chip 9, Channel 8, Enable 2, Hold=30, ADC Residuals vs DAC

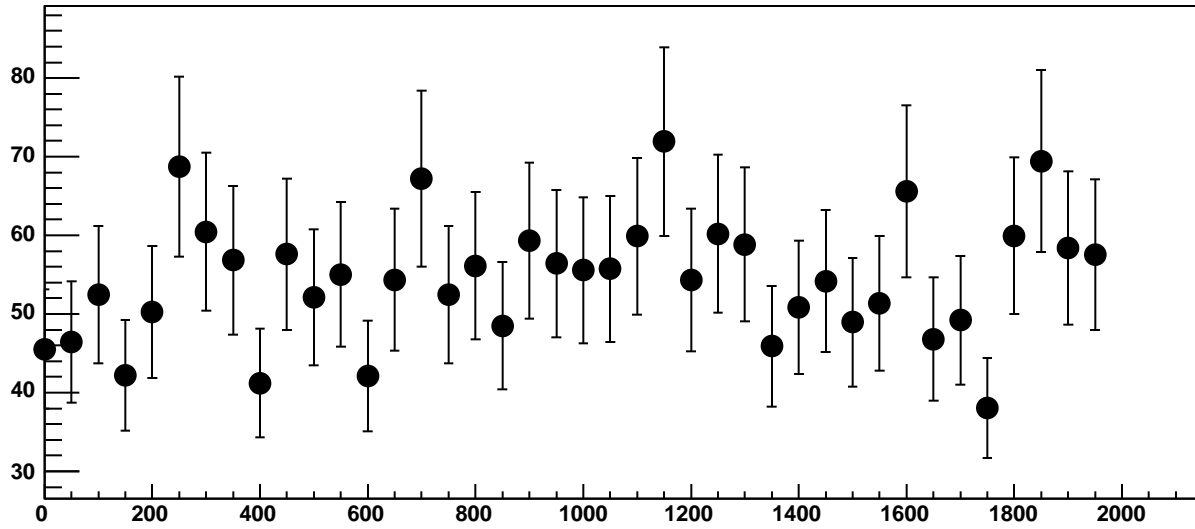


Chip 9, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC

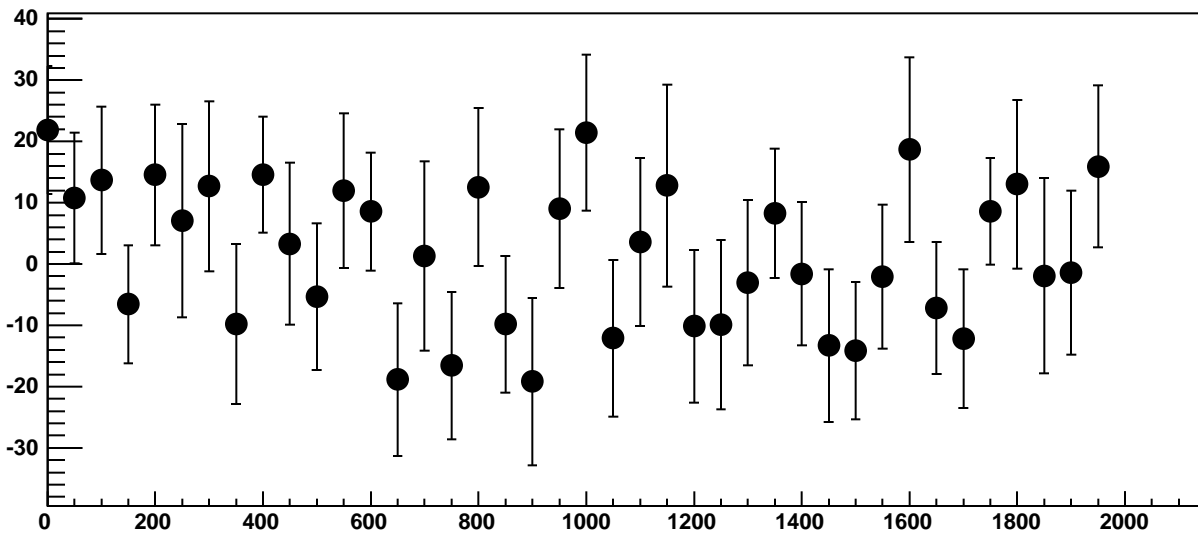


$\chi^2 / \text{ndf}$  10.47 / 11  
p0  $-1754 \pm 20.14$   
p1  $0.03689 \pm 0.01794$

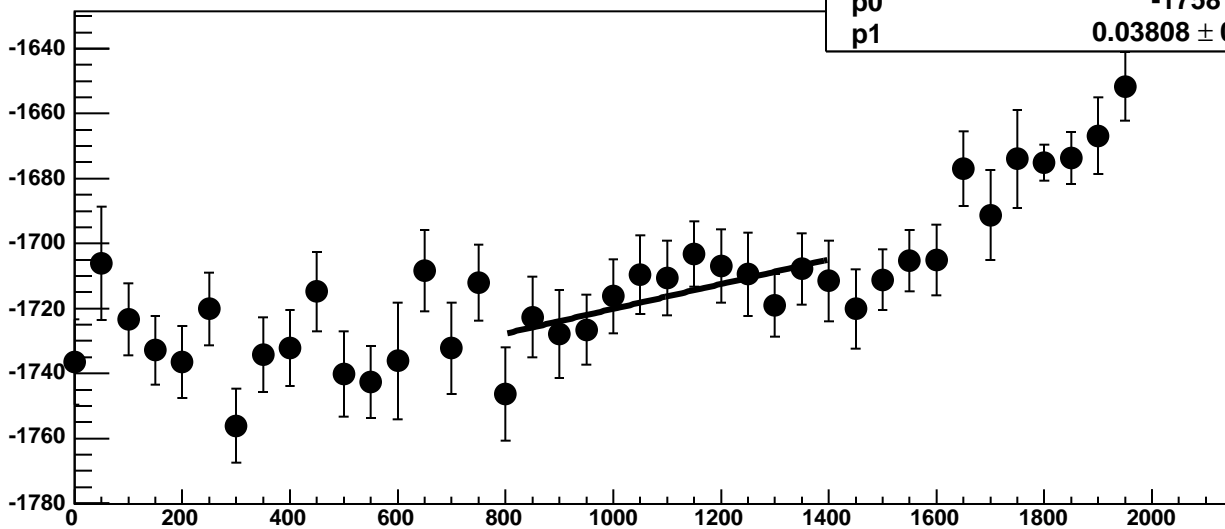
Chip 9, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC



Chip 9, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC

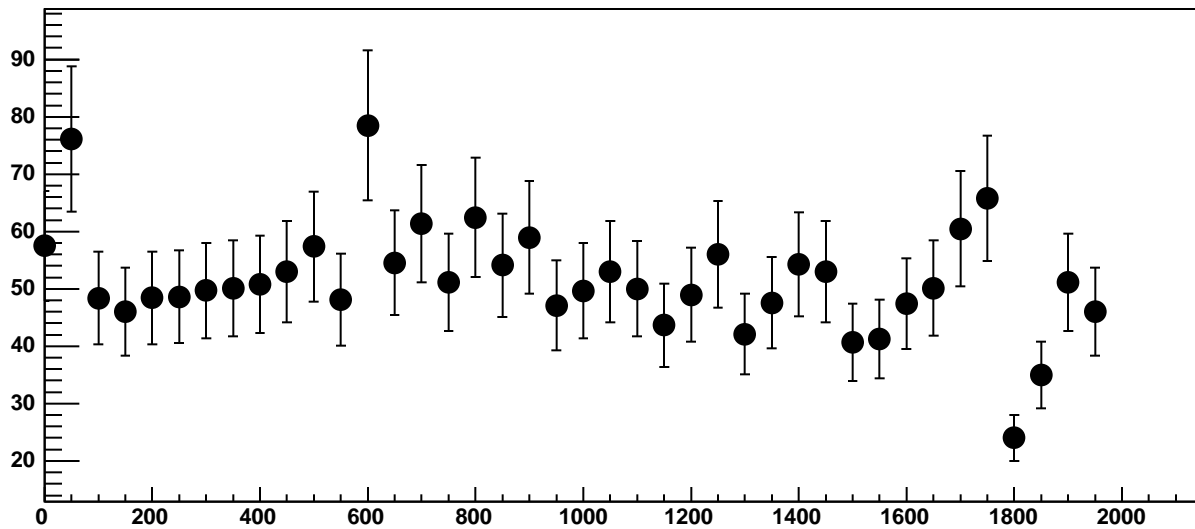


Chip 9, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC

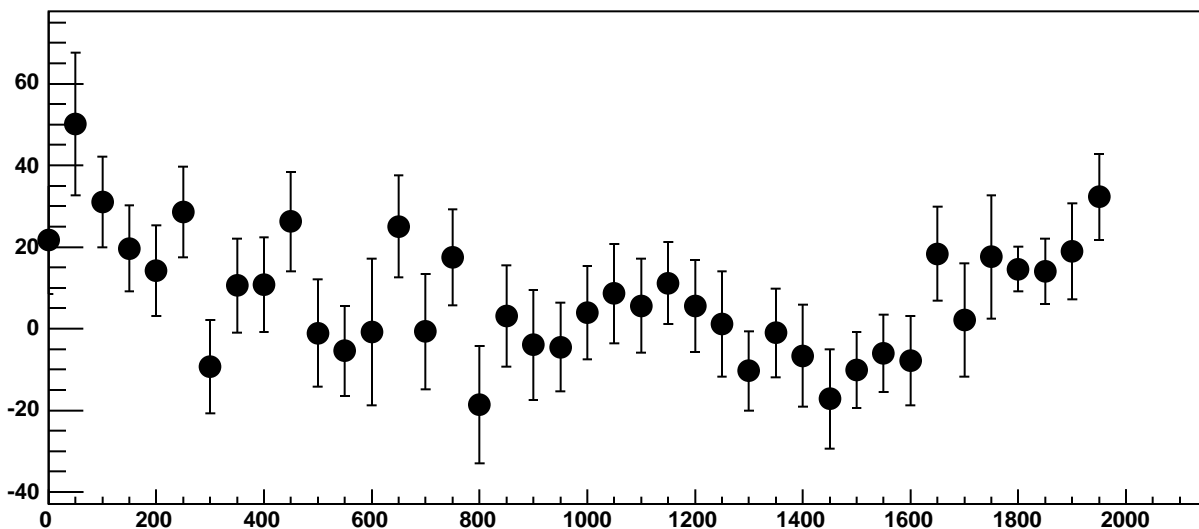


$\chi^2 / \text{ndf}$  5.803 / 11  
p0  $-1758 \pm 20.38$   
p1  $0.03808 \pm 0.01799$

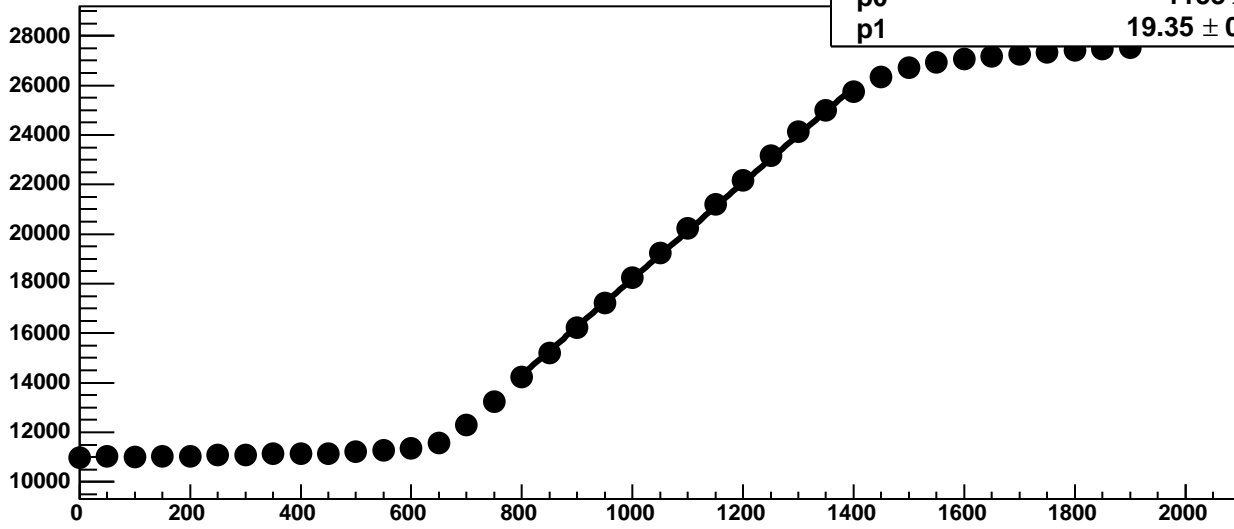
Chip 9, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC



Chip 9, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC

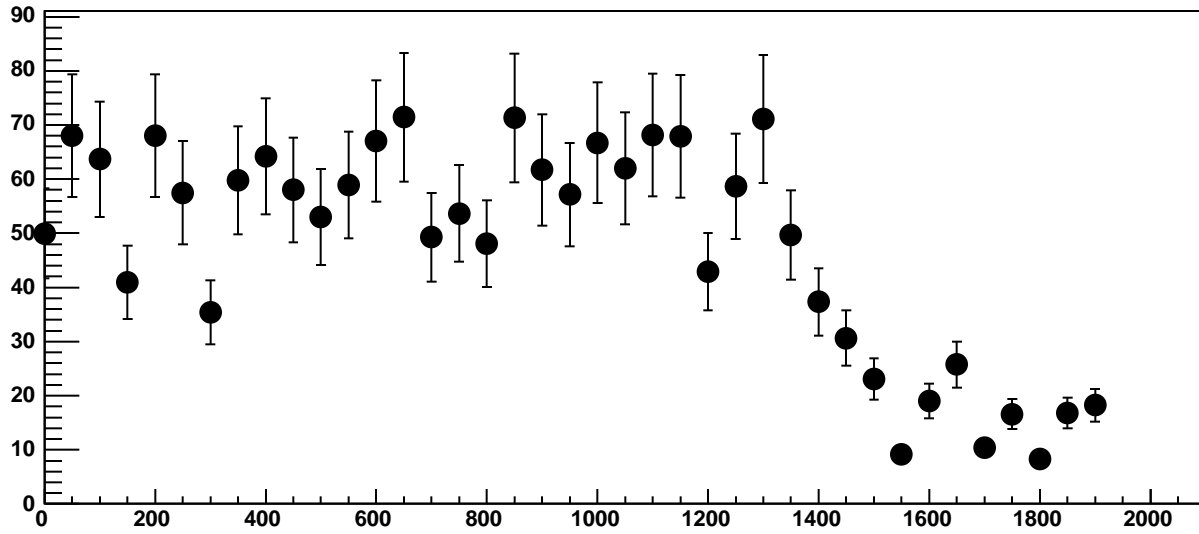


Chip 9, Channel 8, Enable 5!, Hold=30, ADC Mean vs DAC

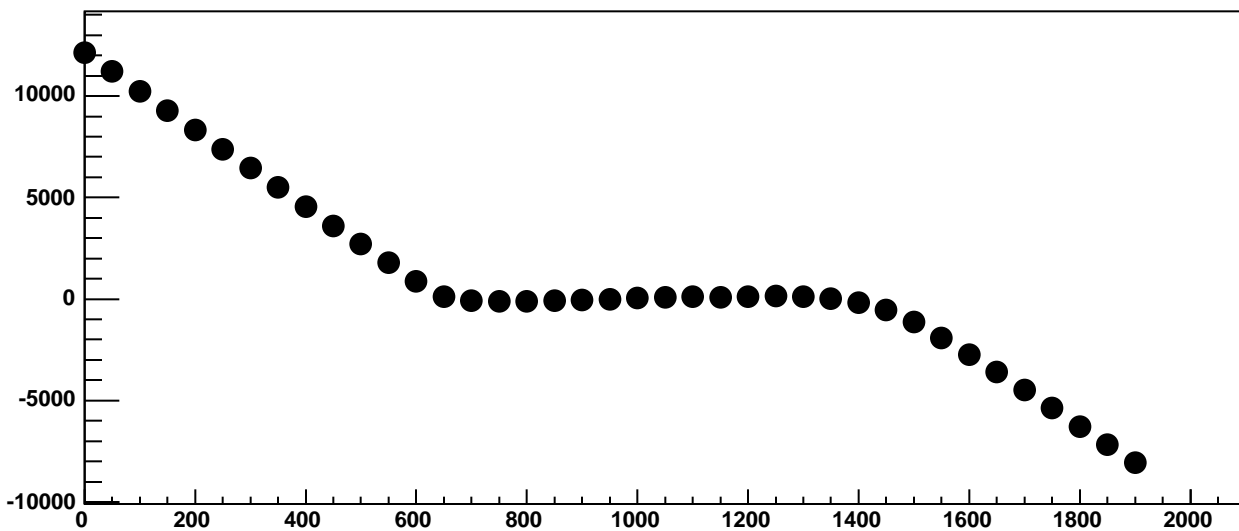


$\chi^2 / \text{ndf}$  1055 / 11  
p0  $-1153 \pm 19.92$   
p1  $19.35 \pm 0.01727$

Chip 9, Channel 8, Enable 5!, Hold=30, ADC Noise vs DAC

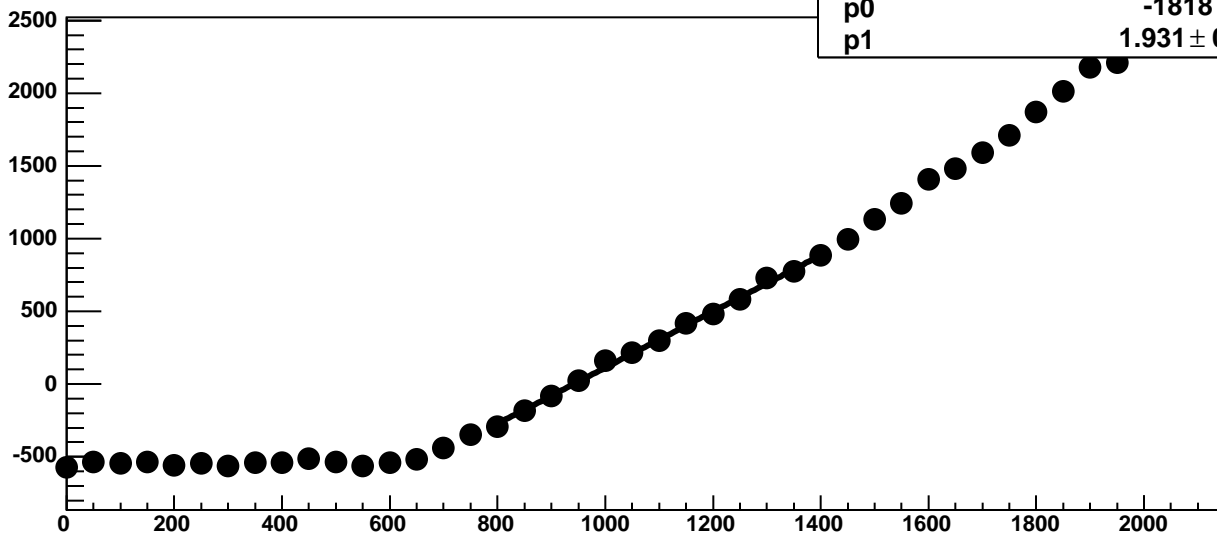


Chip 9, Channel 8, Enable 5!, Hold=30, ADC Residuals vs DAC



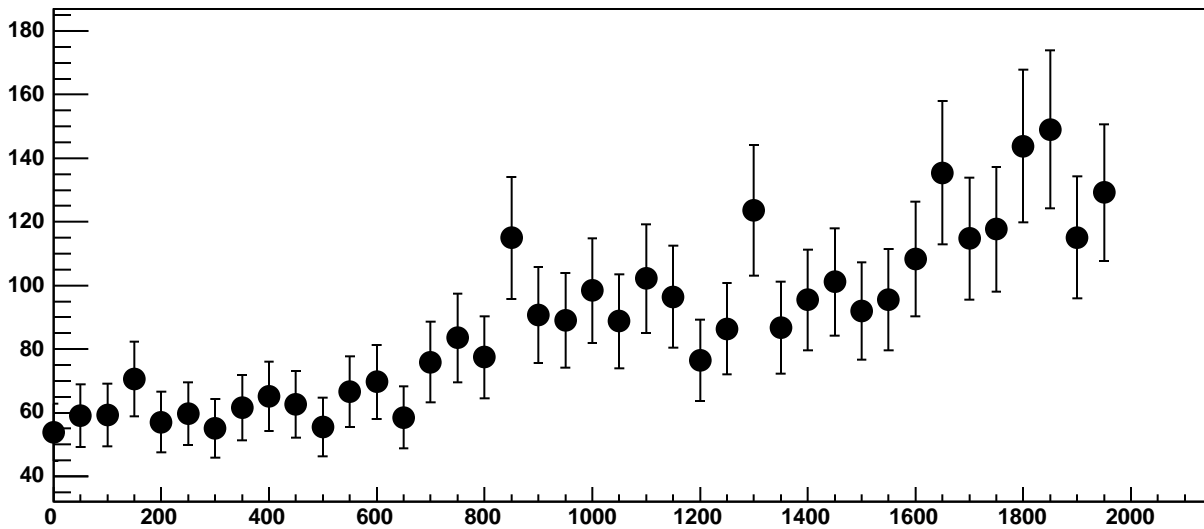


Chip 9, Channel 9, Enable 0, Hold=30, ADC Mean vs DAC

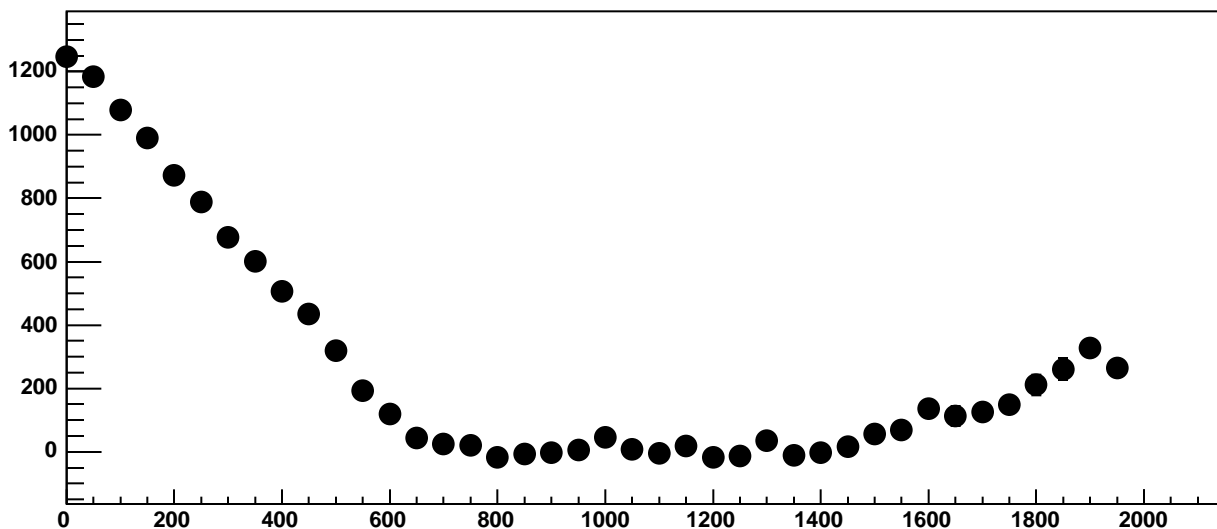


$\chi^2 / \text{ndf}$  9.547 / 11  
p0  $-1818 \pm 34.53$   
p1  $1.931 \pm 0.03107$

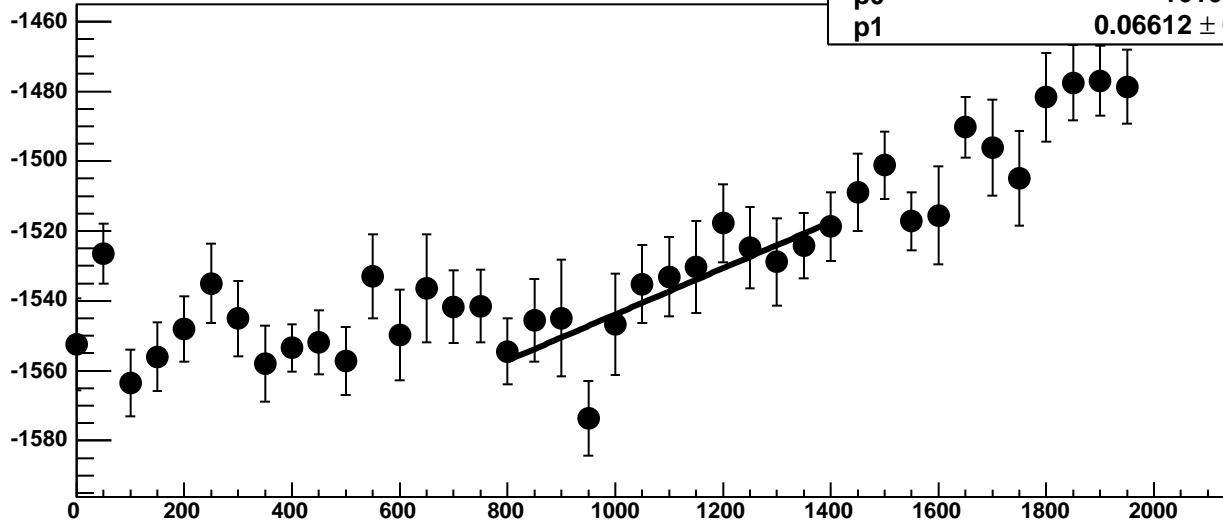
Chip 9, Channel 9, Enable 0, Hold=30, ADC Noise vs DAC



Chip 9, Channel 9, Enable 0, Hold=30, ADC Residuals vs DAC

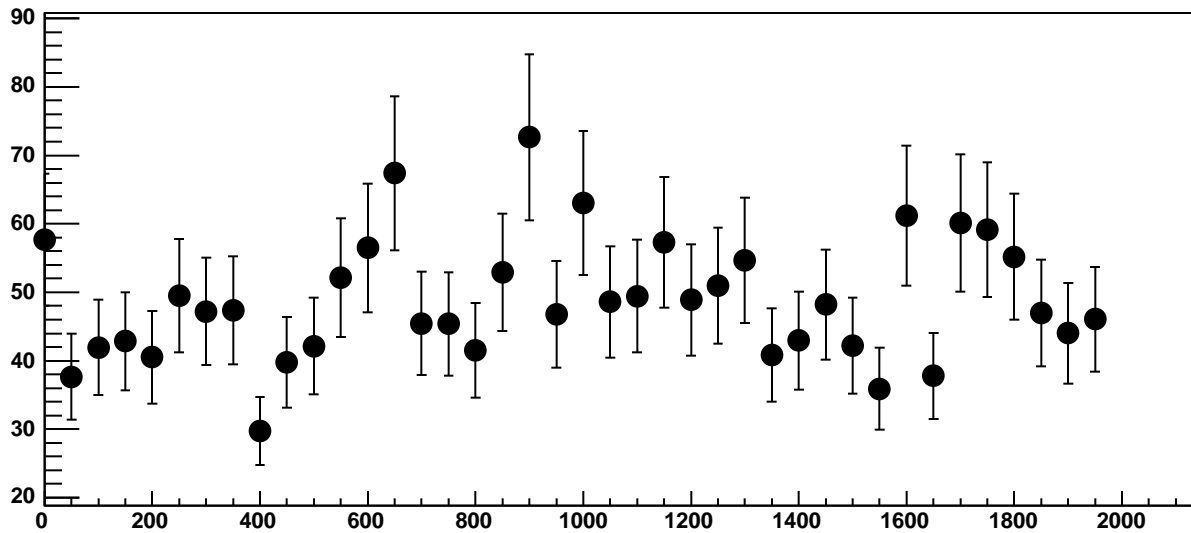


Chip 9, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC

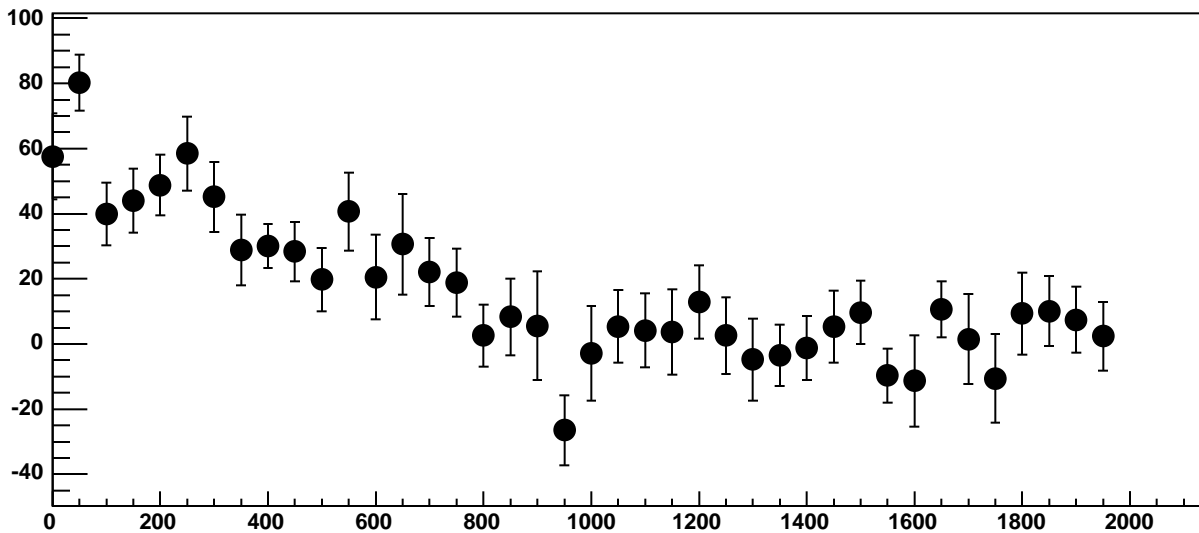


$\chi^2 / \text{ndf}$  8.897 / 11  
p0  $-1610 \pm 17.94$   
p1  $0.06612 \pm 0.01585$

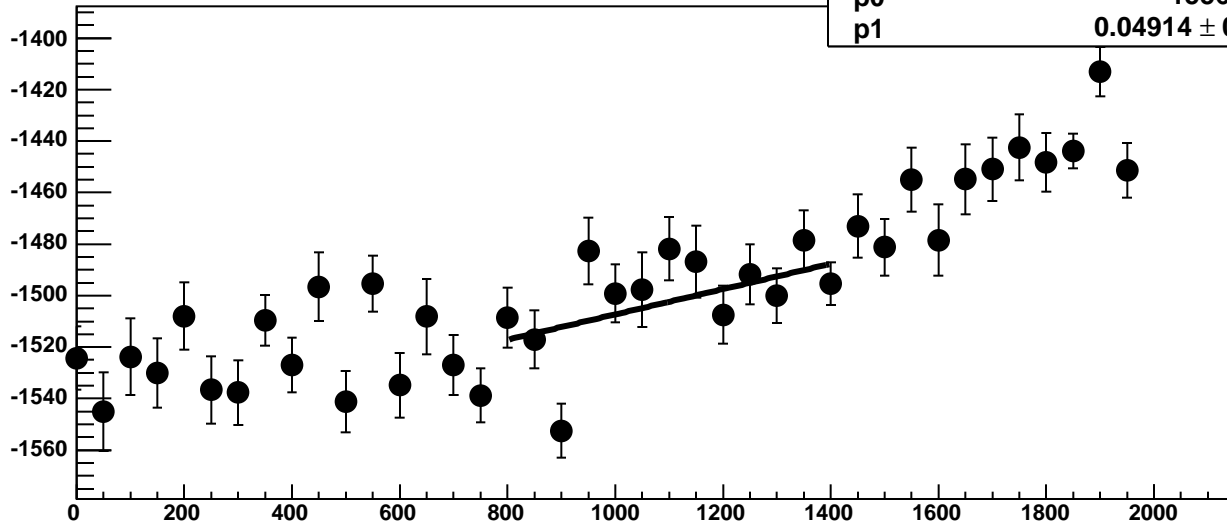
Chip 9, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC



Chip 9, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC

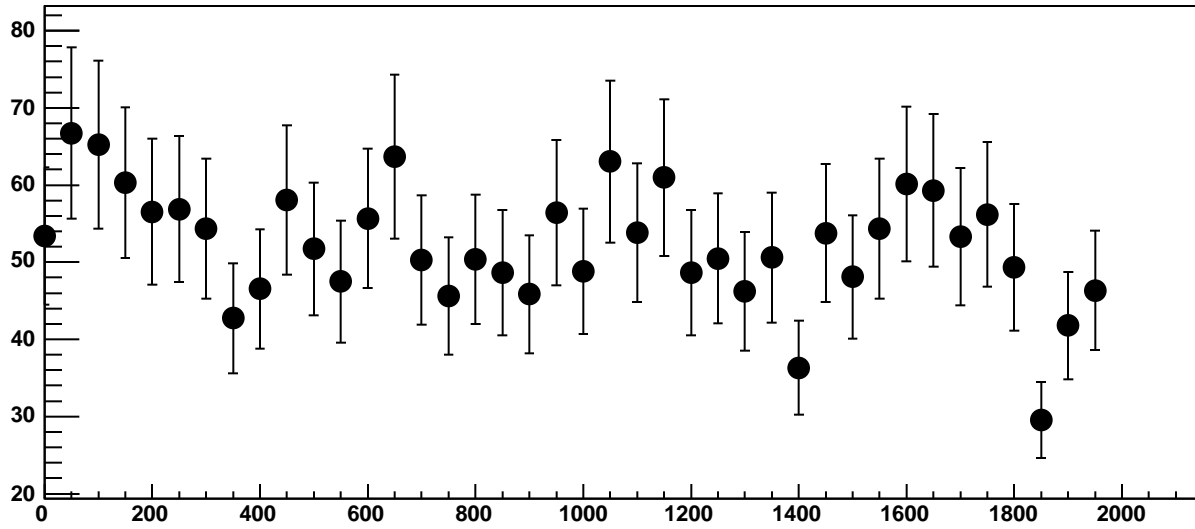


Chip 9, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC

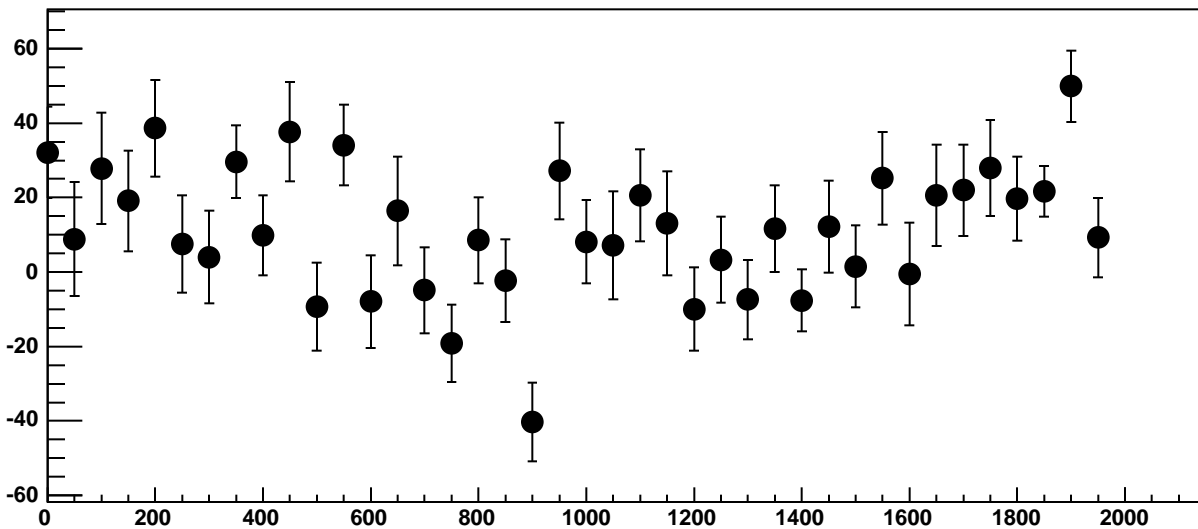


$\chi^2 / \text{ndf}$  27.25 / 11  
p0  $-1556 \pm 17.8$   
p1  $0.04914 \pm 0.01563$

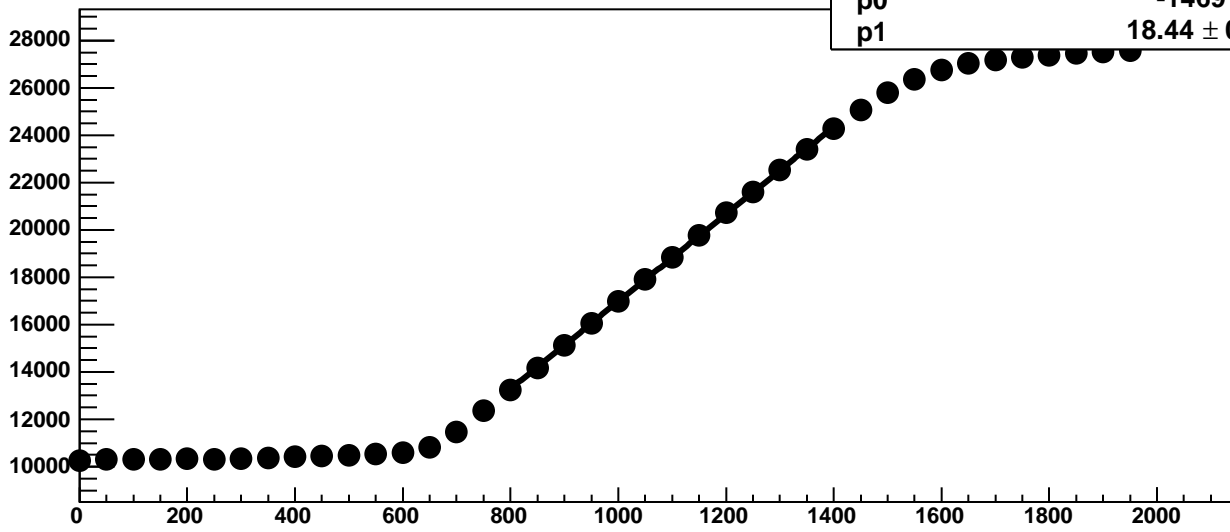
Chip 9, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC



Chip 9, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC

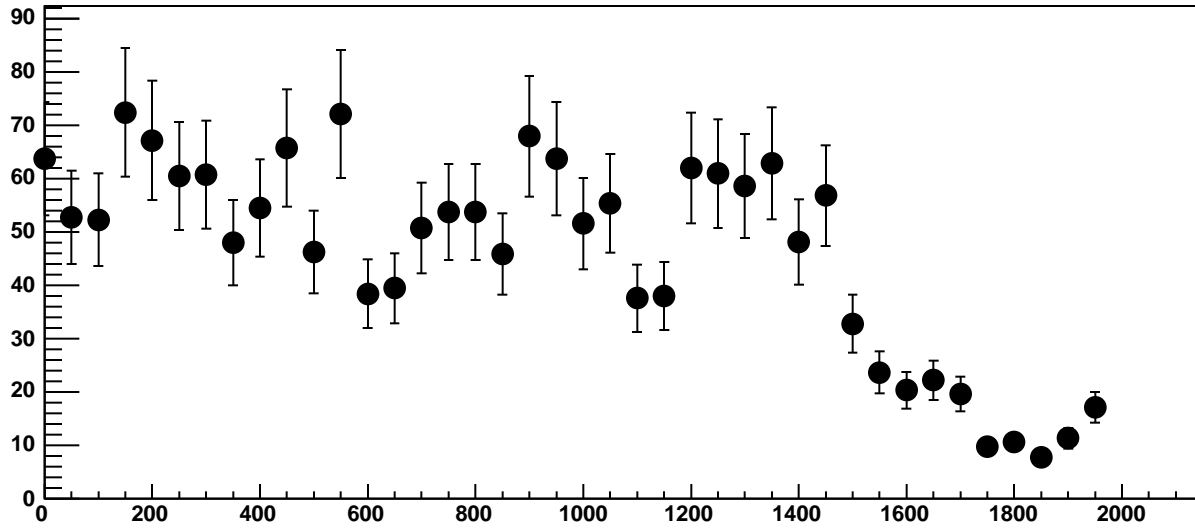


Chip 9, Channel 9, Enable 3!, Hold=30, ADC Mean vs DAC

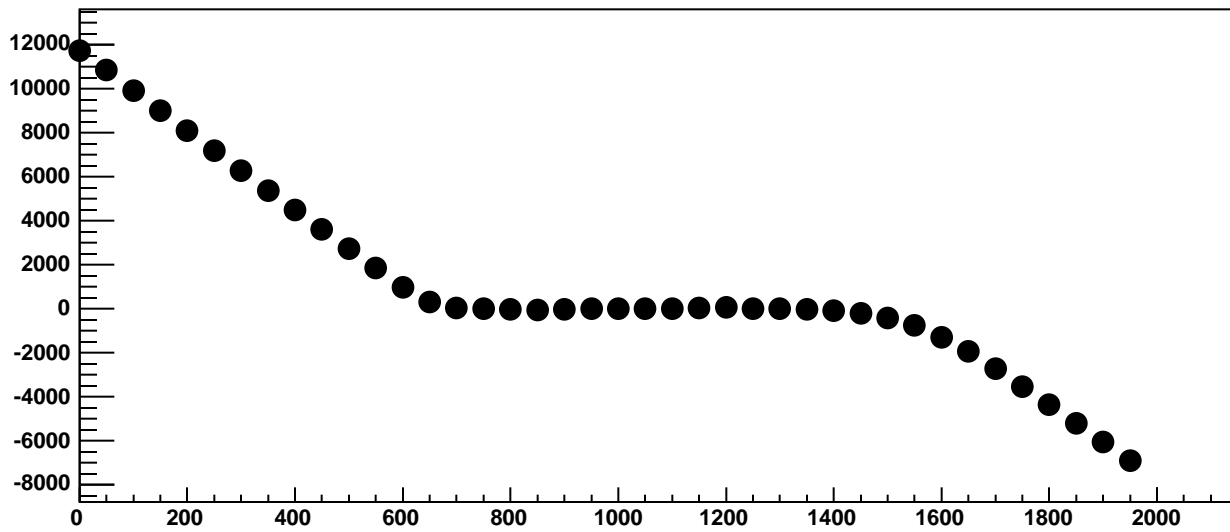


$\chi^2 / \text{ndf}$  130.3 / 11  
p0  $-1469 \pm 20.46$   
p1  $18.44 \pm 0.01837$

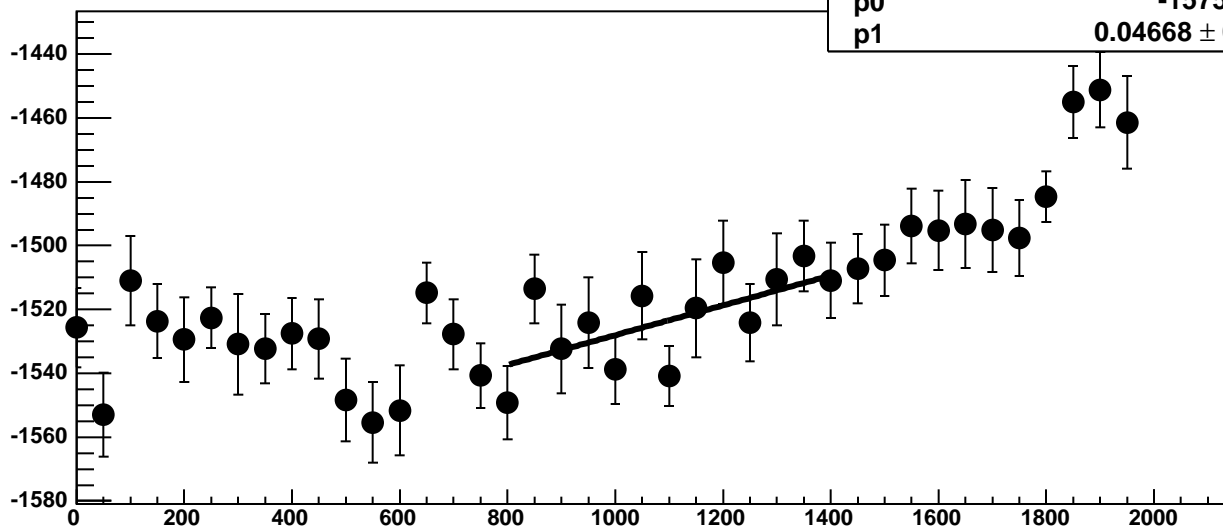
Chip 9, Channel 9, Enable 3!, Hold=30, ADC Noise vs DAC



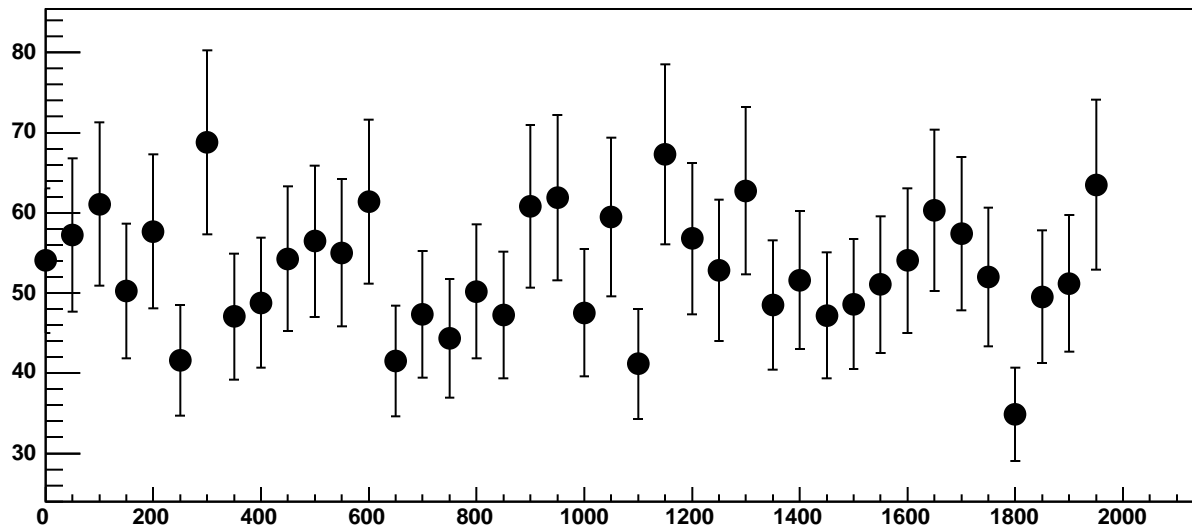
Chip 9, Channel 9, Enable 3!, Hold=30, ADC Residuals vs DAC



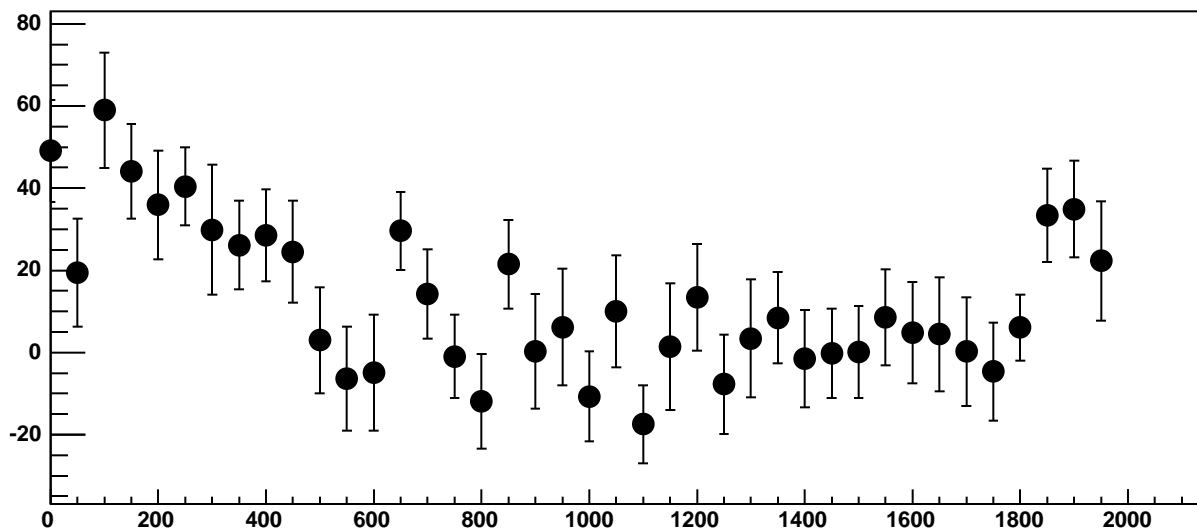
Chip 9, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC



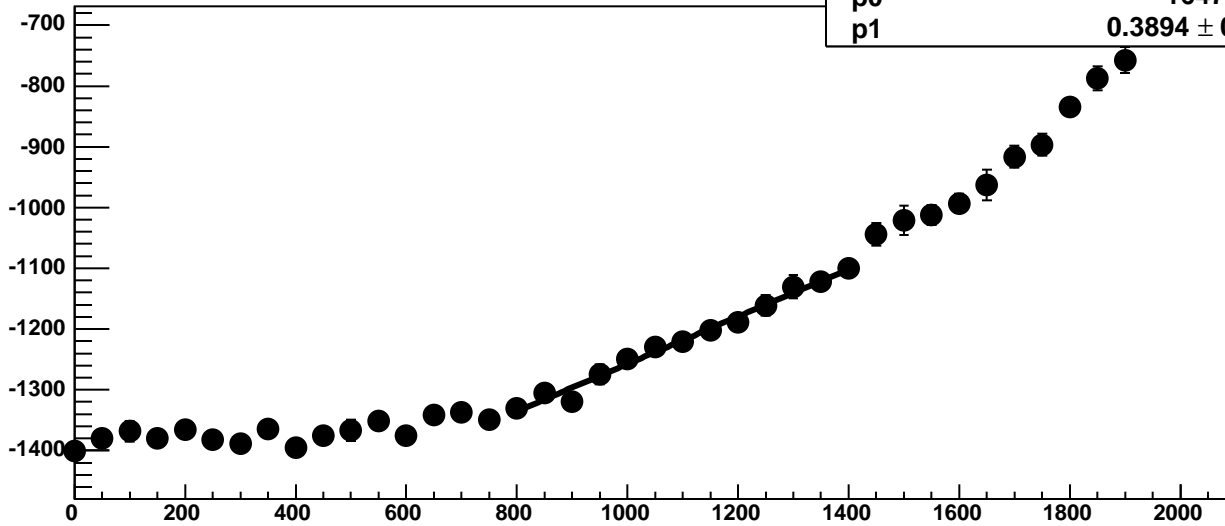
Chip 9, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC



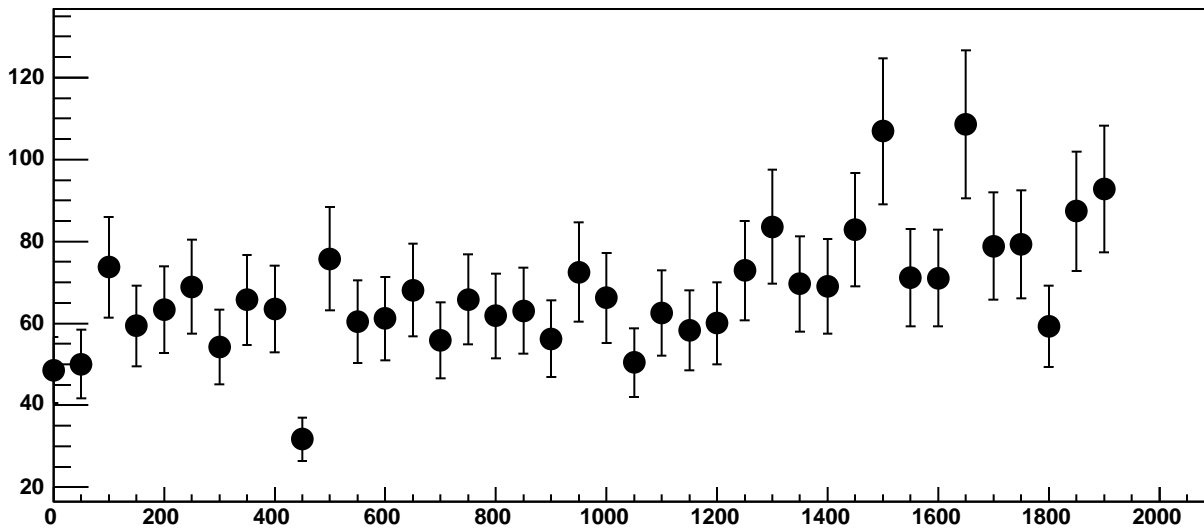
Chip 9, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC



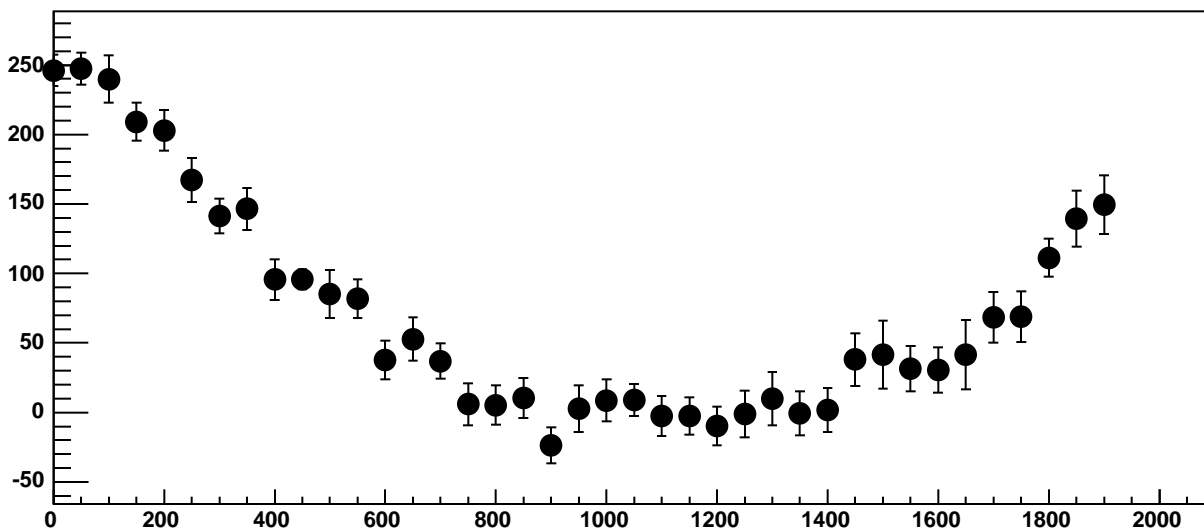
Chip 9, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC



Chip 9, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC



Chip 9, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 9, Channel 10, Enable 0!, Hold=30, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

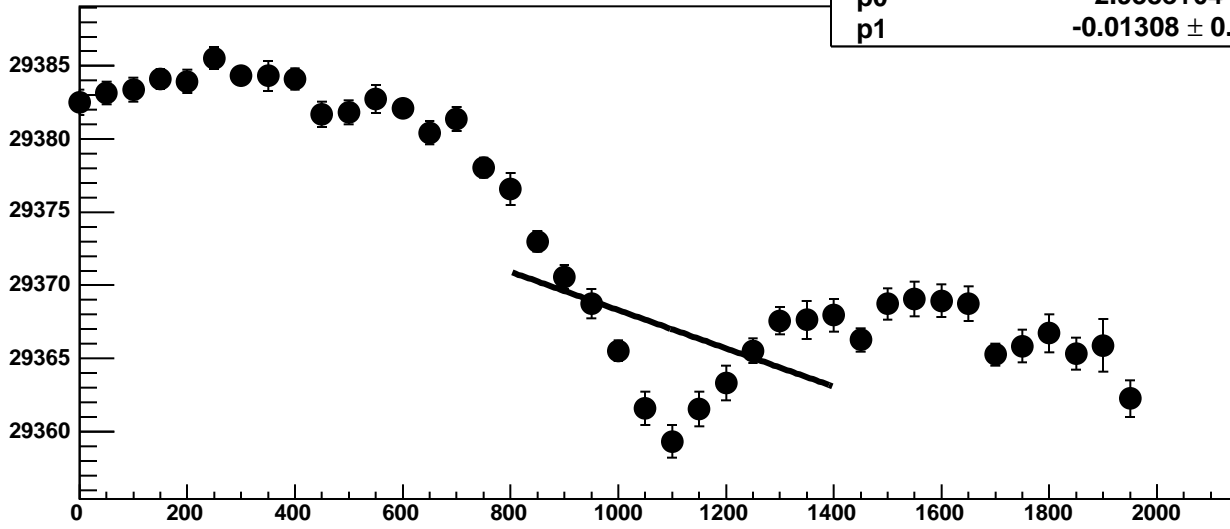
195.7 / 11

p0

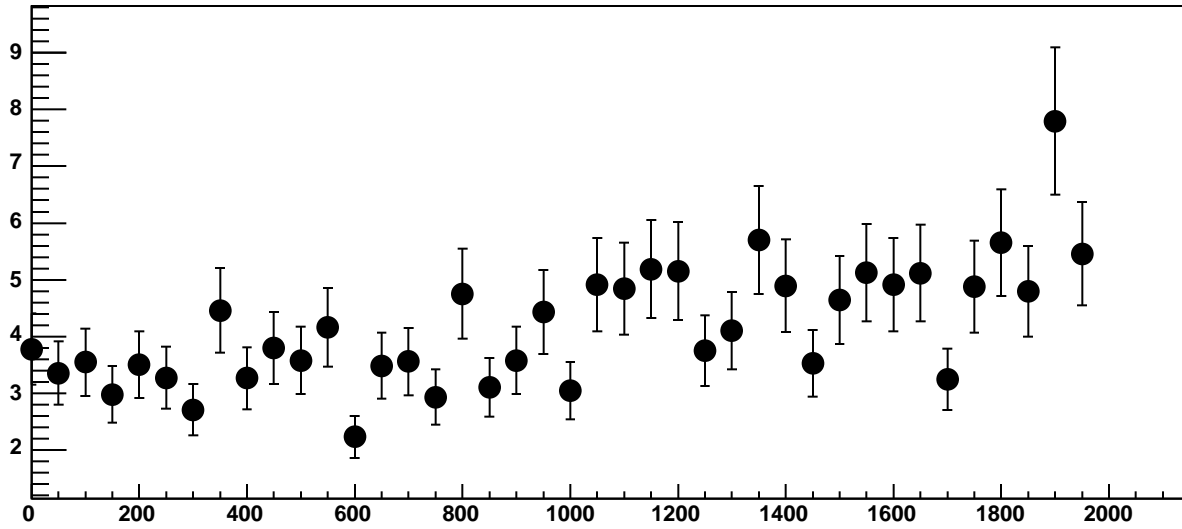
$2.938e+04 \pm 1.564$

p1

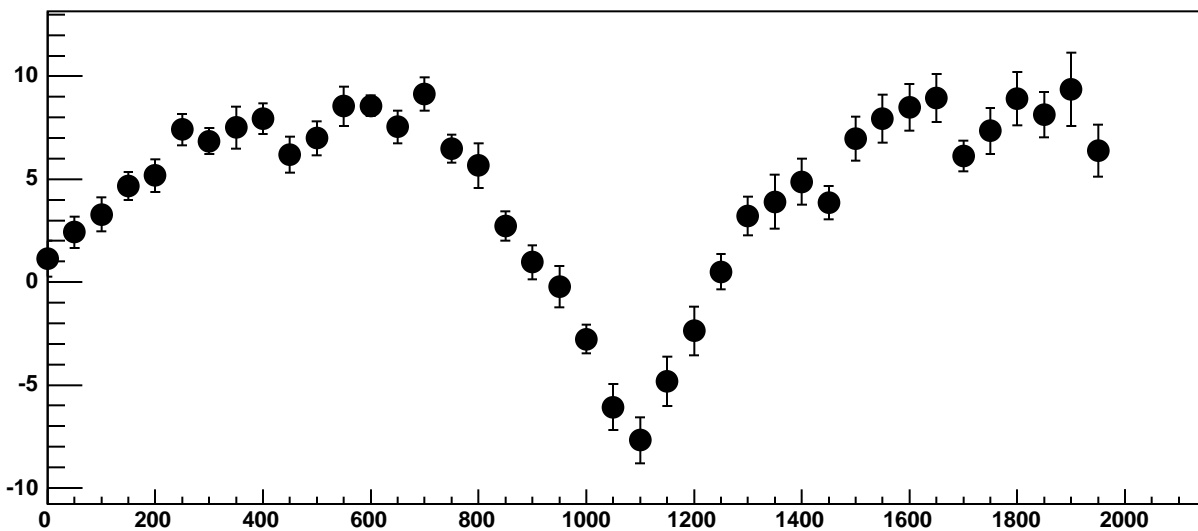
$-0.01308 \pm 0.001449$



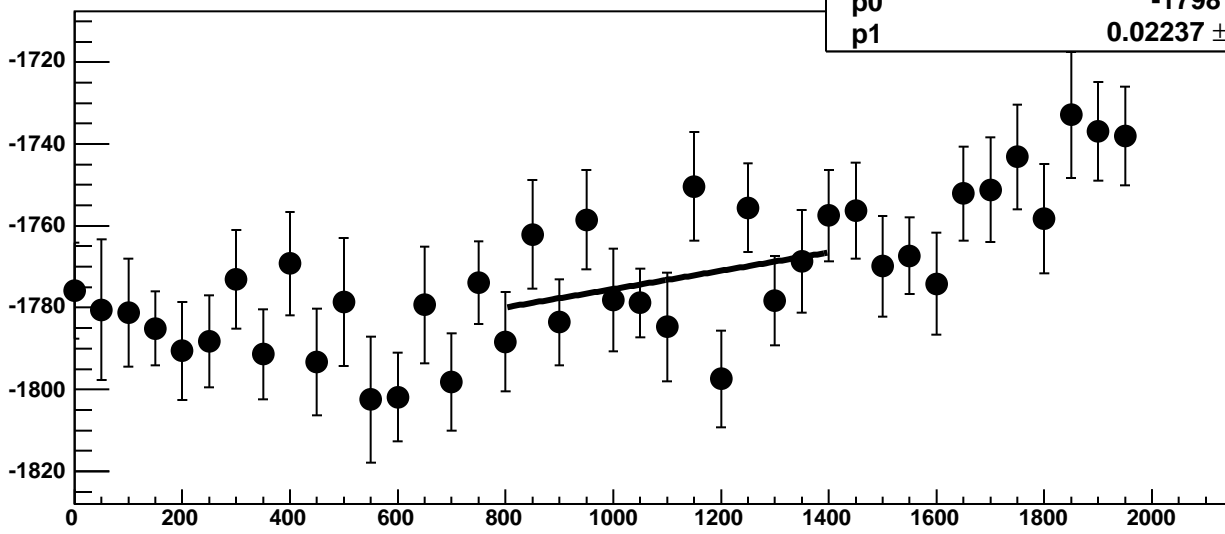
Chip 9, Channel 10, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 9, Channel 10, Enable 0!, Hold=30, ADC Residuals vs DAC



Chip 9, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

16.52 / 11

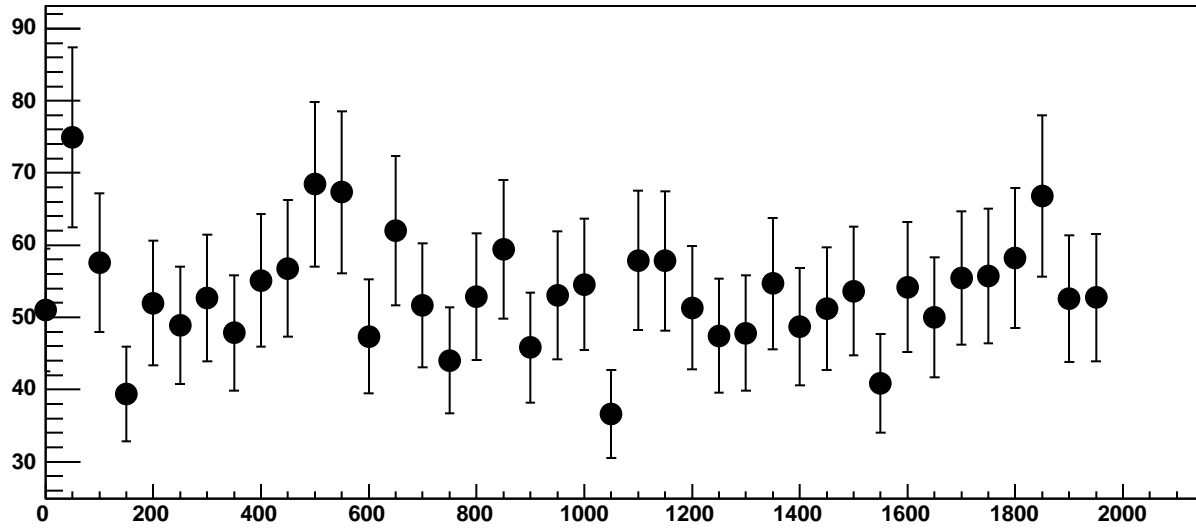
p0

$-1798 \pm 19.45$

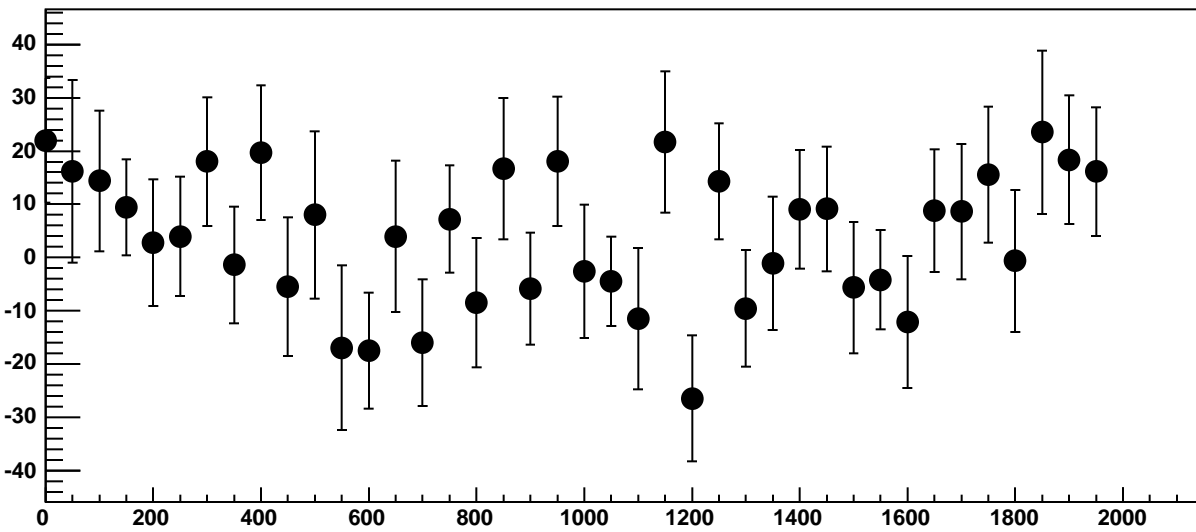
p1

$0.02237 \pm 0.0174$

Chip 9, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC

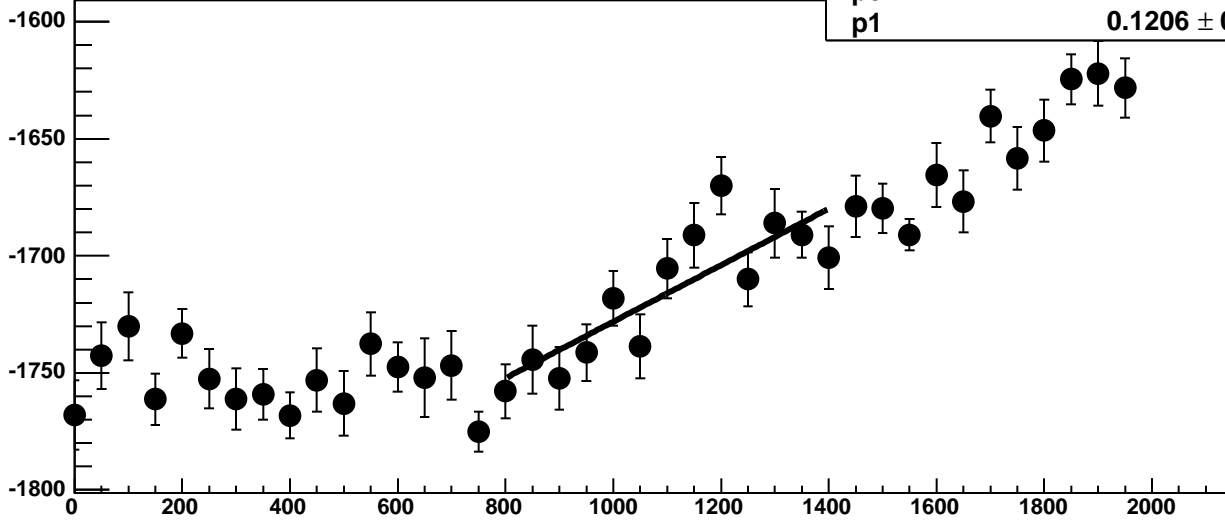


Chip 9, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC



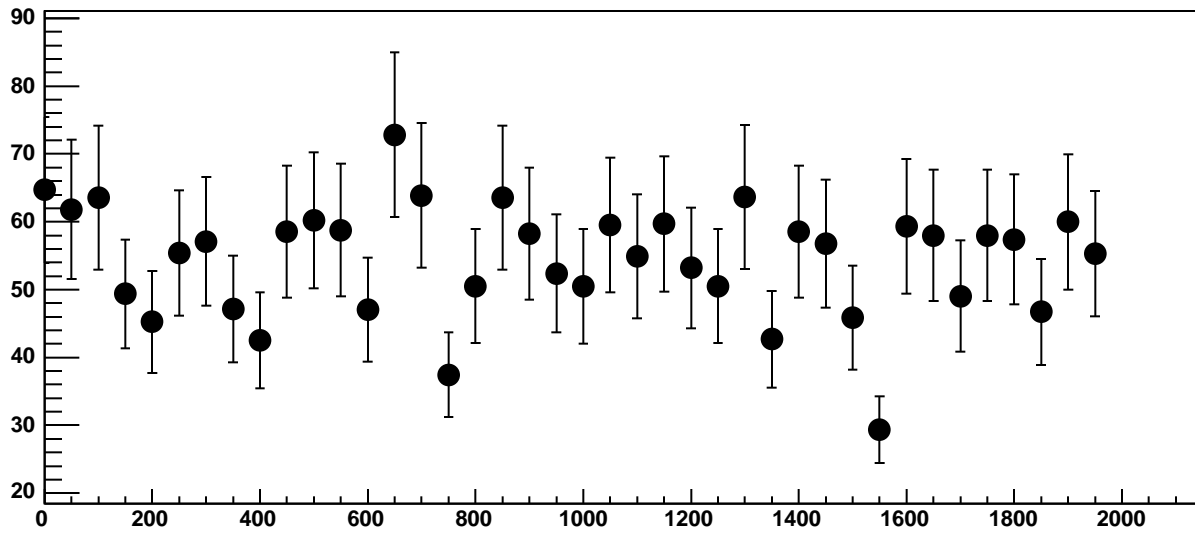


Chip 9, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC

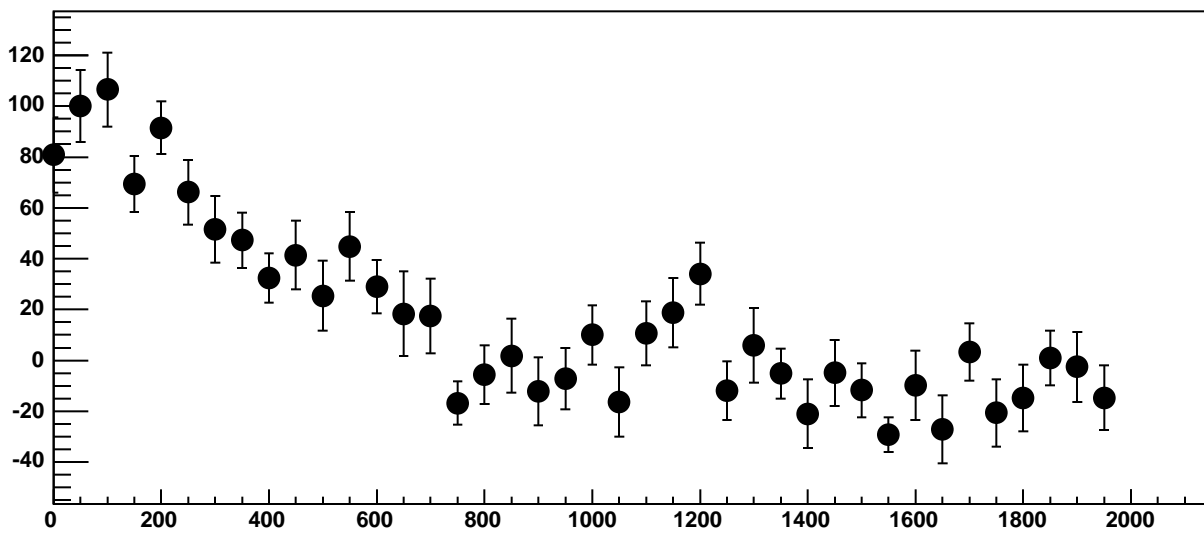


$\chi^2 / \text{ndf}$  17.96 / 11  
p0  $-1849 \pm 20.49$   
p1  $0.1206 \pm 0.01822$

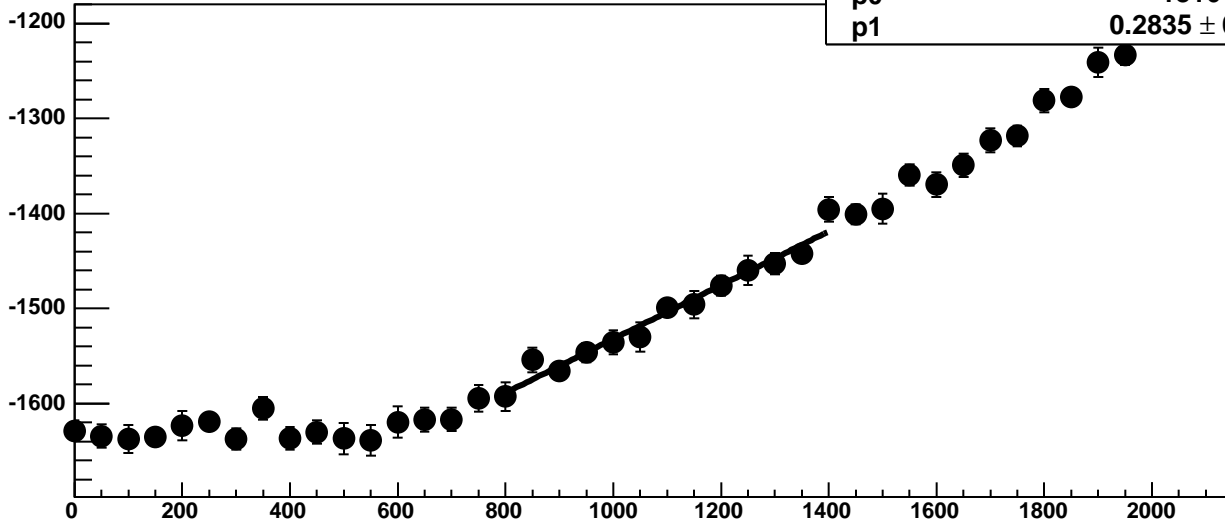
Chip 9, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC



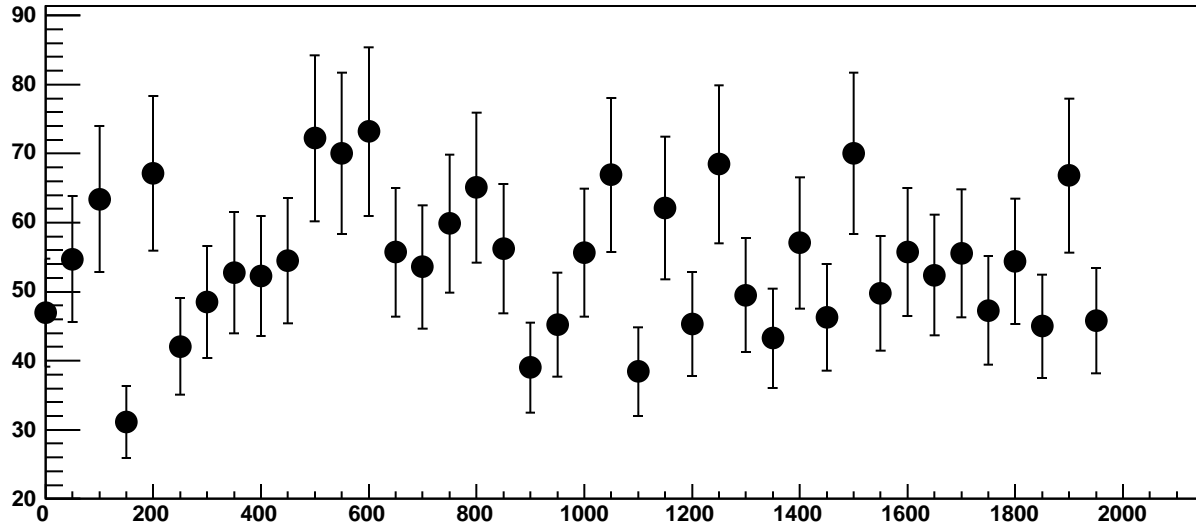
Chip 9, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC



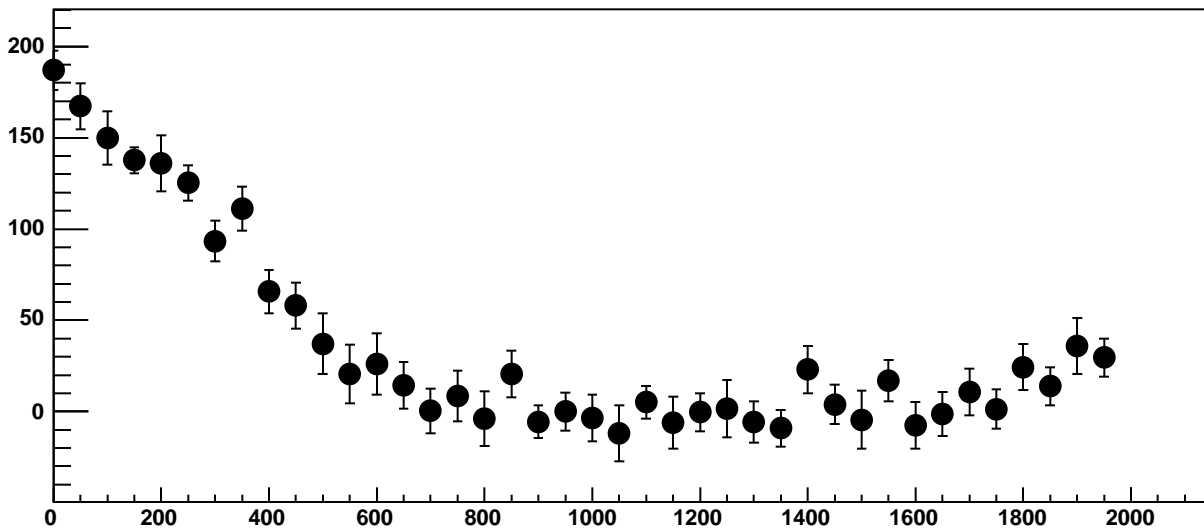
Chip 9, Channel 10, Enable 3, Hold=30, ADC Mean vs DAC



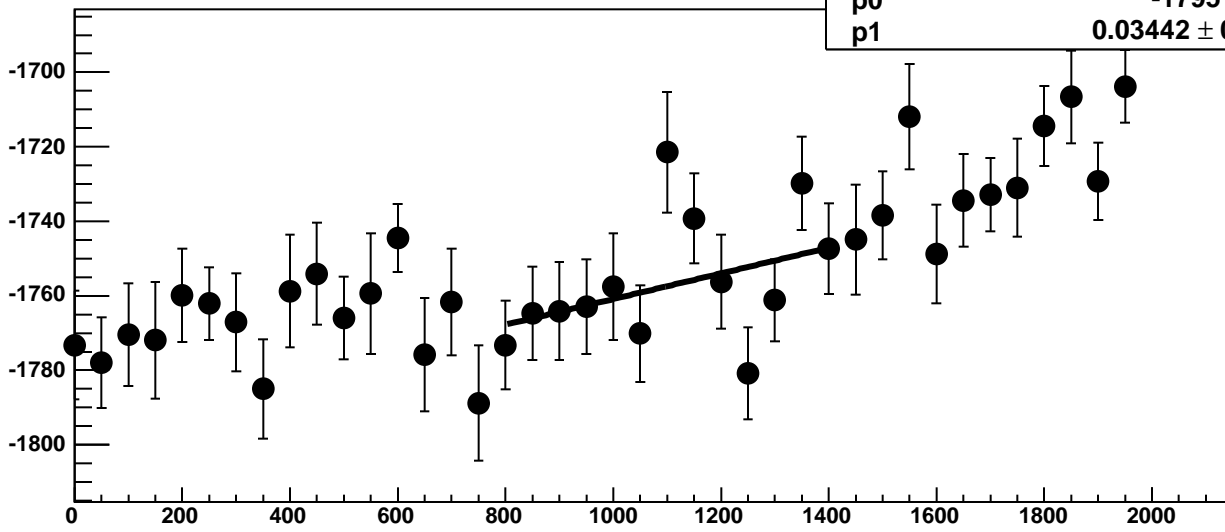
Chip 9, Channel 10, Enable 3, Hold=30, ADC Noise vs DAC



Chip 9, Channel 10, Enable 3, Hold=30, ADC Residuals vs DAC

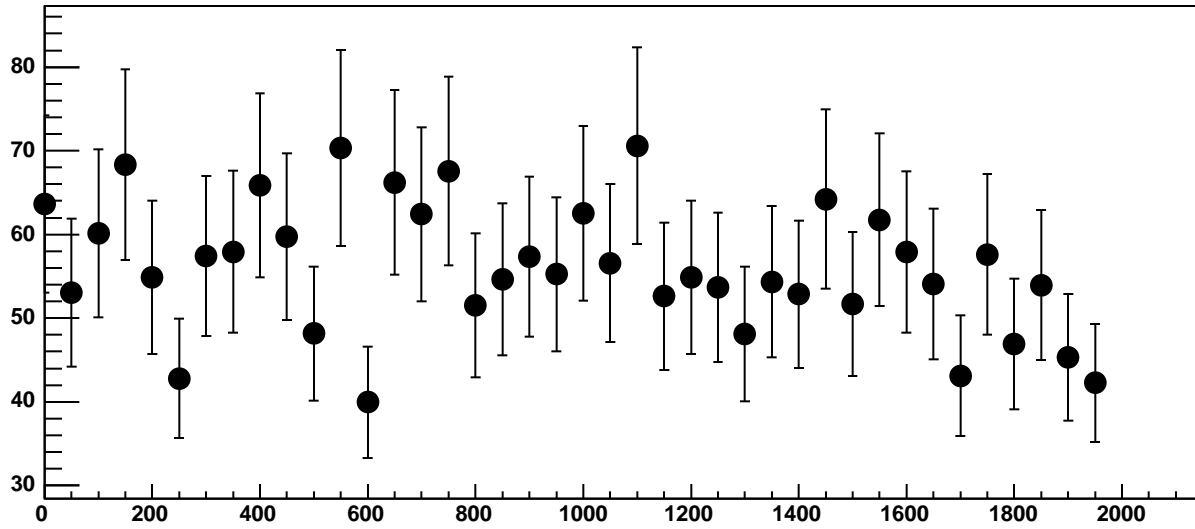


Chip 9, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC

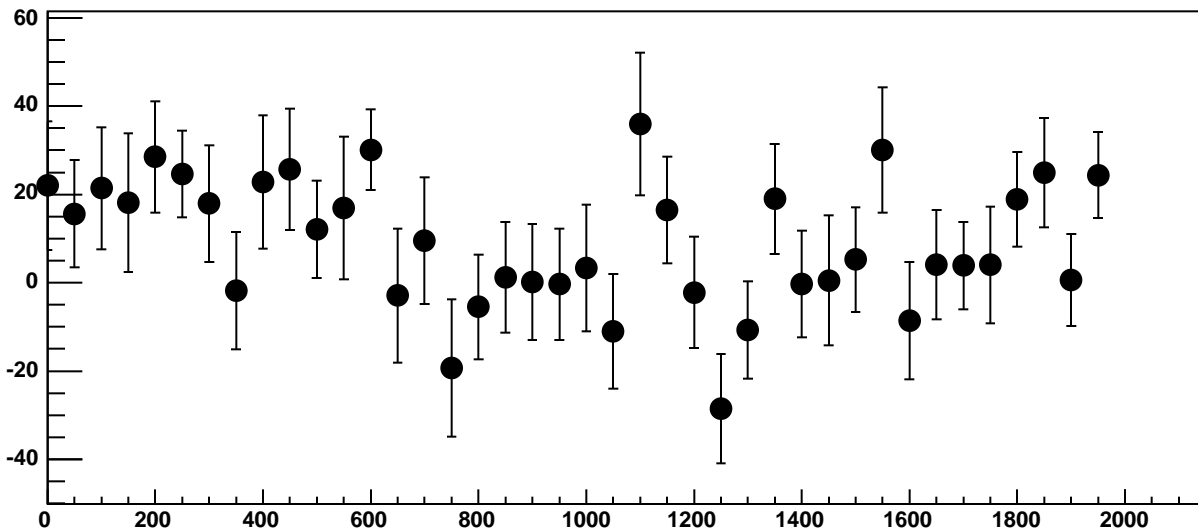


$\chi^2 / \text{ndf}$  16.44 / 11  
p0  $-1795 \pm 20.39$   
p1  $0.03442 \pm 0.01813$

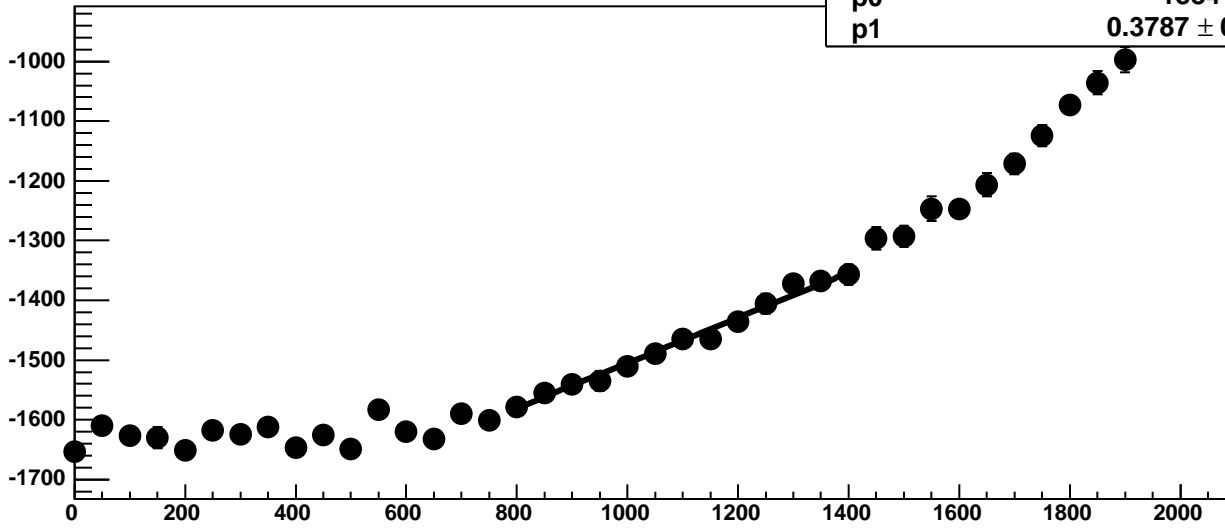
Chip 9, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC



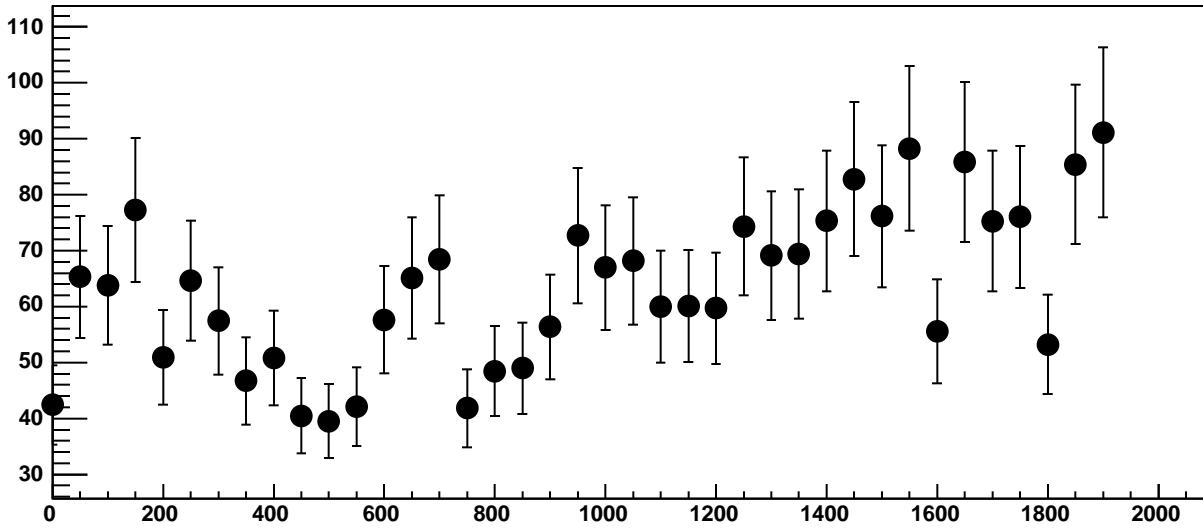
Chip 9, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC



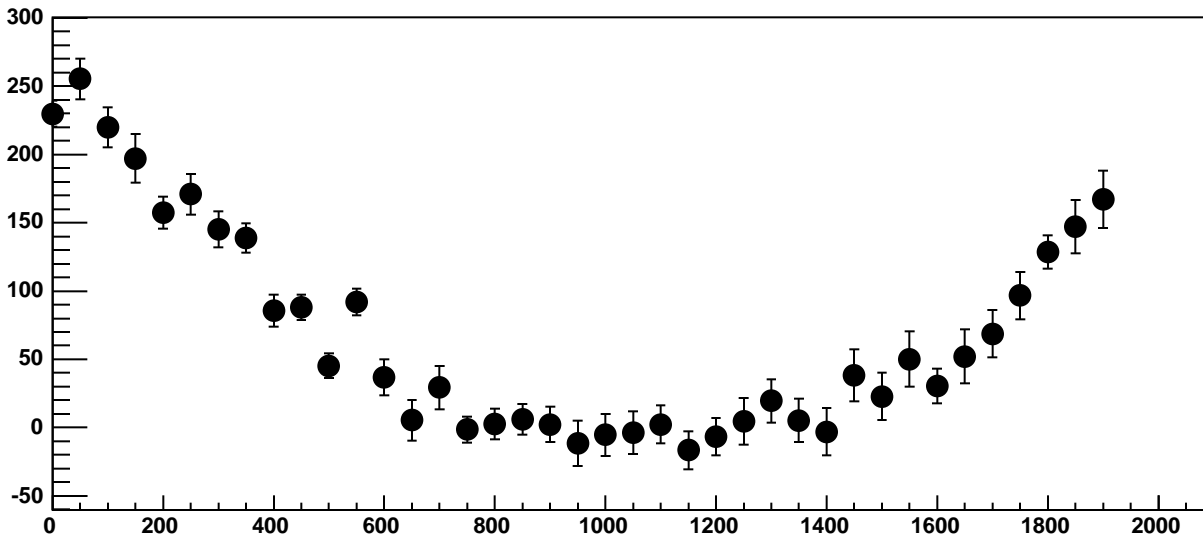
Chip 9, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC



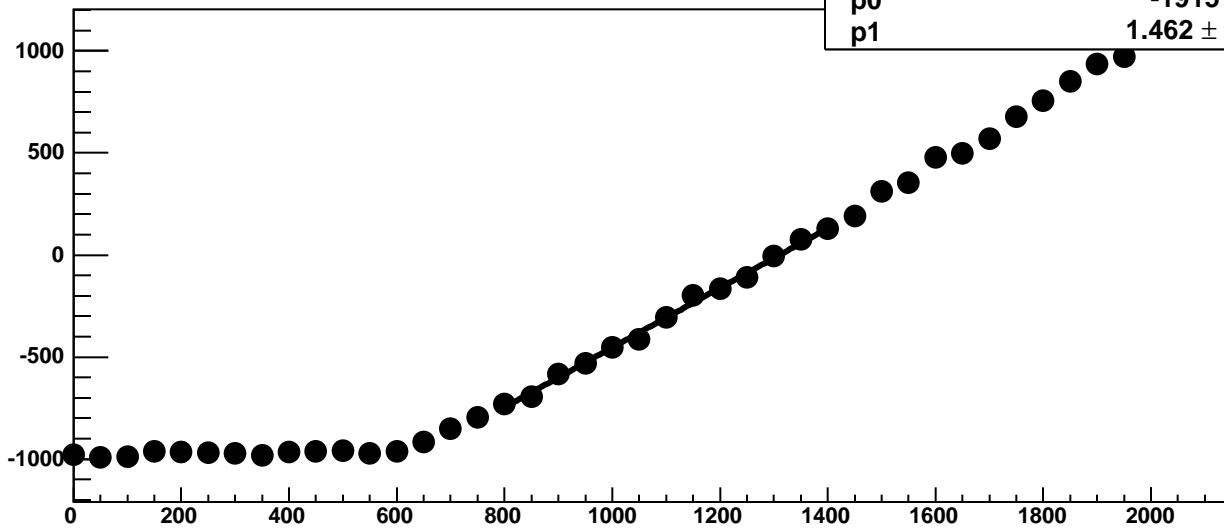
Chip 9, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC



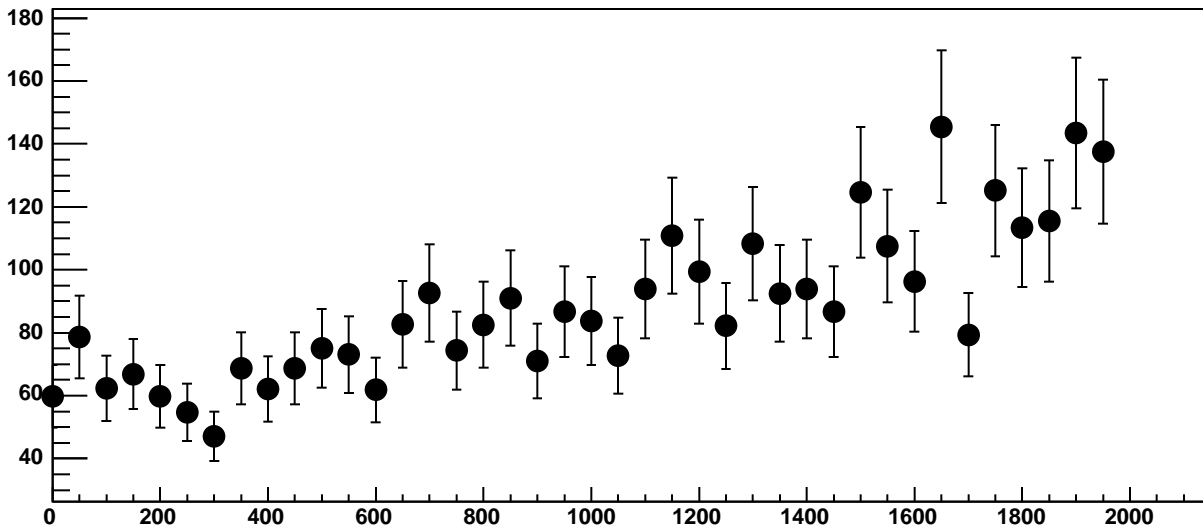
Chip 9, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC



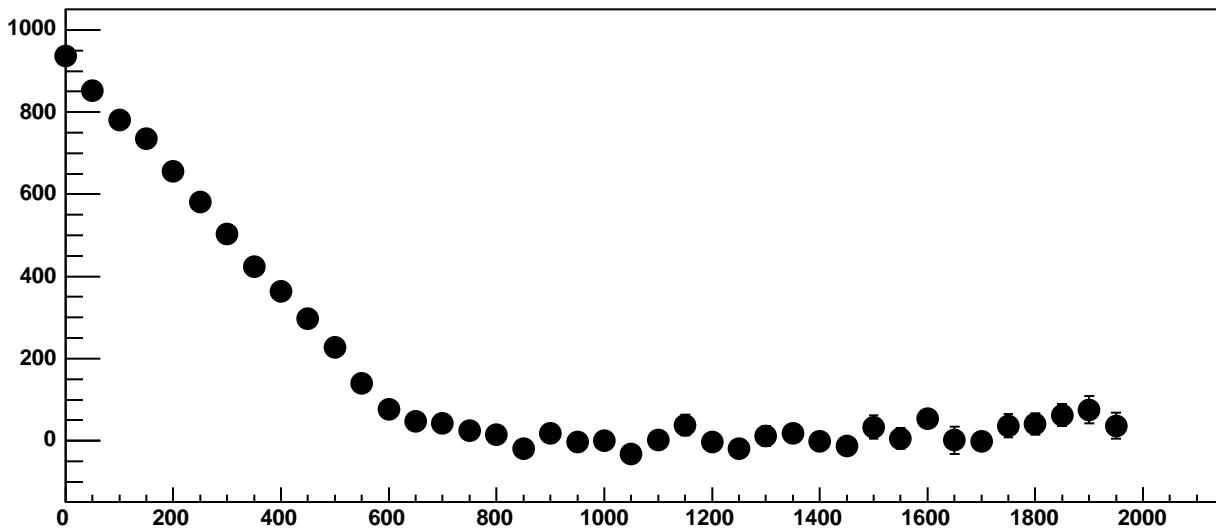
Chip 9, Channel 11, Enable 0, Hold=30, ADC Mean vs DAC



Chip 9, Channel 11, Enable 0, Hold=30, ADC Noise vs DAC

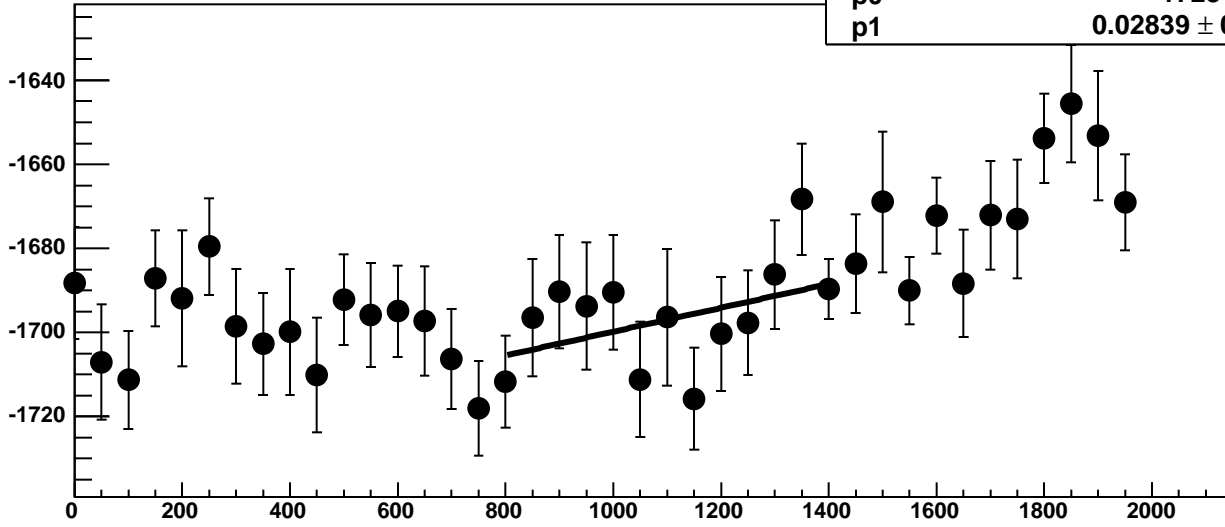


Chip 9, Channel 11, Enable 0, Hold=30, ADC Residuals vs DAC

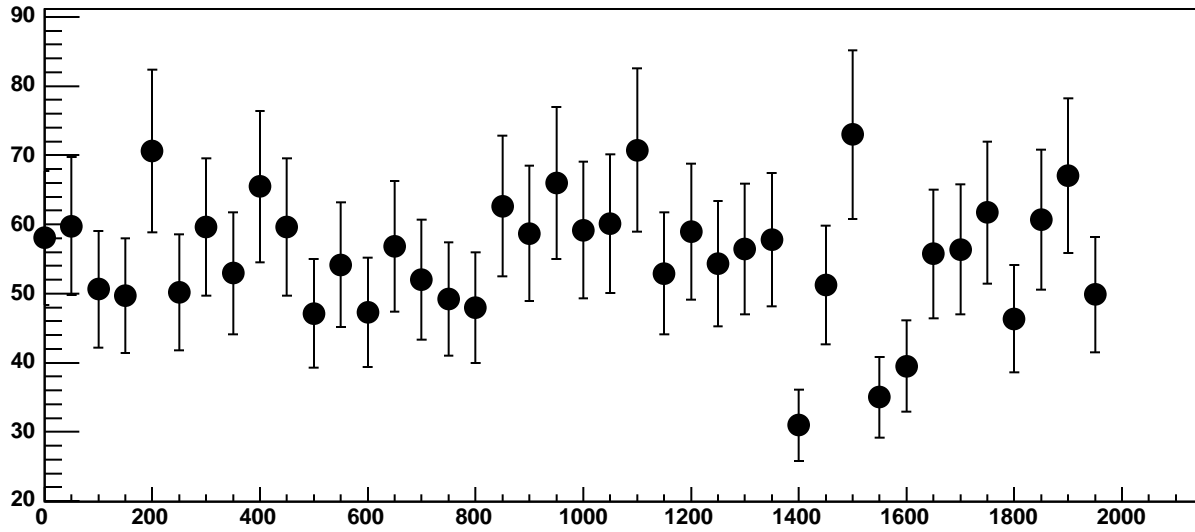


Chip 9, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC

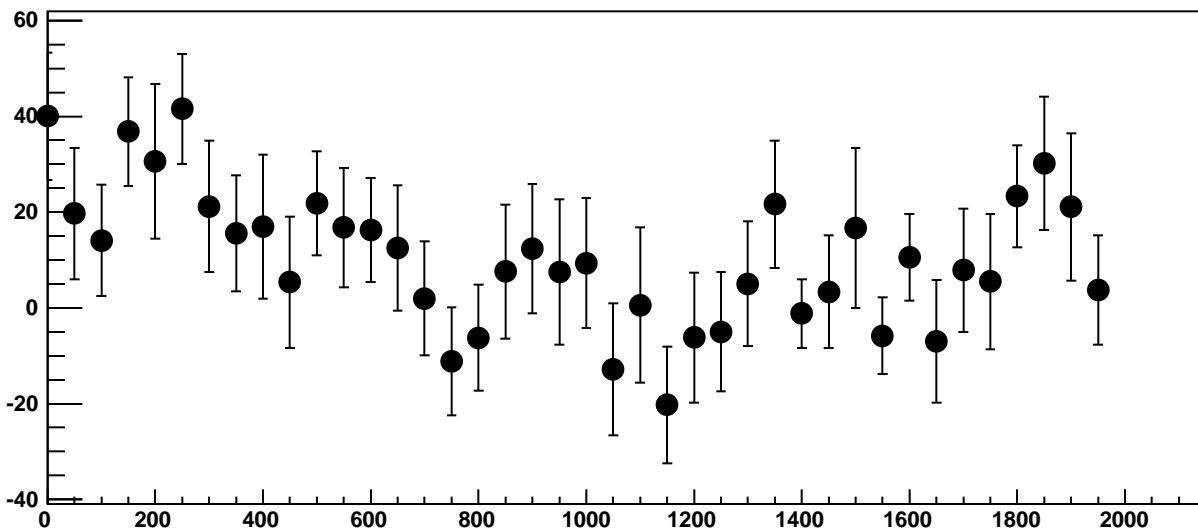
$\chi^2 / \text{ndf}$  9.044 / 11  
p0  $-1728 \pm 18.78$   
p1  $0.02839 \pm 0.01612$



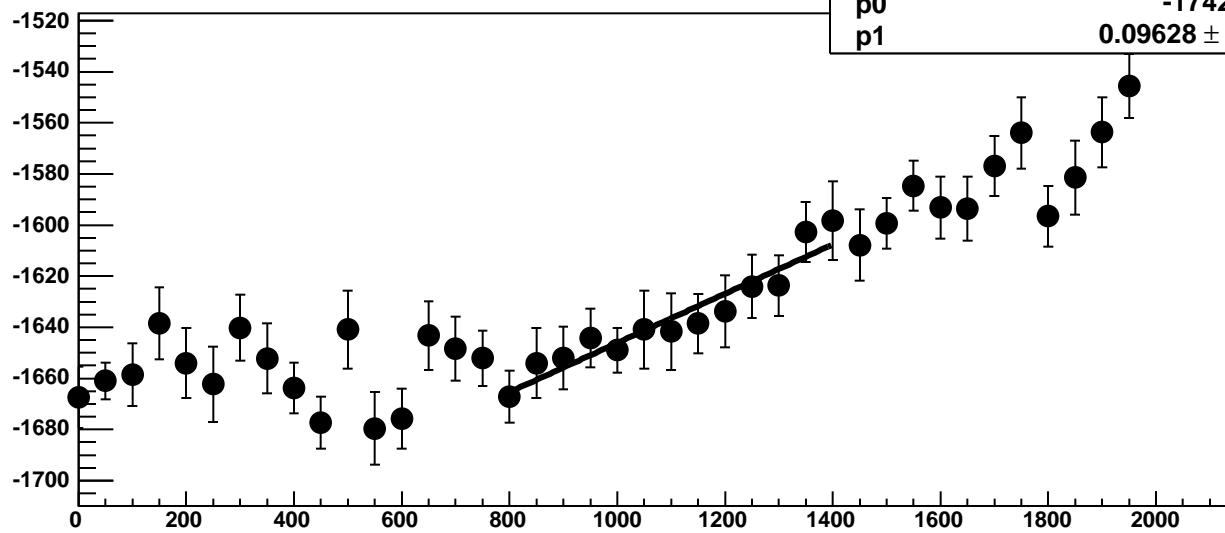
Chip 9, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC



Chip 9, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC

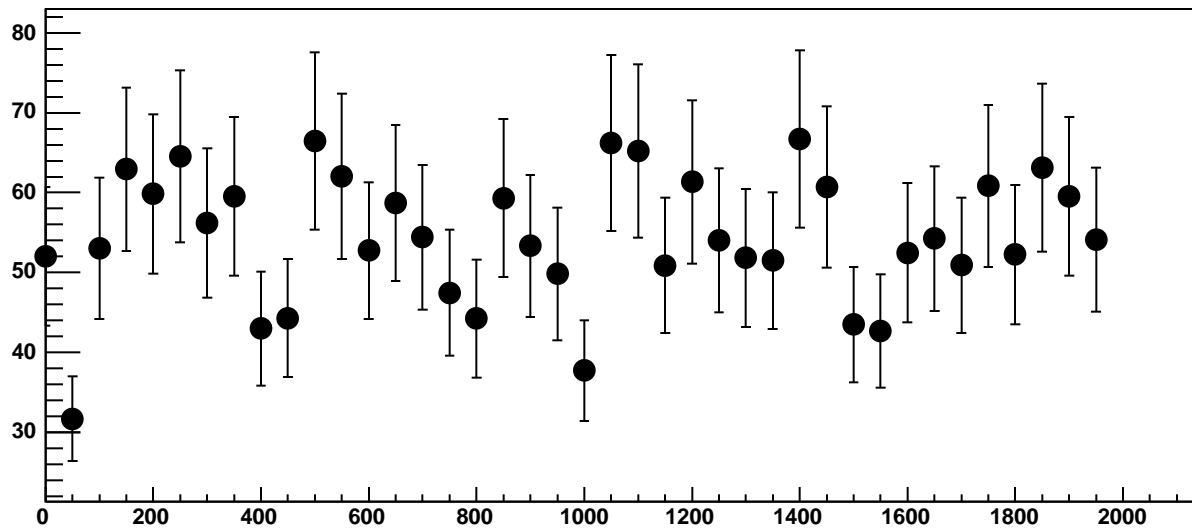


Chip 9, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC

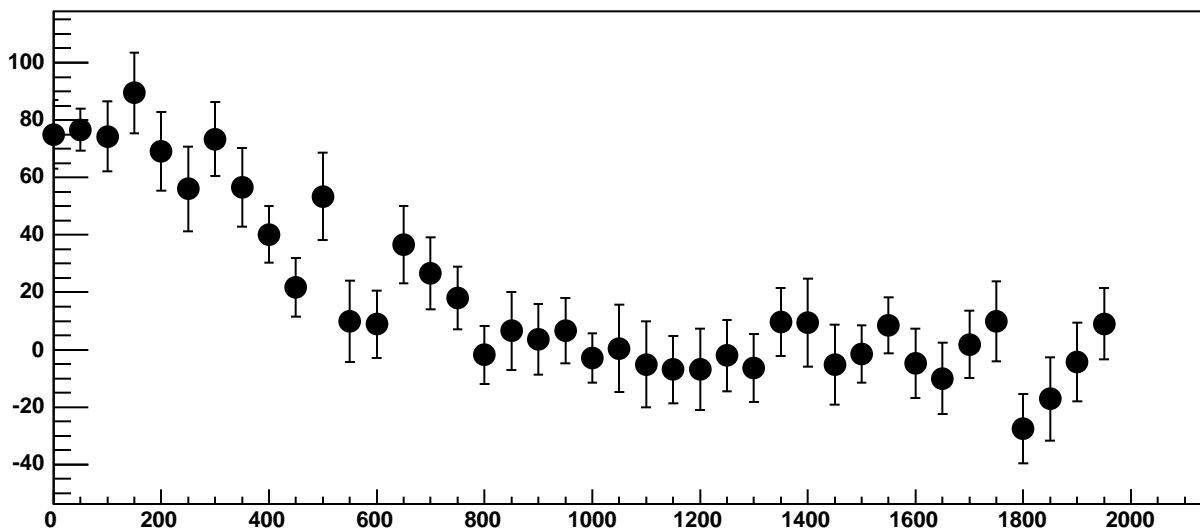


$\chi^2 / \text{ndf}$  2.874 / 11  
p0 -1742 ± 19.7  
p1 0.09628 ± 0.01801

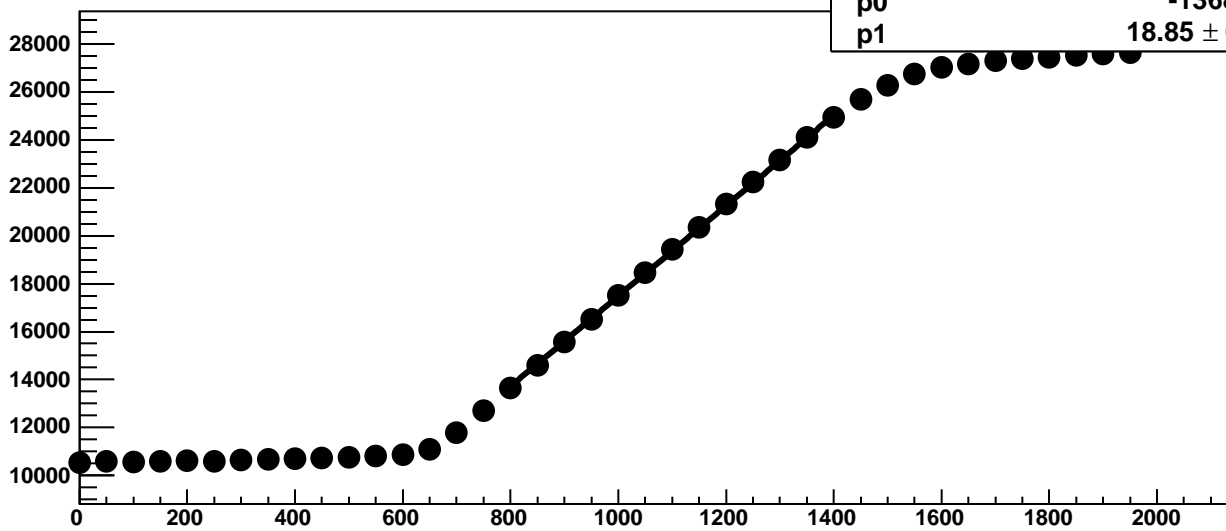
Chip 9, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC



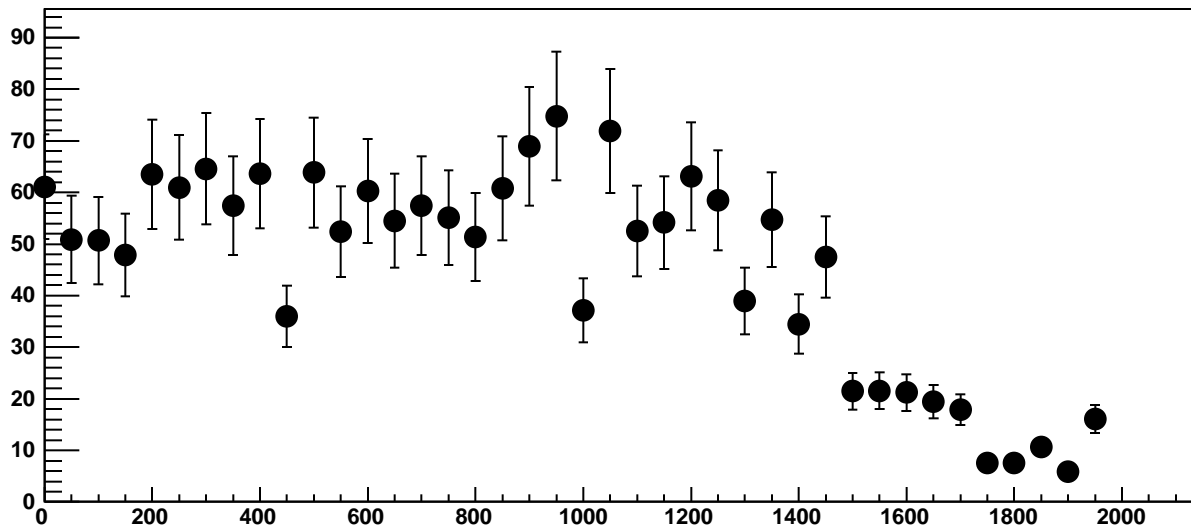
Chip 9, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC



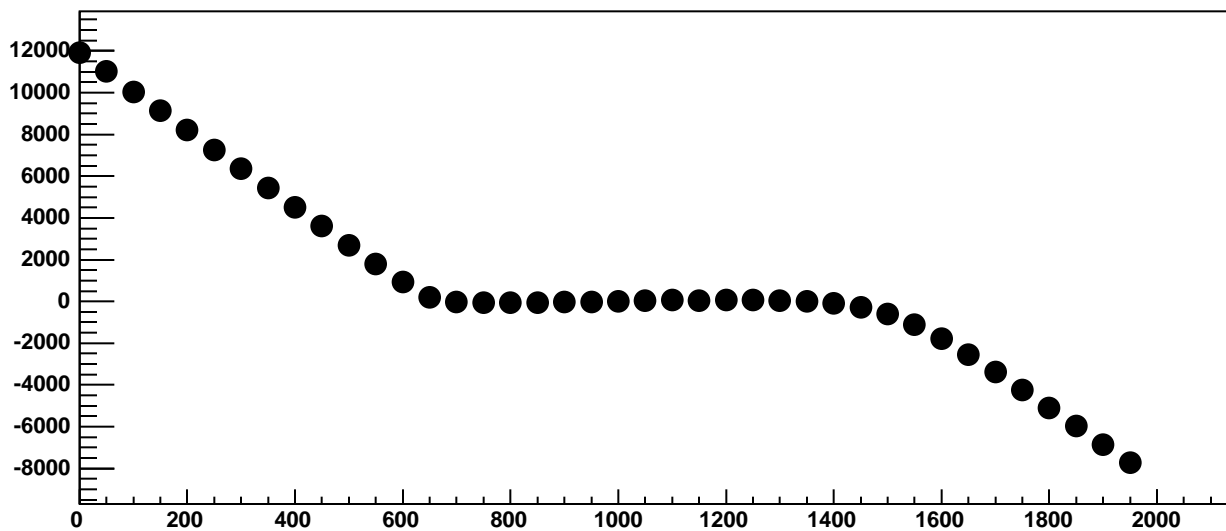
Chip 9, Channel 11, Enable 3!, Hold=30, ADC Mean vs DAC



Chip 9, Channel 11, Enable 3!, Hold=30, ADC Noise vs DAC

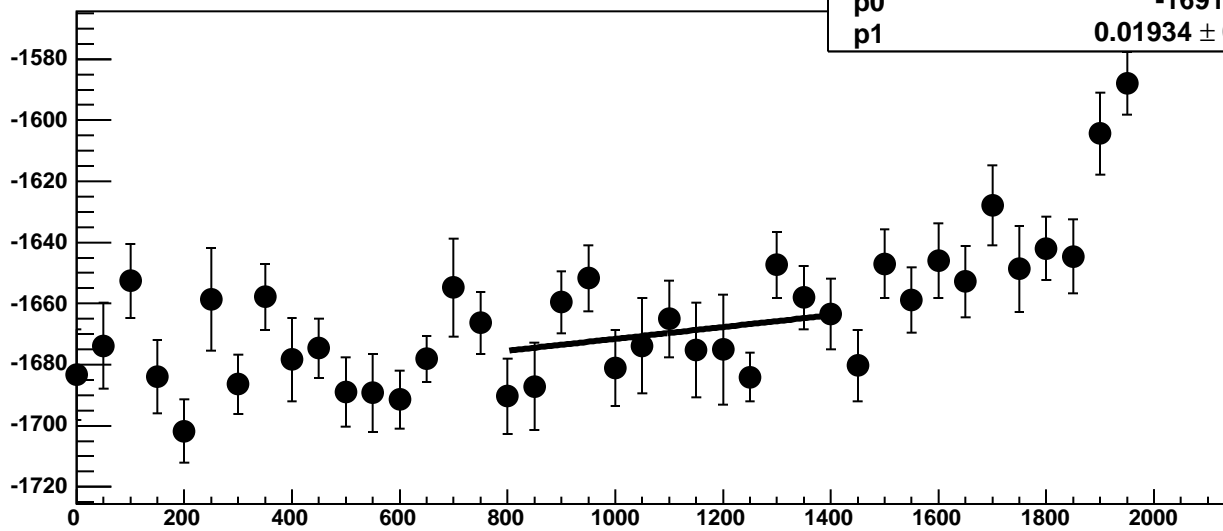


Chip 9, Channel 11, Enable 3!, Hold=30, ADC Residuals vs DAC





Chip 9, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

17.11 / 11

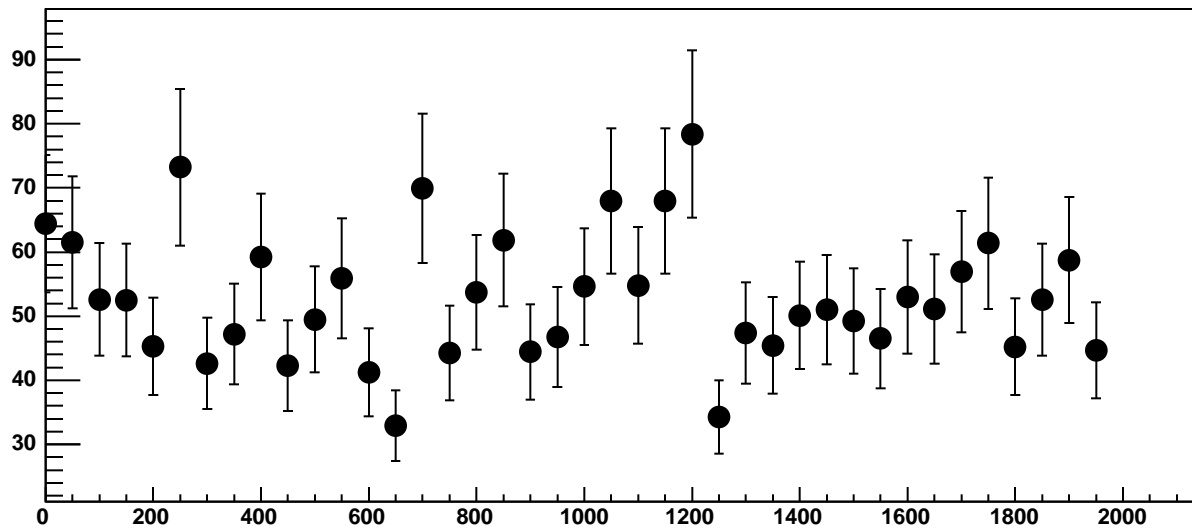
p0

$-1691 \pm 19.16$

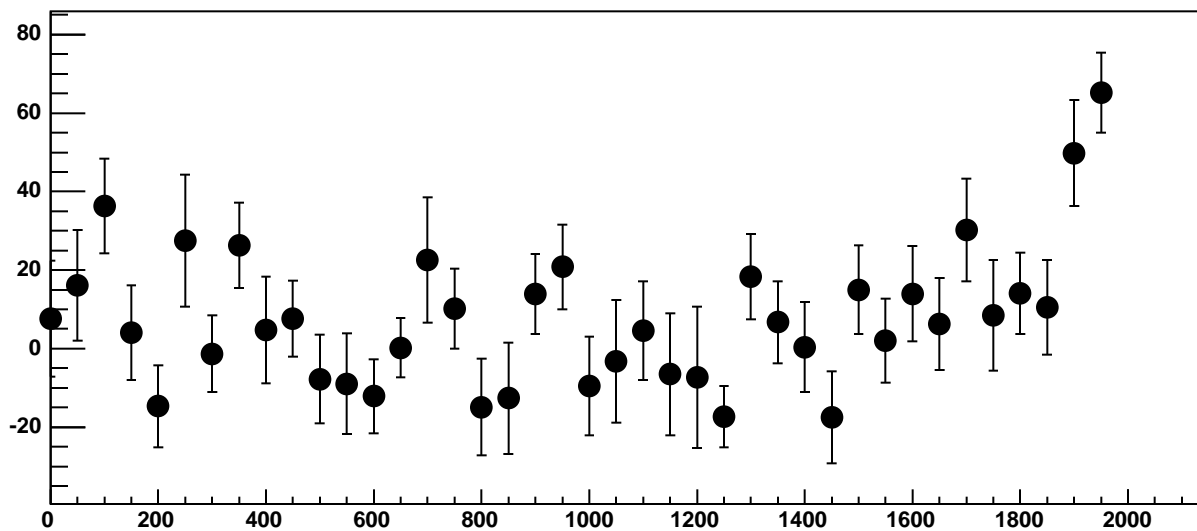
p1

$0.01934 \pm 0.01686$

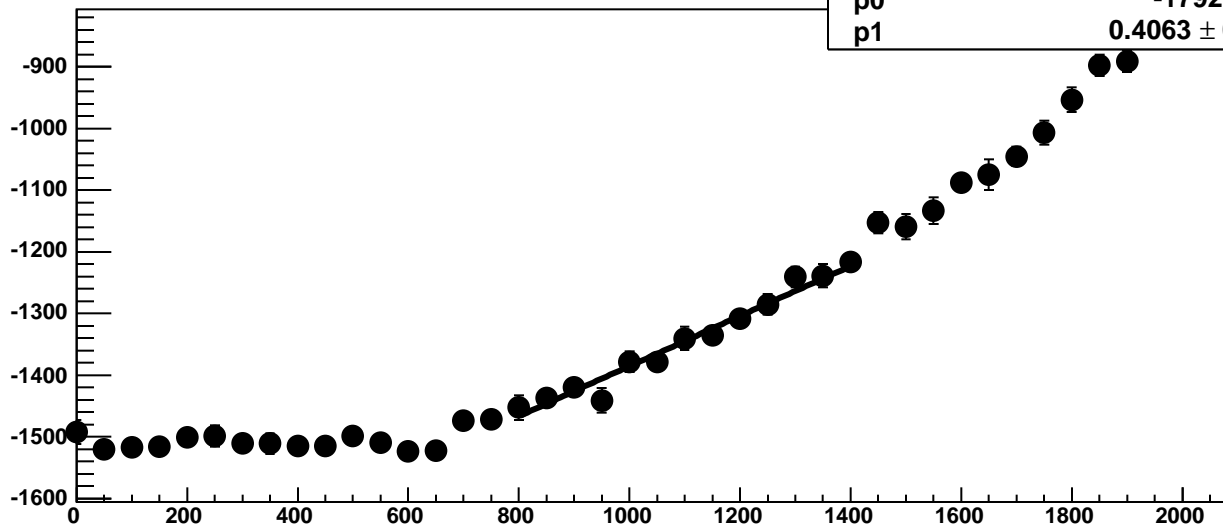
Chip 9, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC



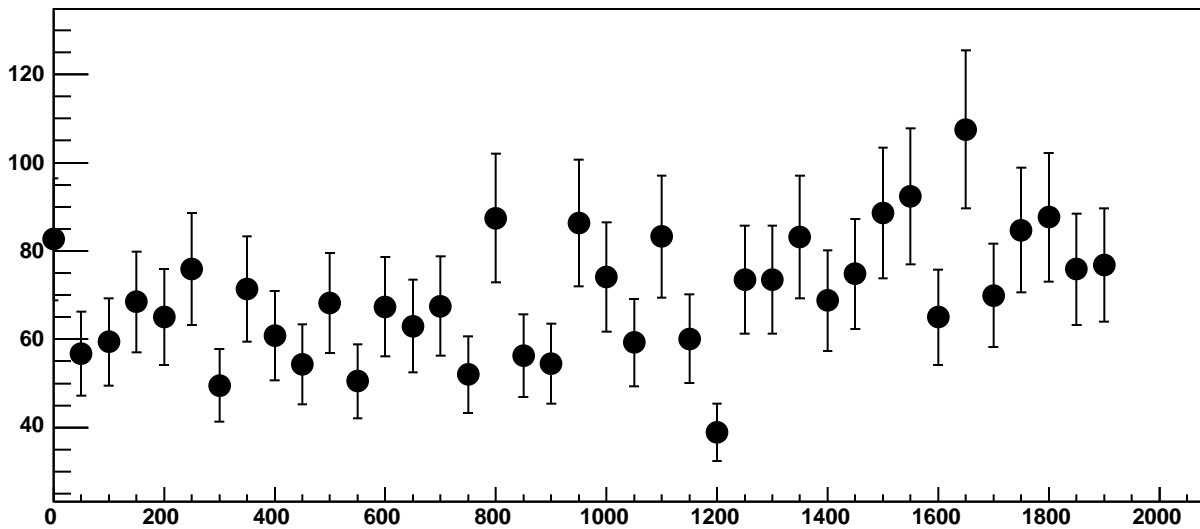
Chip 9, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC



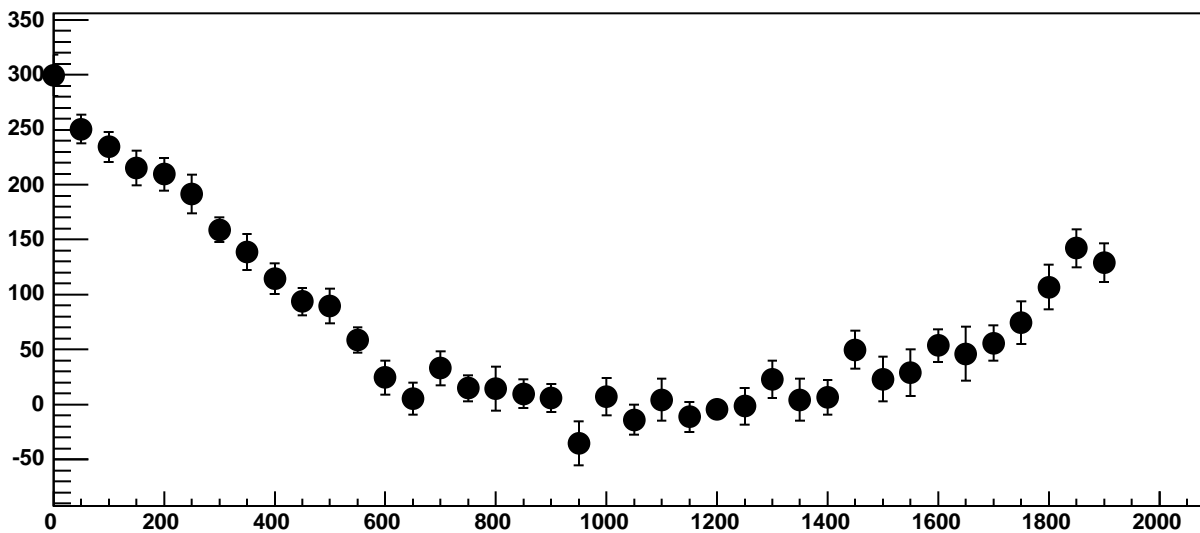
Chip 9, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC



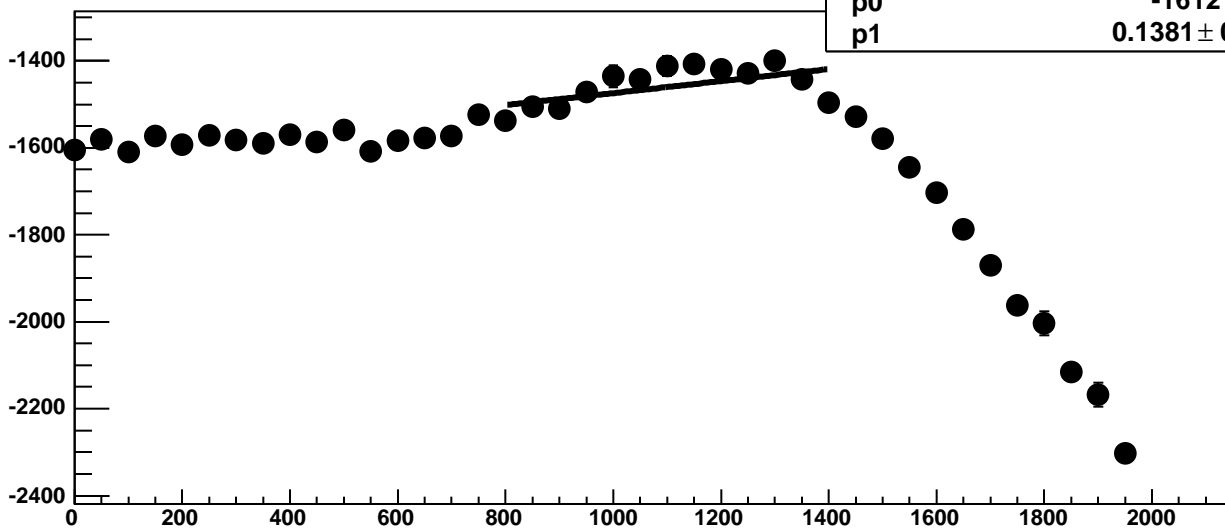
Chip 9, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC



Chip 9, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC

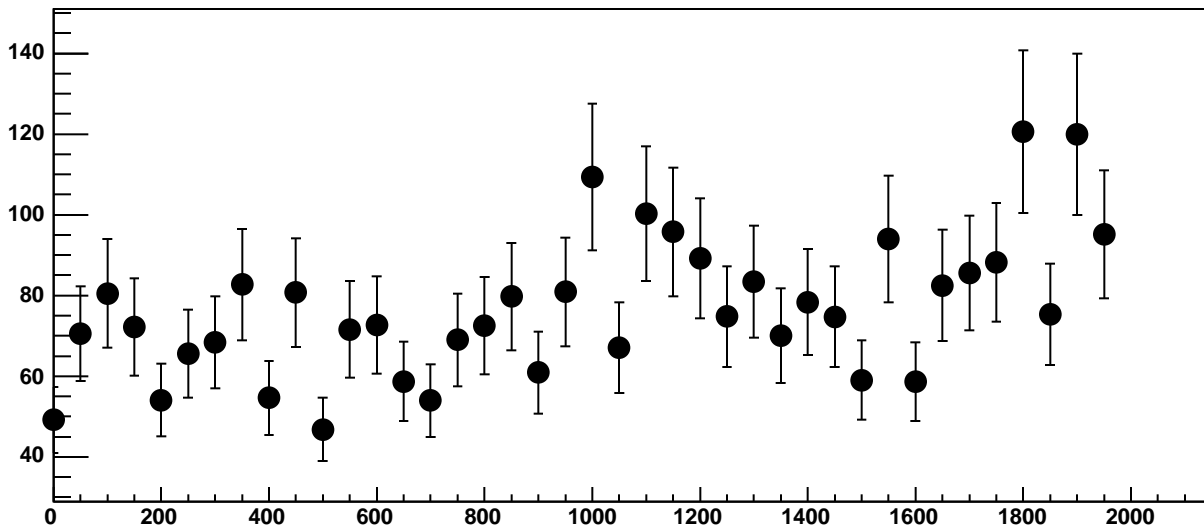


Chip 9, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC

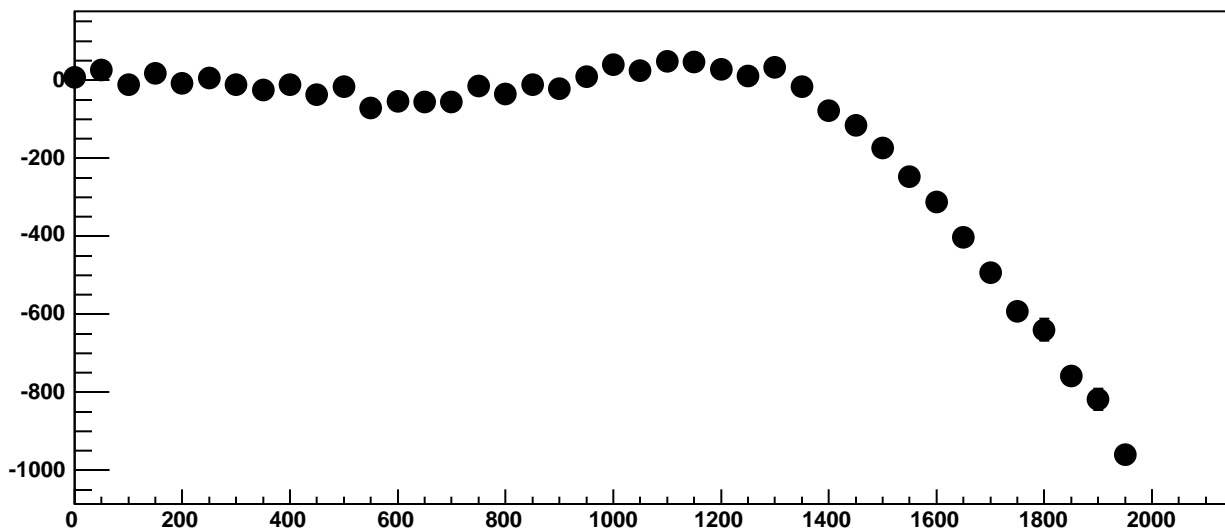


$\chi^2 / \text{ndf}$	46.35 / 11
p0	$-1612 \pm 28.25$
p1	$0.1381 \pm 0.02549$

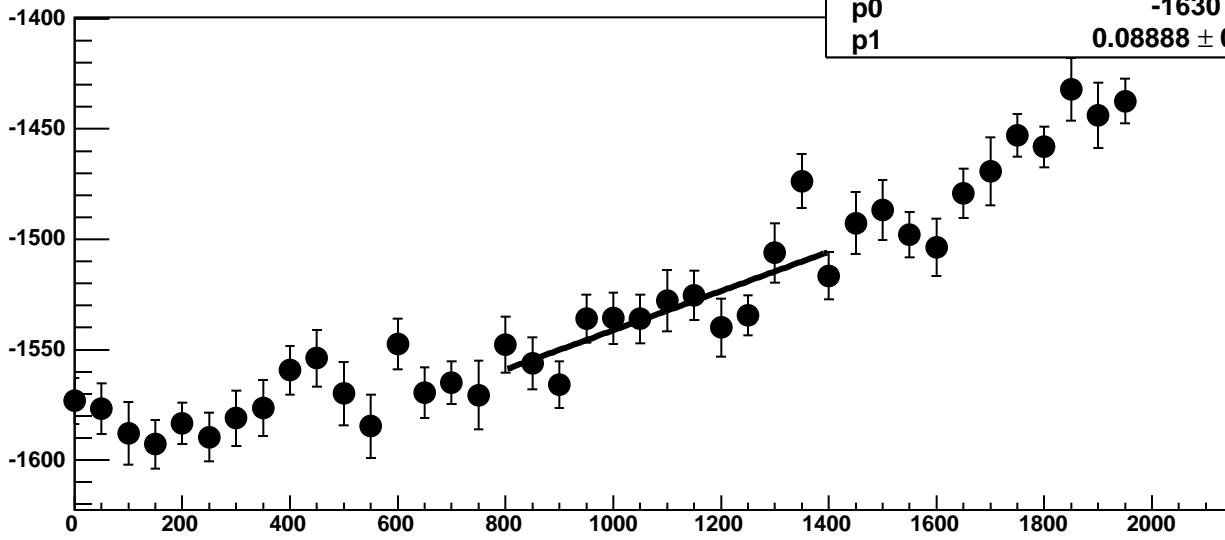
Chip 9, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC



Chip 9, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC

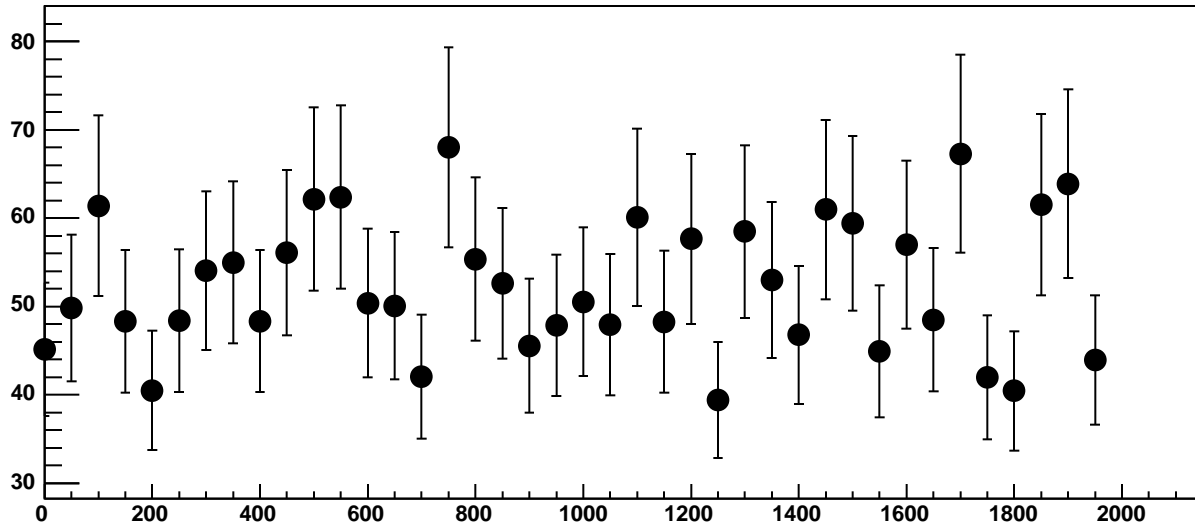


Chip 9, Channel 12, Enable 1, Hold=30, ADC Mean vs DAC

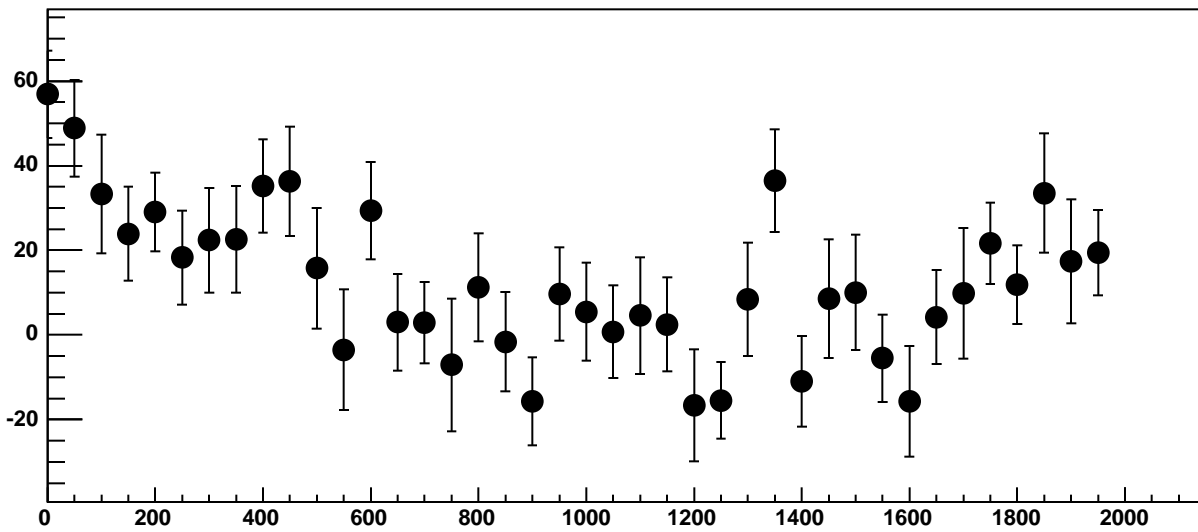


$\chi^2 / \text{ndf}$  19.14 / 11  
p0  $-1630 \pm 19.09$   
p1  $0.08888 \pm 0.01706$

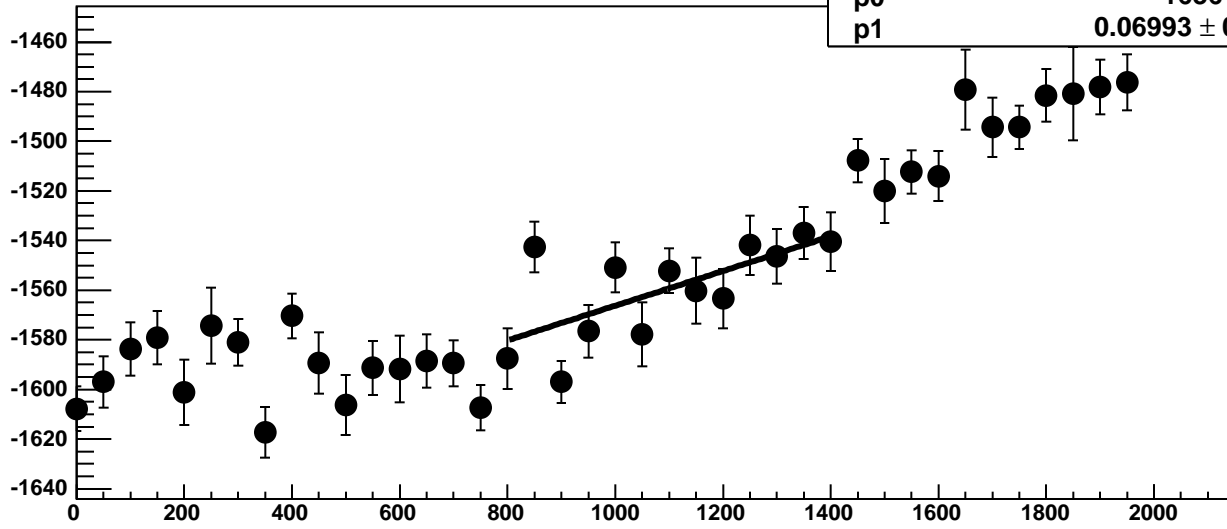
Chip 9, Channel 12, Enable 1, Hold=30, ADC Noise vs DAC



Chip 9, Channel 12, Enable 1, Hold=30, ADC Residuals vs DAC

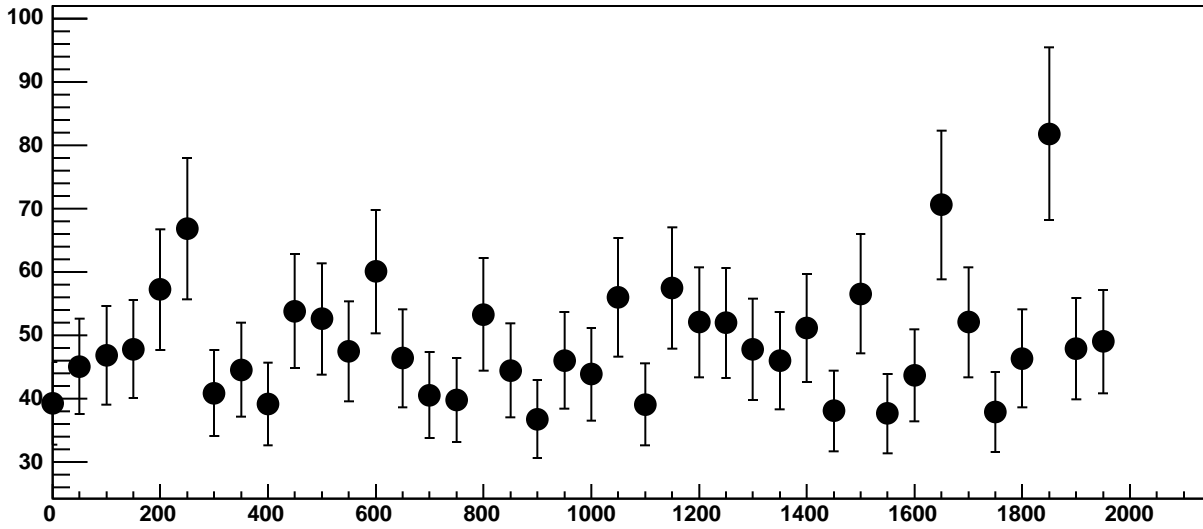


Chip 9, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC

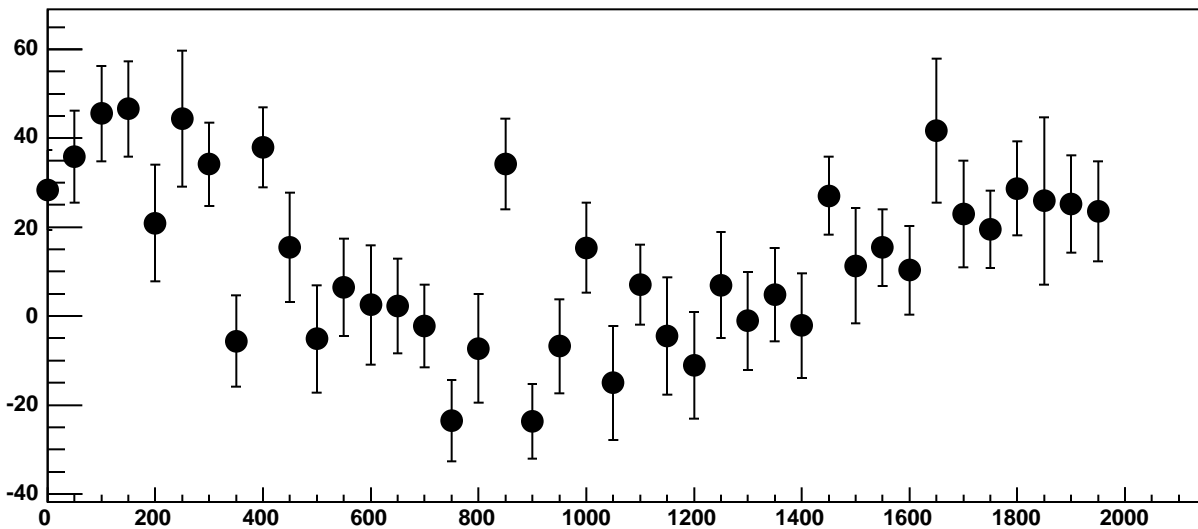


$\chi^2 / \text{ndf}$  25.79 / 11  
p0  $-1636 \pm 17.76$   
p1  $0.06993 \pm 0.01614$

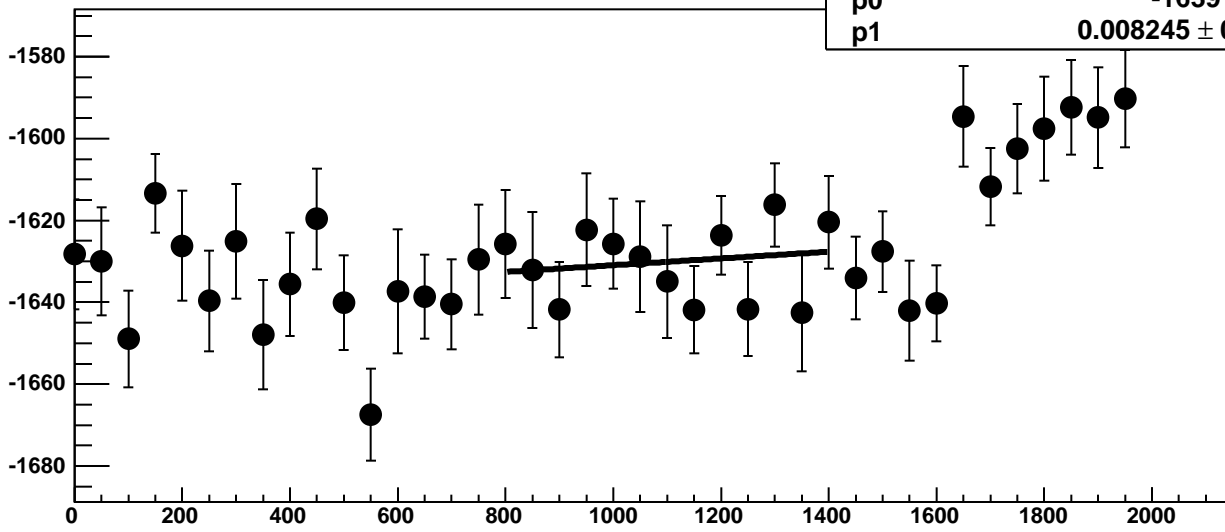
Chip 9, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC



Chip 9, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 9, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

7.571 / 11

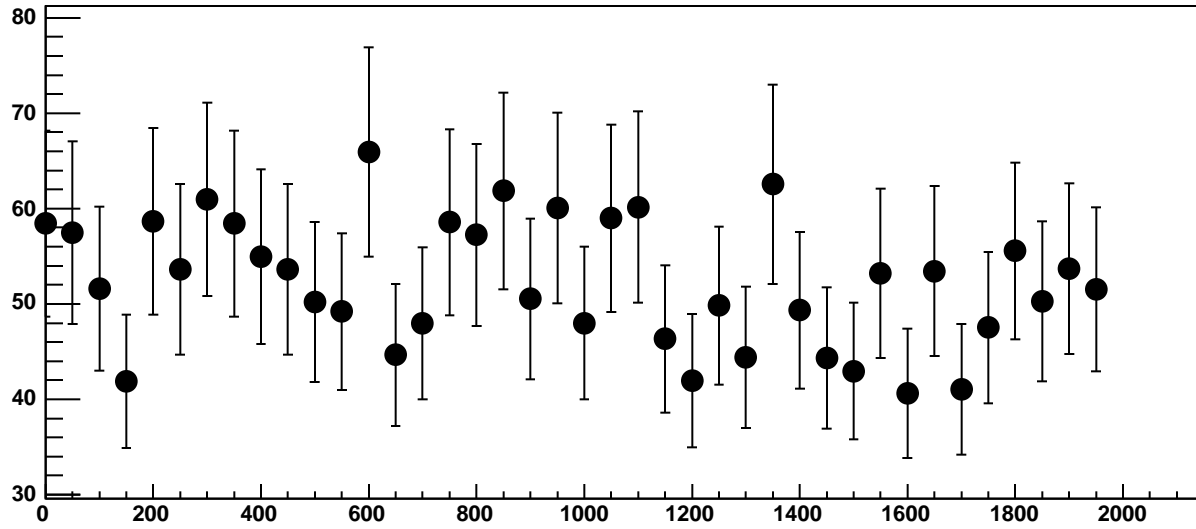
p0

$-1639 \pm 20.69$

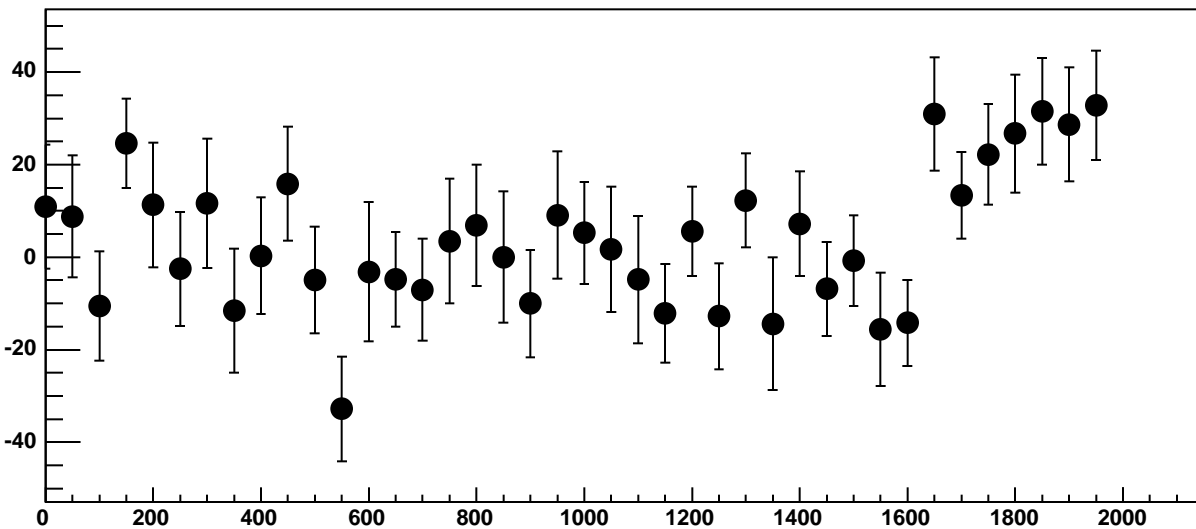
p1

$0.008245 \pm 0.01825$

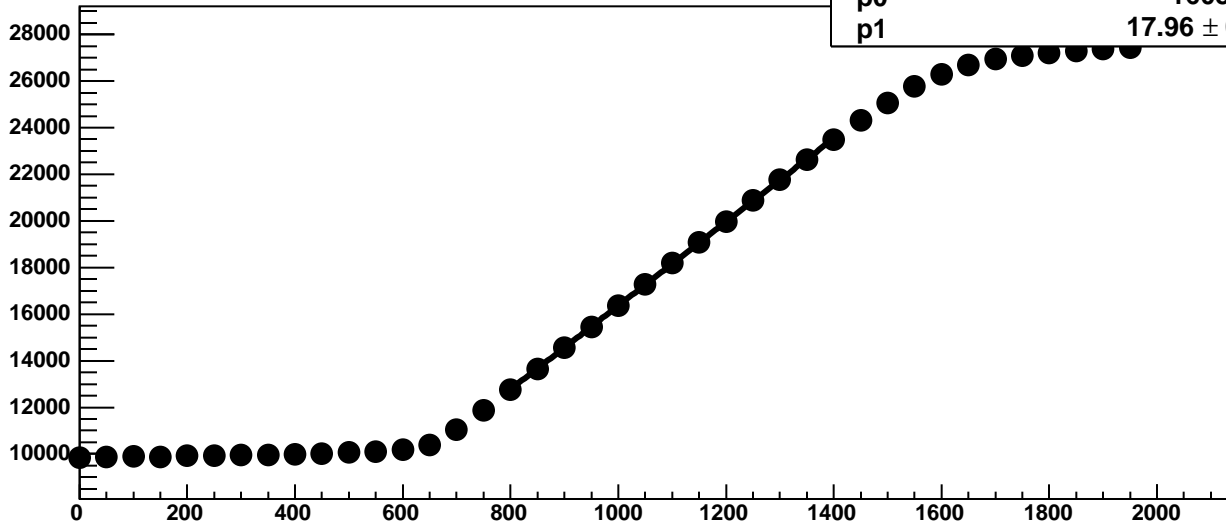
Chip 9, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC



Chip 9, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC

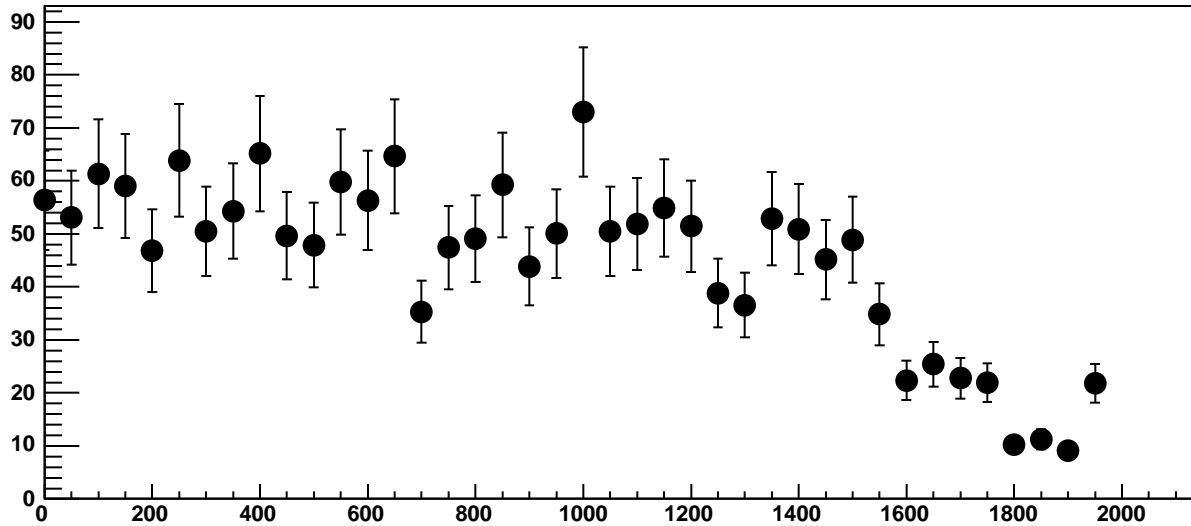


Chip 9, Channel 12, Enable 4!, Hold=30, ADC Mean vs DAC

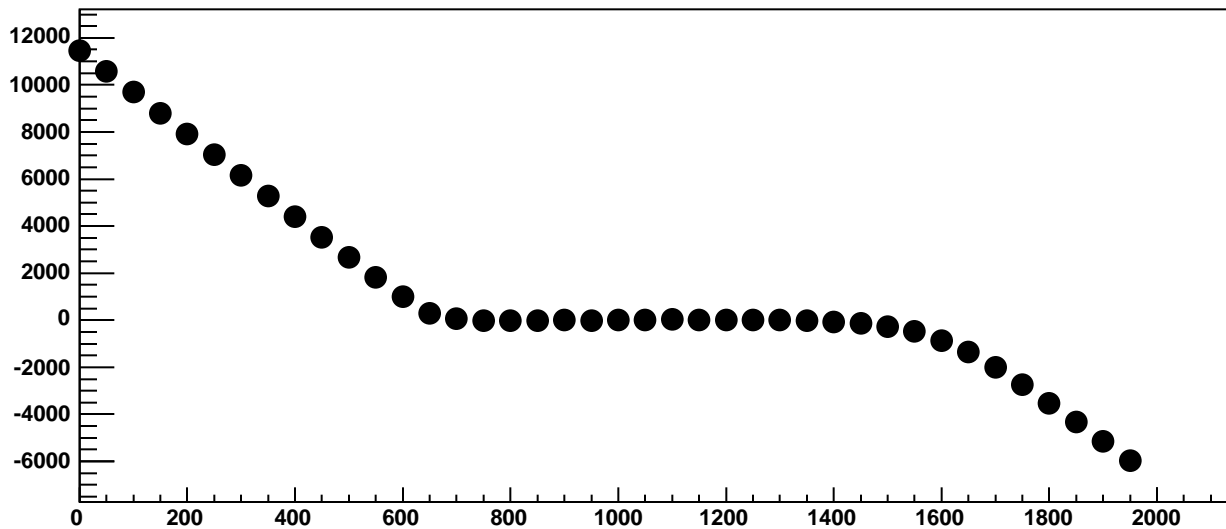


$\chi^2 / \text{ndf}$  73.85 / 11  
p0  $-1603 \pm 18.81$   
p1  $17.96 \pm 0.01656$

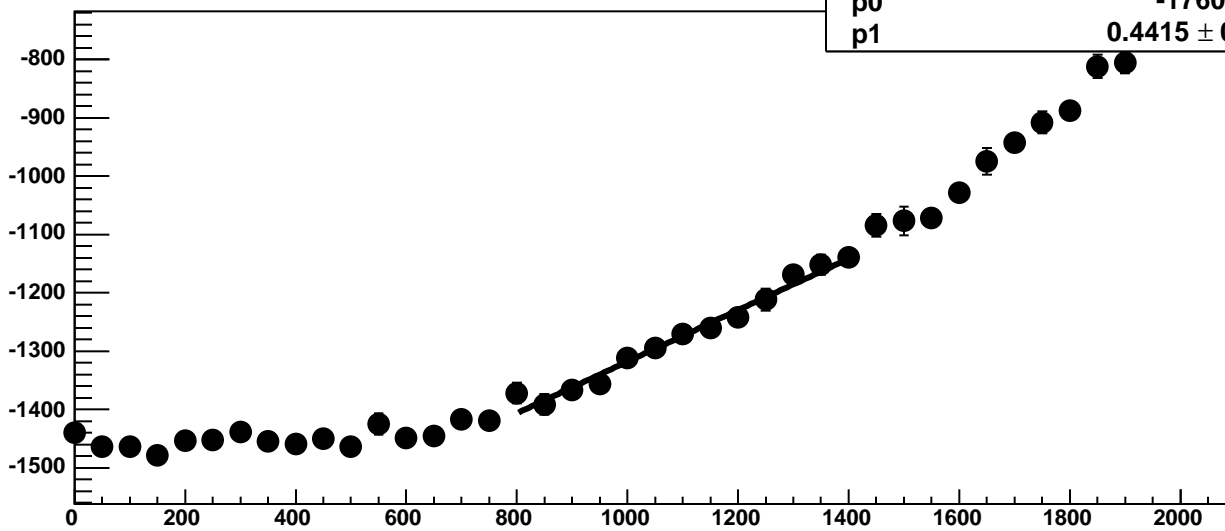
Chip 9, Channel 12, Enable 4!, Hold=30, ADC Noise vs DAC



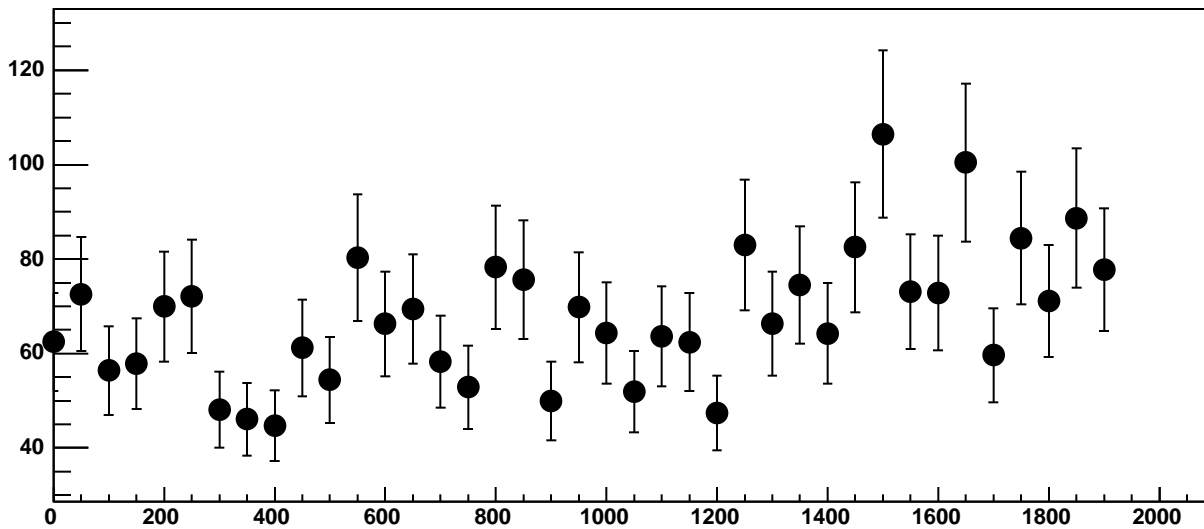
Chip 9, Channel 12, Enable 4!, Hold=30, ADC Residuals vs DAC



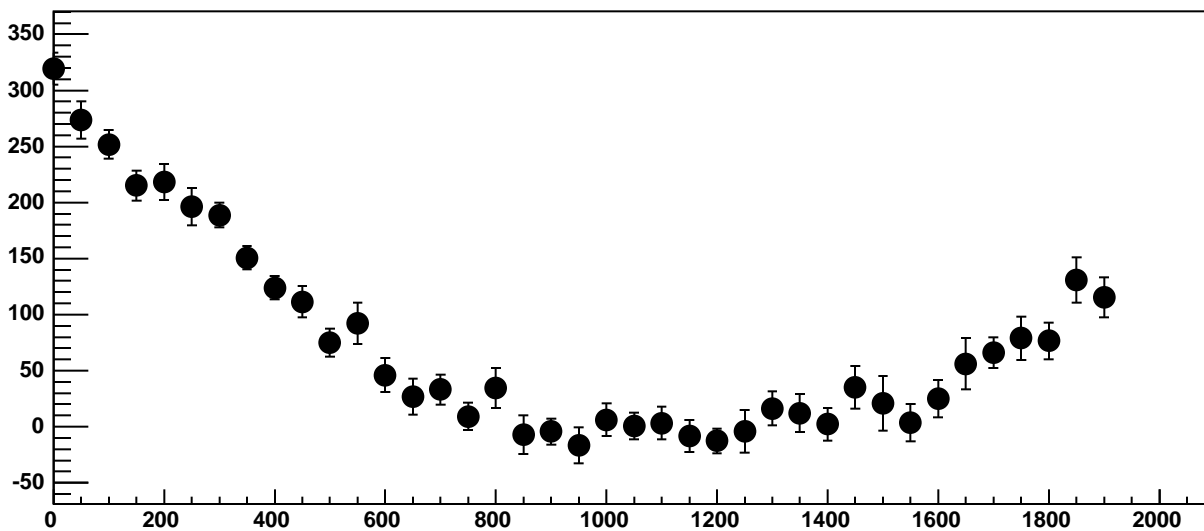
Chip 9, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC



Chip 9, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC

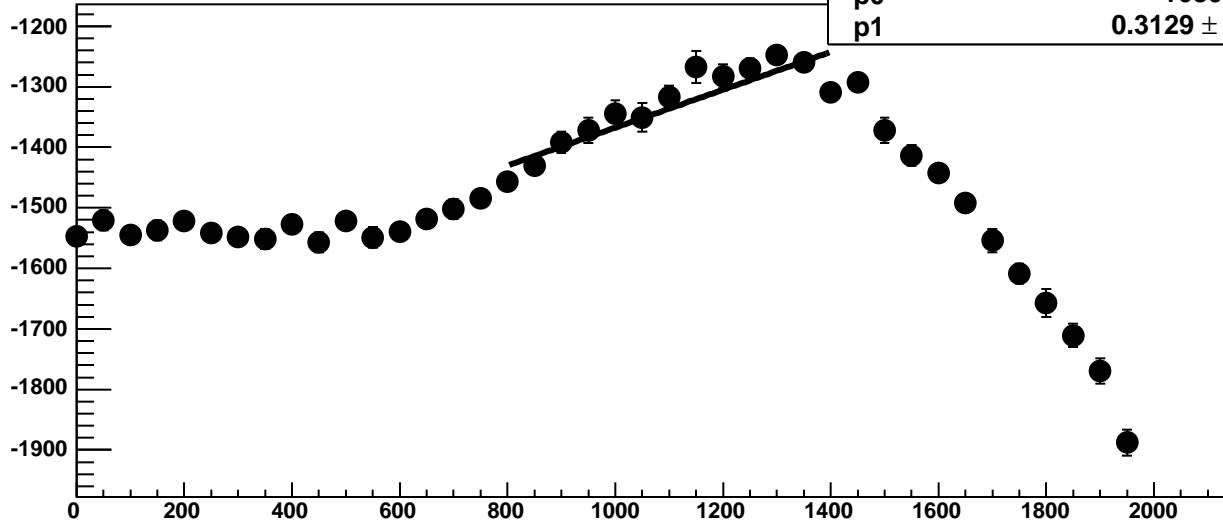


Chip 9, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC

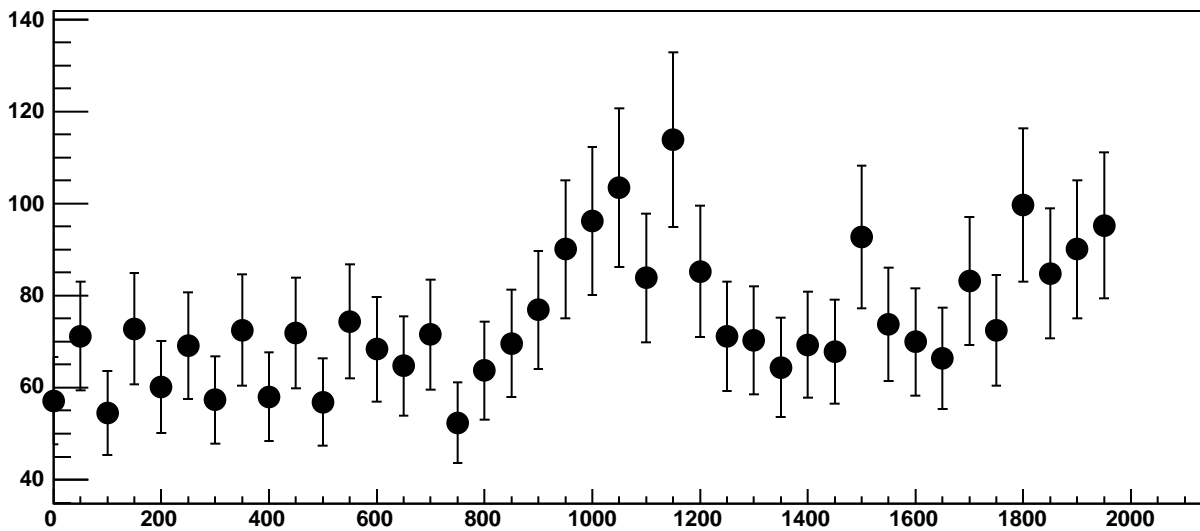




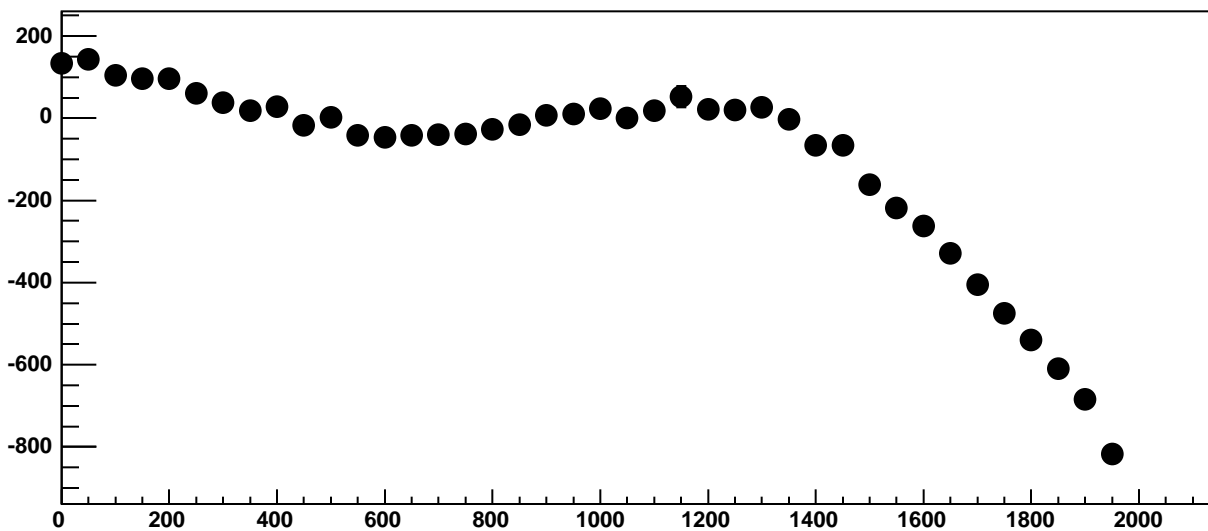
Chip 9, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC



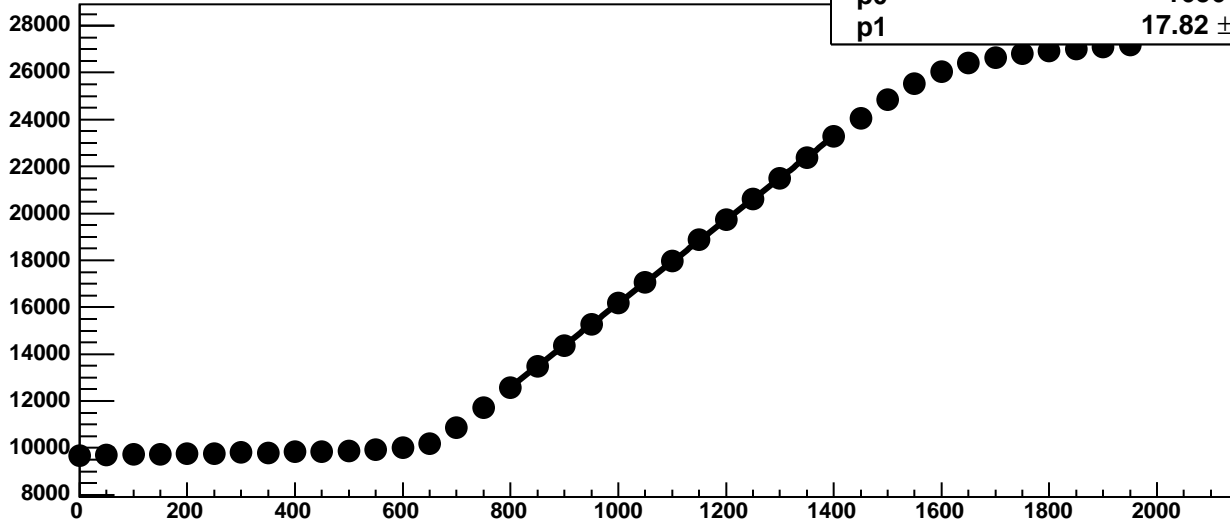
Chip 9, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC



Chip 9, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC

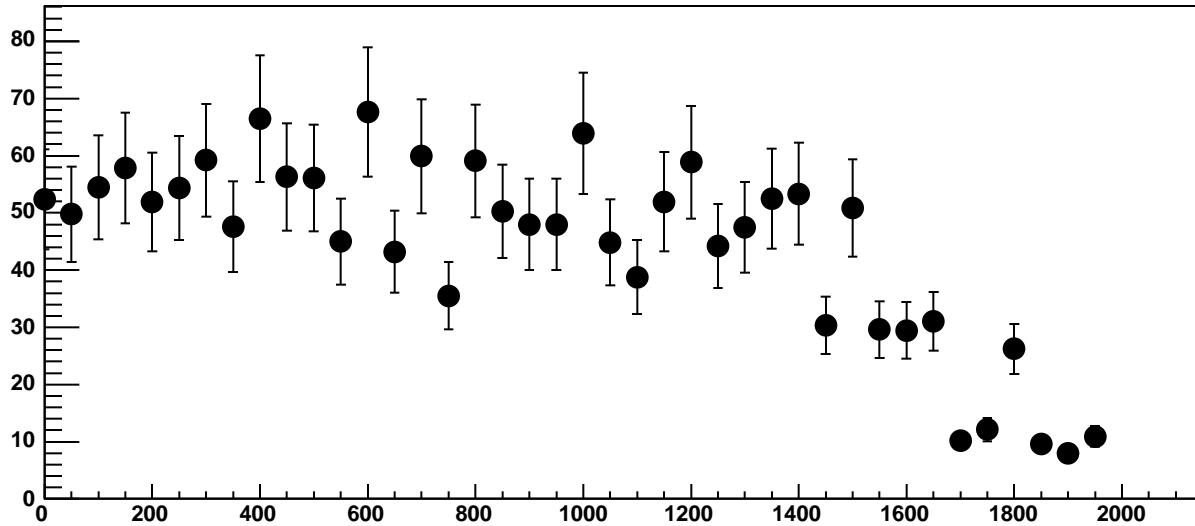


Chip 9, Channel 13, Enable 1!, Hold=30, ADC Mean vs DAC

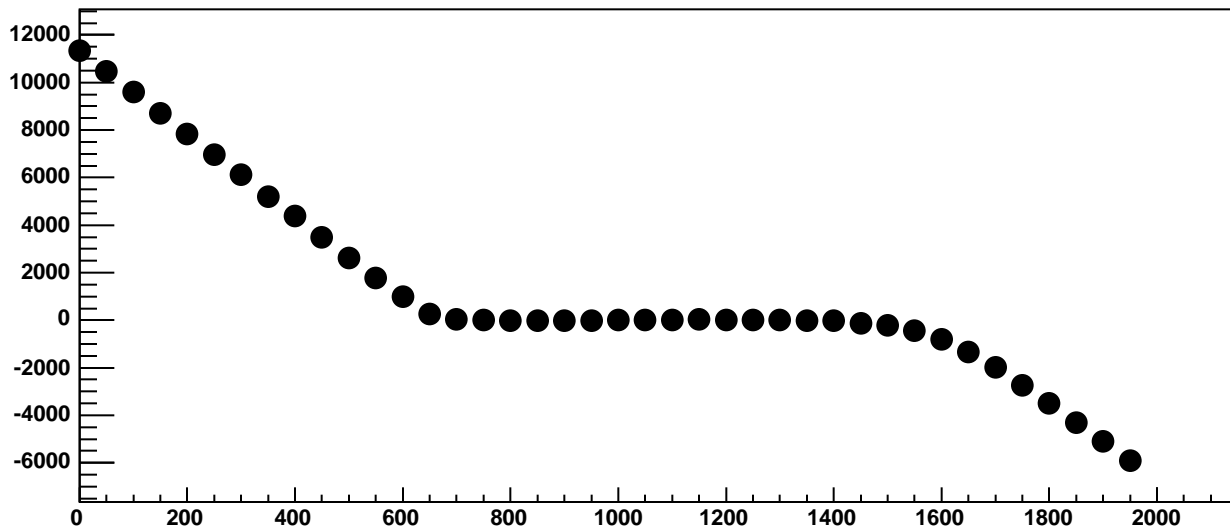


$\chi^2 / \text{ndf}$  25.47 / 11  
p0  $-1656 \pm 19.67$   
p1  $17.82 \pm 0.0176$

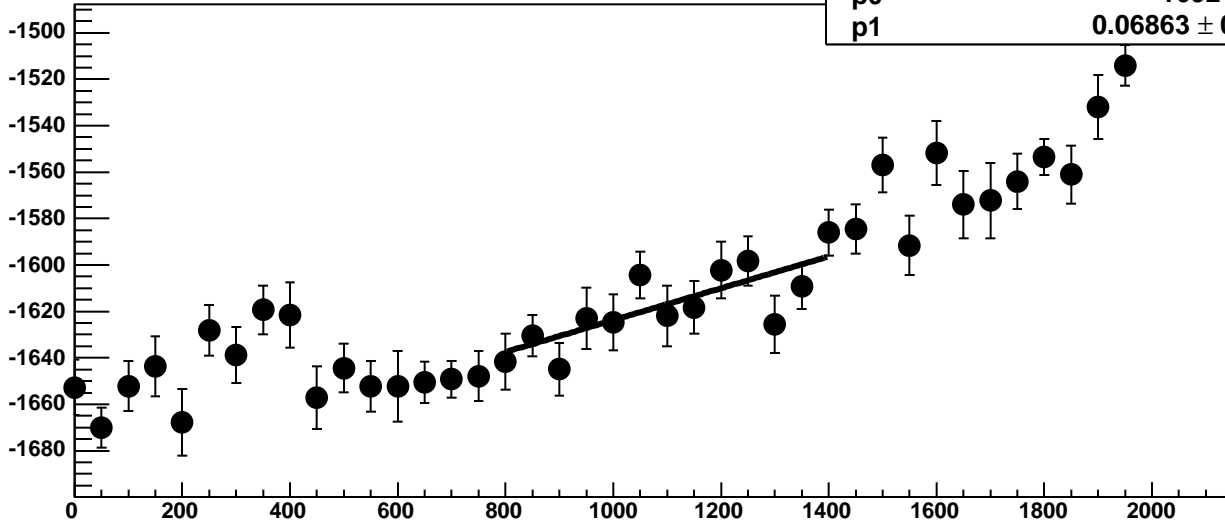
Chip 9, Channel 13, Enable 1!, Hold=30, ADC Noise vs DAC



Chip 9, Channel 13, Enable 1!, Hold=30, ADC Residuals vs DAC



Chip 9, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

11.2 / 11

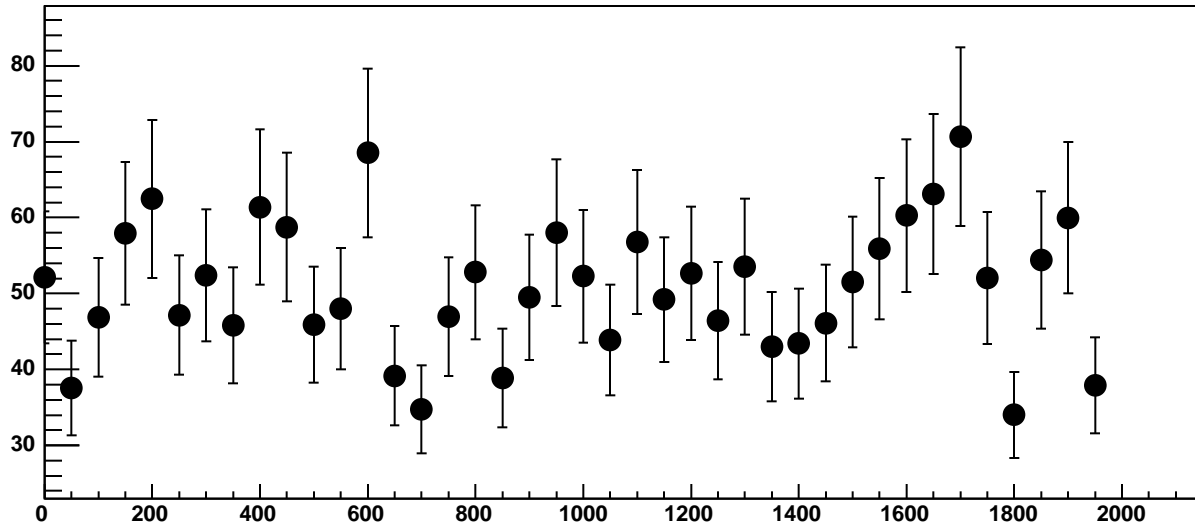
p0

$-1692 \pm 17.68$

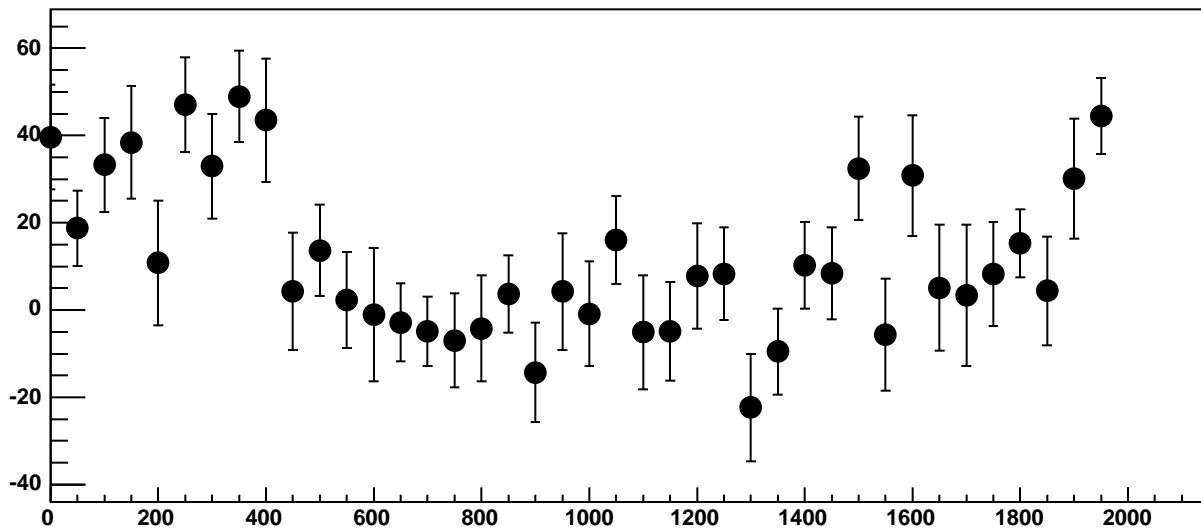
p1

$0.06863 \pm 0.01576$

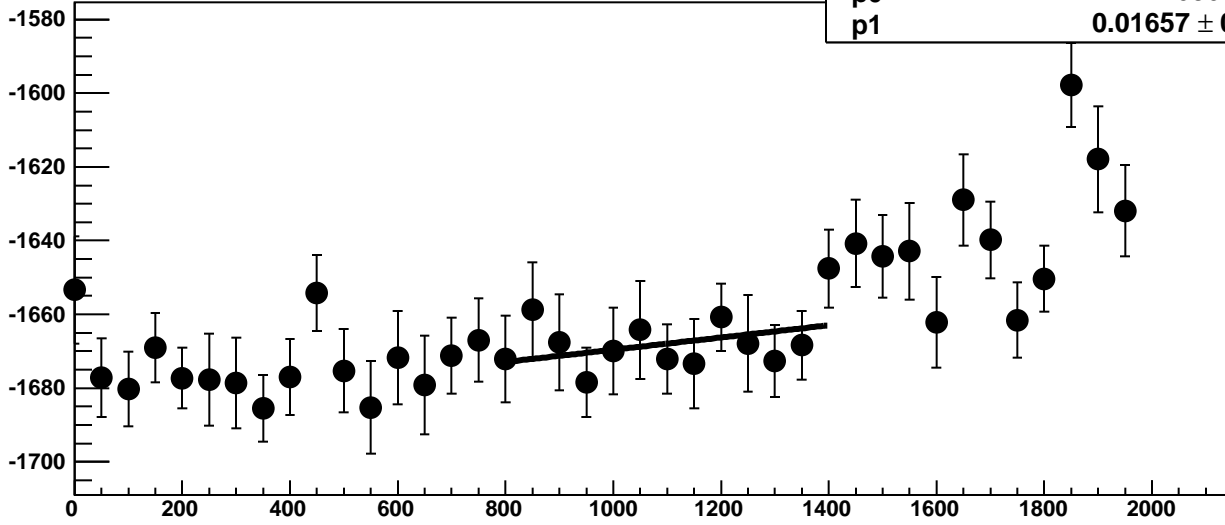
Chip 9, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC



Chip 9, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC

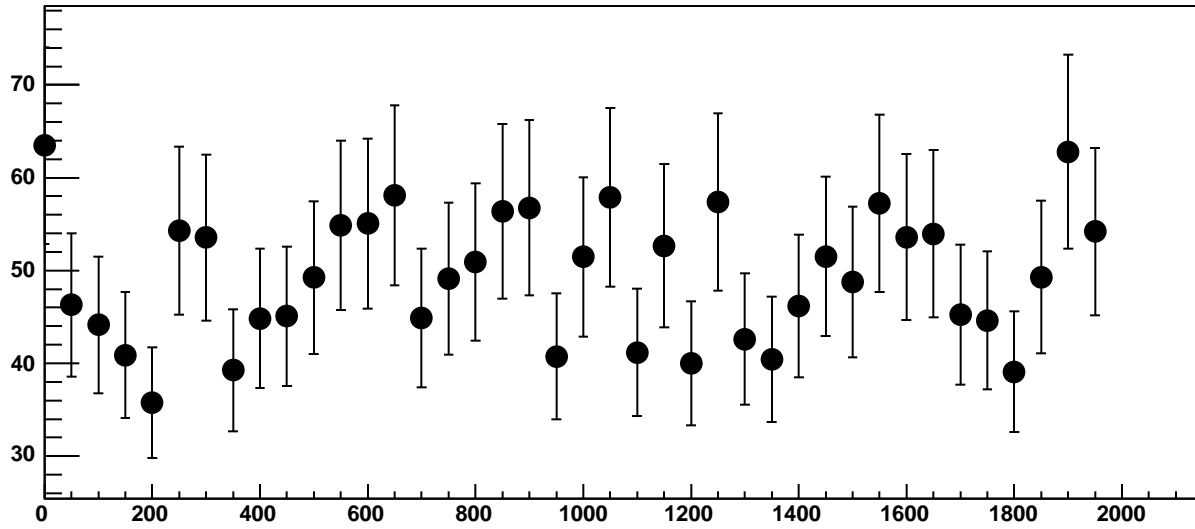


Chip 9, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC

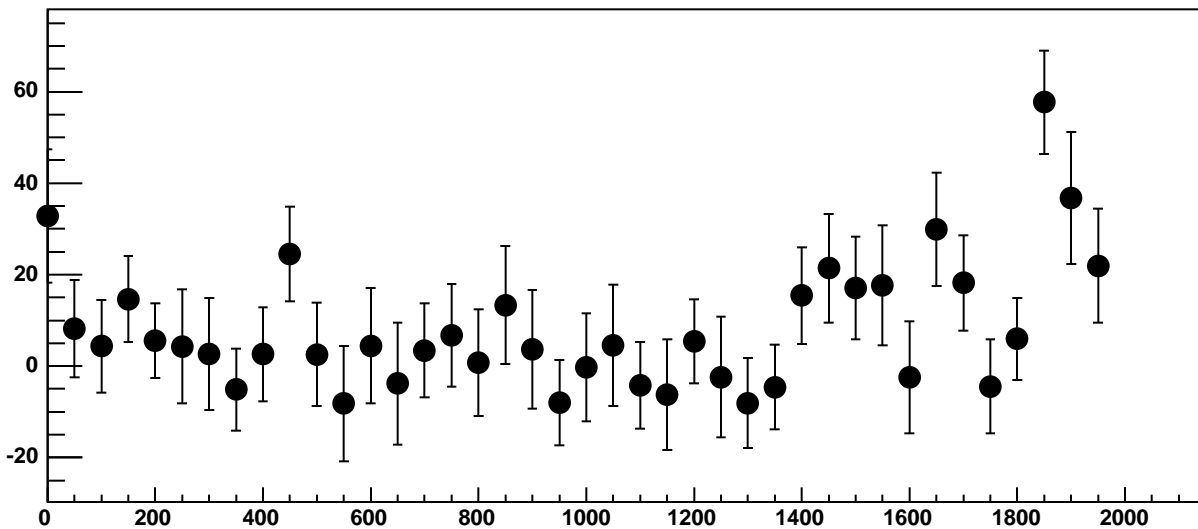


$\chi^2 / \text{ndf}$  5.898 / 11  
p0  $-1686 \pm 18.53$   
p1  $0.01657 \pm 0.01629$

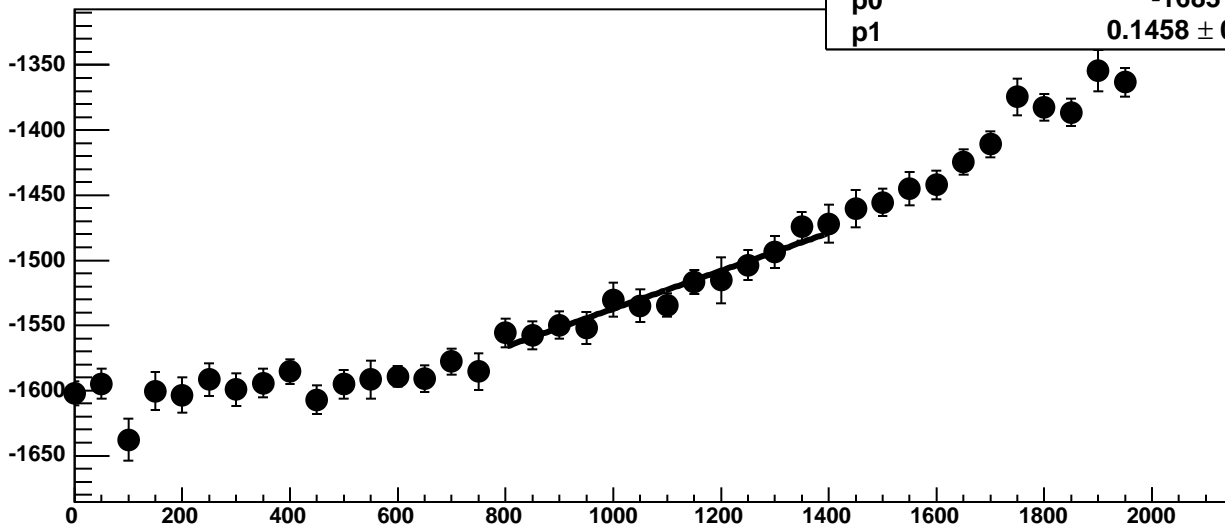
Chip 9, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC



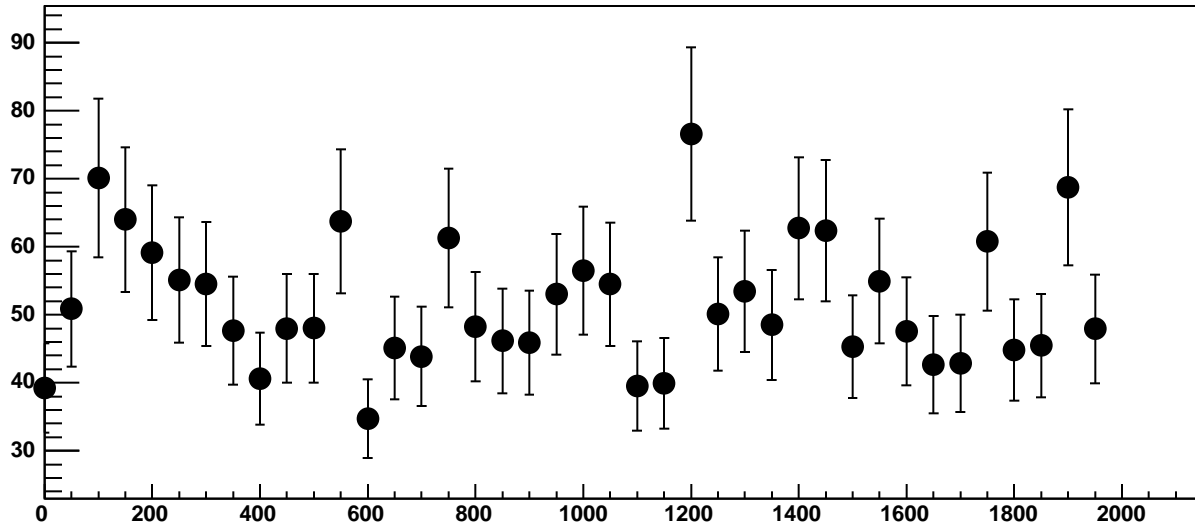
Chip 9, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC



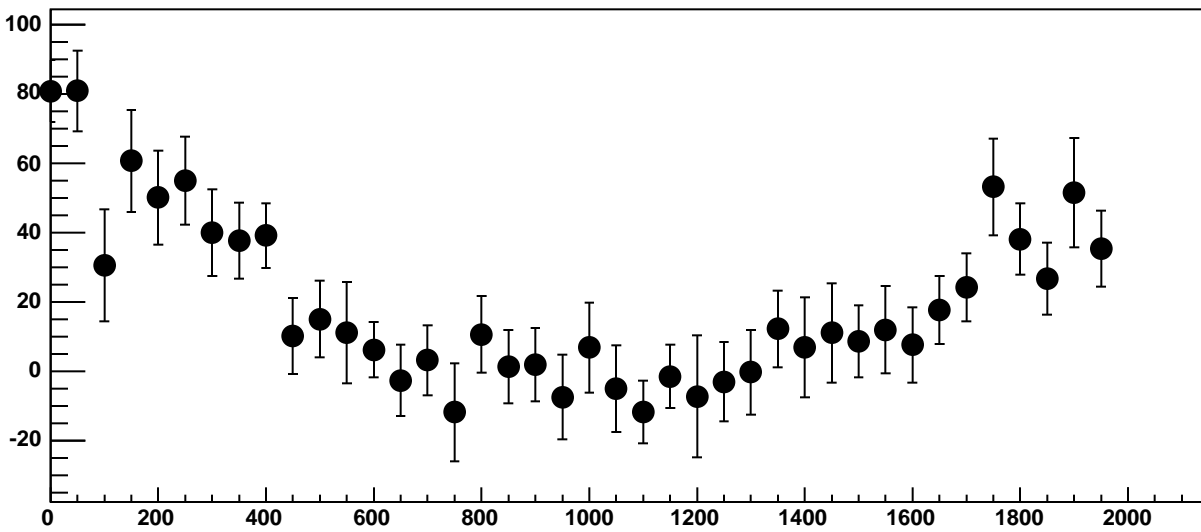
Chip 9, Channel 13, Enable 4, Hold=30, ADC Mean vs DAC



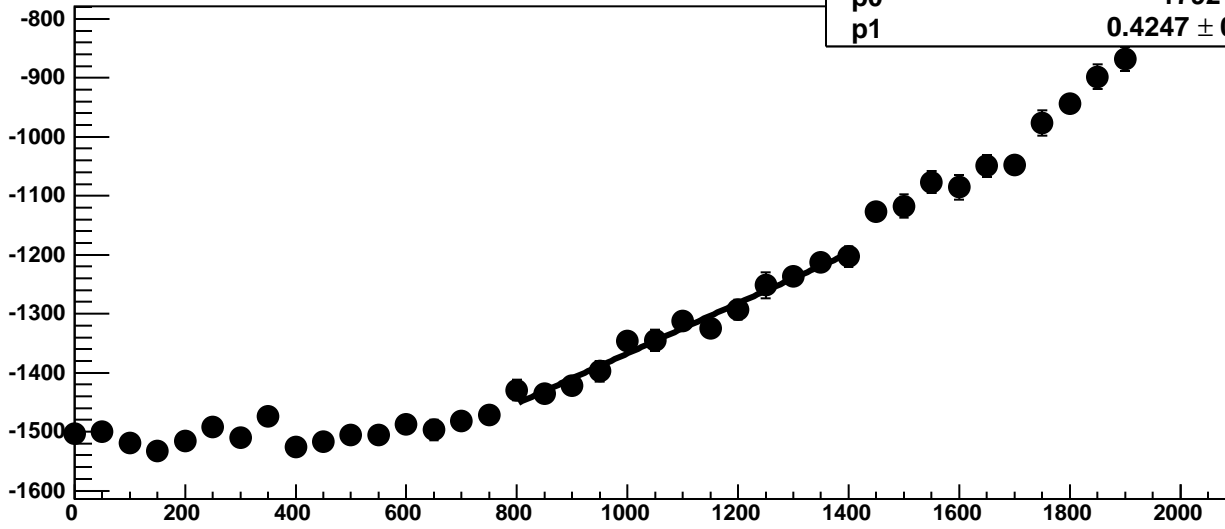
Chip 9, Channel 13, Enable 4, Hold=30, ADC Noise vs DAC



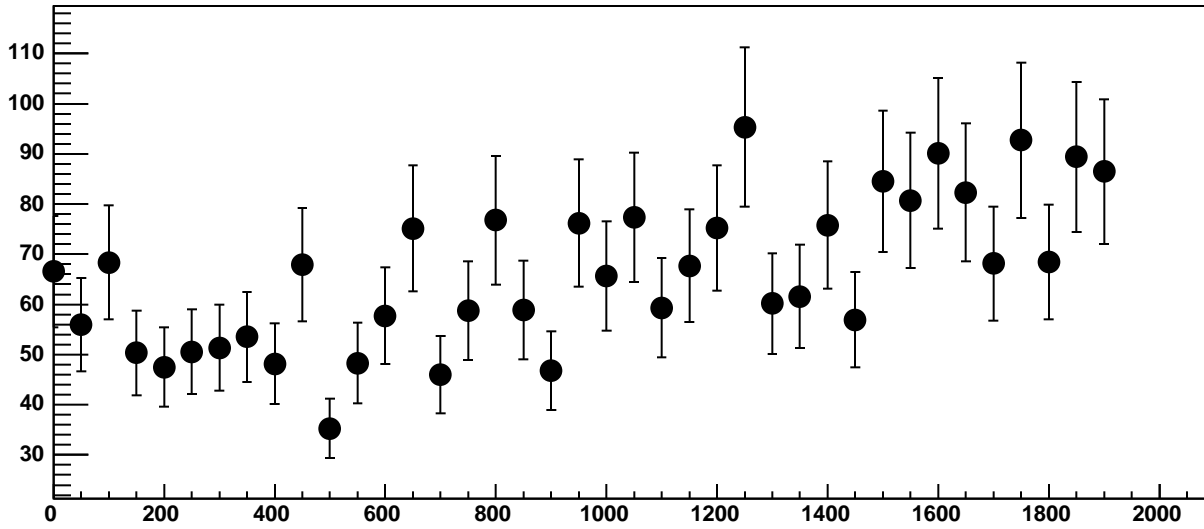
Chip 9, Channel 13, Enable 4, Hold=30, ADC Residuals vs DAC



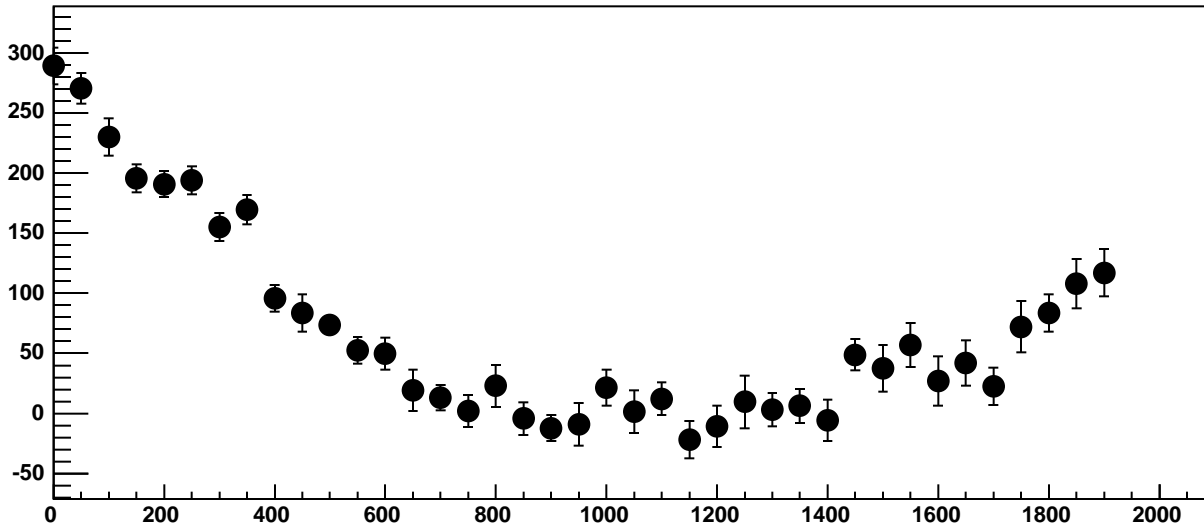
Chip 9, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC



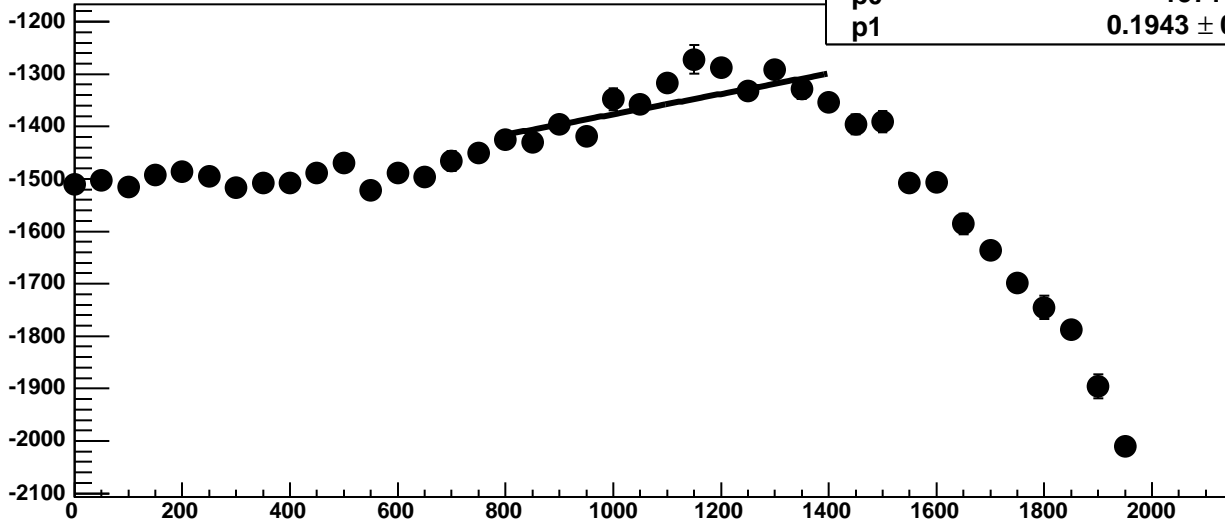
Chip 9, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC



Chip 9, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC

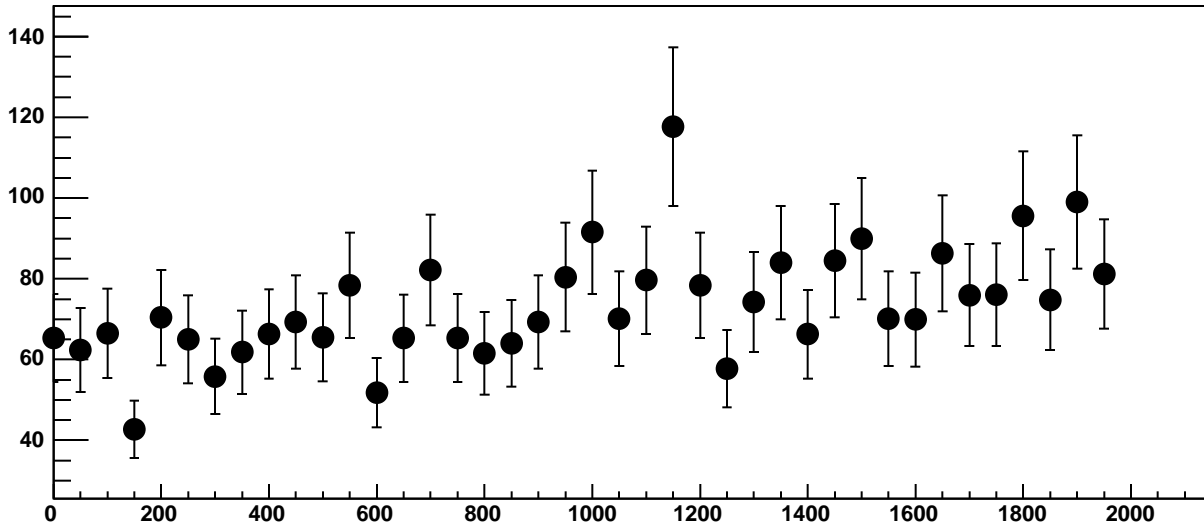


Chip 9, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC

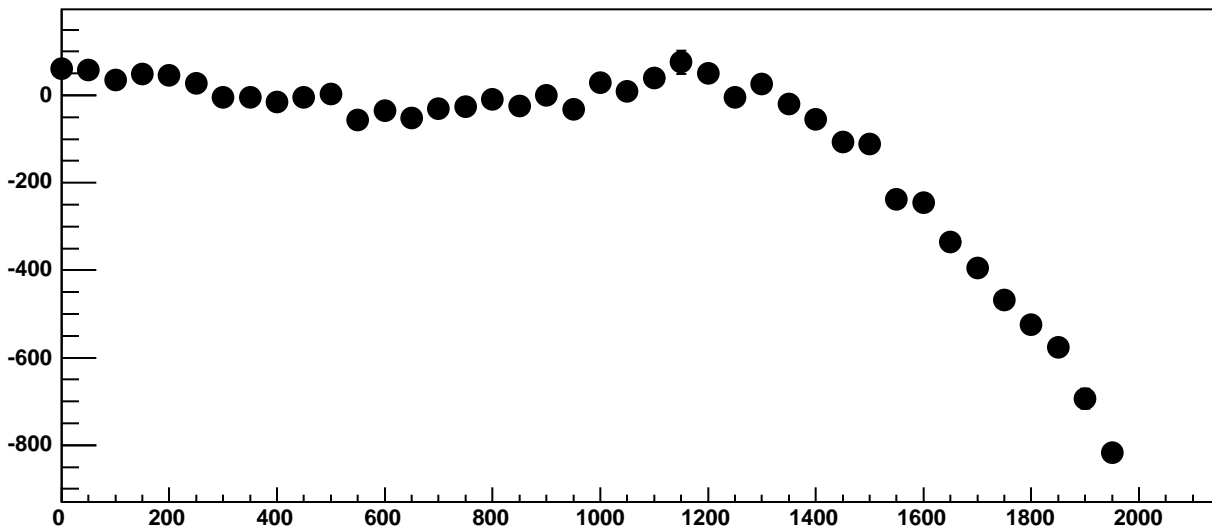


$\chi^2 / \text{ndf}$  45.82 / 11  
p0  $-1571 \pm 25.76$   
p1  $0.1943 \pm 0.02323$

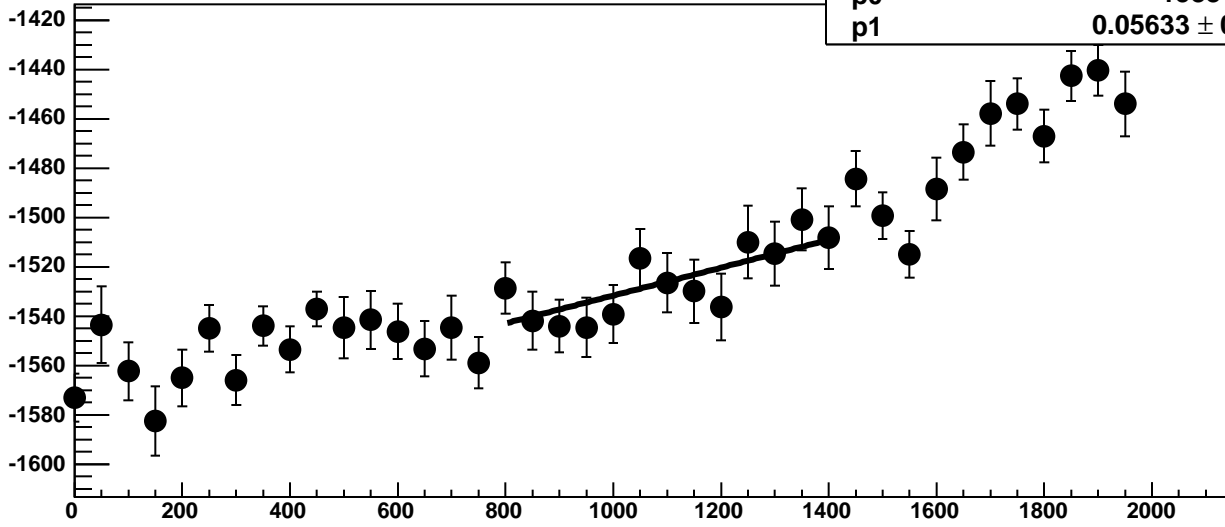
Chip 9, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC



Chip 9, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC

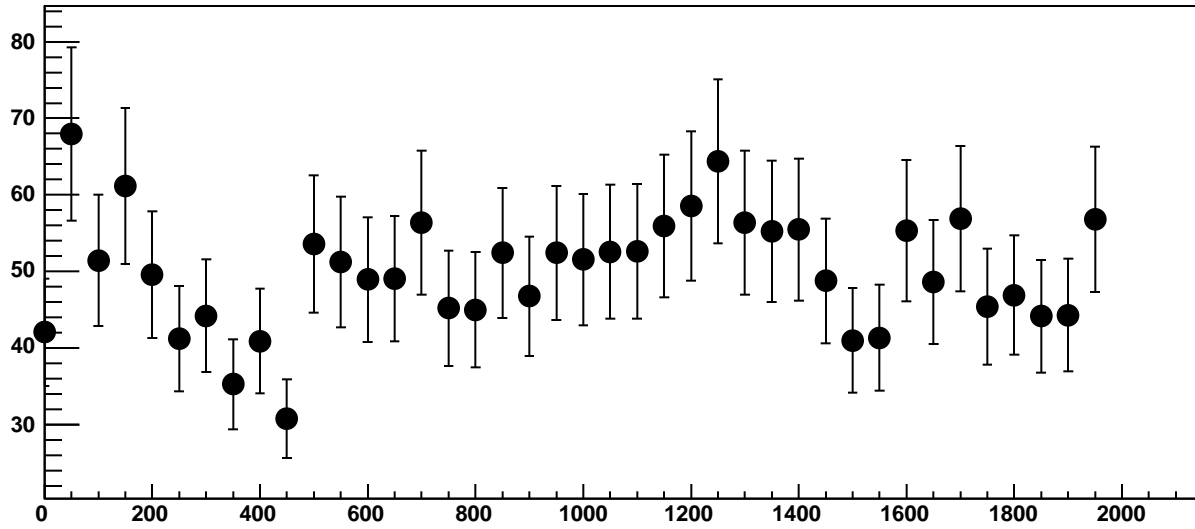


Chip 9, Channel 14, Enable 1, Hold=30, ADC Mean vs DAC

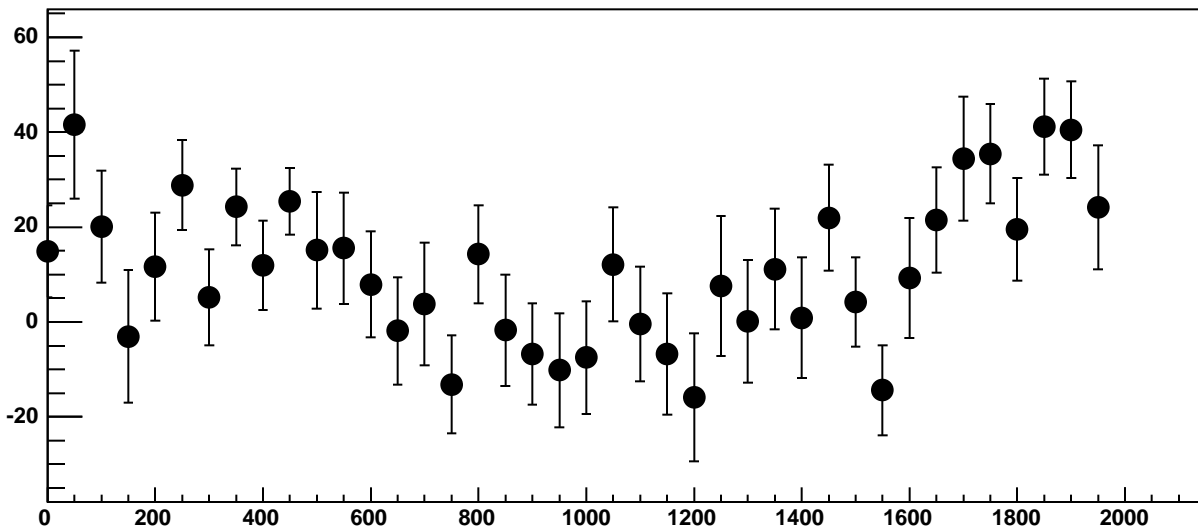


$\chi^2 / \text{ndf}$  7.182 / 11  
p0  $-1588 \pm 19.32$   
p1  $0.05633 \pm 0.01772$

Chip 9, Channel 14, Enable 1, Hold=30, ADC Noise vs DAC

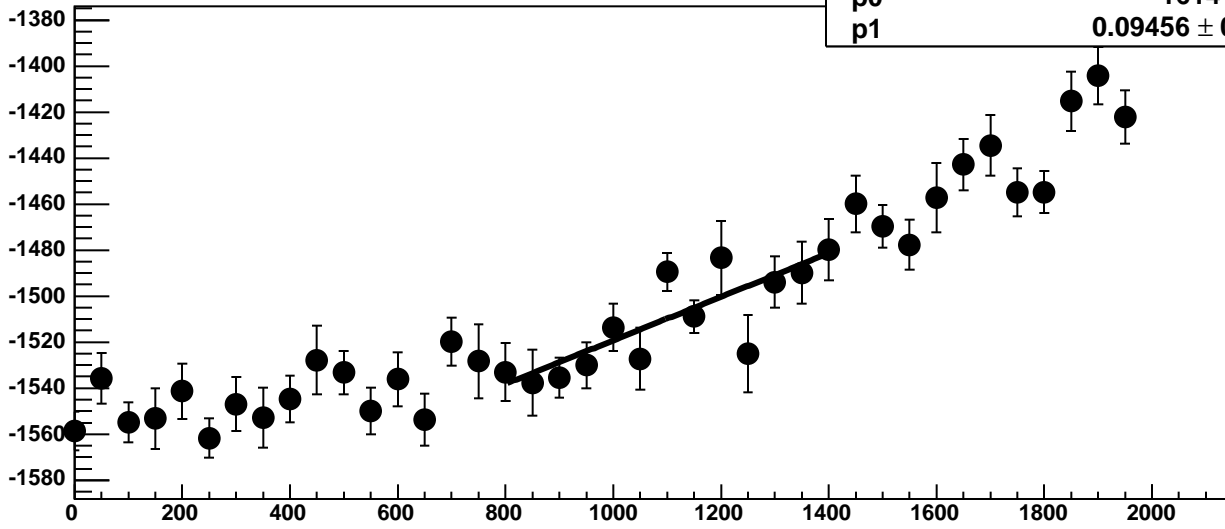


Chip 9, Channel 14, Enable 1, Hold=30, ADC Residuals vs DAC



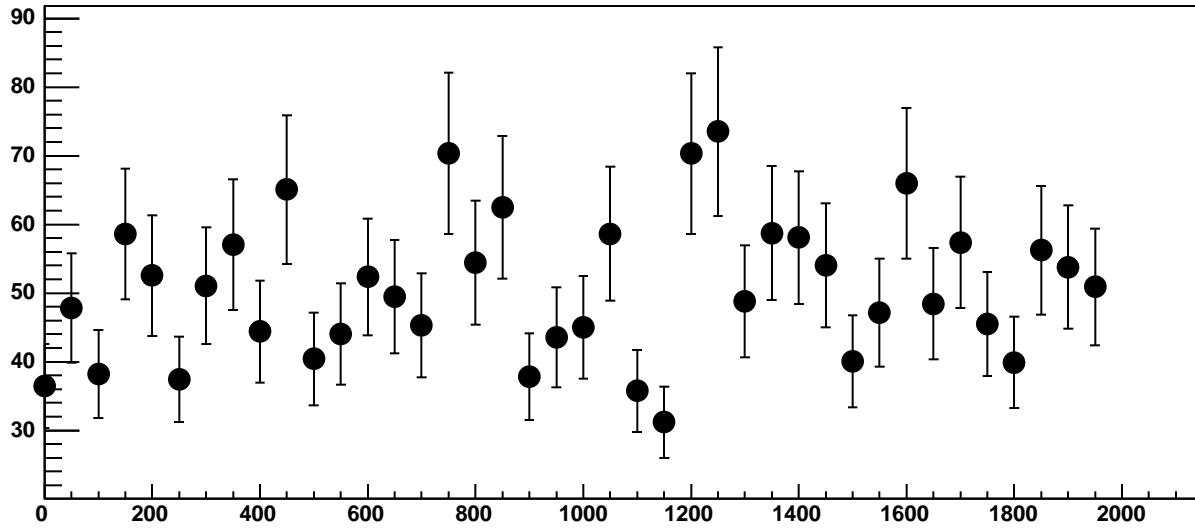


Chip 9, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC

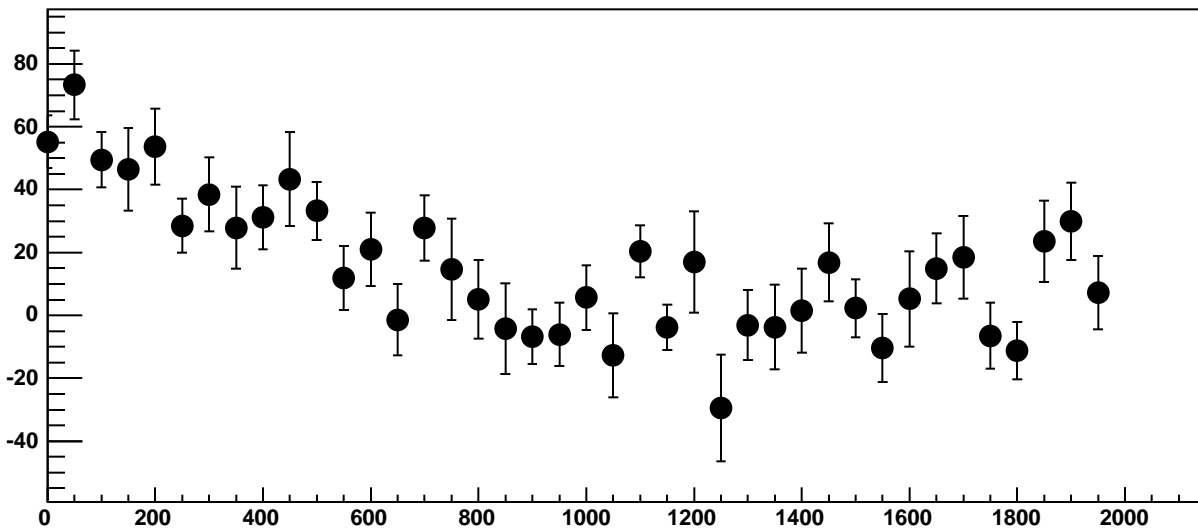


$\chi^2 / \text{ndf}$  13.15 / 11  
p0  $-1614 \pm 19.84$   
p1  $0.09456 \pm 0.01812$

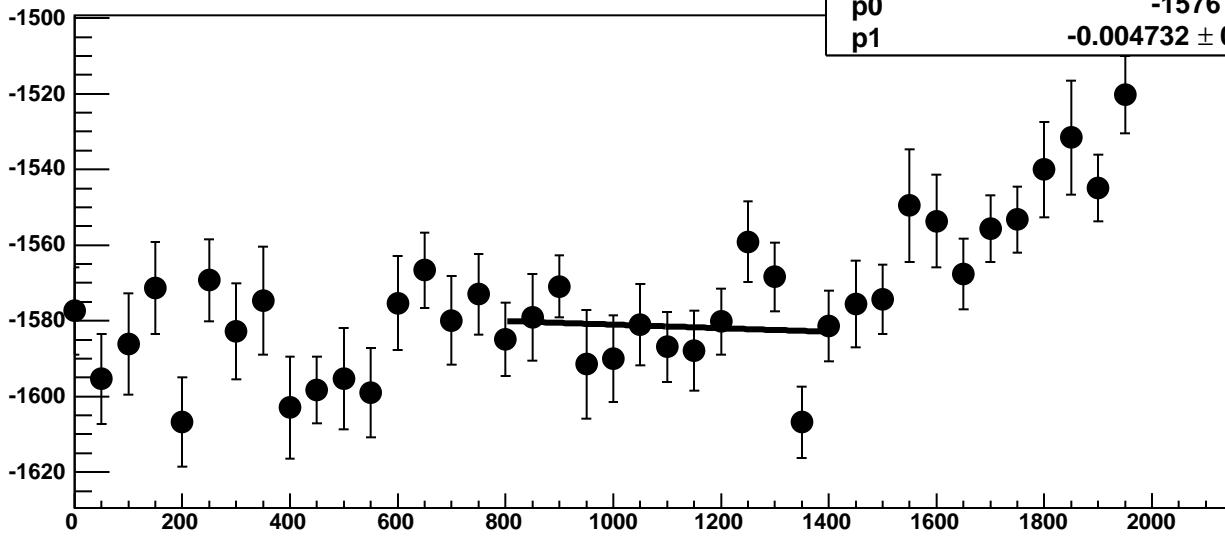
Chip 9, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC



Chip 9, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 9, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

17.15 / 11

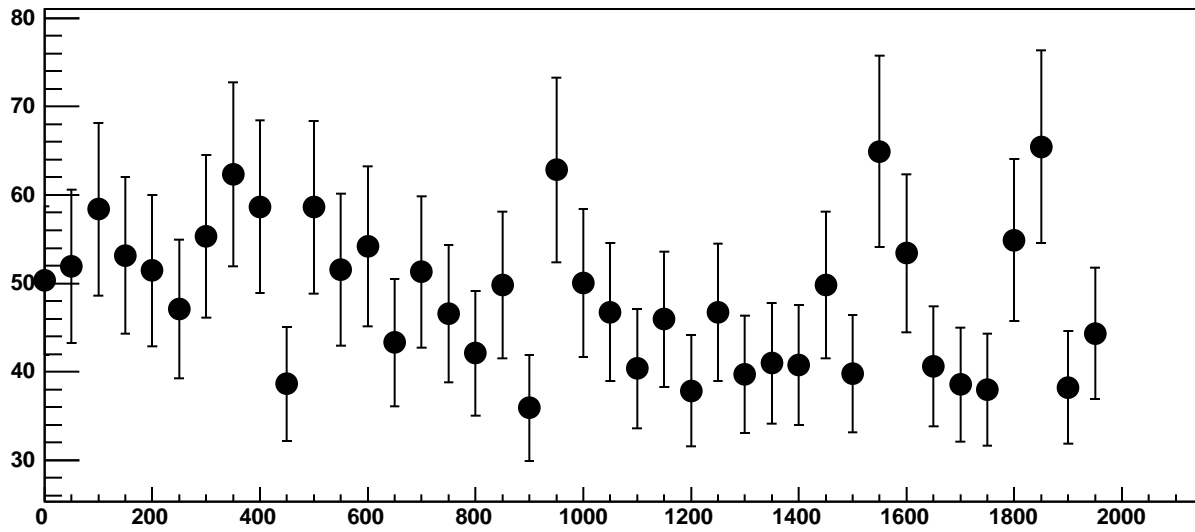
p0

$-1576 \pm 16.33$

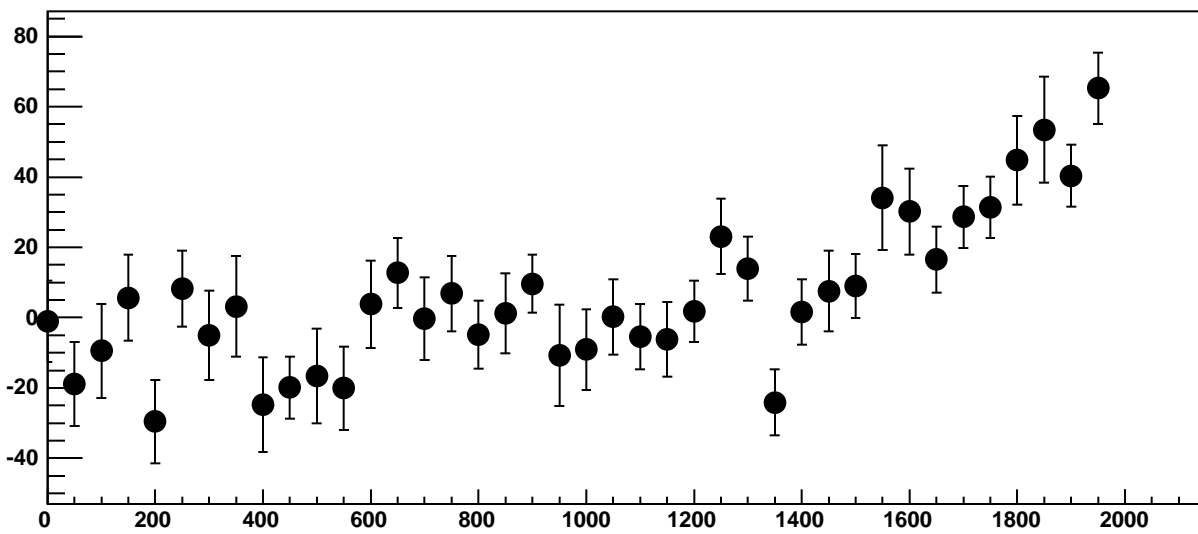
p1

$-0.004732 \pm 0.01446$

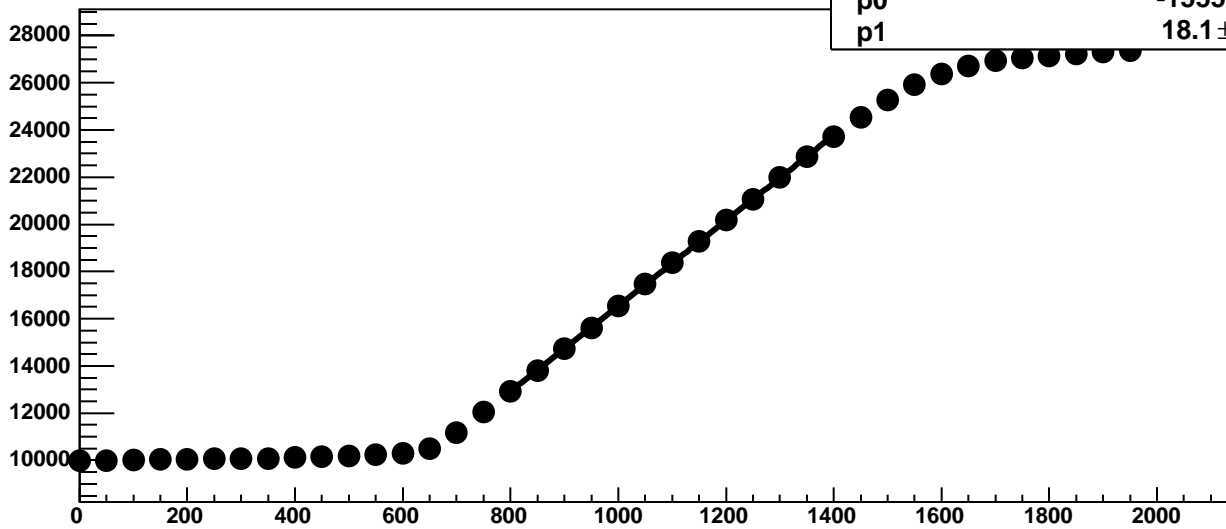
Chip 9, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC



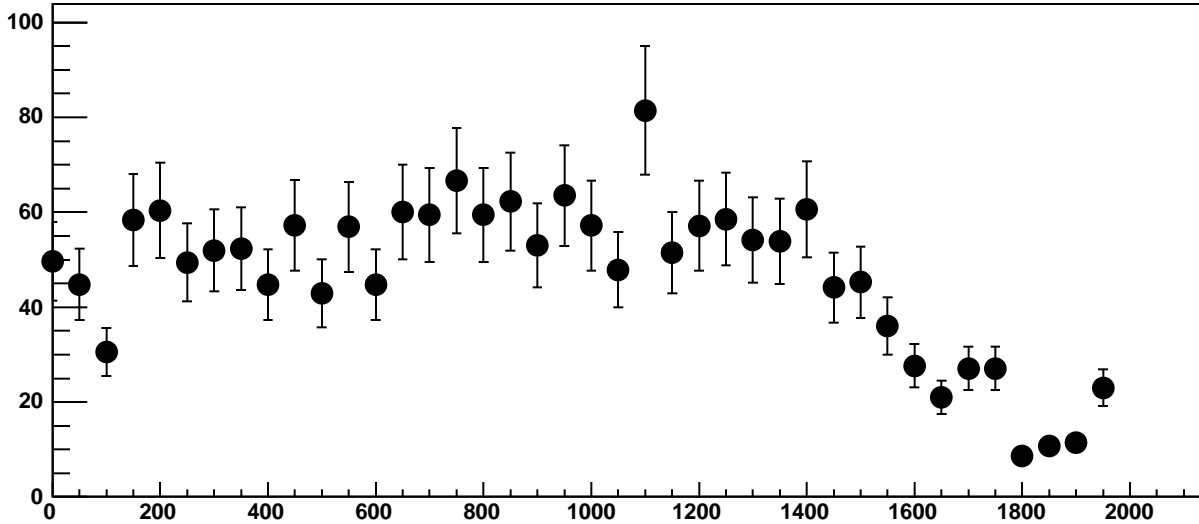
Chip 9, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC



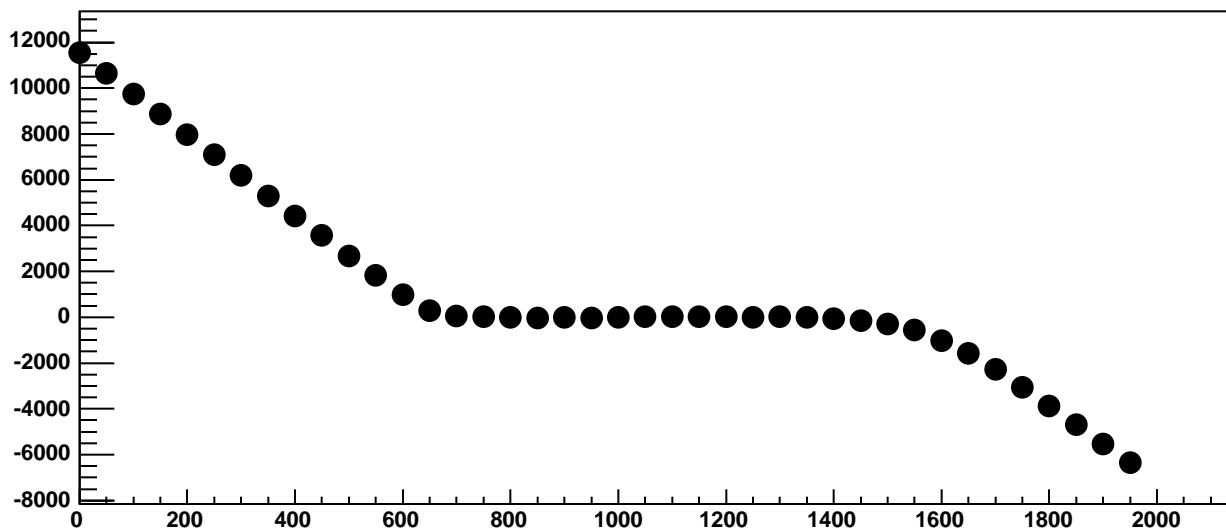
Chip 9, Channel 14, Enable 4!, Hold=30, ADC Mean vs DAC



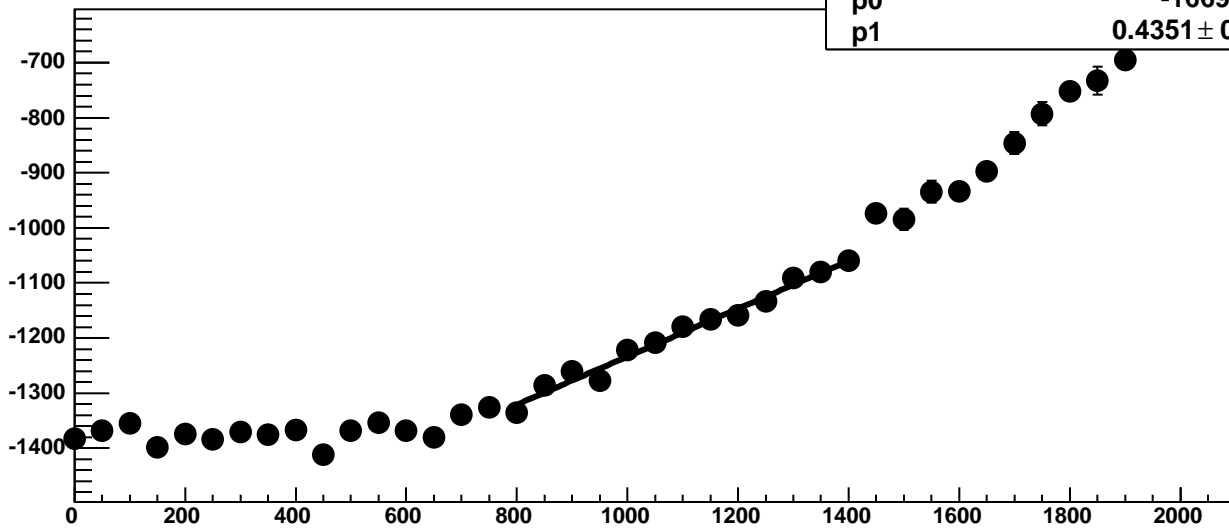
Chip 9, Channel 14, Enable 4!, Hold=30, ADC Noise vs DAC



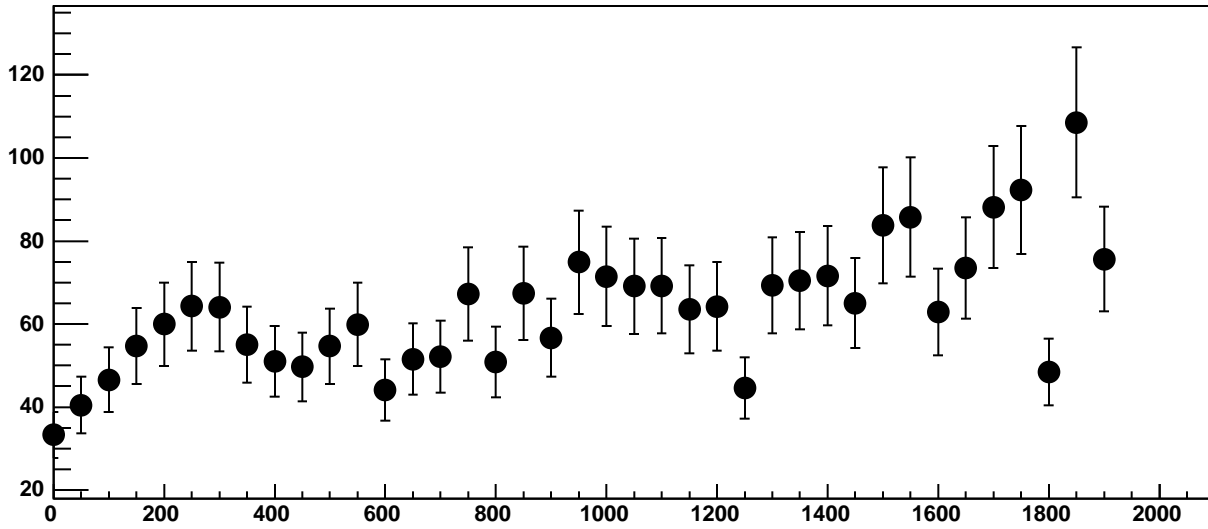
Chip 9, Channel 14, Enable 4!, Hold=30, ADC Residuals vs DAC



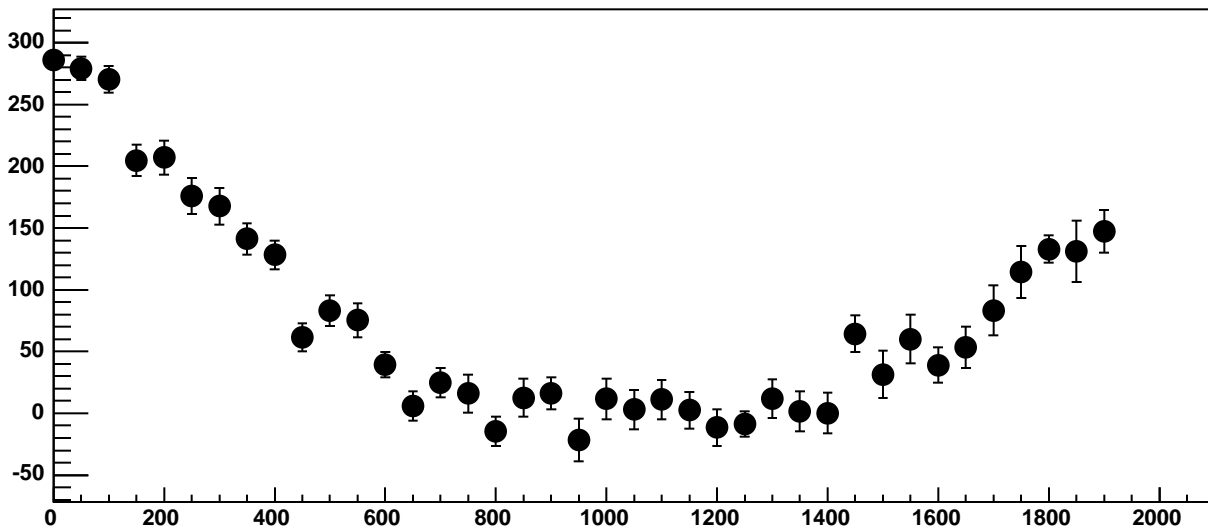
Chip 9, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC



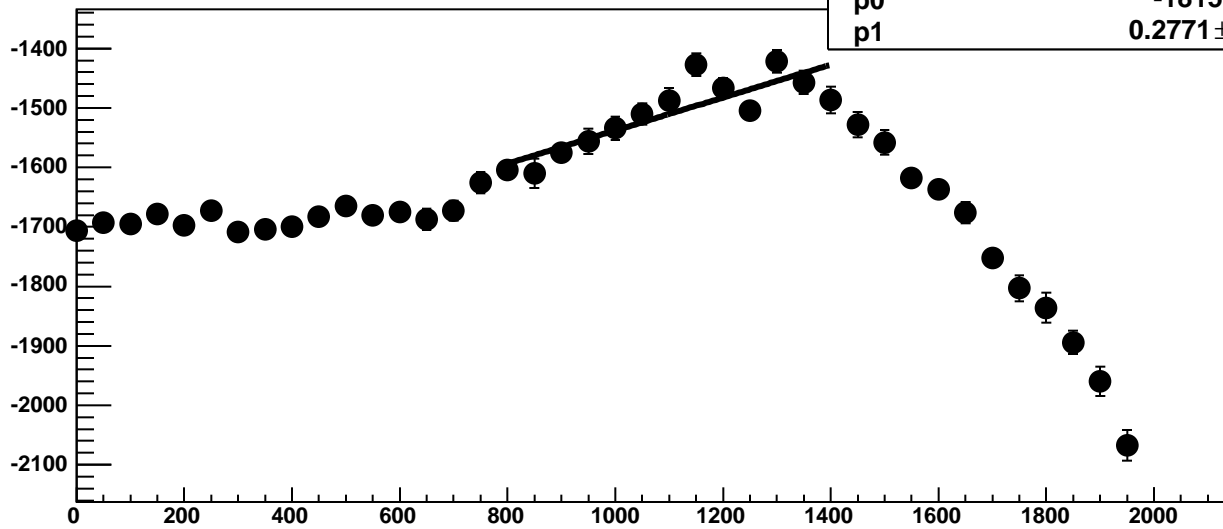
Chip 9, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC



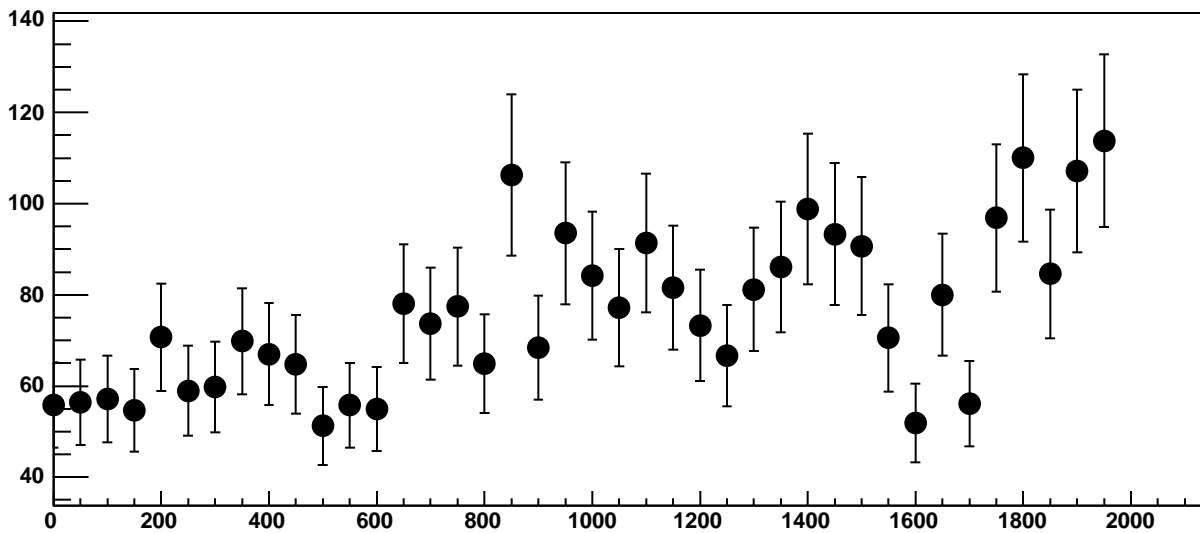
Chip 9, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC



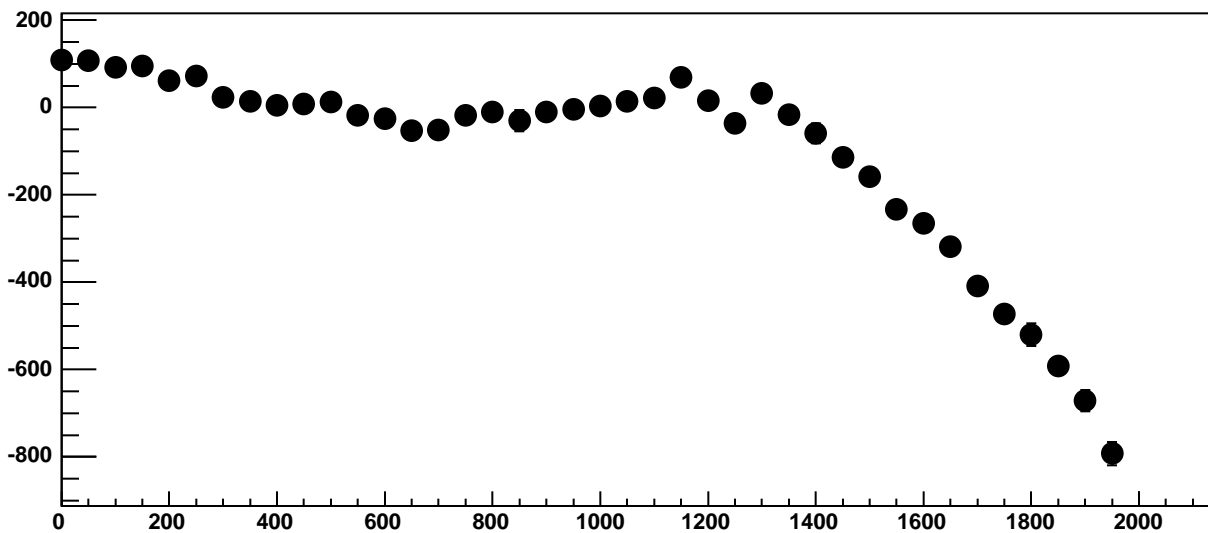
Chip 9, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC



Chip 9, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC

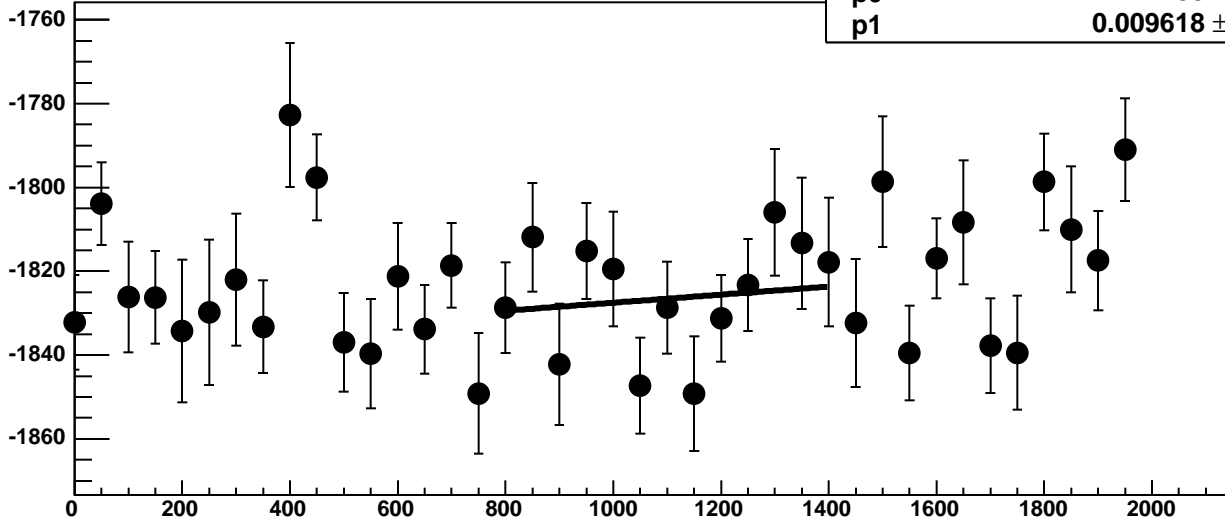


Chip 9, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC

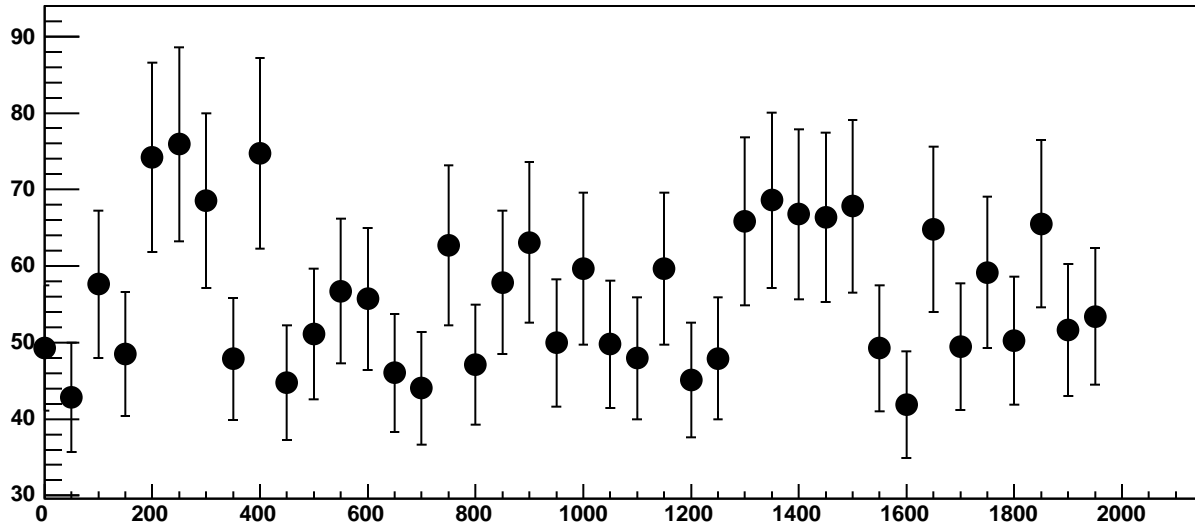


Chip 9, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC

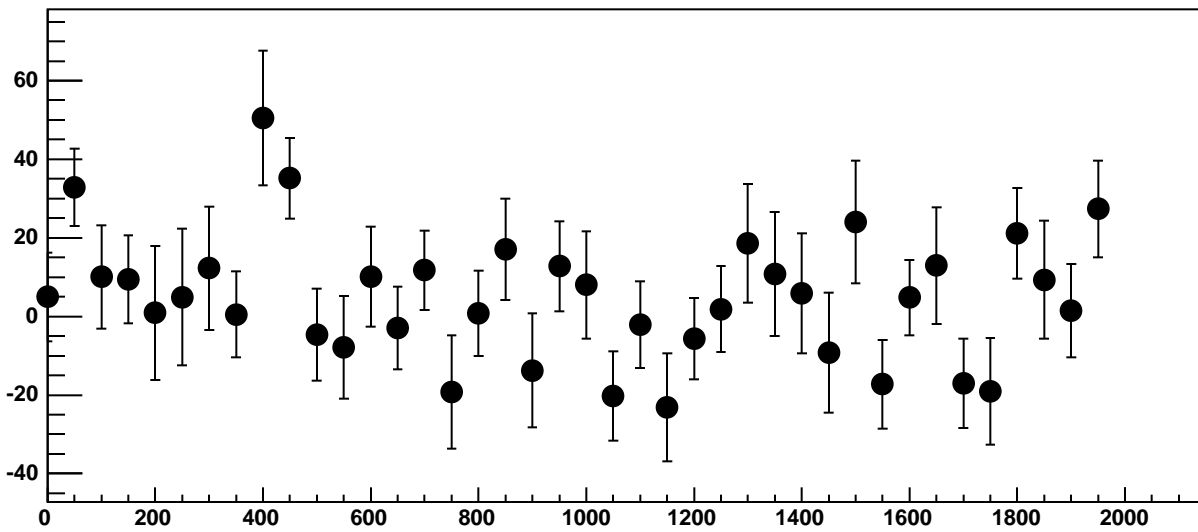
$\chi^2 / \text{ndf}$  12.74 / 11  
p0  $-1837 \pm 21.28$   
p1  $0.009618 \pm 0.0194$



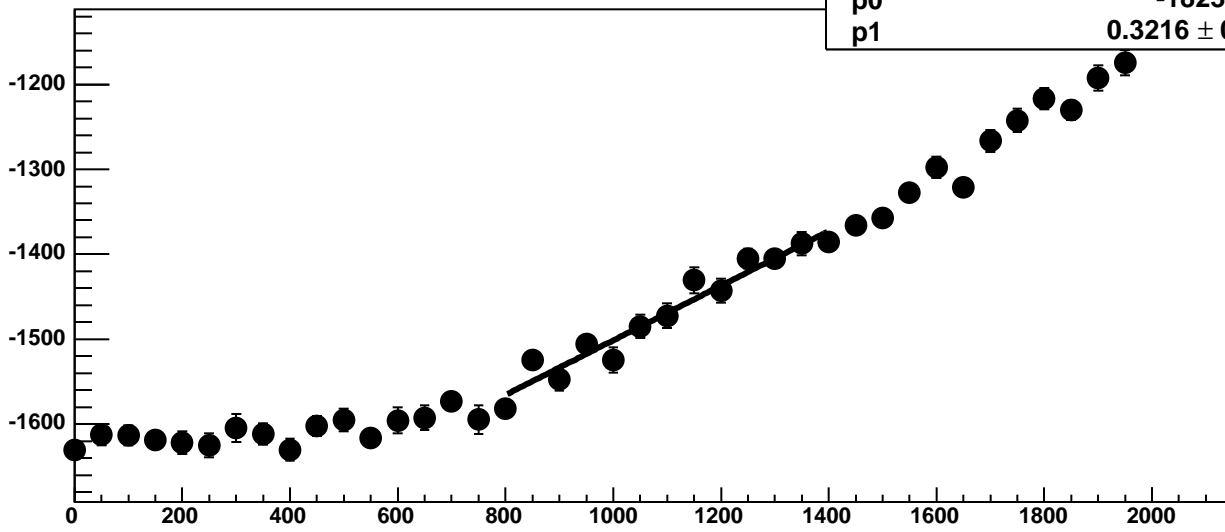
Chip 9, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC



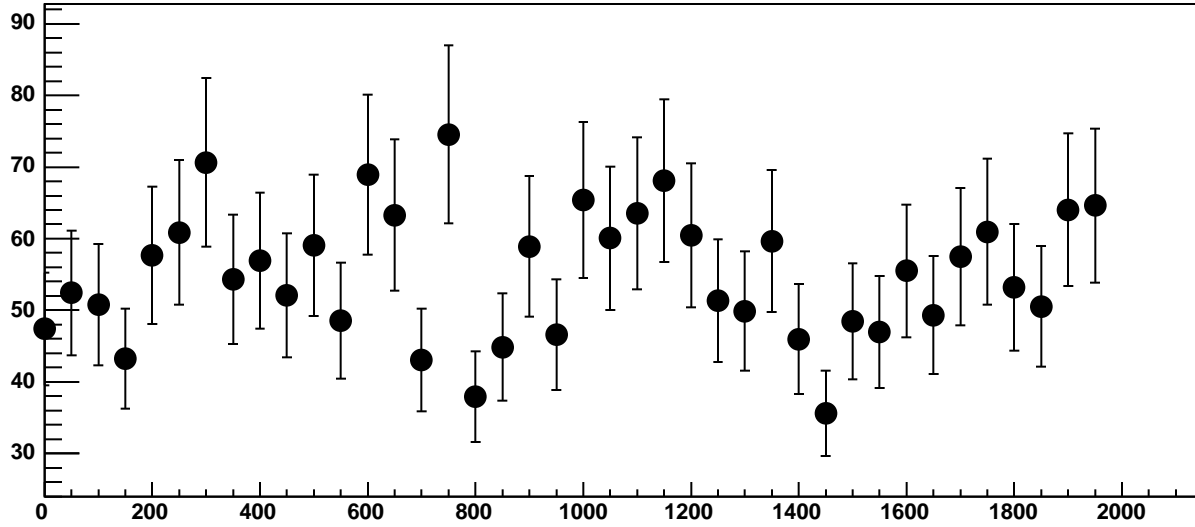
Chip 9, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC



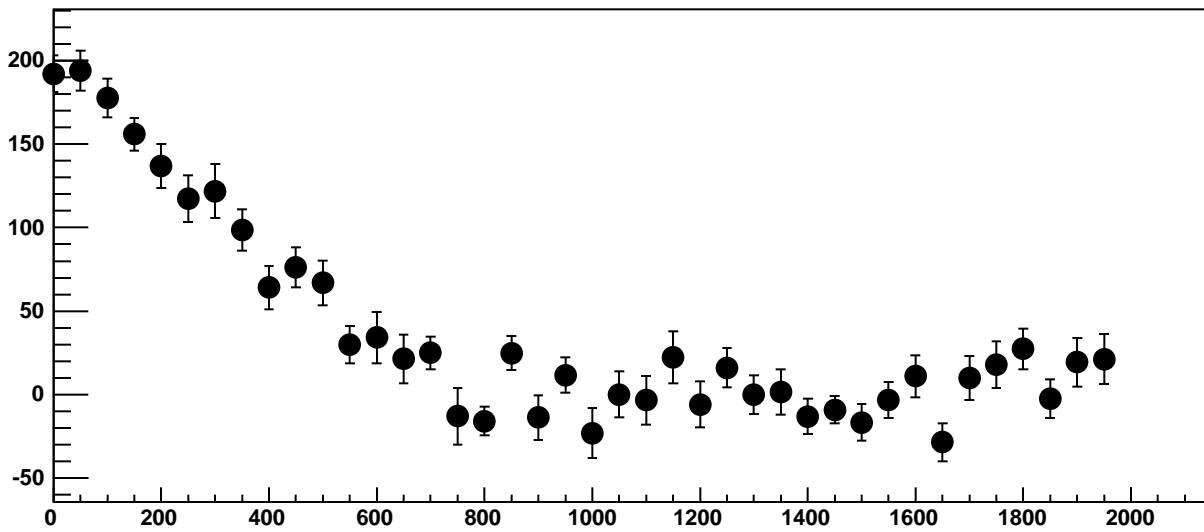
Chip 9, Channel 15, Enable 2, Hold=30, ADC Mean vs DAC



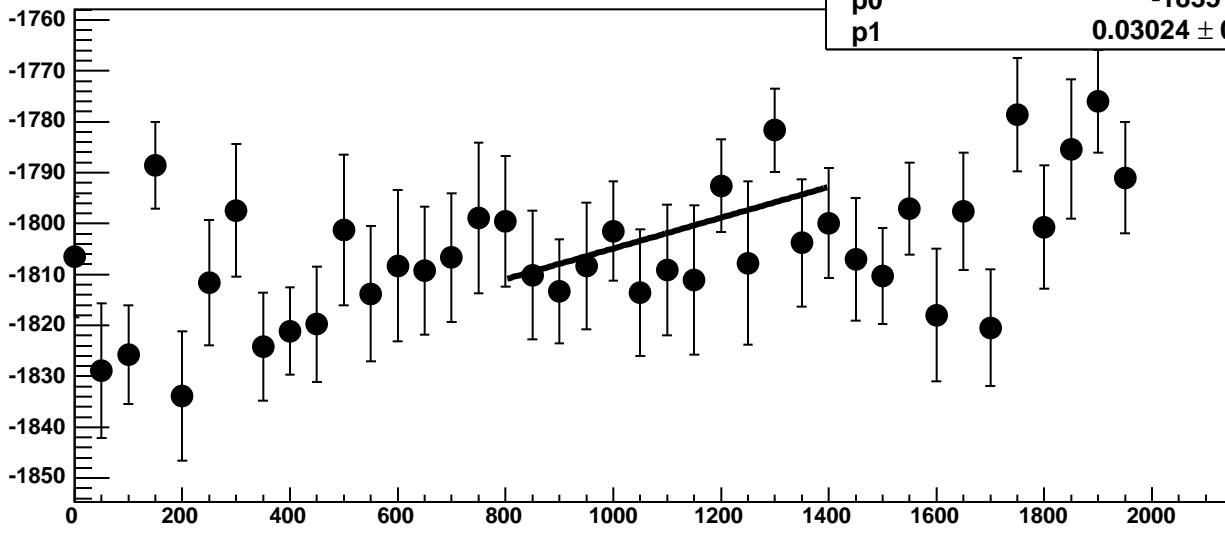
Chip 9, Channel 15, Enable 2, Hold=30, ADC Noise vs DAC



Chip 9, Channel 15, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 9, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

7.619 / 11

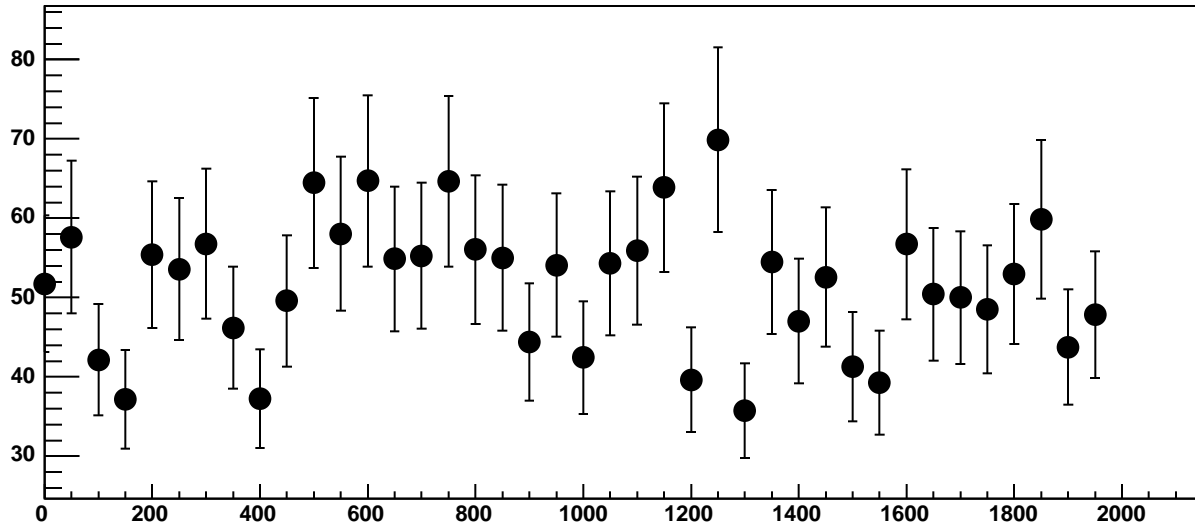
p0

$-1835 \pm 18.96$

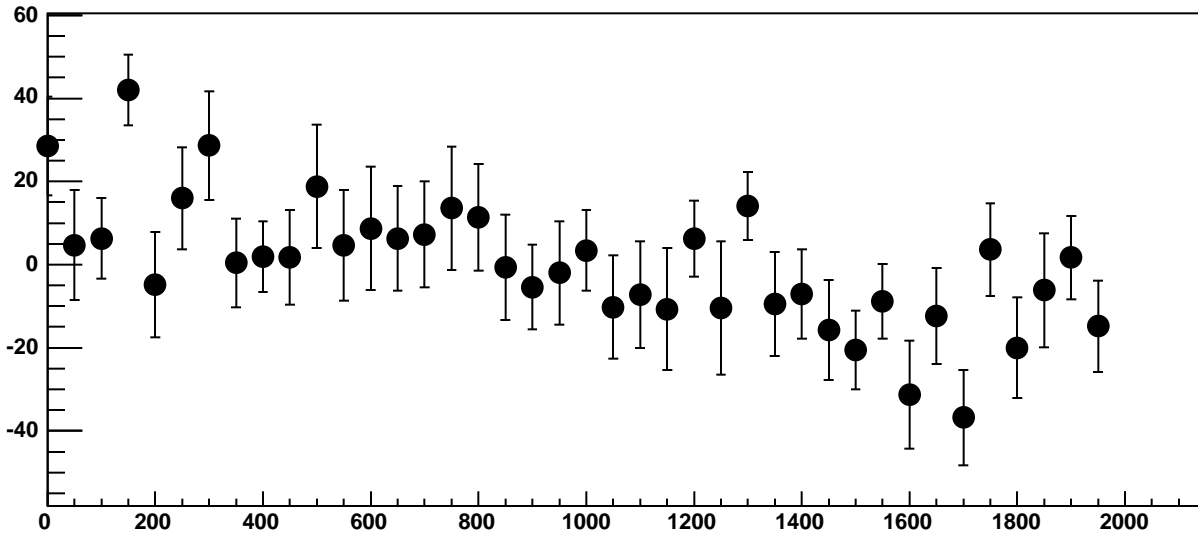
p1

$0.03024 \pm 0.01677$

Chip 9, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC

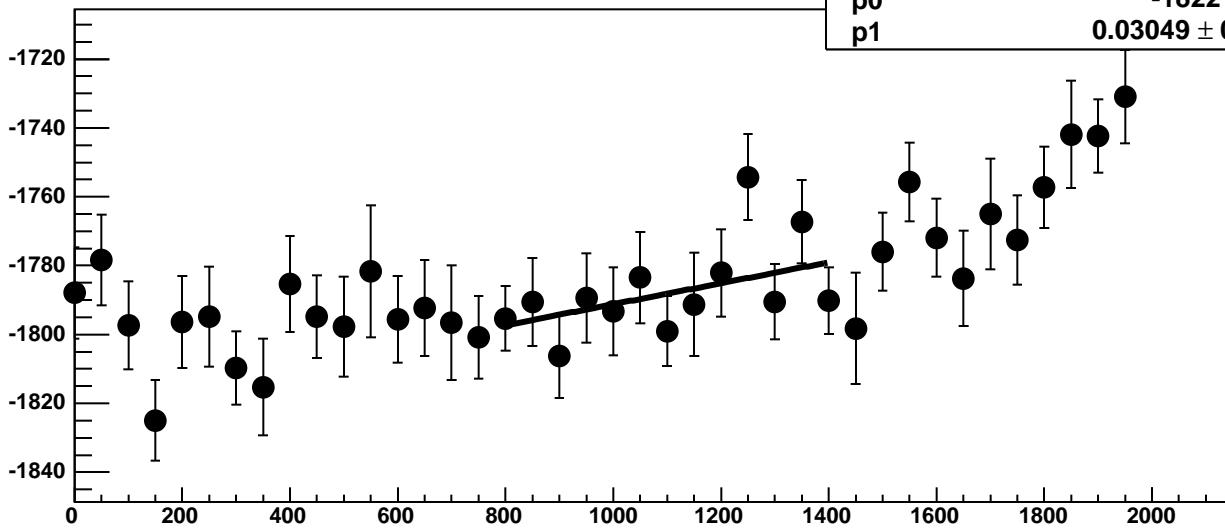


Chip 9, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC





Chip 9, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

11.43 / 11

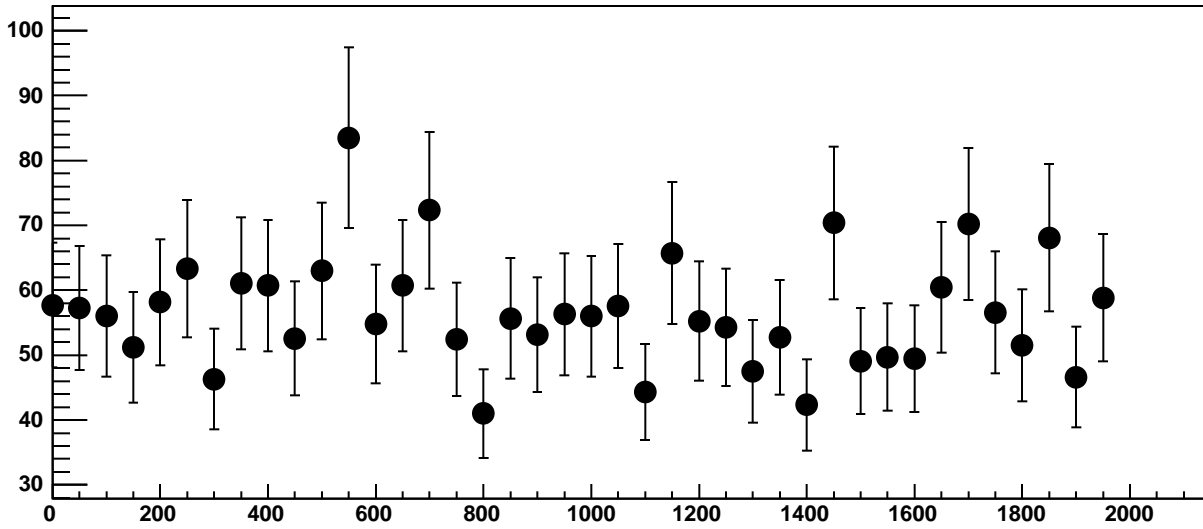
p0

$-1822 \pm 18.16$

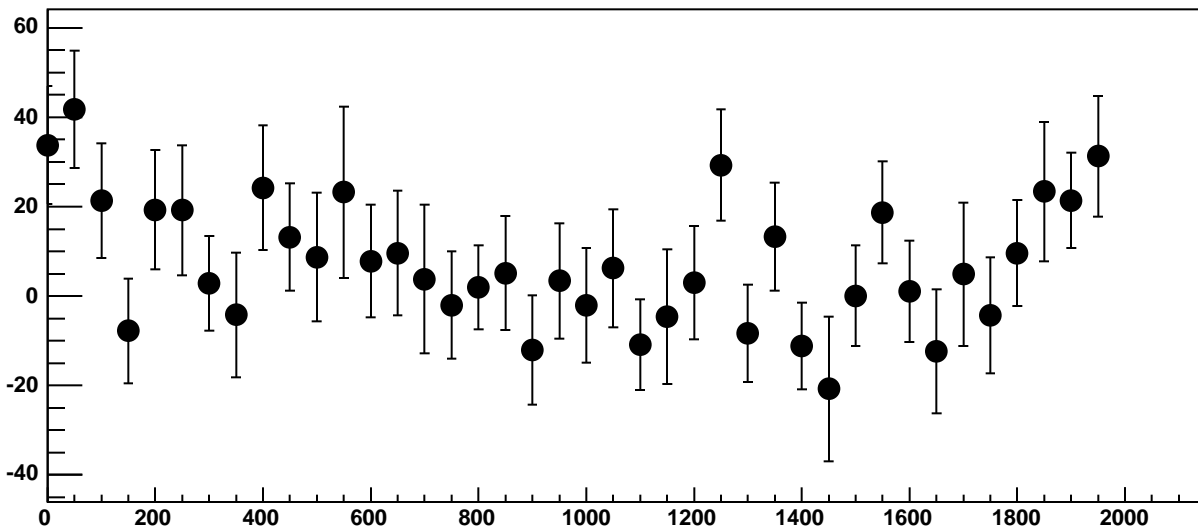
p1

$0.03049 \pm 0.01619$

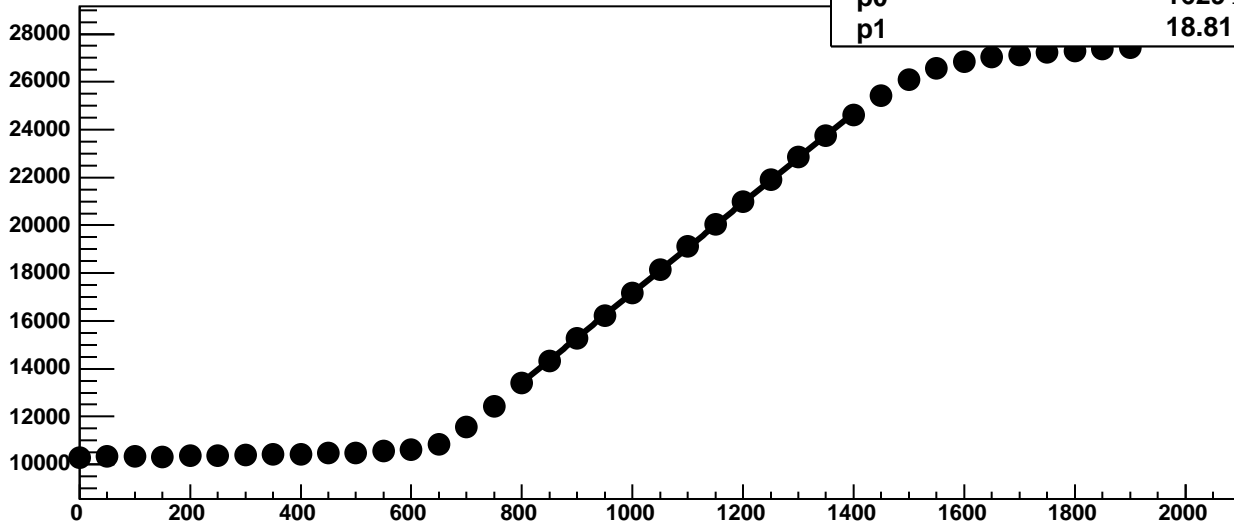
Chip 9, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC



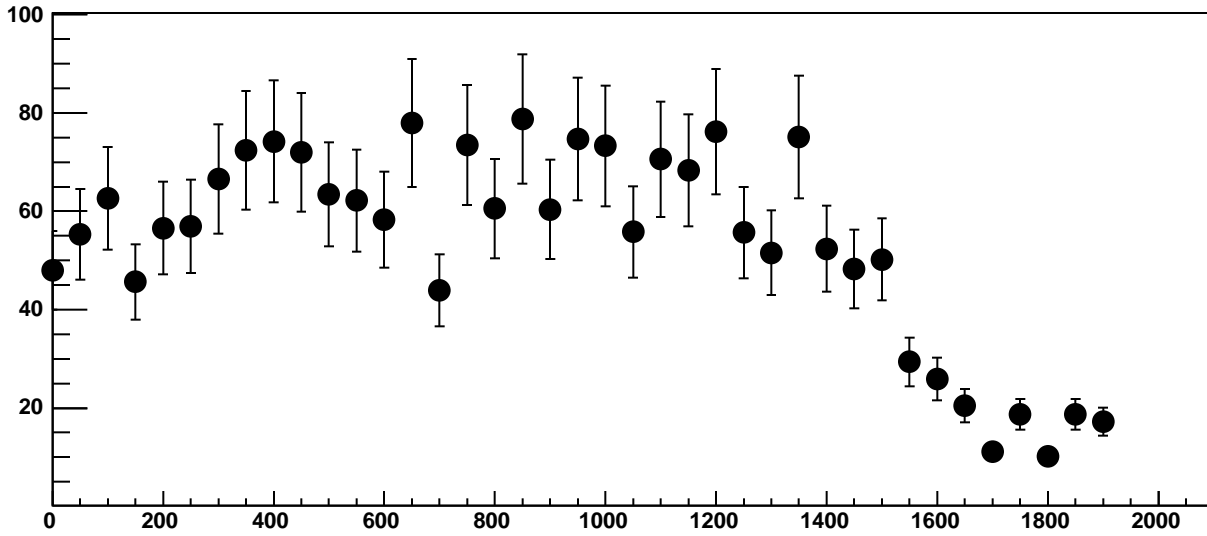
Chip 9, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC



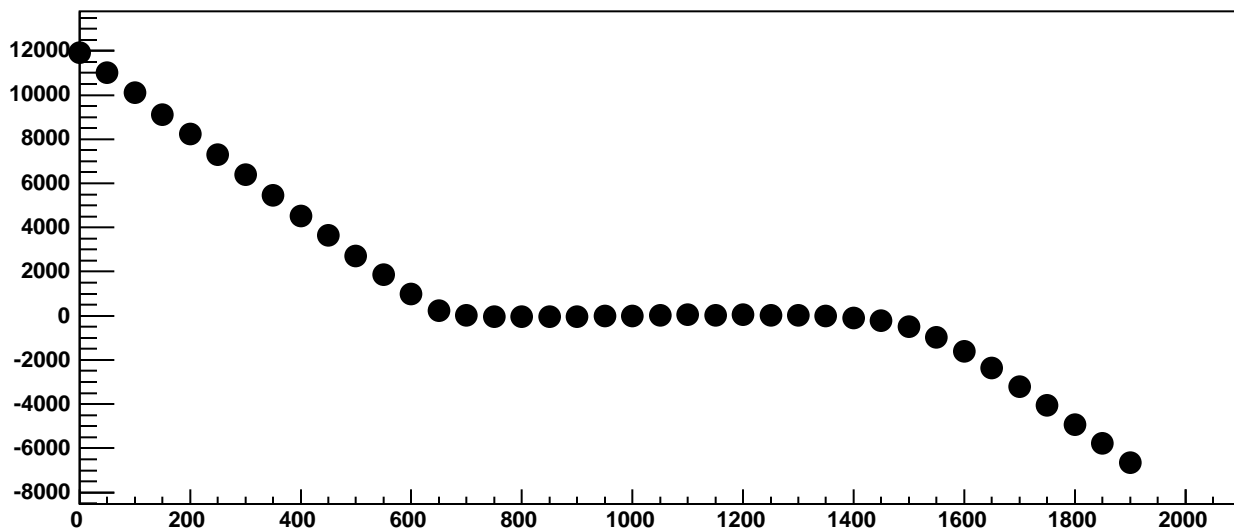
Chip 9, Channel 15, Enable 5!, Hold=30, ADC Mean vs DAC



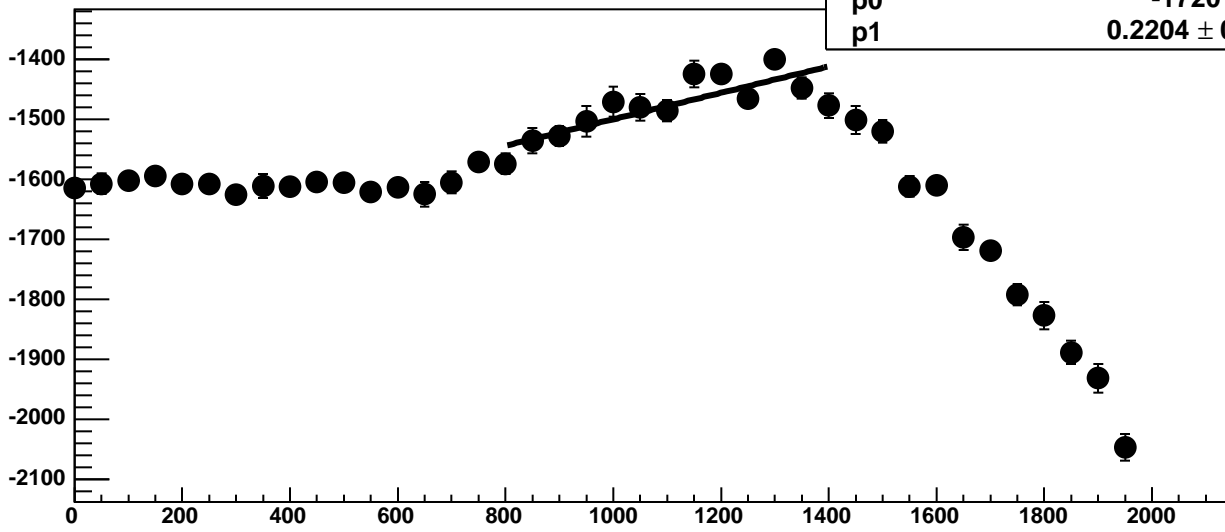
Chip 9, Channel 15, Enable 5!, Hold=30, ADC Noise vs DAC



Chip 9, Channel 15, Enable 5!, Hold=30, ADC Residuals vs DAC

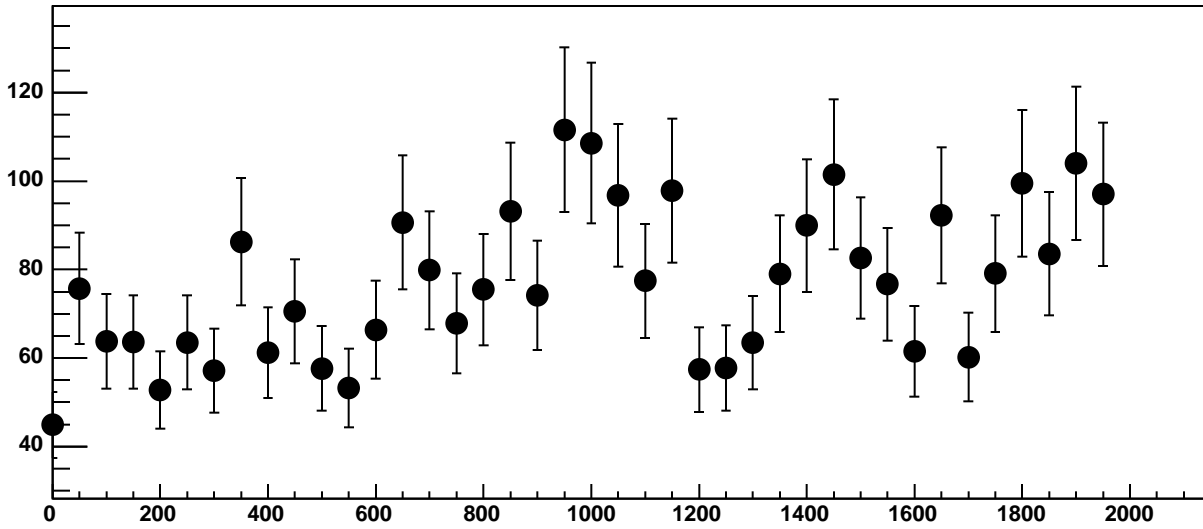


Chip 9, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC

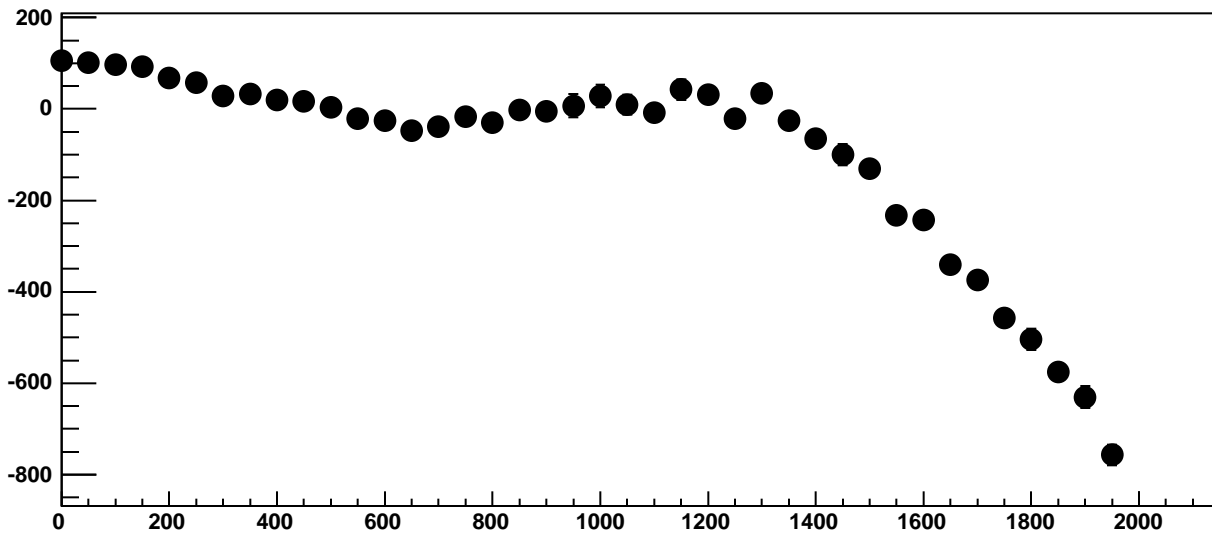


$\chi^2 / \text{ndf}$  34.28 / 11  
p0  $-1720 \pm 30.86$   
p1  $0.2204 \pm 0.02696$

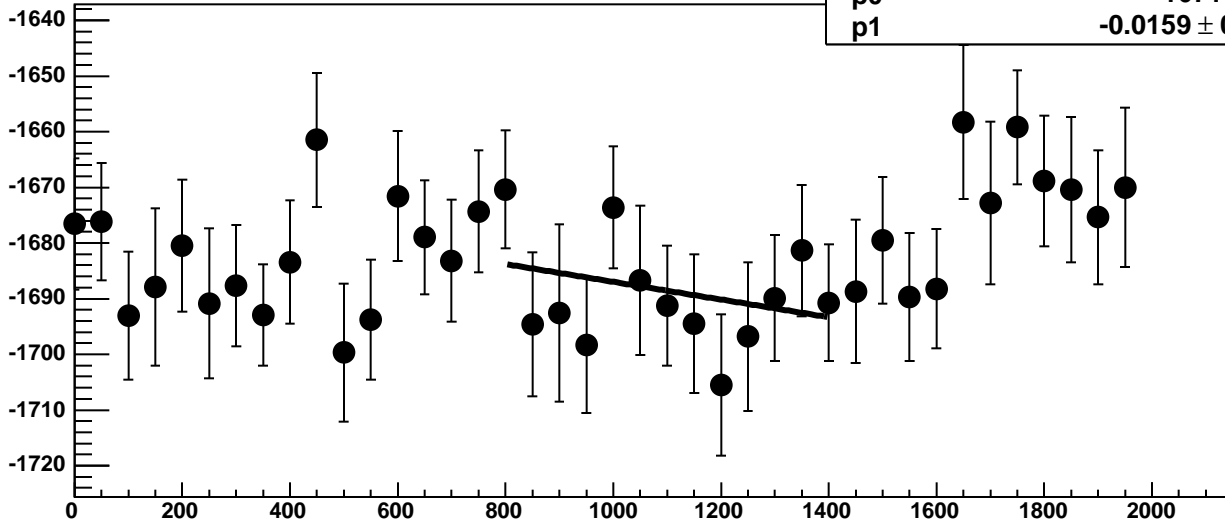
Chip 9, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC



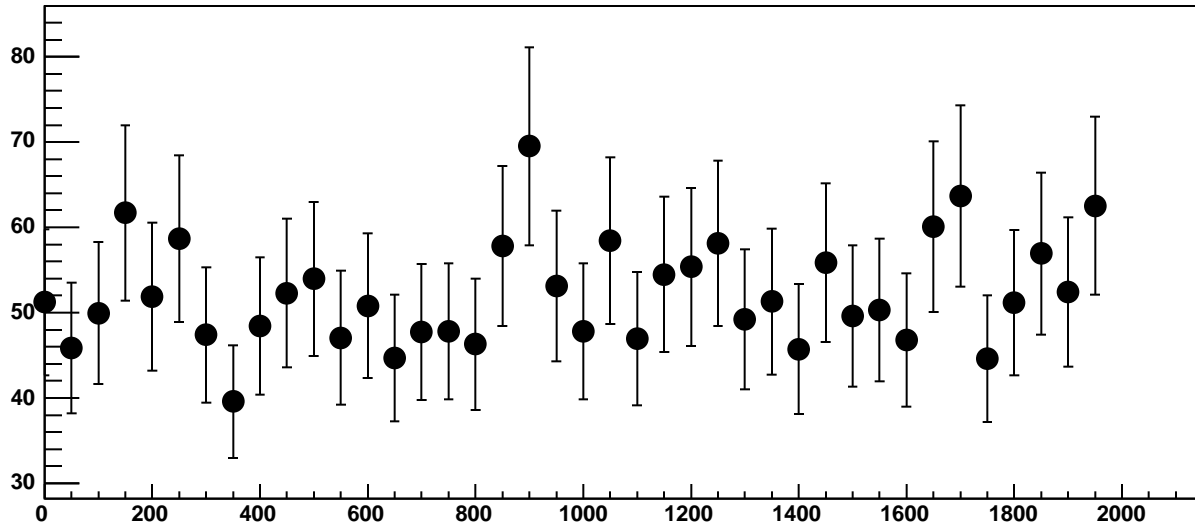
Chip 9, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC



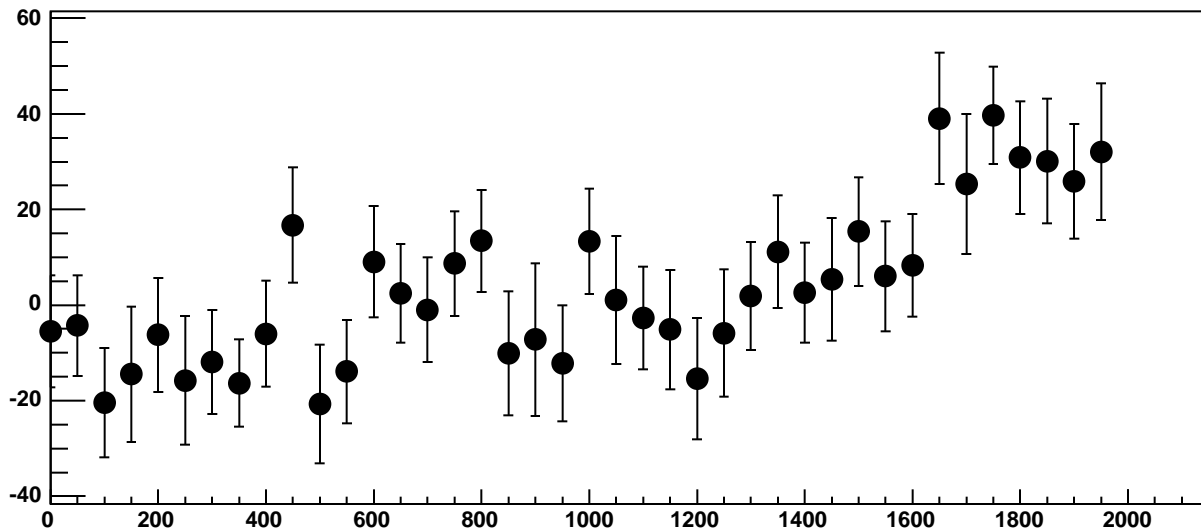
Chip 9, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC



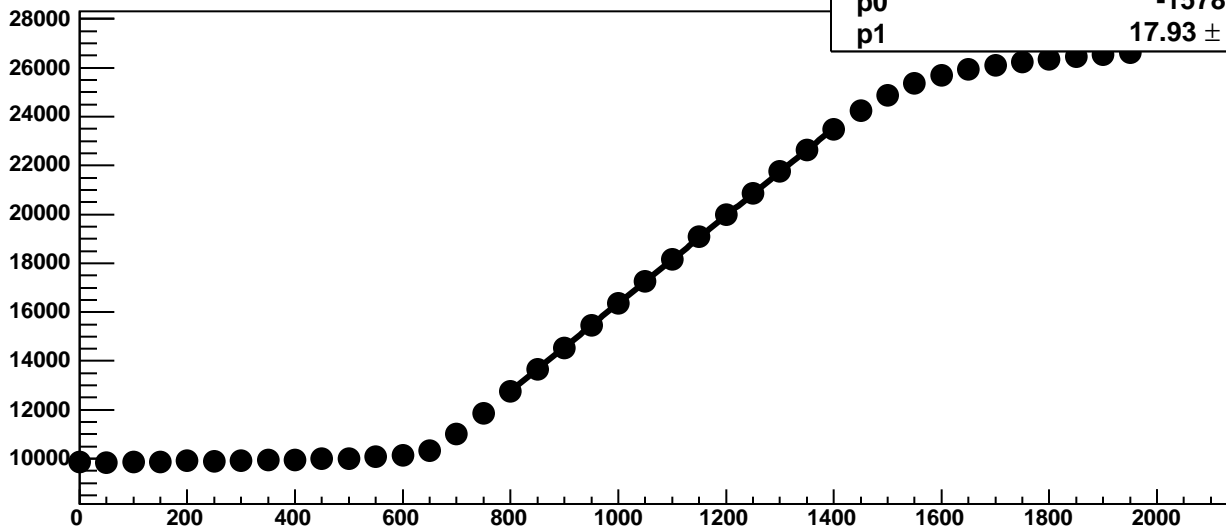
Chip 9, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC



Chip 9, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC

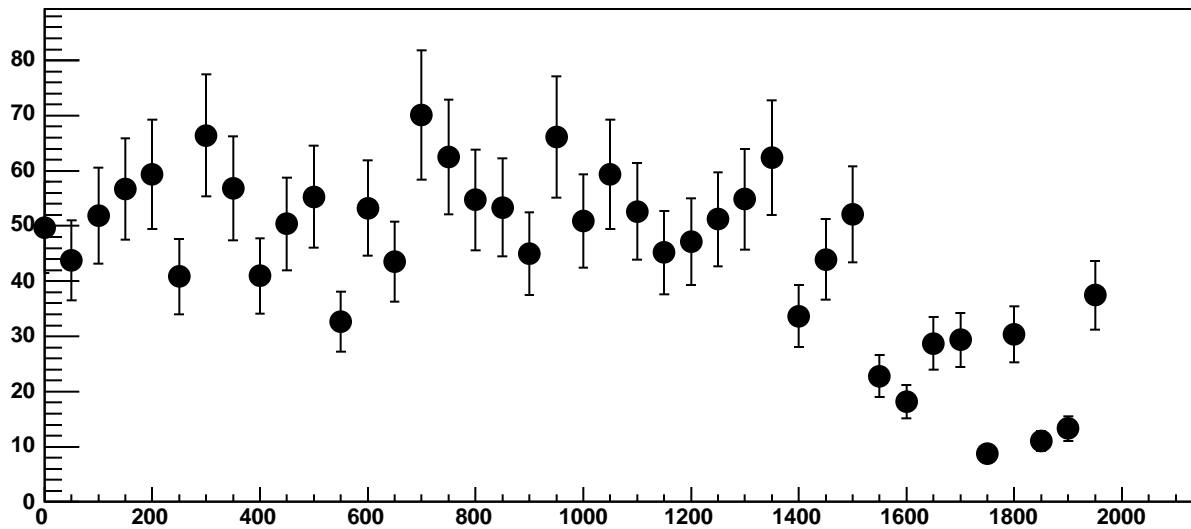


Chip 9, Channel 16, Enable 2!, Hold=30, ADC Mean vs DAC

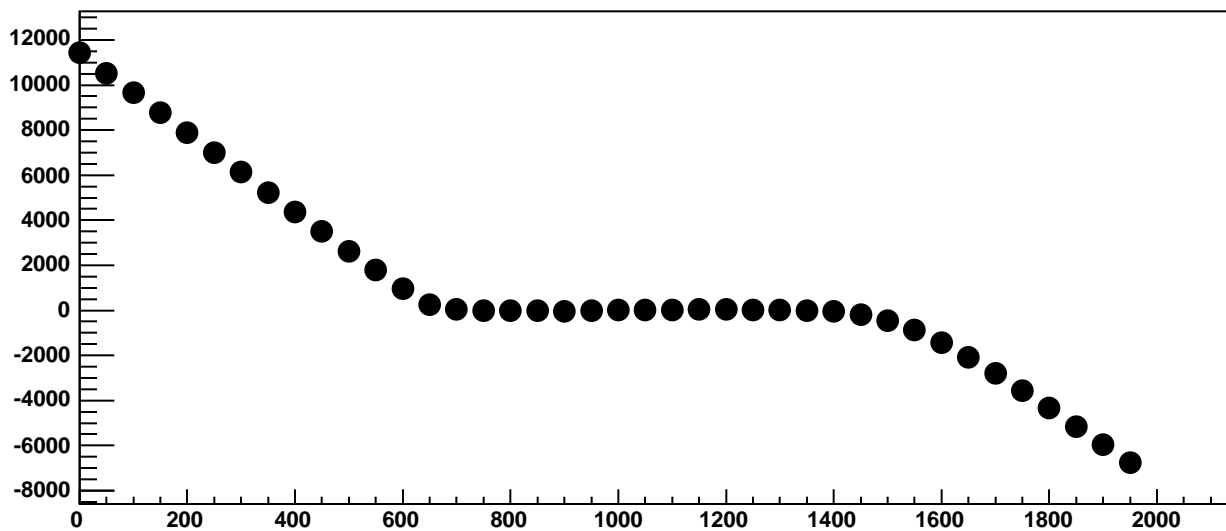


$\chi^2 / \text{ndf}$  82.41 / 11  
p0  $-1578 \pm 18.56$   
p1  $17.93 \pm 0.01621$

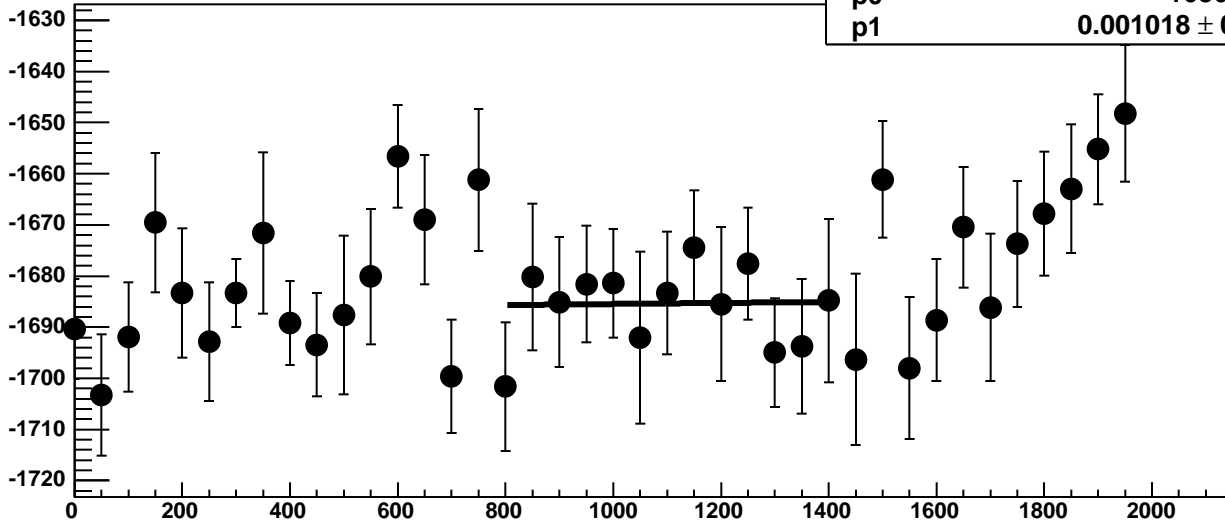
Chip 9, Channel 16, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 9, Channel 16, Enable 2!, Hold=30, ADC Residuals vs DAC

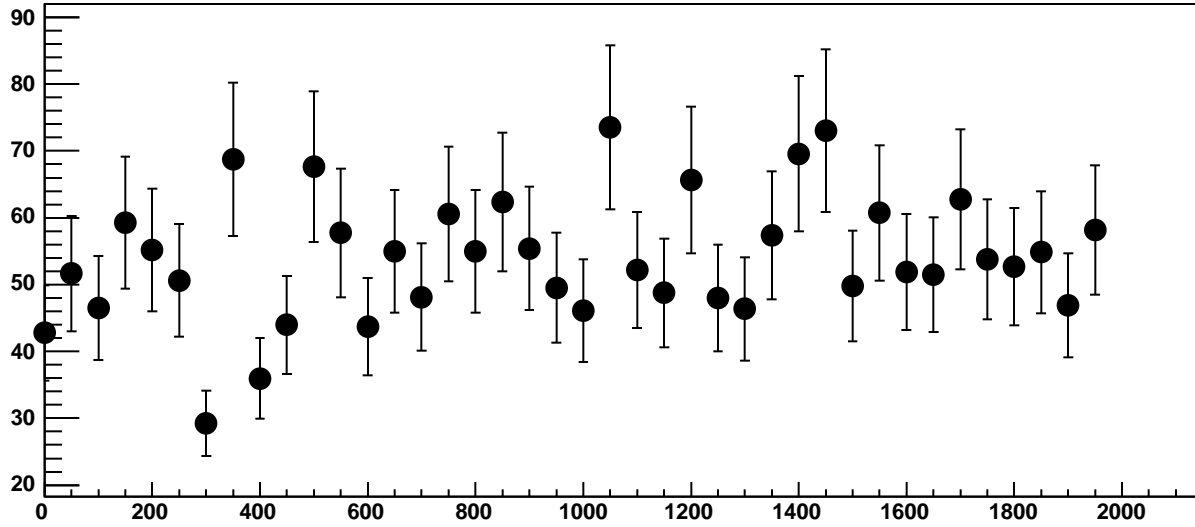


Chip 9, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC

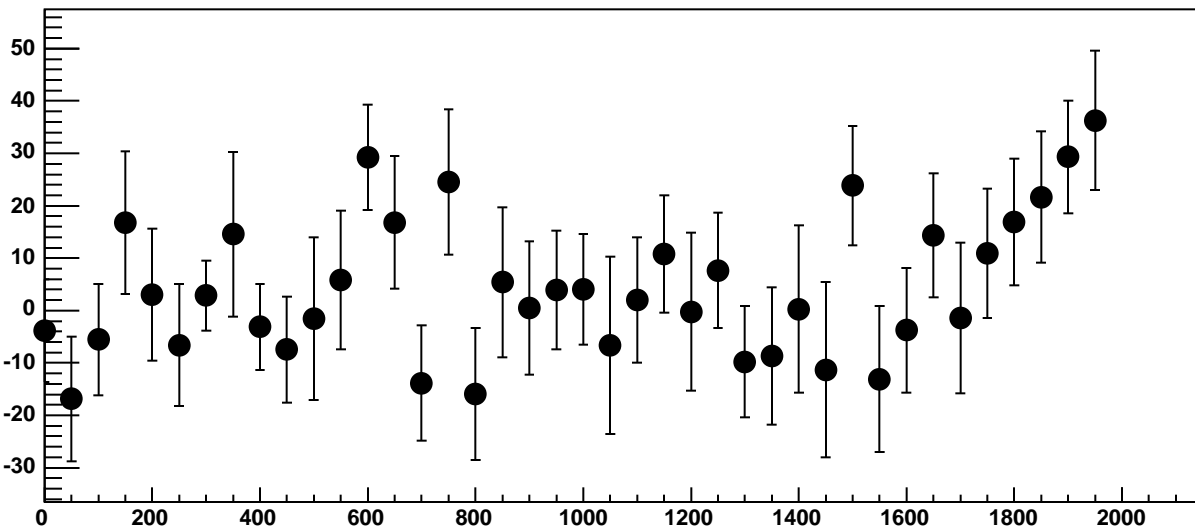


$\chi^2 / \text{ndf}$  4.896 / 11  
p0  $-1686 \pm 21.4$   
p1  $0.001018 \pm 0.01922$

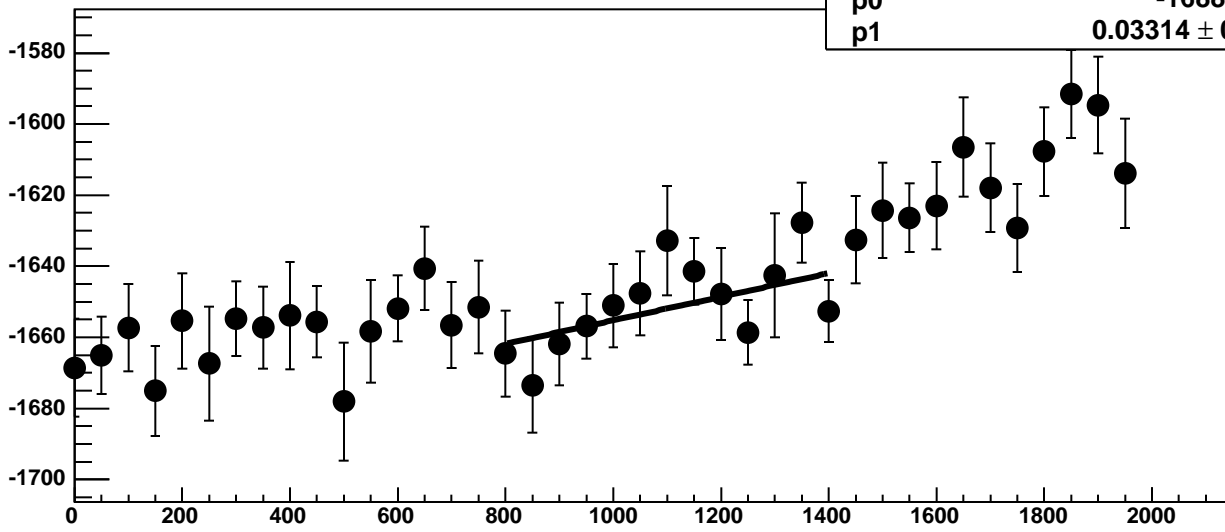
Chip 9, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC



Chip 9, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC

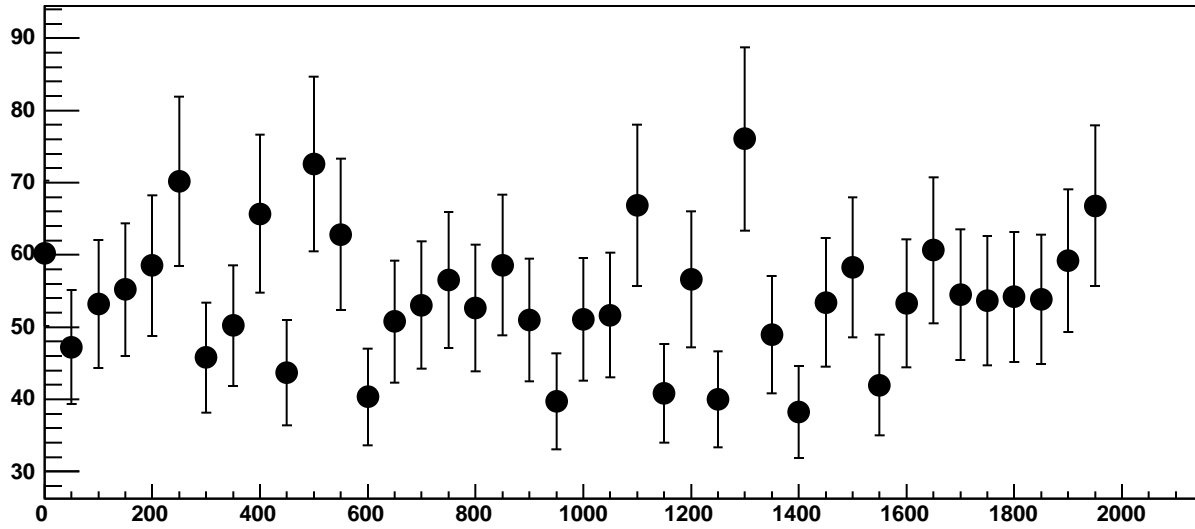


Chip 9, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC

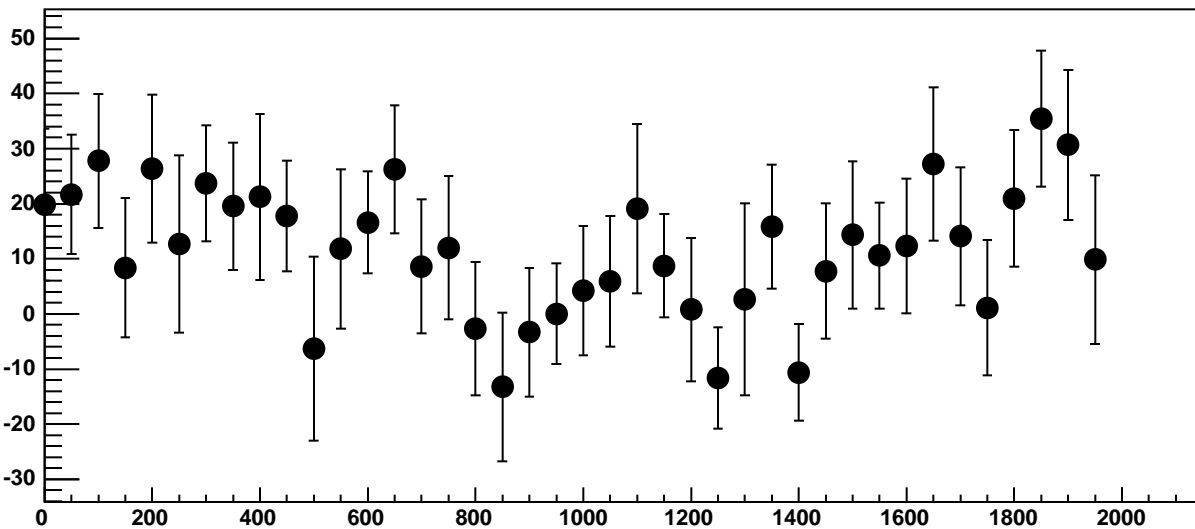


$\chi^2 / \text{ndf}$  9.012 / 11  
p0  $-1688 \pm 18.51$   
p1  $0.03314 \pm 0.01635$

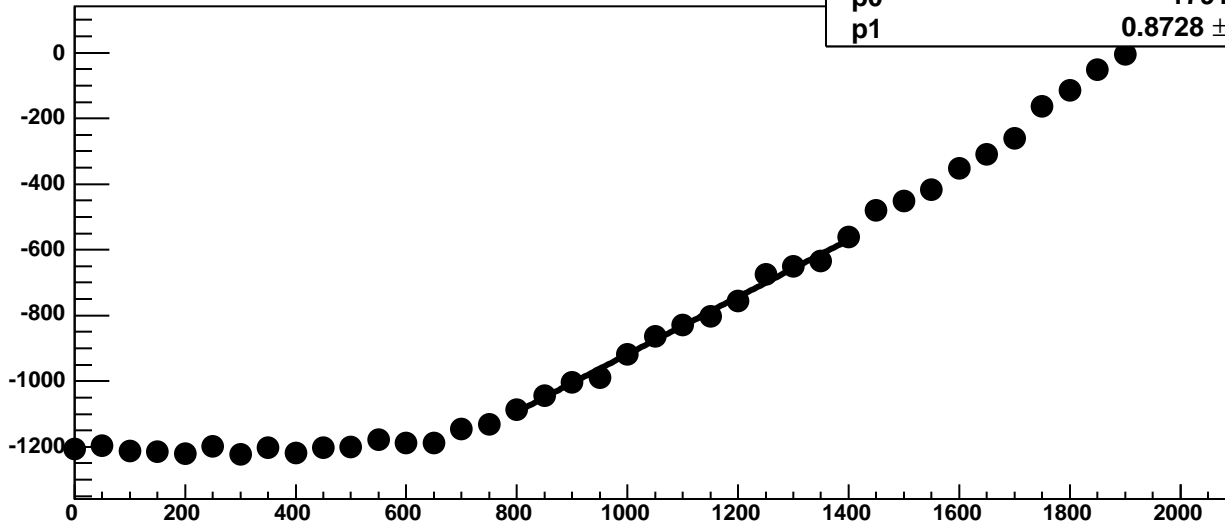
Chip 9, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC



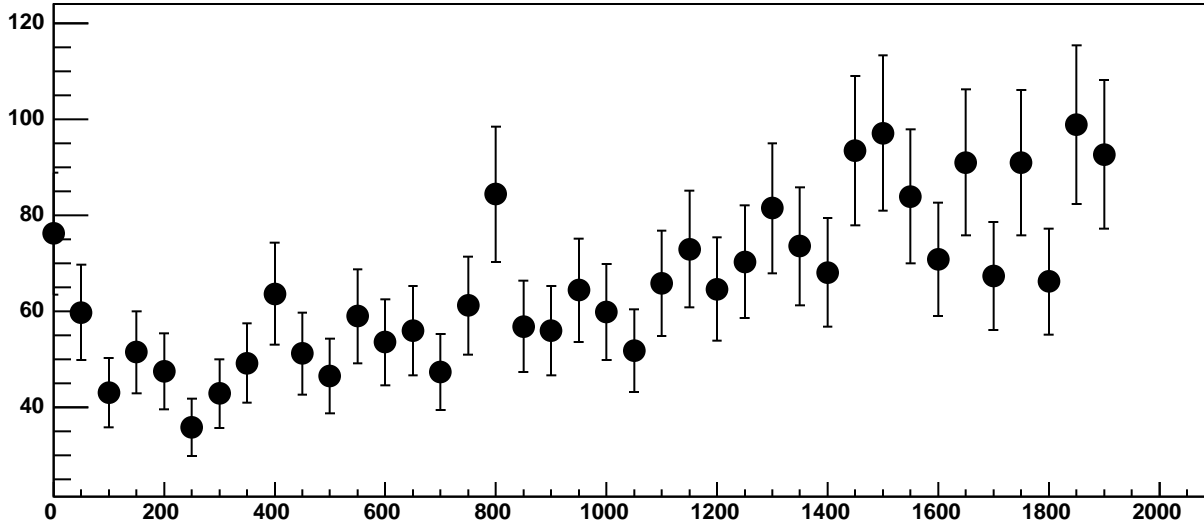
Chip 9, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC



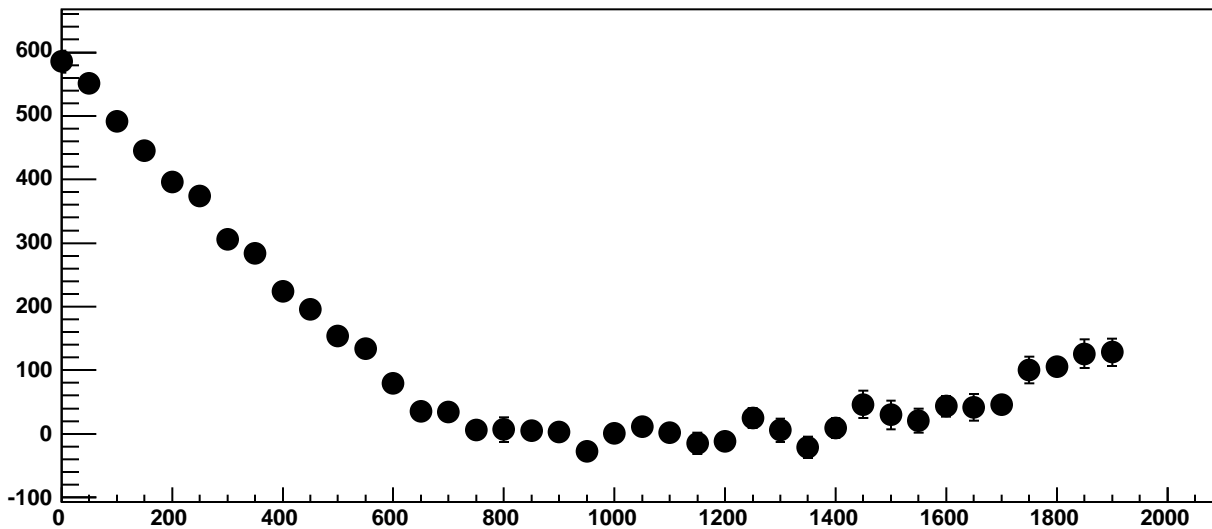
Chip 9, Channel 16, Enable 5, Hold=30, ADC Mean vs DAC



Chip 9, Channel 16, Enable 5, Hold=30, ADC Noise vs DAC

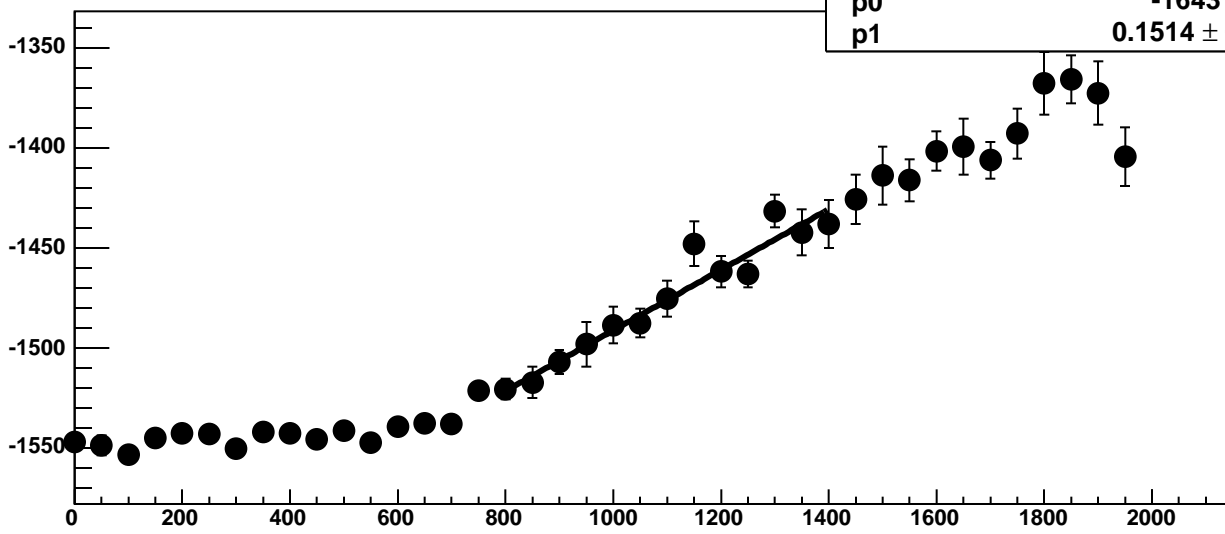


Chip 9, Channel 16, Enable 5, Hold=30, ADC Residuals vs DAC

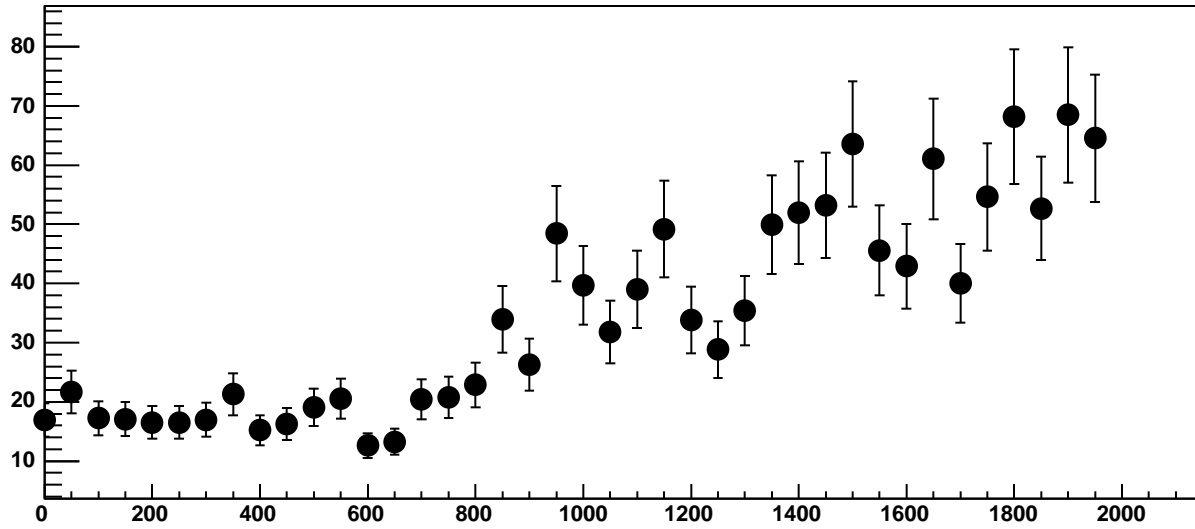




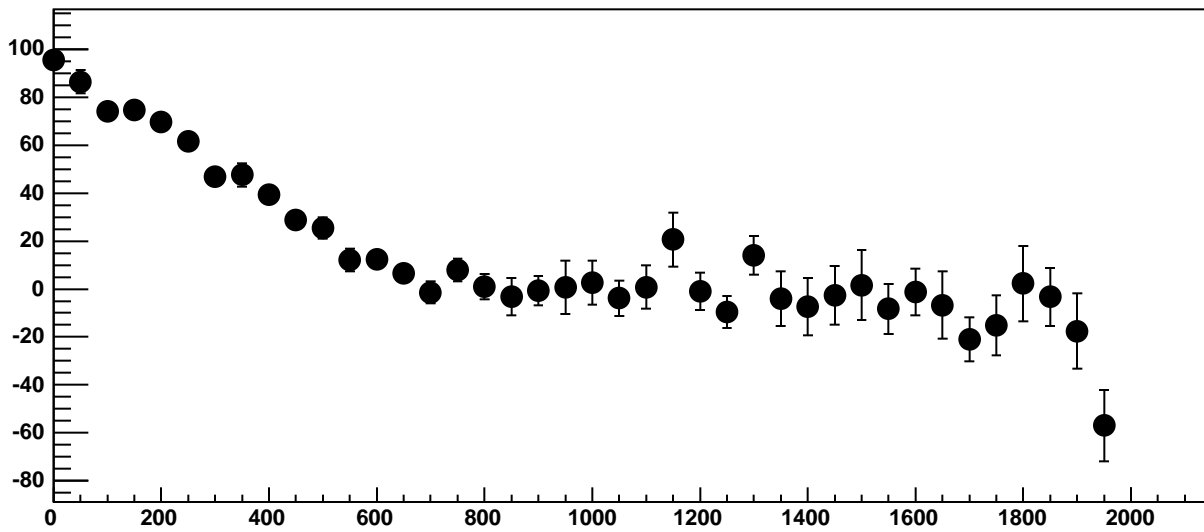
Chip 9, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC



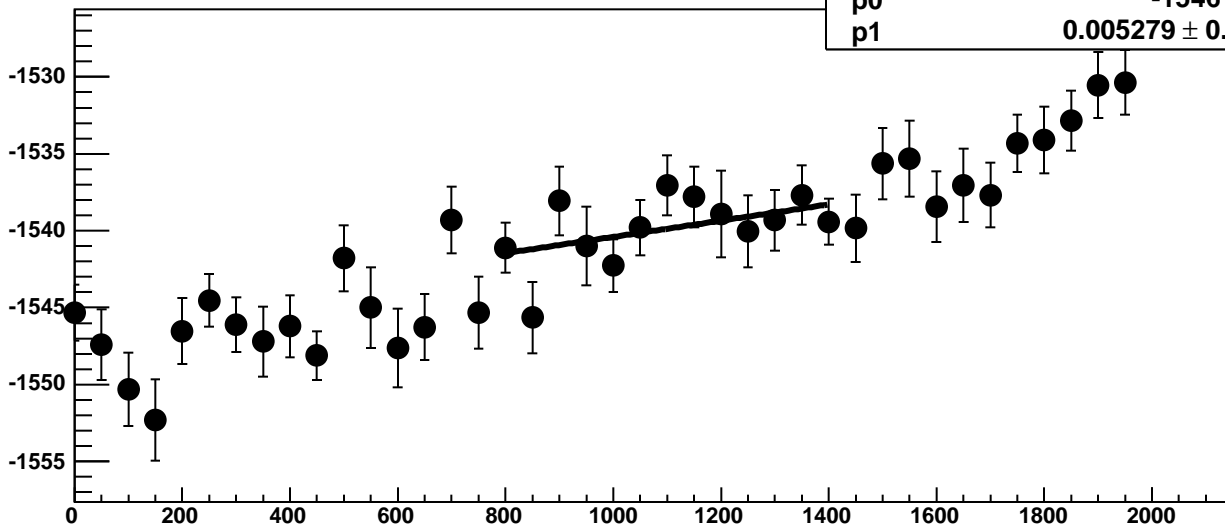
Chip 9, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC



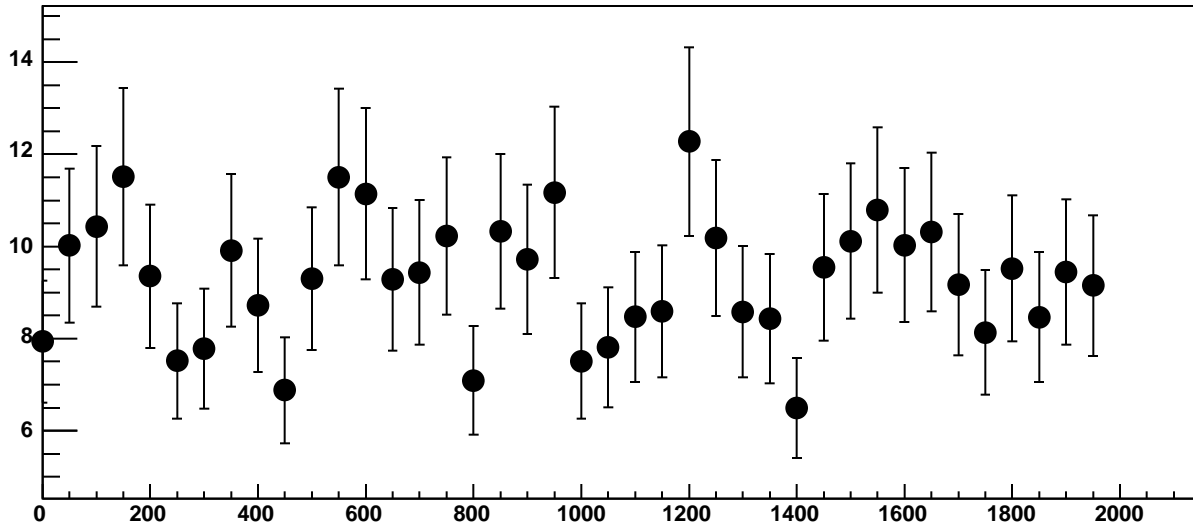
Chip 9, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC



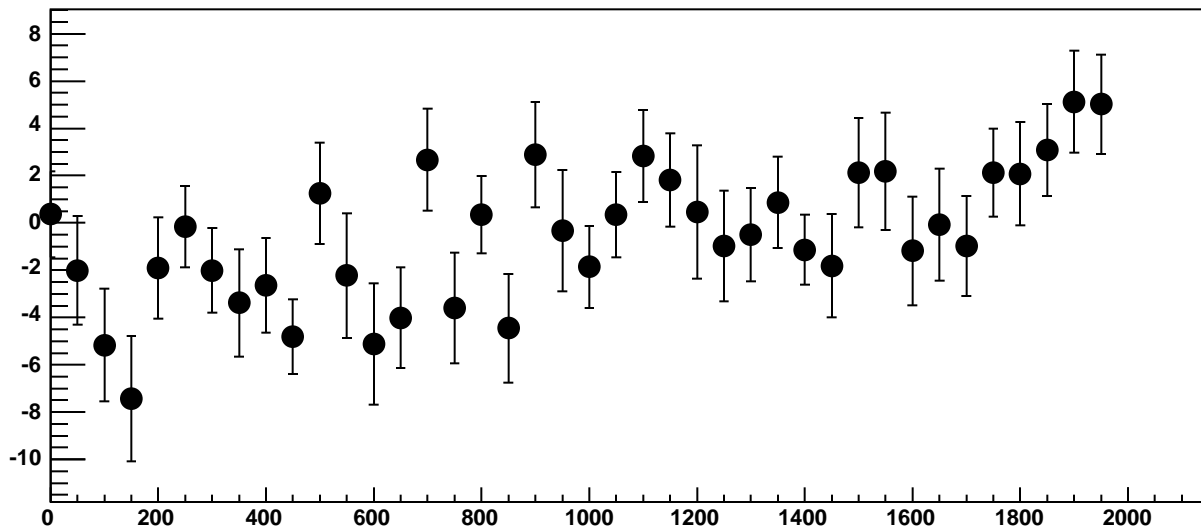
Chip 9, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC



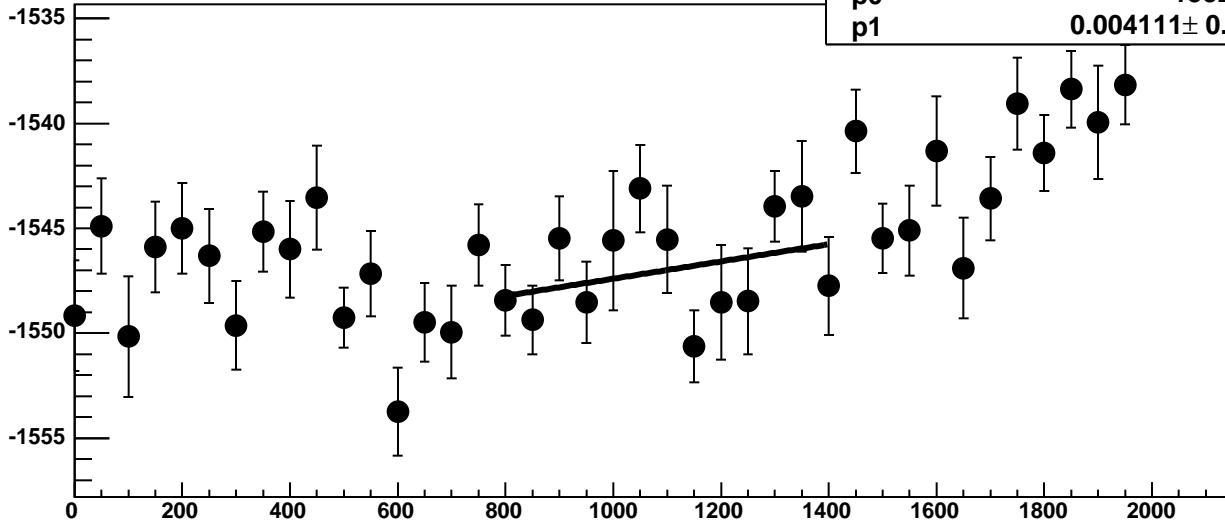
Chip 9, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC



Chip 9, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC

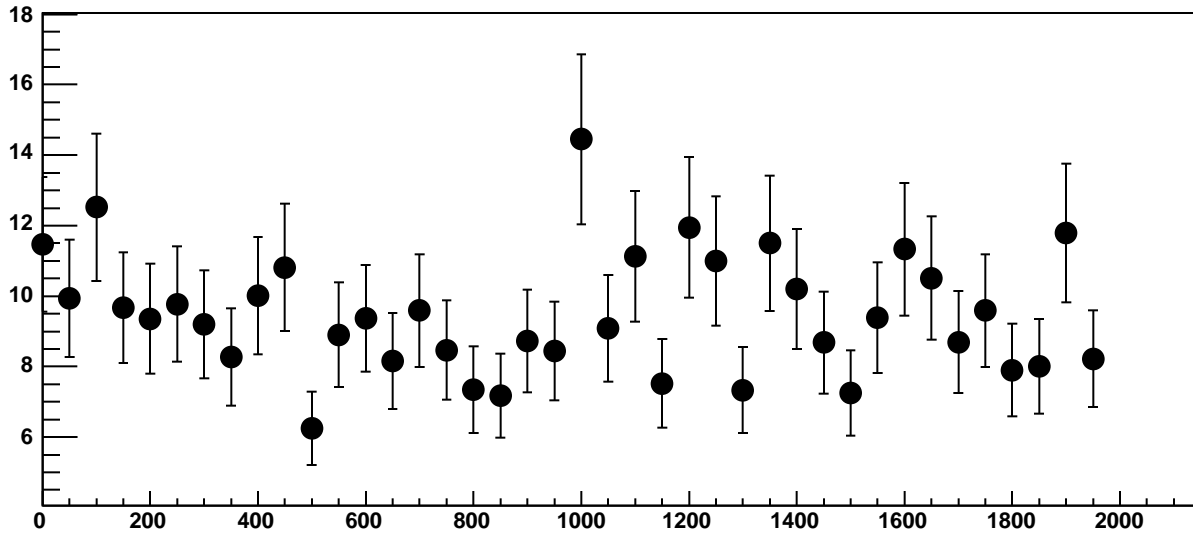


Chip 9, Channel 17, Enable 2, Hold=30, ADC Mean vs DAC

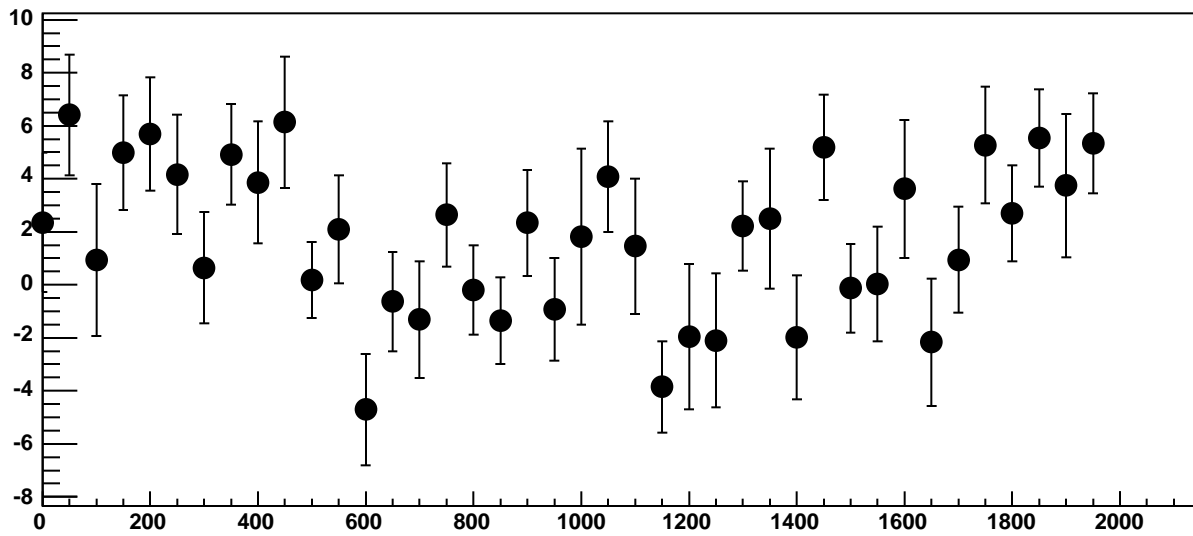


$\chi^2 / \text{ndf}$  16.28 / 11  
p0  $-1552 \pm 3.21$   
p1  $0.004111 \pm 0.002954$

Chip 9, Channel 17, Enable 2, Hold=30, ADC Noise vs DAC

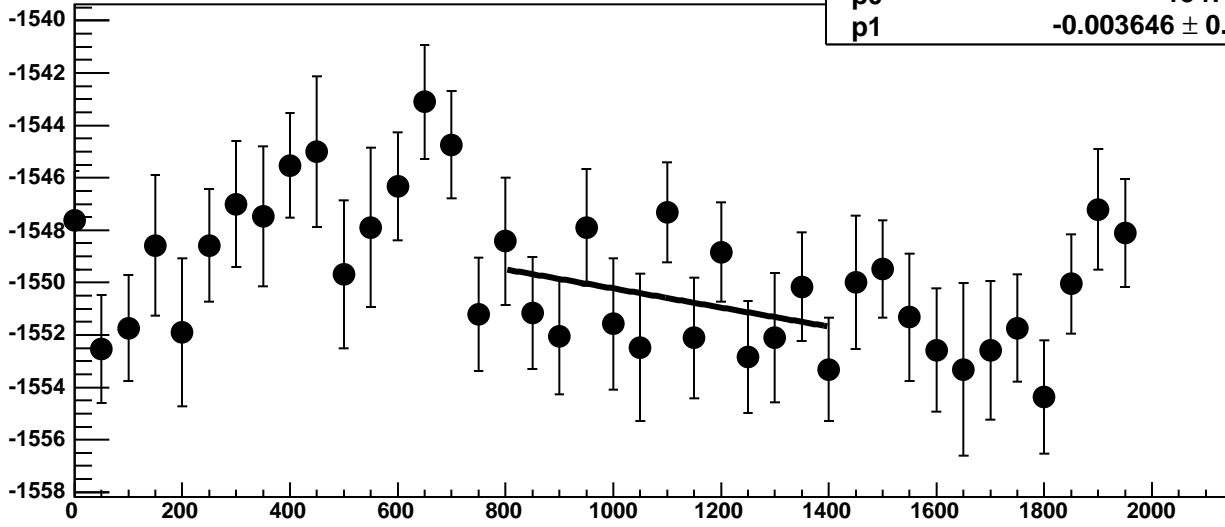


Chip 9, Channel 17, Enable 2, Hold=30, ADC Residuals vs DAC

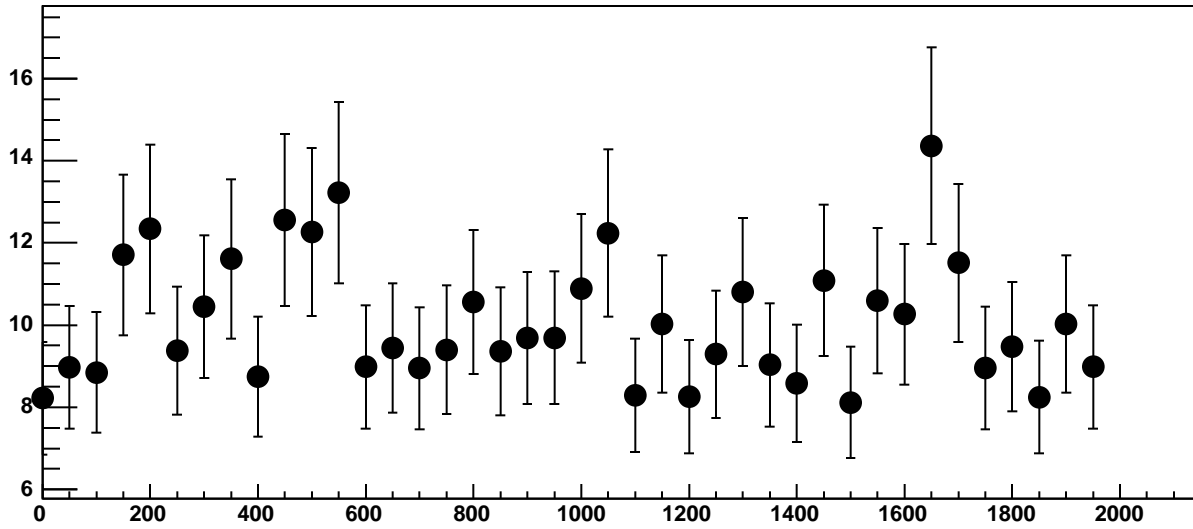


Chip 9, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC

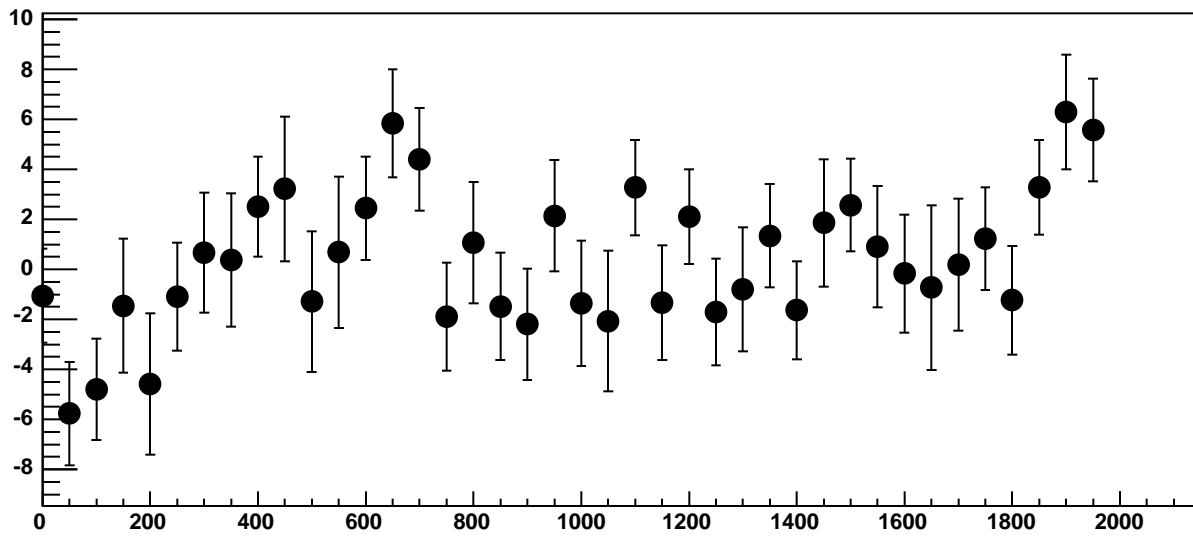
$\chi^2 / \text{ndf}$  9.806 / 11  
p0  $-1547 \pm 3.661$   
p1  $-0.003646 \pm 0.003239$



Chip 9, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC

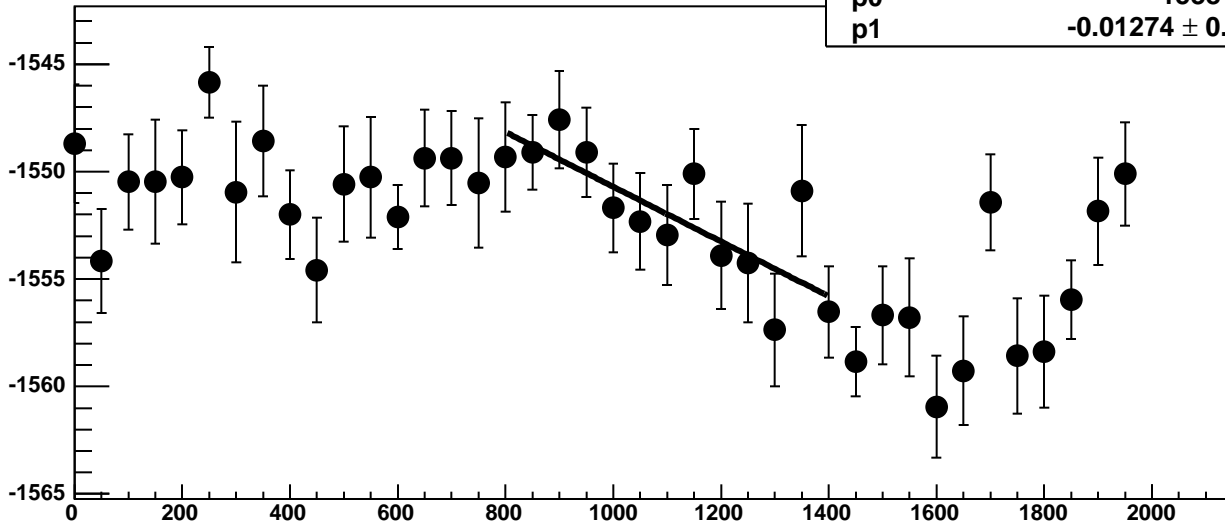


Chip 9, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC

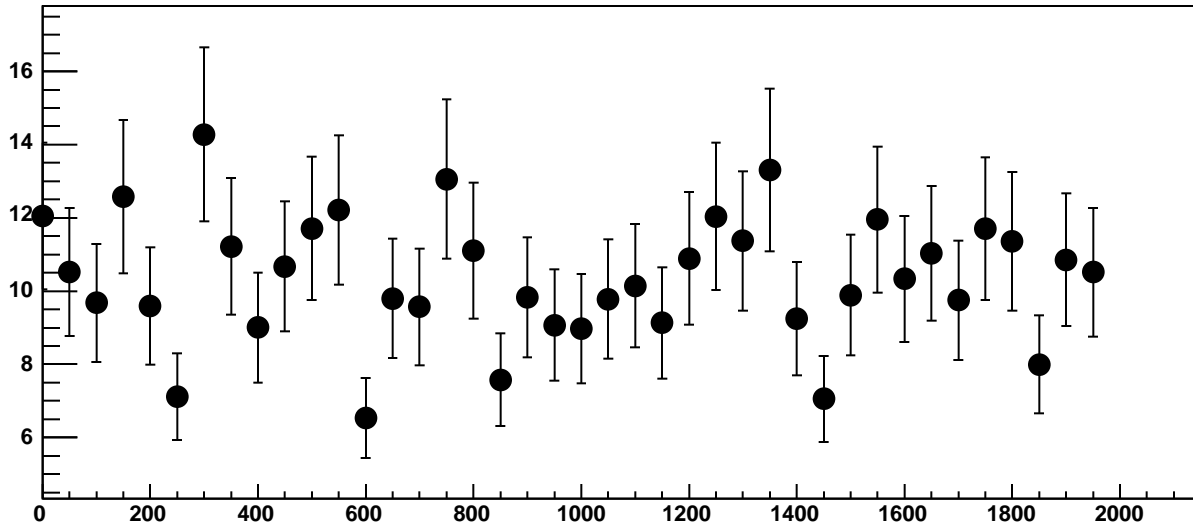


Chip 9, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC

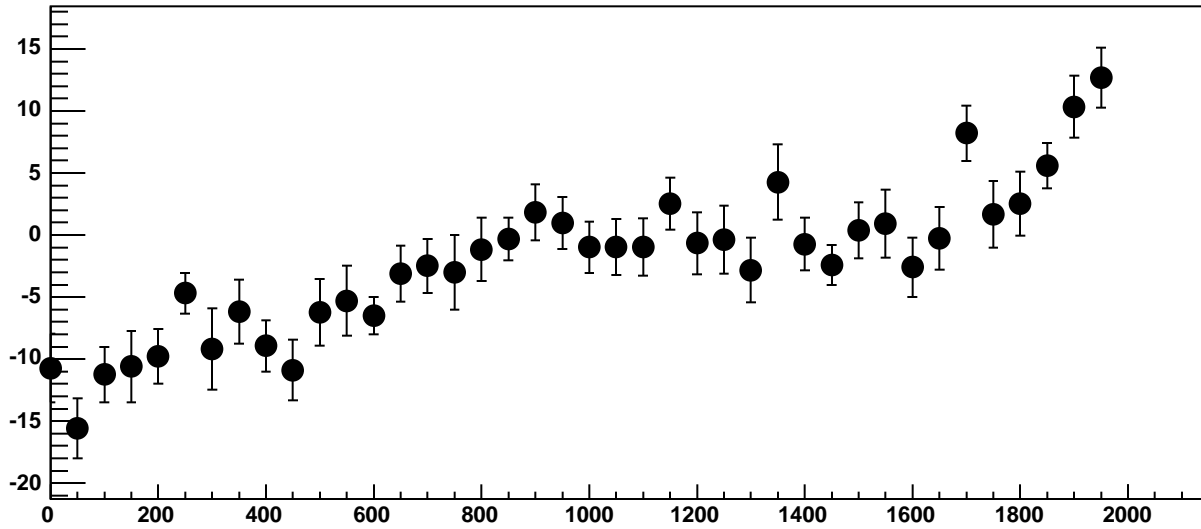
$\chi^2 / \text{ndf}$  6.496 / 11  
p0  $-1538 \pm 3.706$   
p1  $-0.01274 \pm 0.003402$



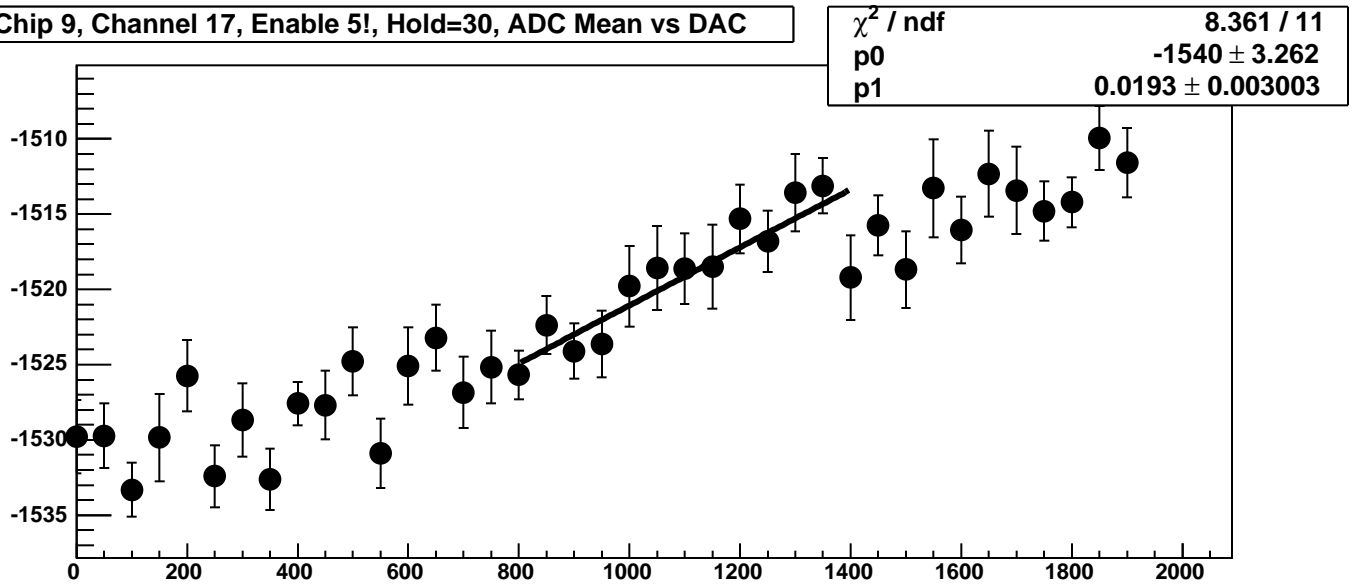
Chip 9, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC



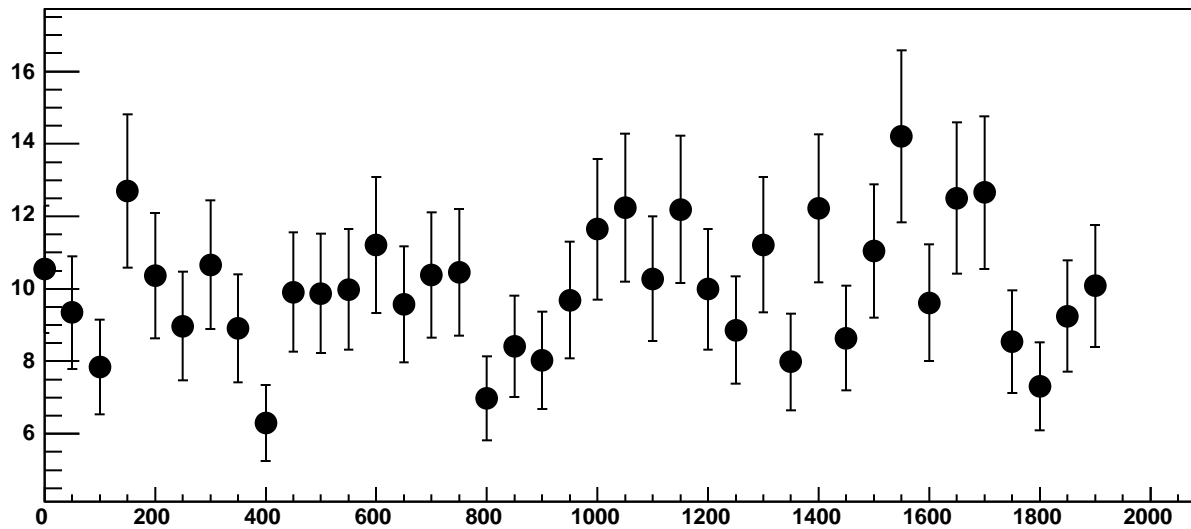
Chip 9, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC



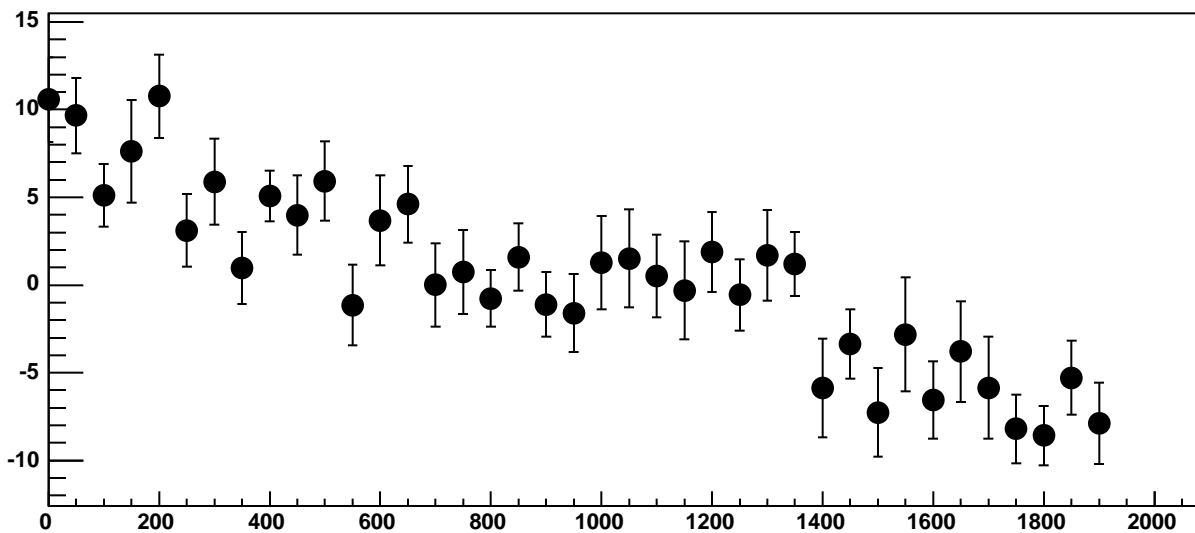
Chip 9, Channel 17, Enable 5!, Hold=30, ADC Mean vs DAC



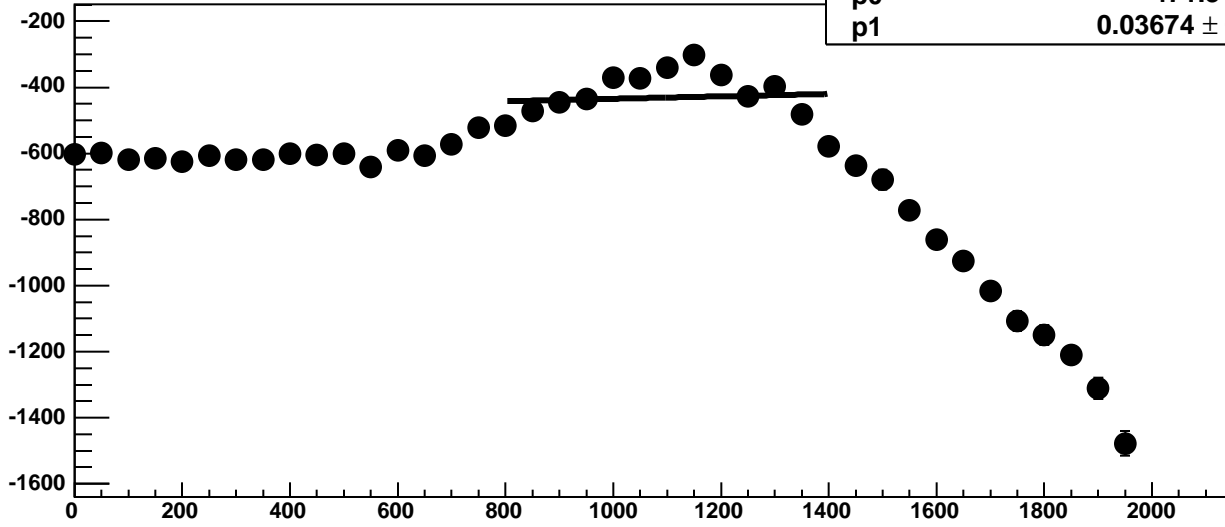
Chip 9, Channel 17, Enable 5!, Hold=30, ADC Noise vs DAC



Chip 9, Channel 17, Enable 5!, Hold=30, ADC Residuals vs DAC



Chip 10, Channel 0, Enable 0!, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

115.9 / 11

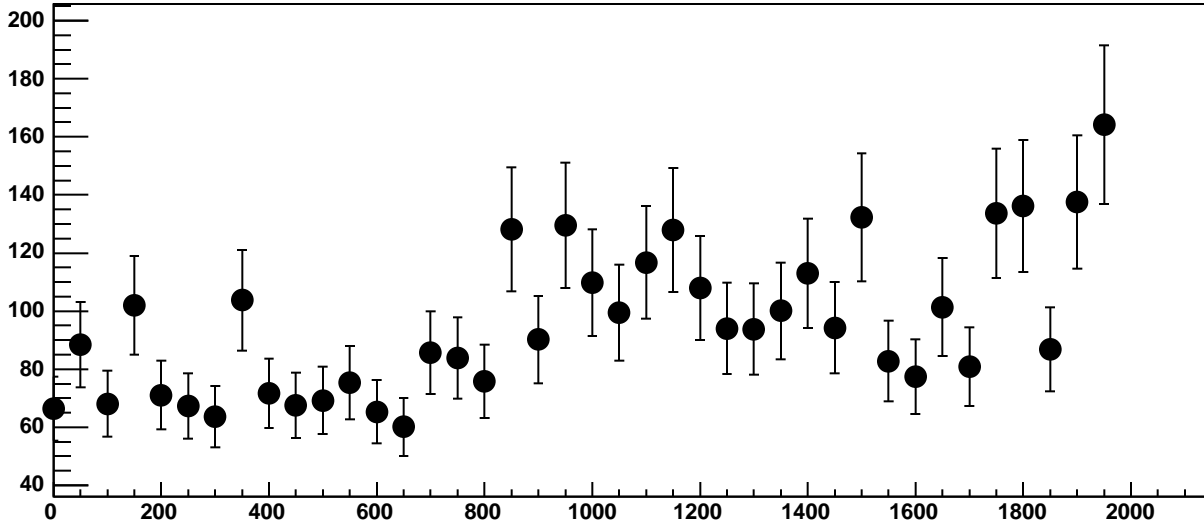
p0

$-471.3 \pm 36.63$

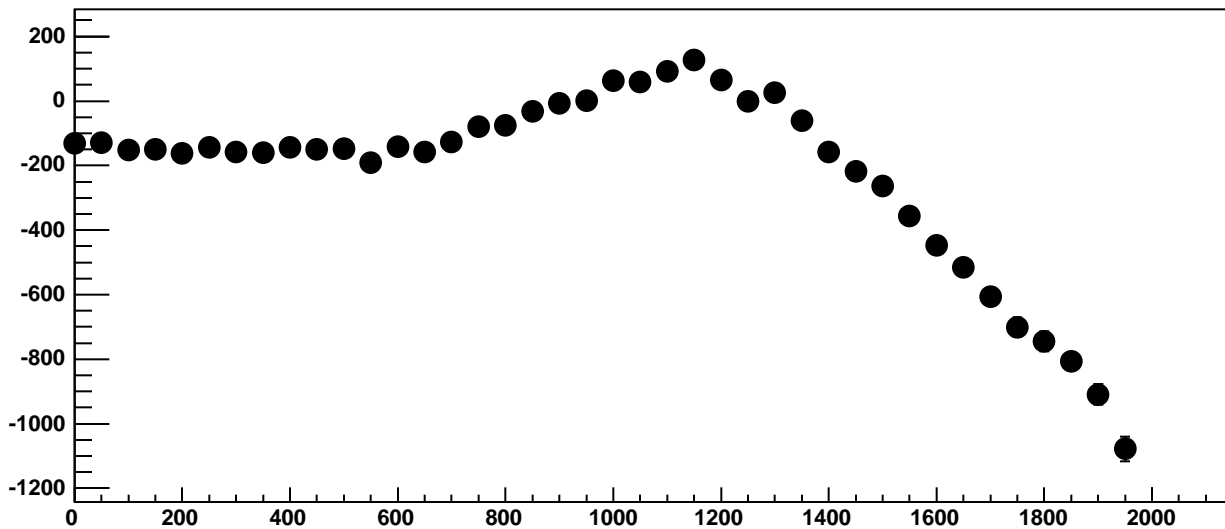
p1

$0.03674 \pm 0.03311$

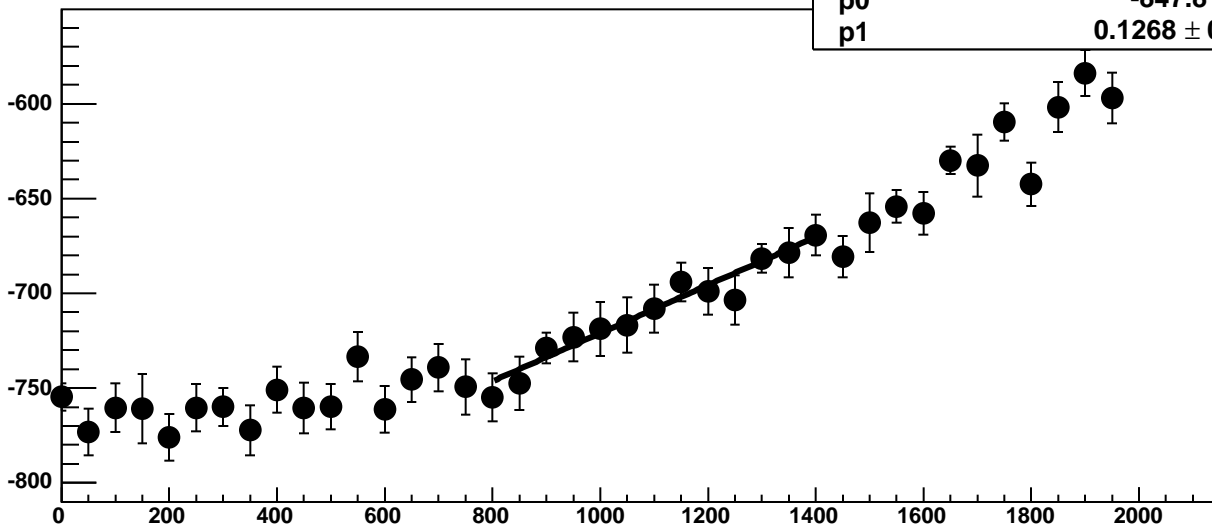
Chip 10, Channel 0, Enable 0!, Hold=30, ADC Noise vs DAC



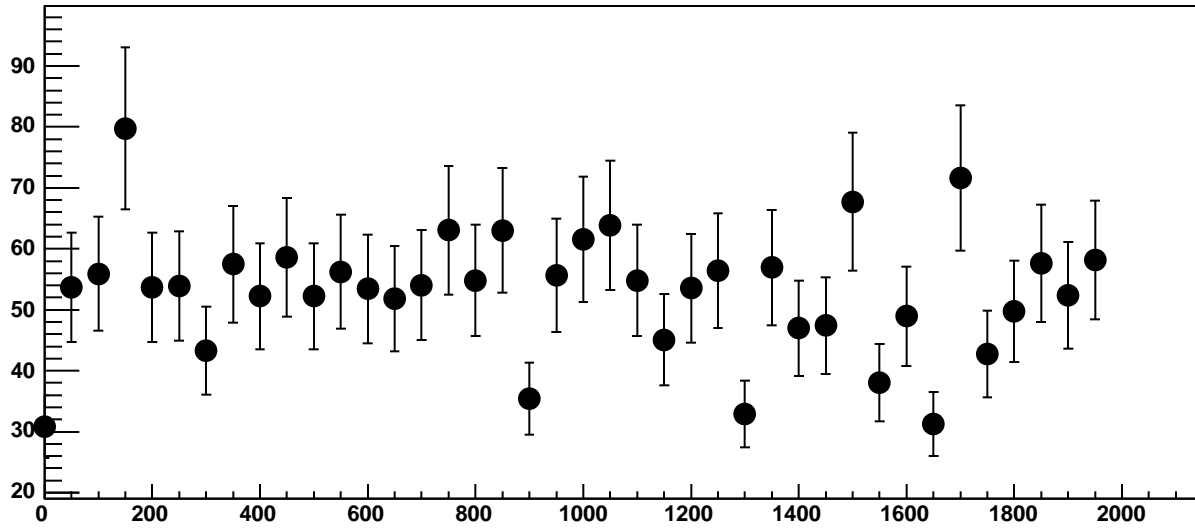
Chip 10, Channel 0, Enable 0!, Hold=30, ADC Residuals vs DAC



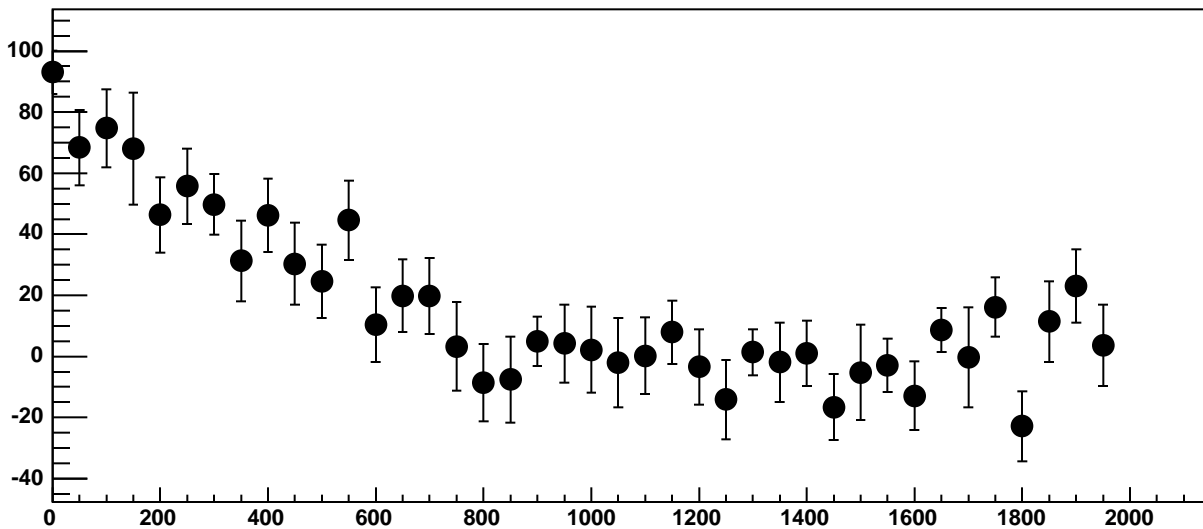
Chip 10, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC



Chip 10, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC

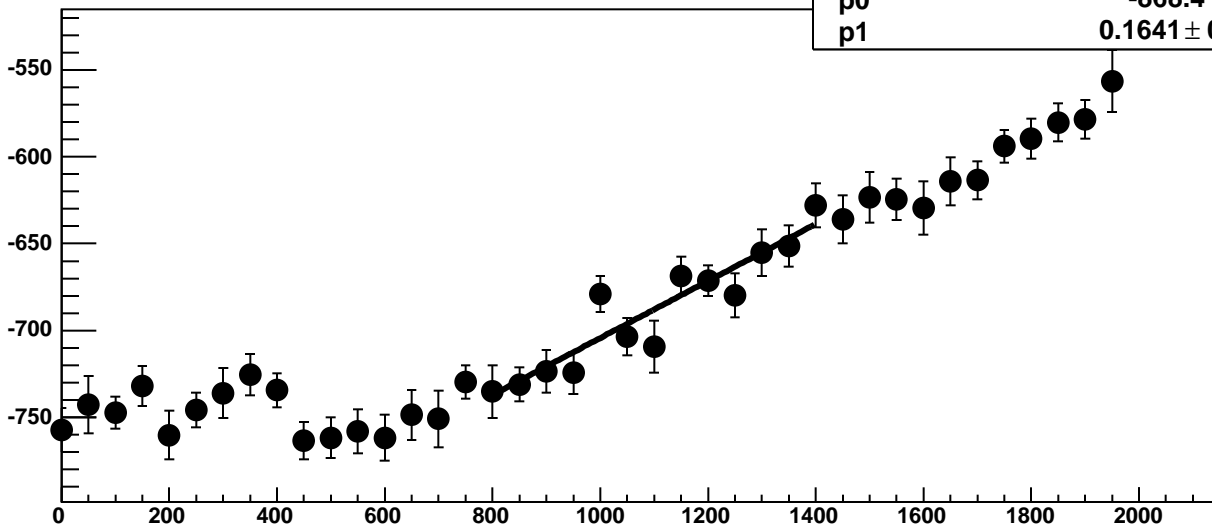


Chip 10, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC



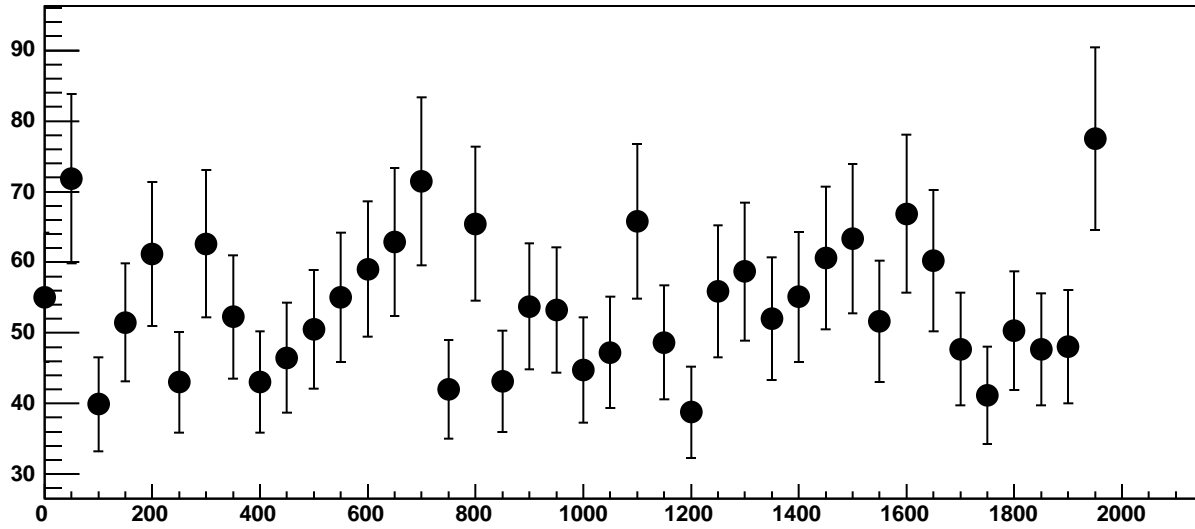


Chip 10, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC

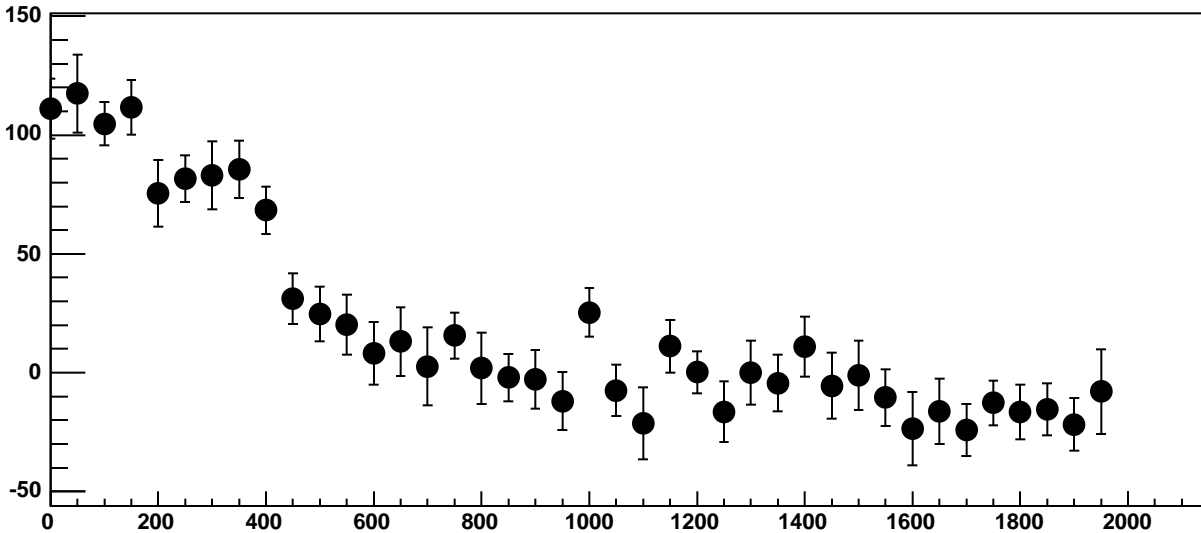


$\chi^2 / \text{ndf}$  13.17 / 11  
p0  $-868.4 \pm 20.06$   
p1  $0.1641 \pm 0.01804$

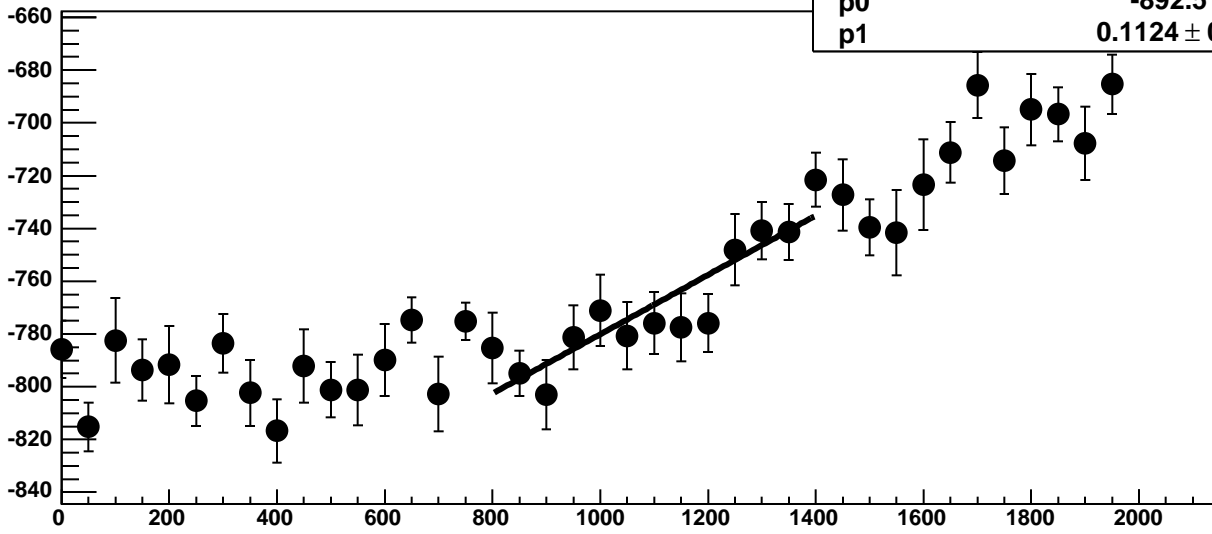
Chip 10, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC



Chip 10, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC

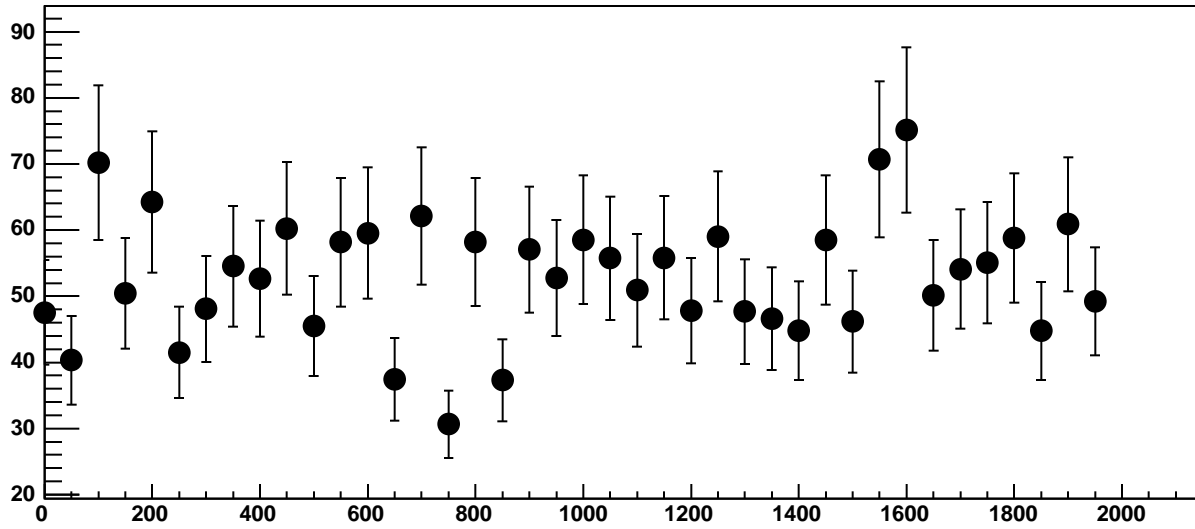


Chip 10, Channel 0, Enable 3, Hold=30, ADC Mean vs DAC

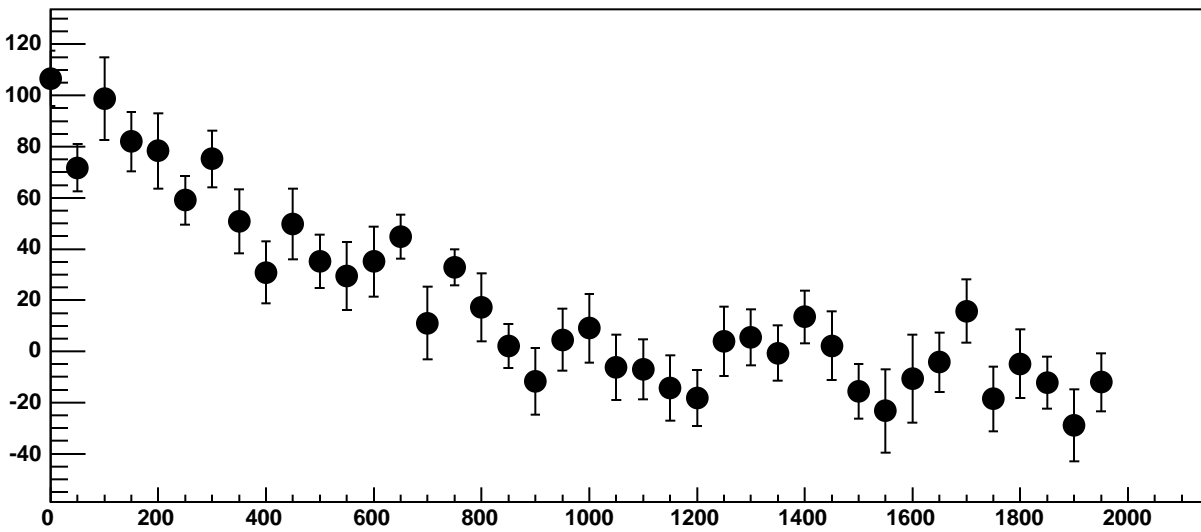


$\chi^2 / \text{ndf}$  9.804 / 11  
p0  $-892.5 \pm 18.29$   
p1  $0.1124 \pm 0.01629$

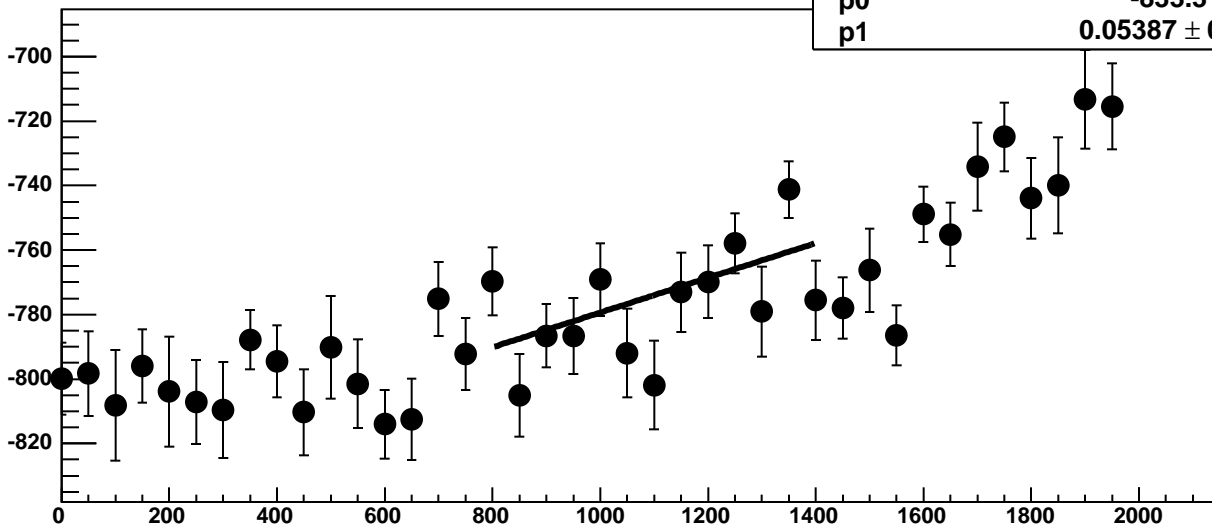
Chip 10, Channel 0, Enable 3, Hold=30, ADC Noise vs DAC



Chip 10, Channel 0, Enable 3, Hold=30, ADC Residuals vs DAC



Chip 10, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

21.04 / 11

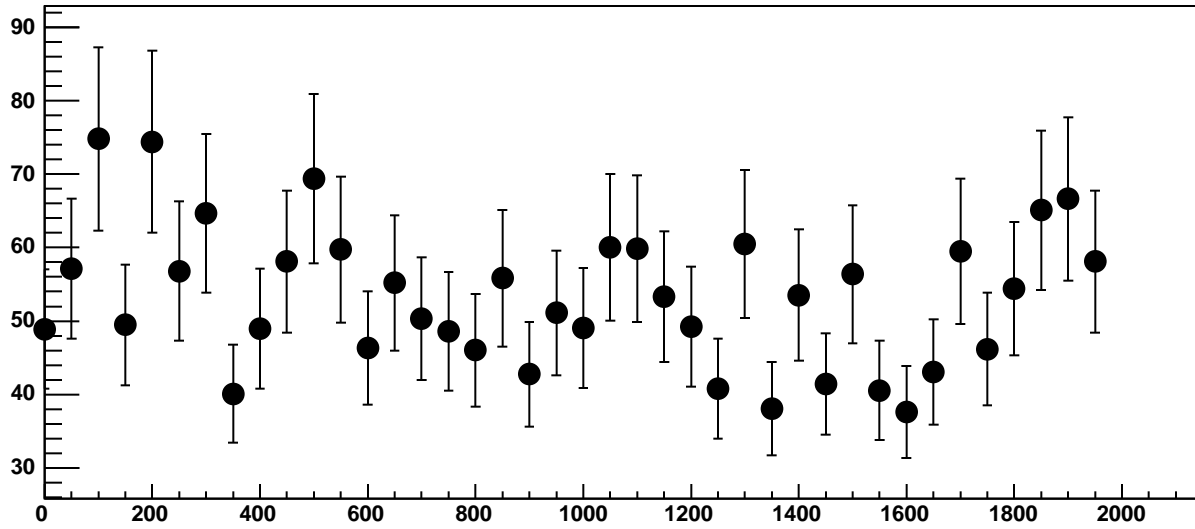
p0

$-833.3 \pm 18.15$

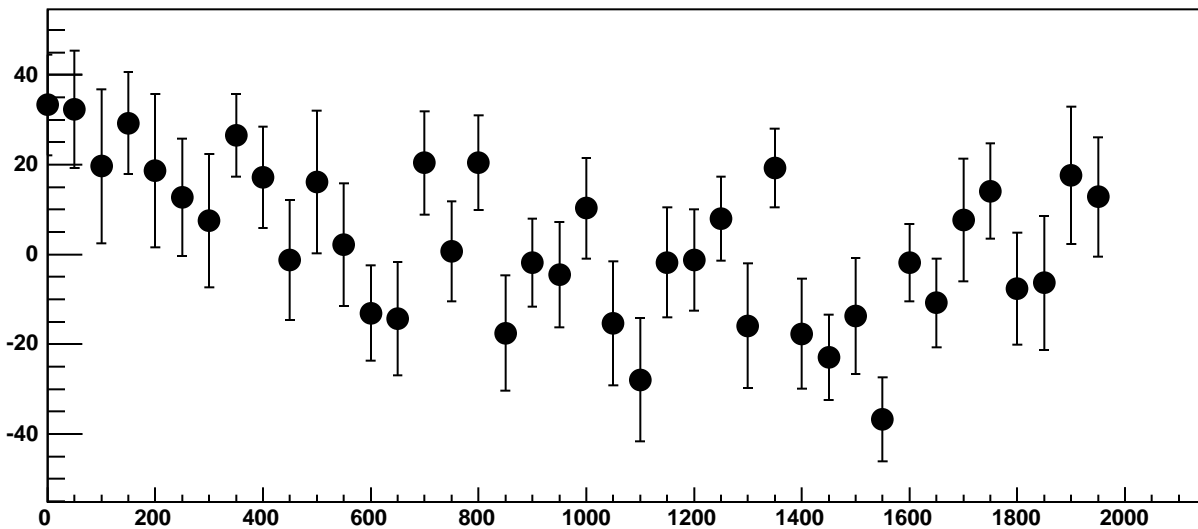
p1

$0.05387 \pm 0.01615$

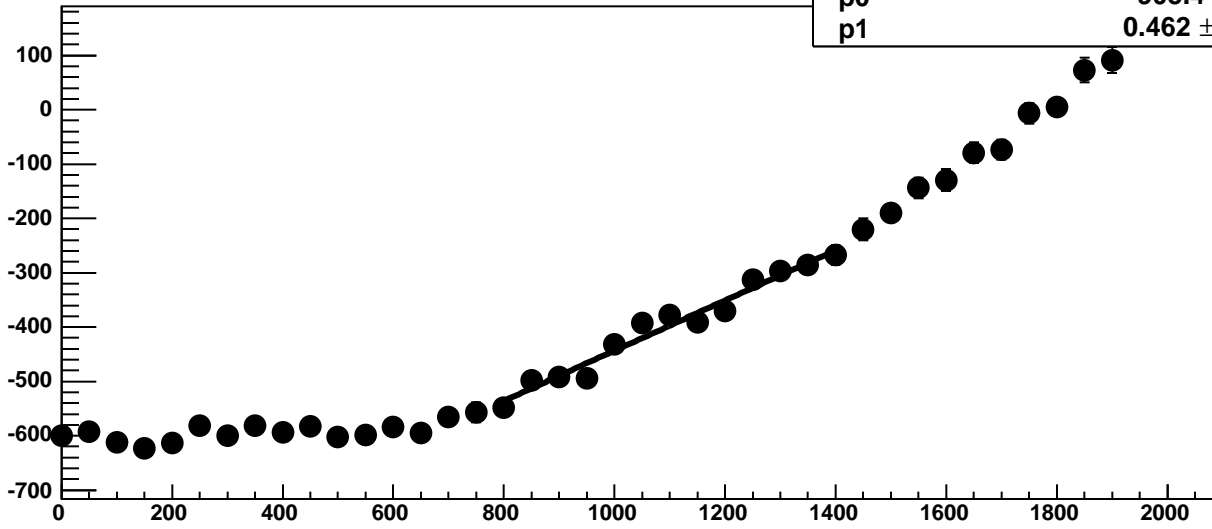
Chip 10, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC



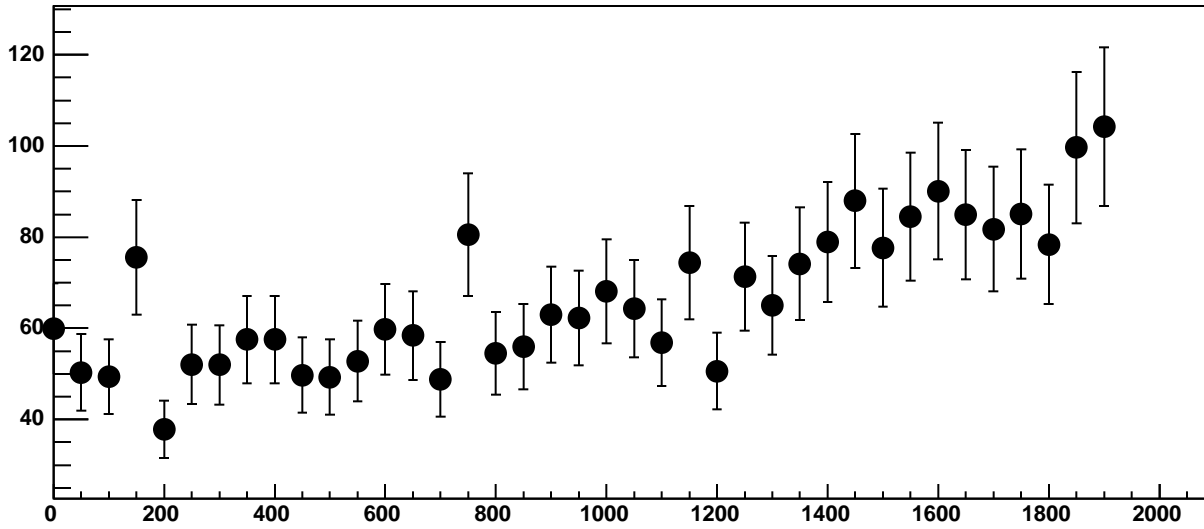
Chip 10, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC



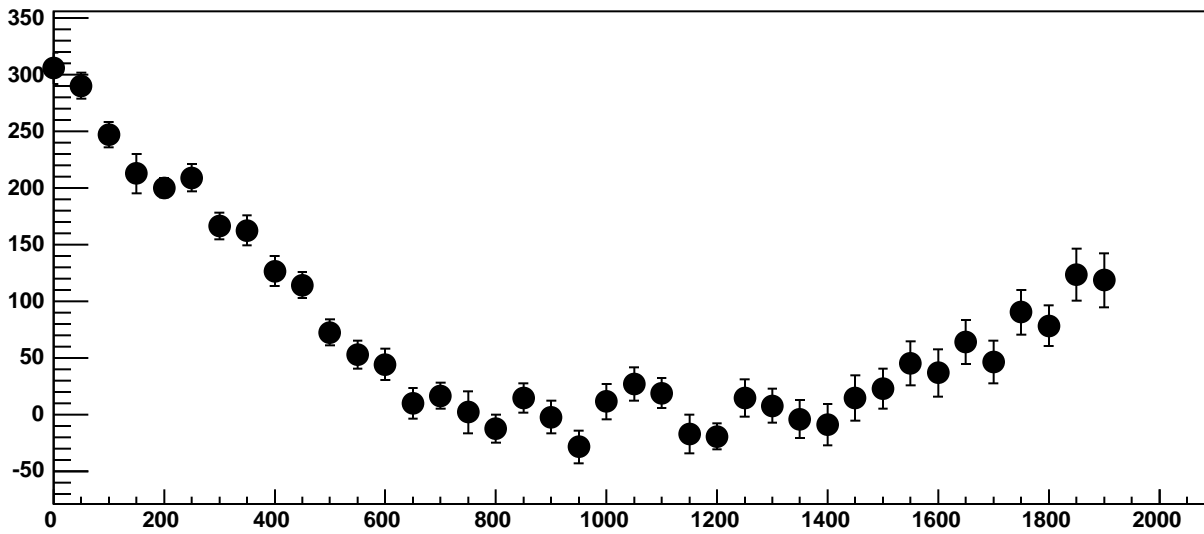
Chip 10, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC



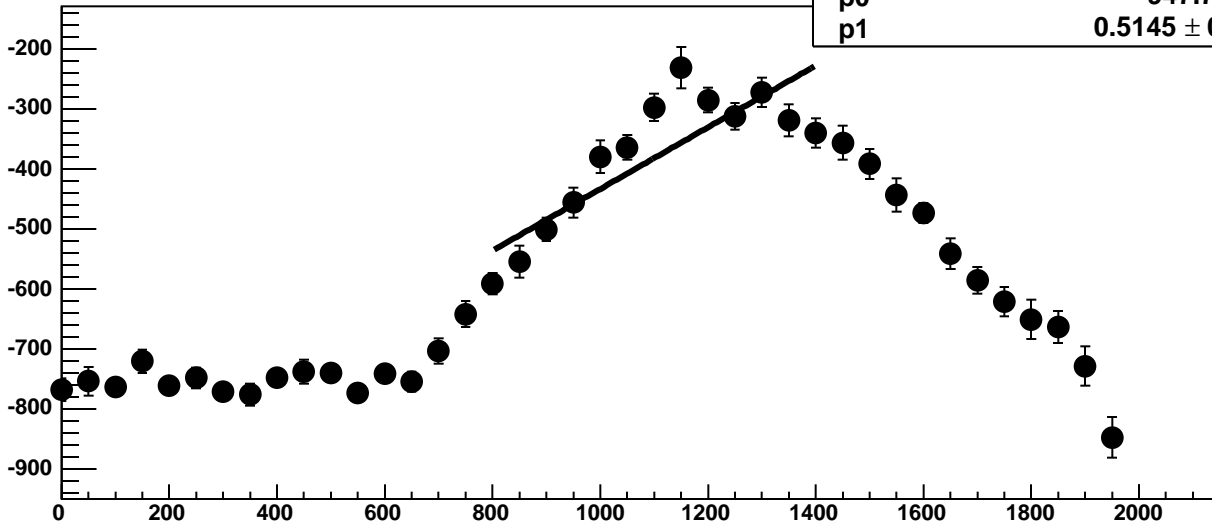
Chip 10, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC



Chip 10, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC

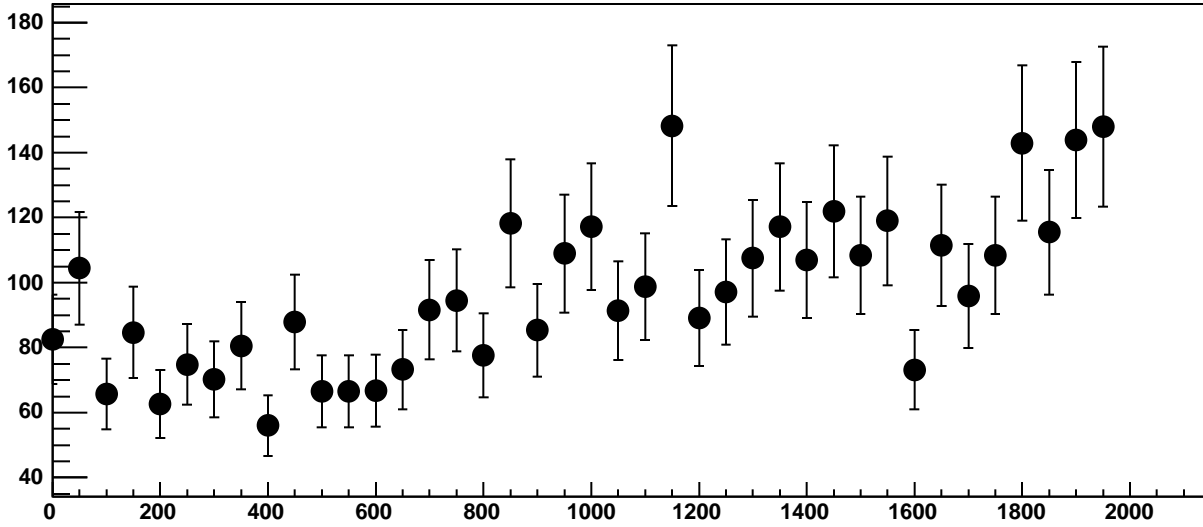


Chip 10, Channel 1, Enable 0, Hold=30, ADC Mean vs DAC

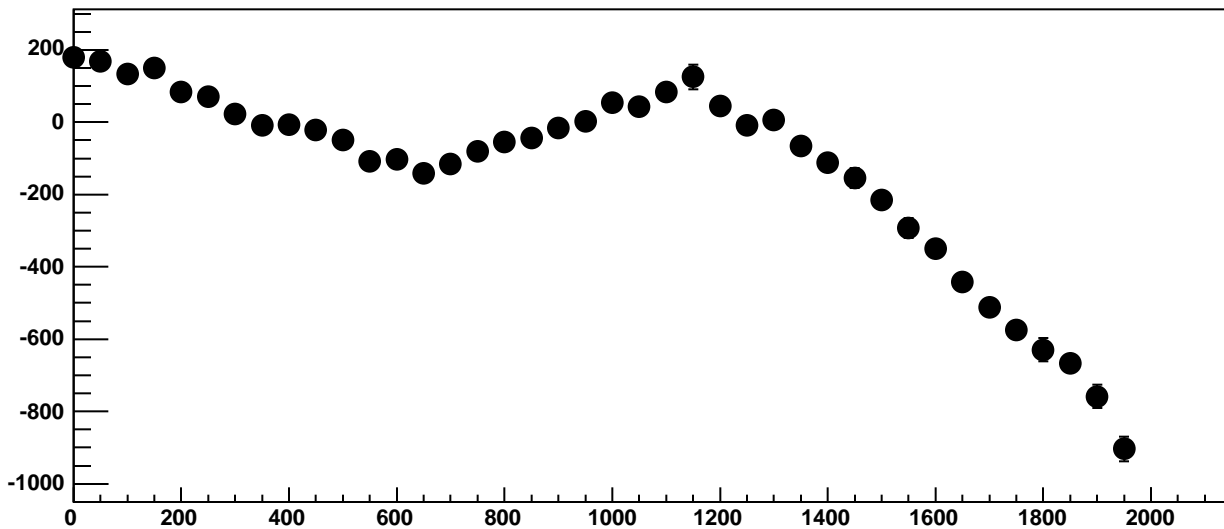


$\chi^2 / \text{ndf}$  80.92 / 11  
p0  $-947.7 \pm 36.7$   
p1  $0.5145 \pm 0.03353$

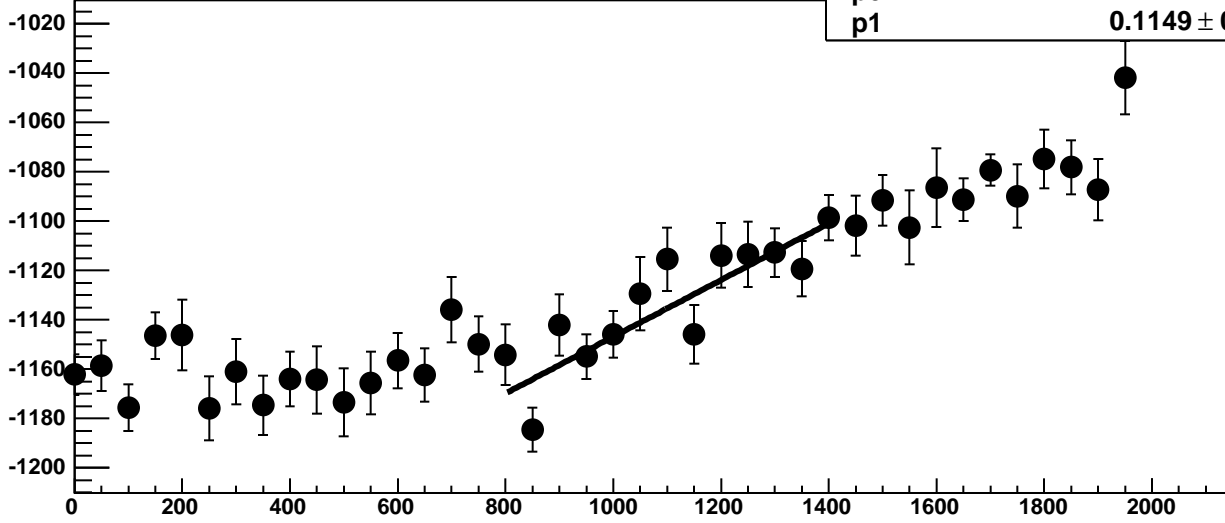
Chip 10, Channel 1, Enable 0, Hold=30, ADC Noise vs DAC



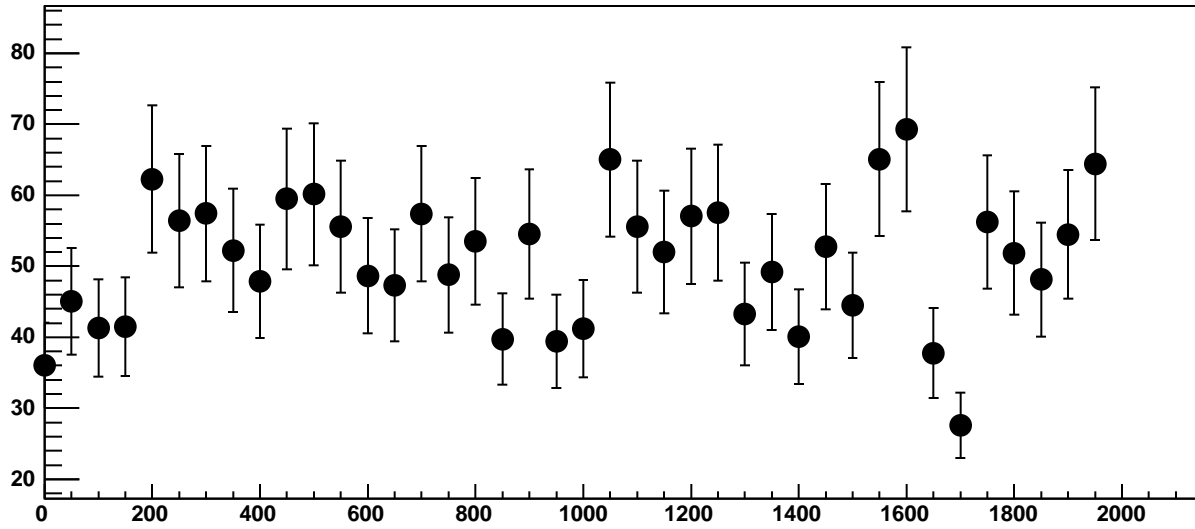
Chip 10, Channel 1, Enable 0, Hold=30, ADC Residuals vs DAC



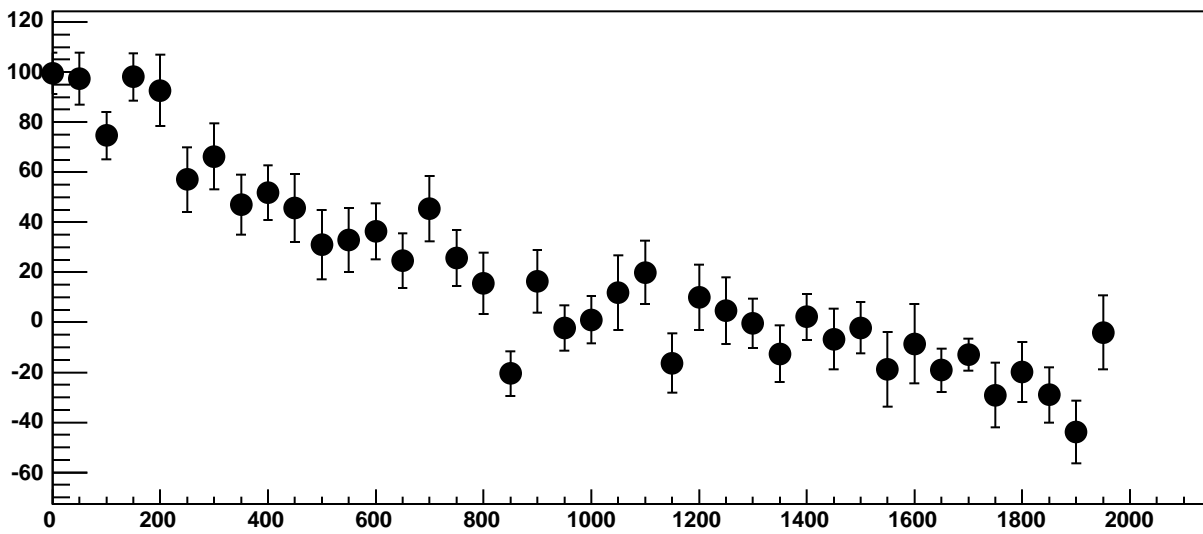
Chip 10, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC



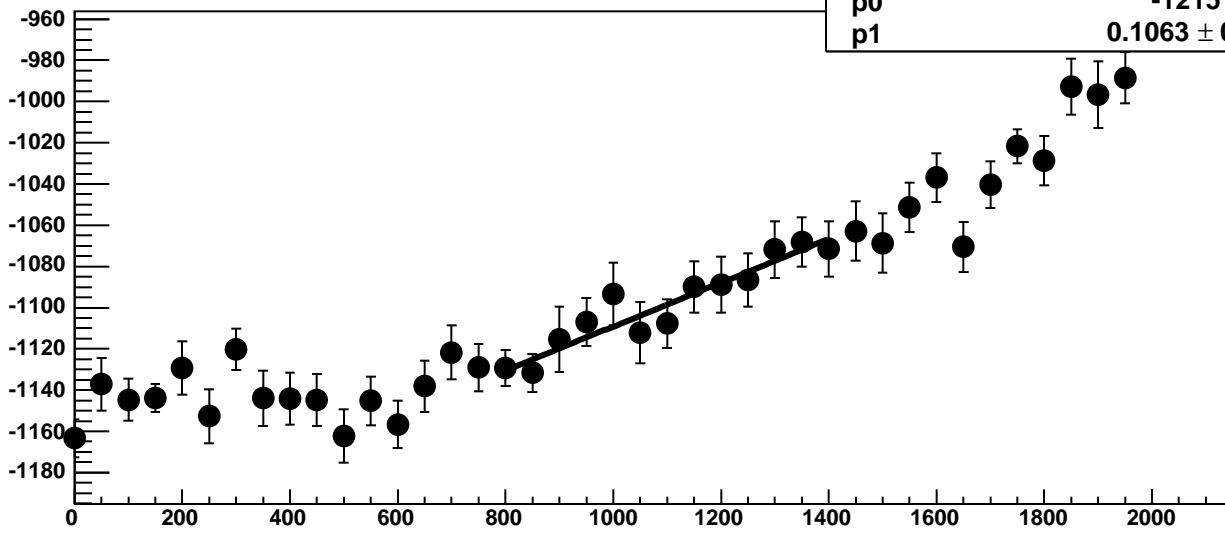
Chip 10, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC



Chip 10, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC

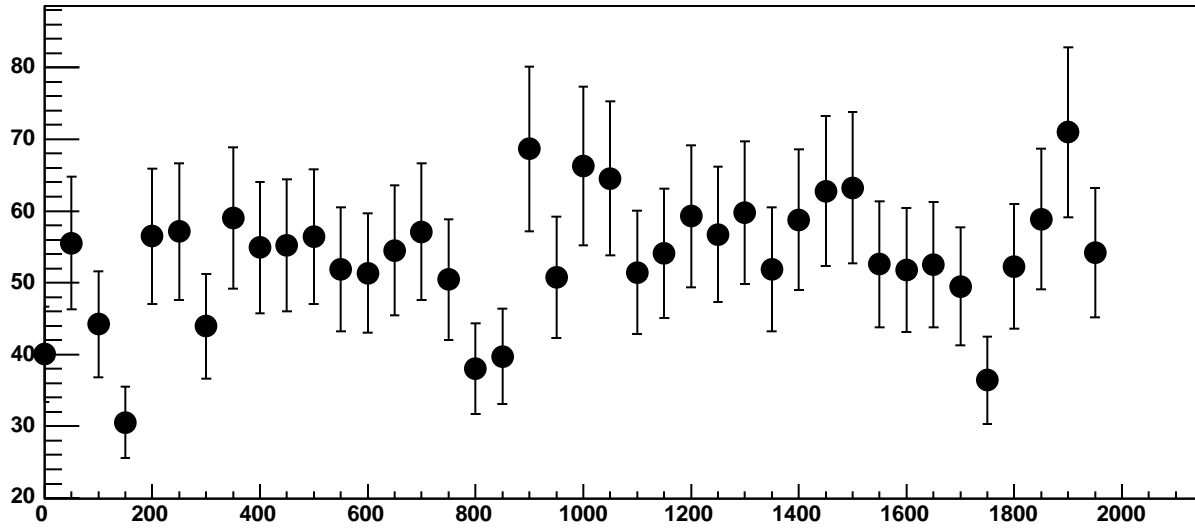


Chip 10, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC

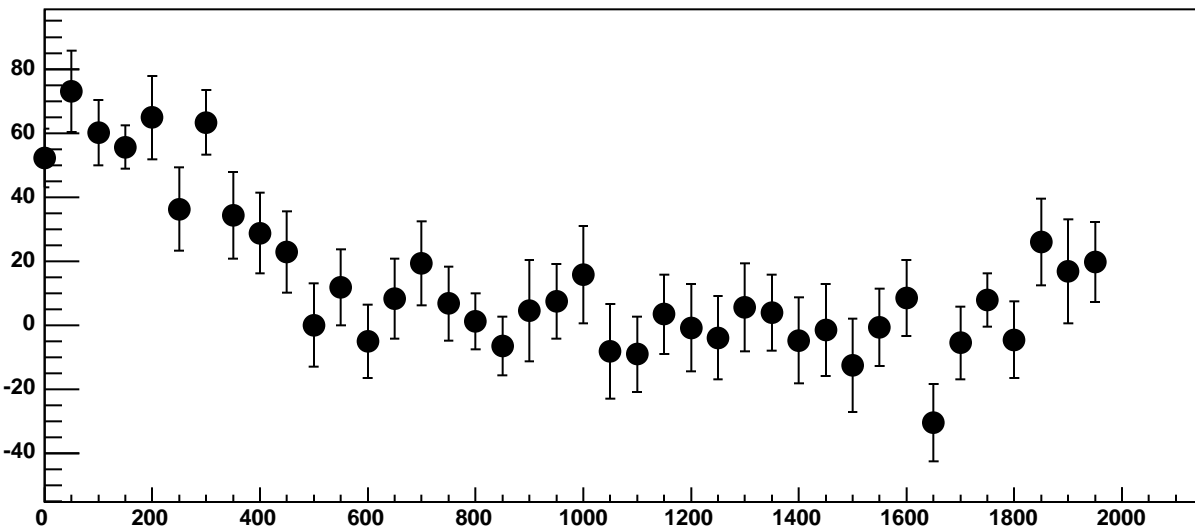


$\chi^2 / \text{ndf}$  3.568 / 11  
p0 -1215 ± 18.16  
p1 0.1063 ± 0.01679

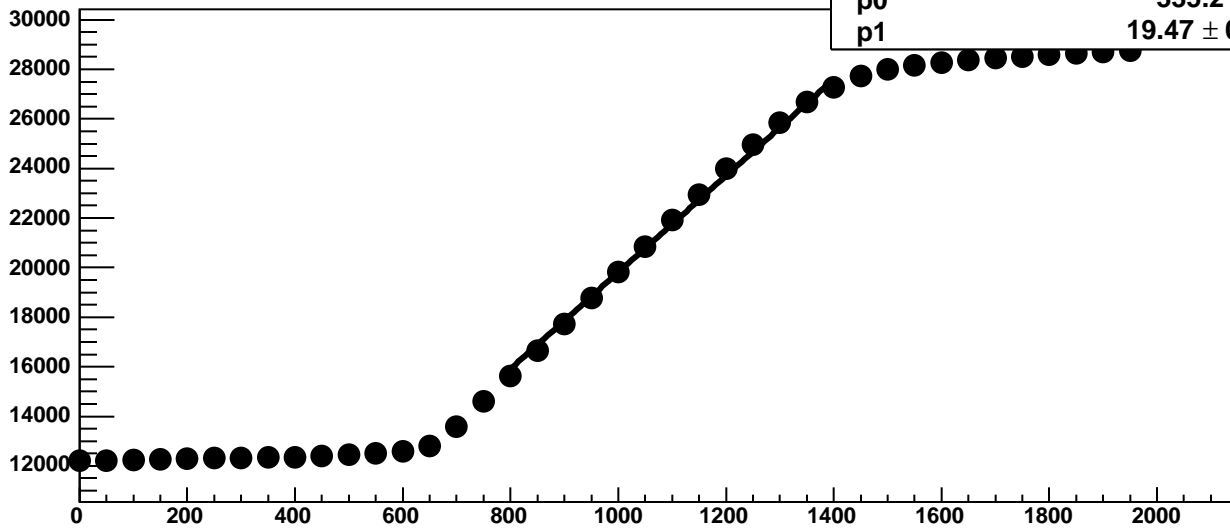
Chip 10, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC



Chip 10, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC

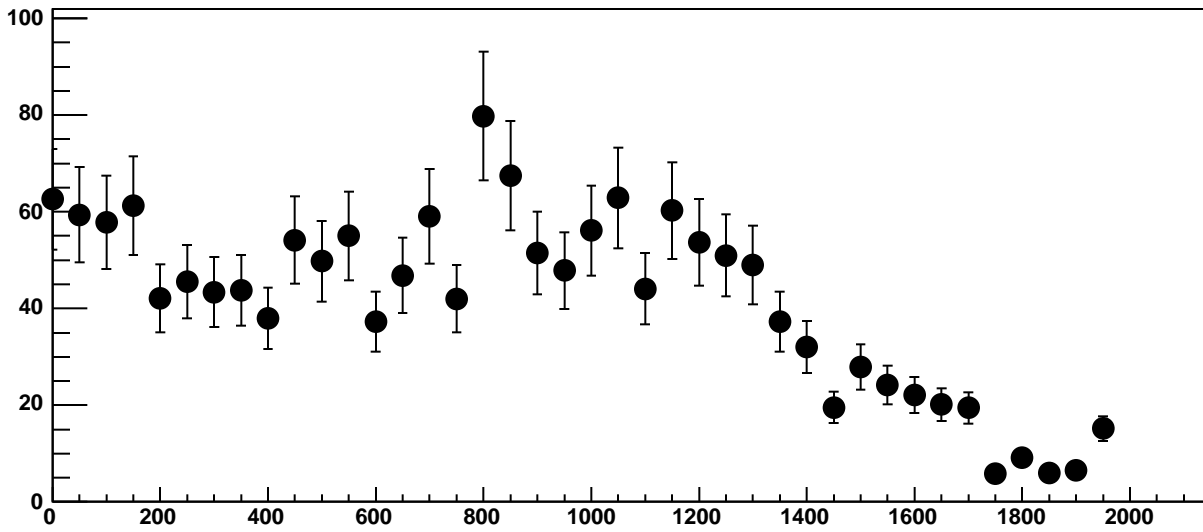


Chip 10, Channel 1, Enable 3!, Hold=30, ADC Mean vs DAC

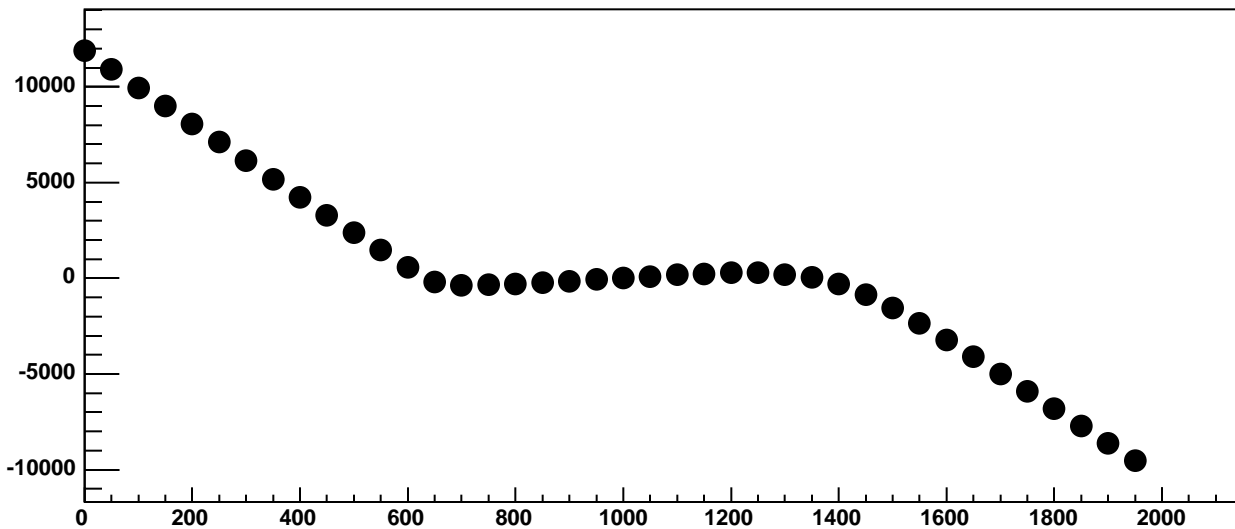


$\chi^2 / \text{ndf}$  4433 / 11  
p0  $335.2 \pm 19.58$   
p1  $19.47 \pm 0.01653$

Chip 10, Channel 1, Enable 3!, Hold=30, ADC Noise vs DAC

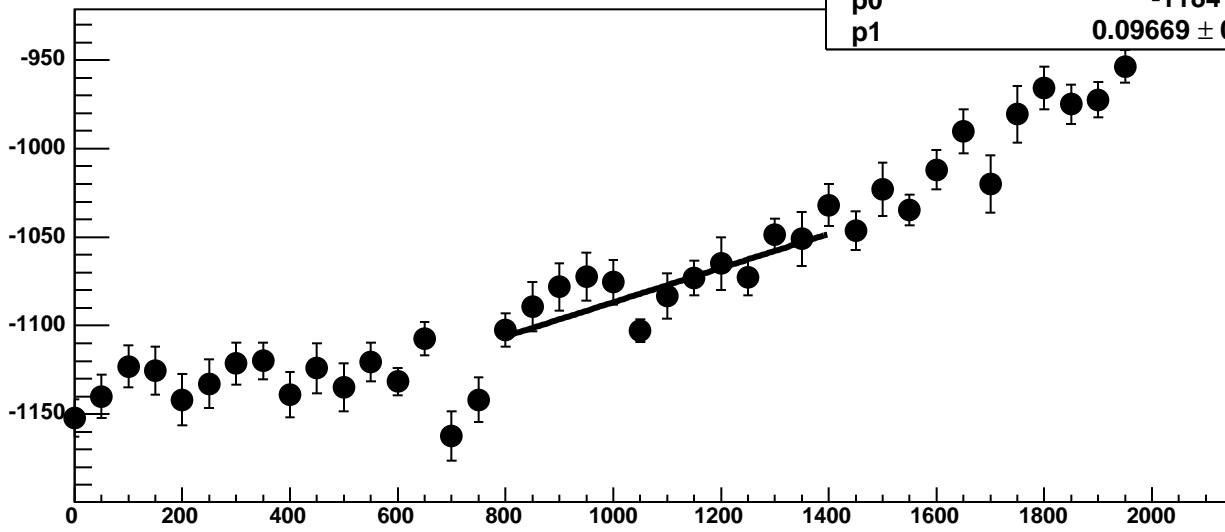


Chip 10, Channel 1, Enable 3!, Hold=30, ADC Residuals vs DAC

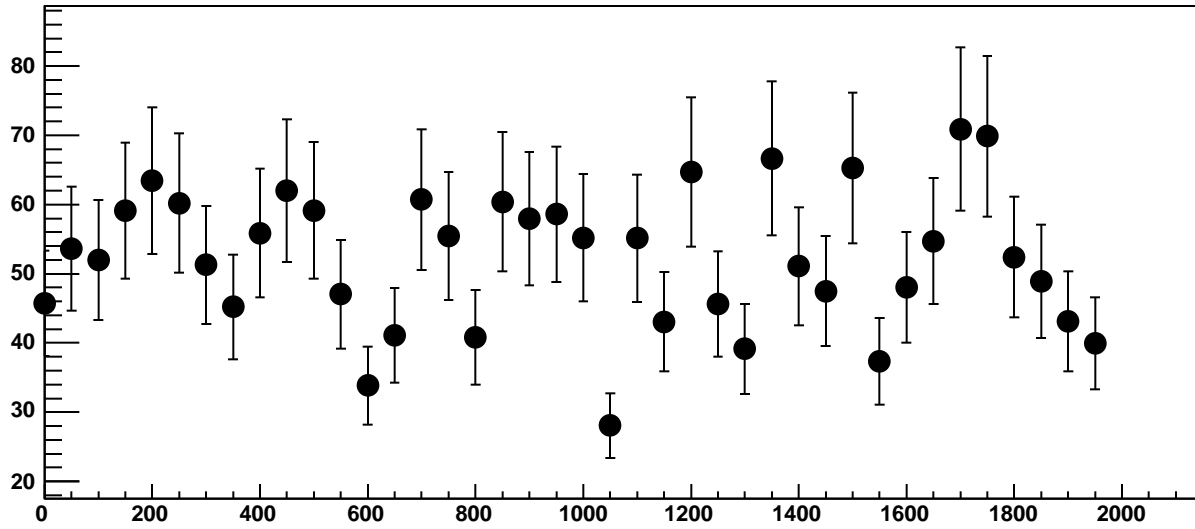




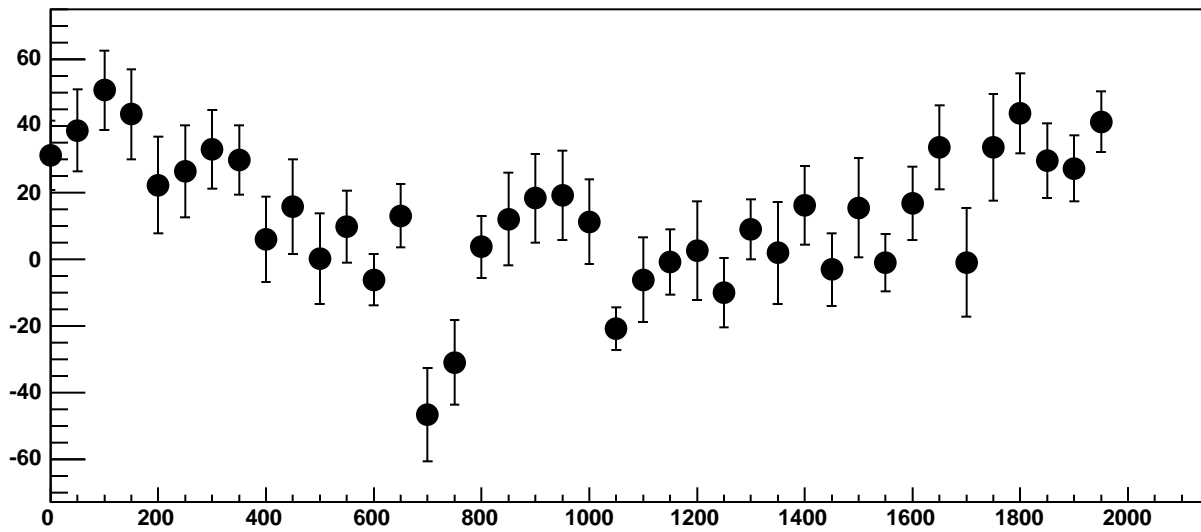
Chip 10, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC



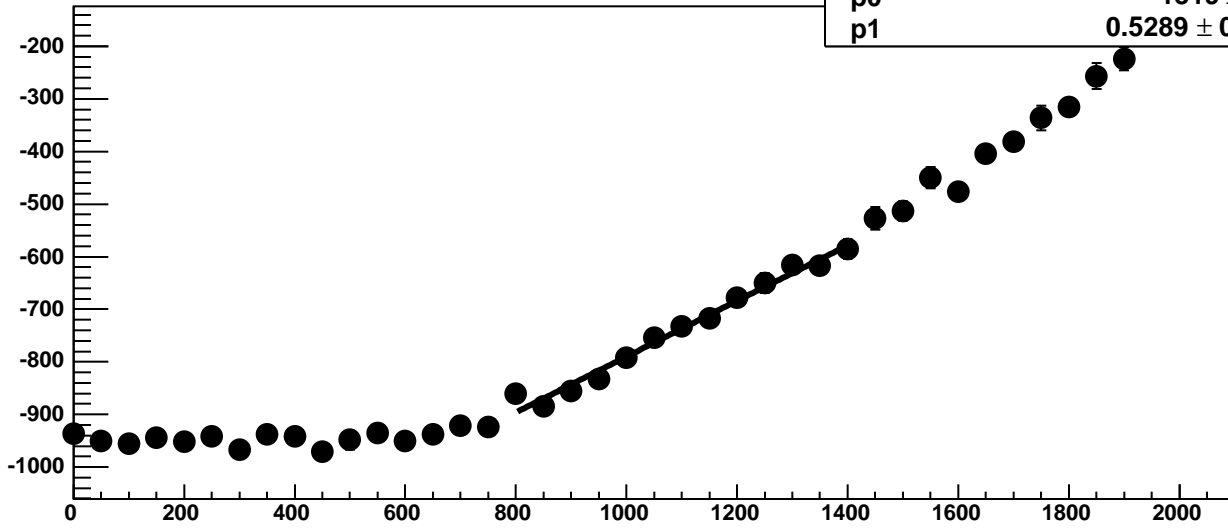
Chip 10, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC



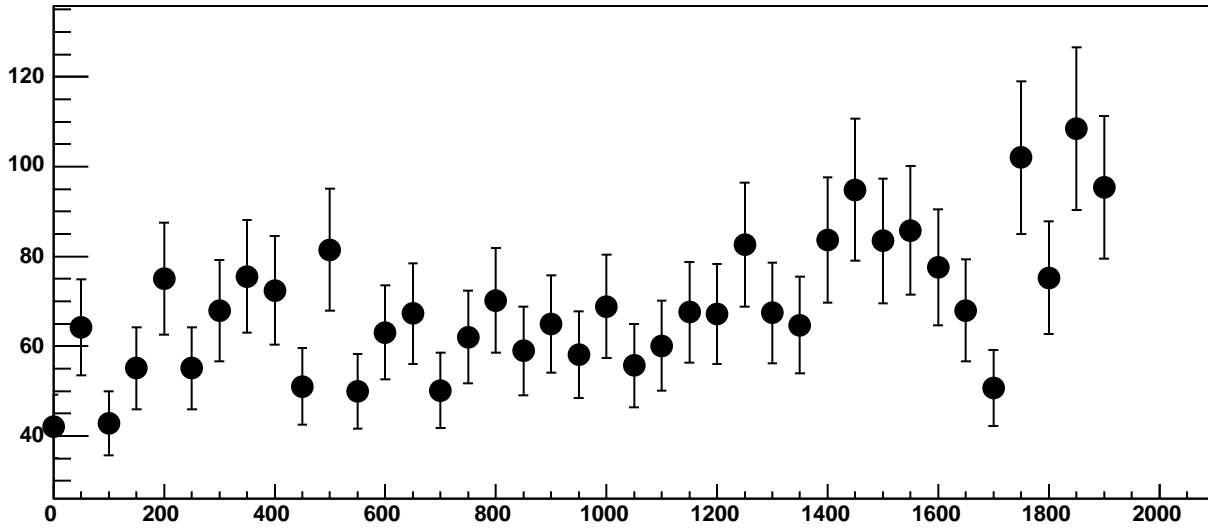
Chip 10, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC



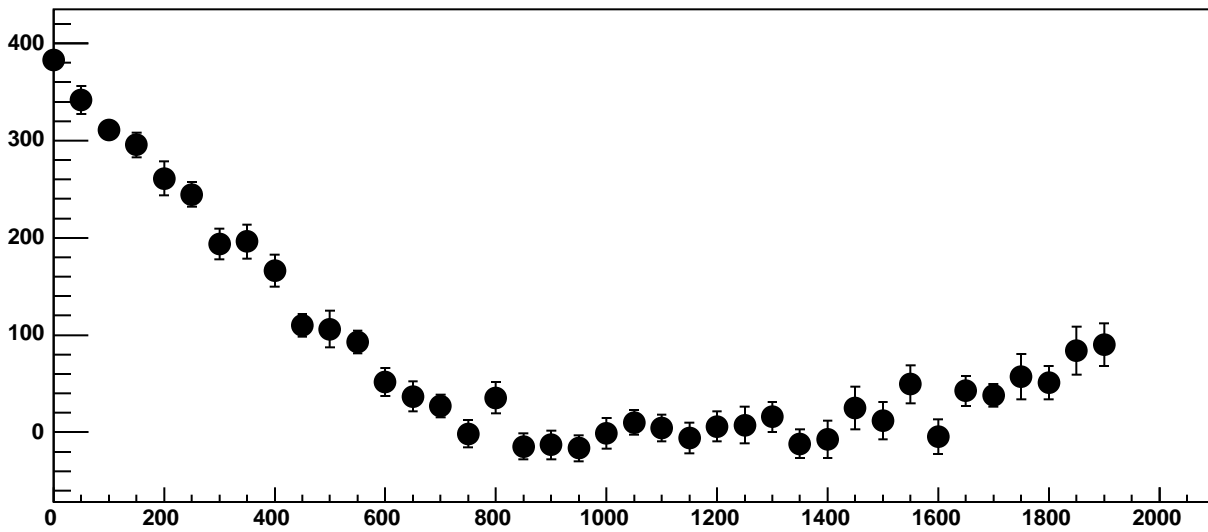
Chip 10, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC



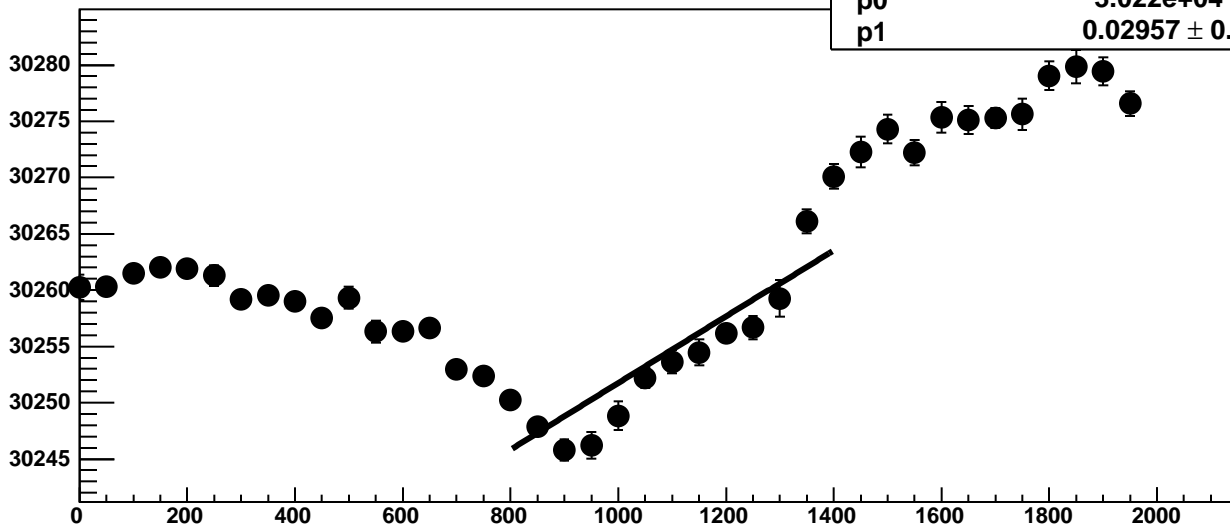
Chip 10, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC



Chip 10, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC

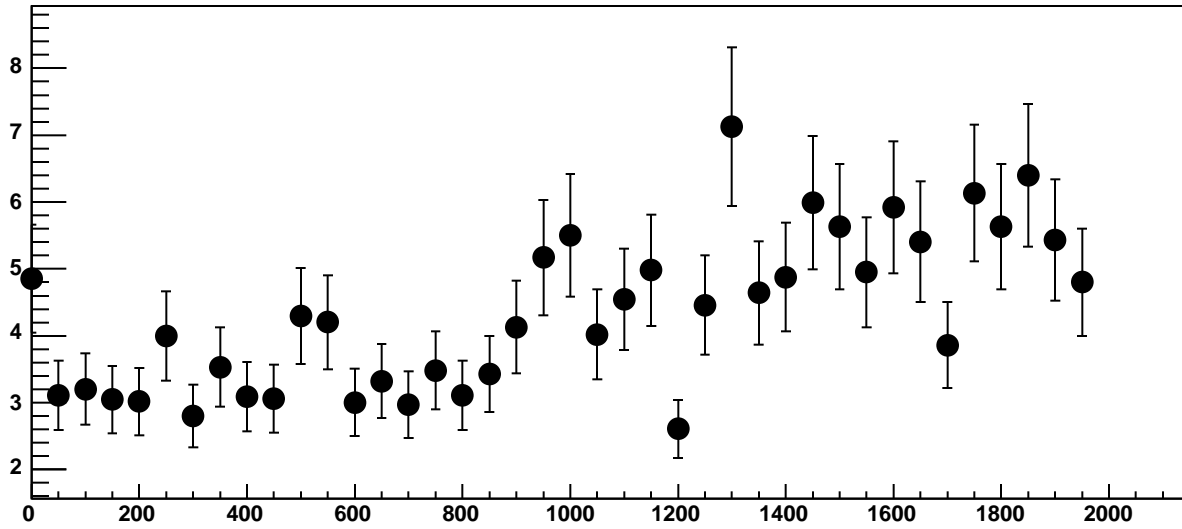


Chip 10, Channel 2, Enable 0!, Hold=30, ADC Mean vs DAC

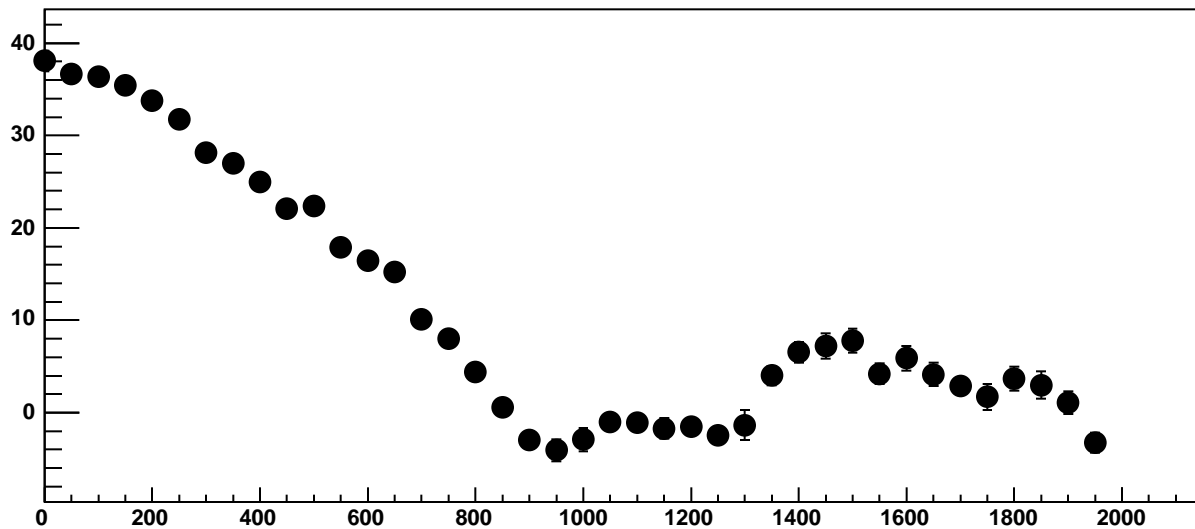


$\chi^2 / \text{ndf}$  131.7 / 11  
p0  $3.022\text{e}+04 \pm 1.495$   
p1  $0.02957 \pm 0.001377$

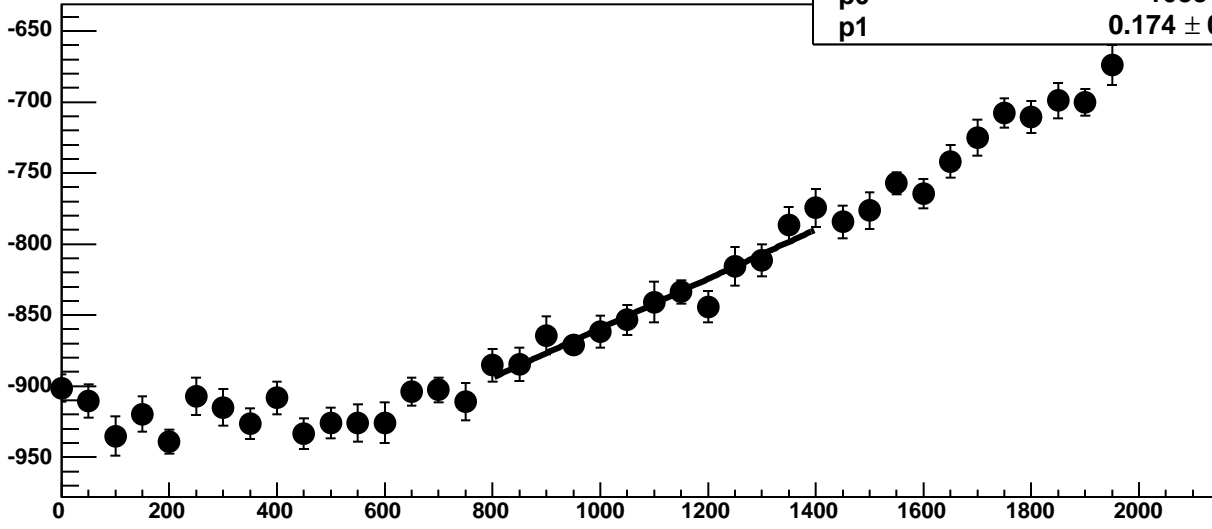
Chip 10, Channel 2, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 10, Channel 2, Enable 0!, Hold=30, ADC Residuals vs DAC

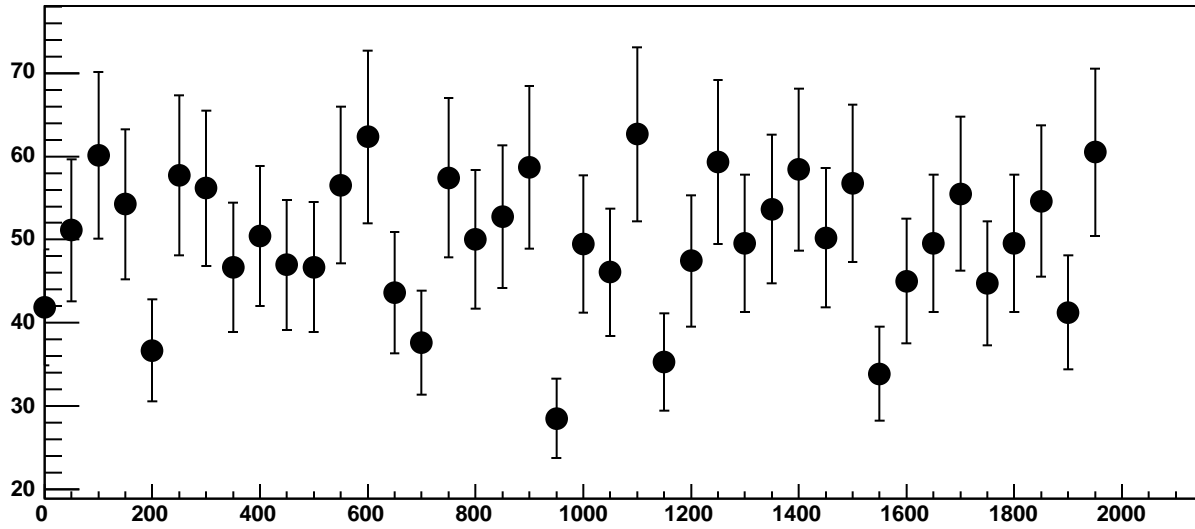


Chip 10, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC

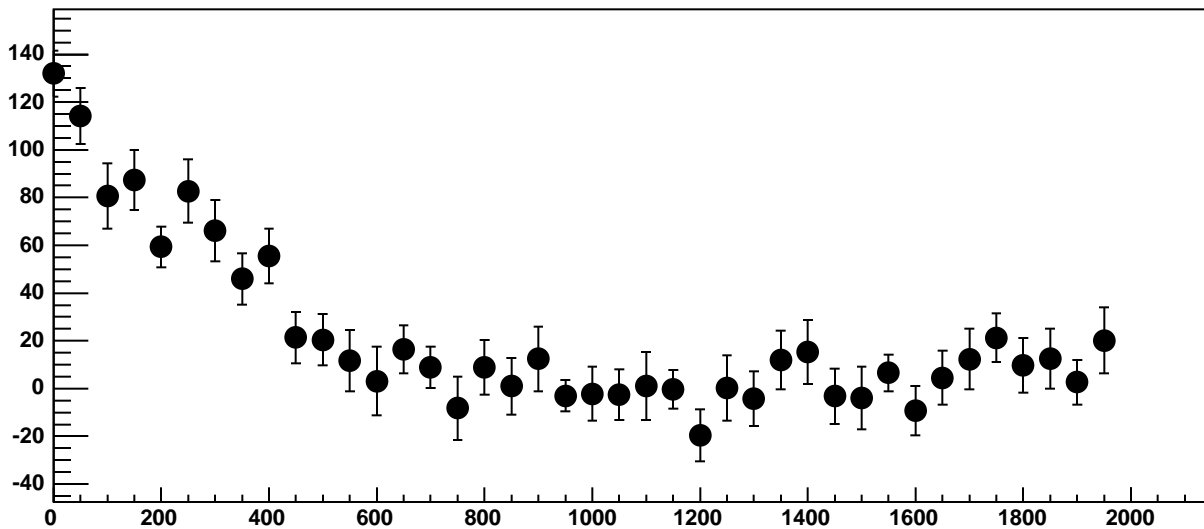


$\chi^2 / \text{ndf}$  7.354 / 11  
p0  $-1033 \pm 18.75$   
p1  $0.174 \pm 0.01719$

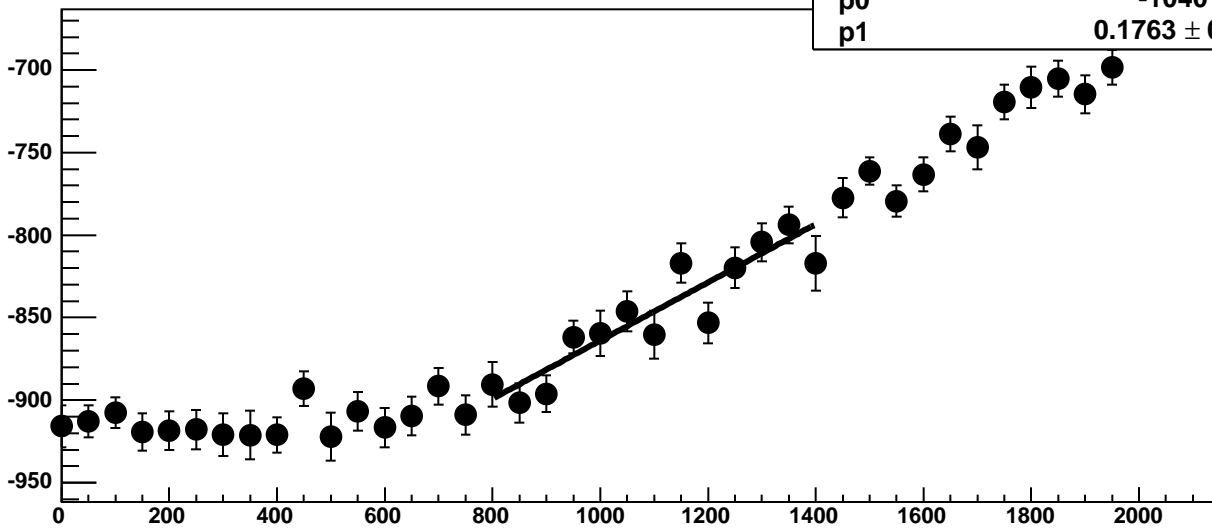
Chip 10, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC



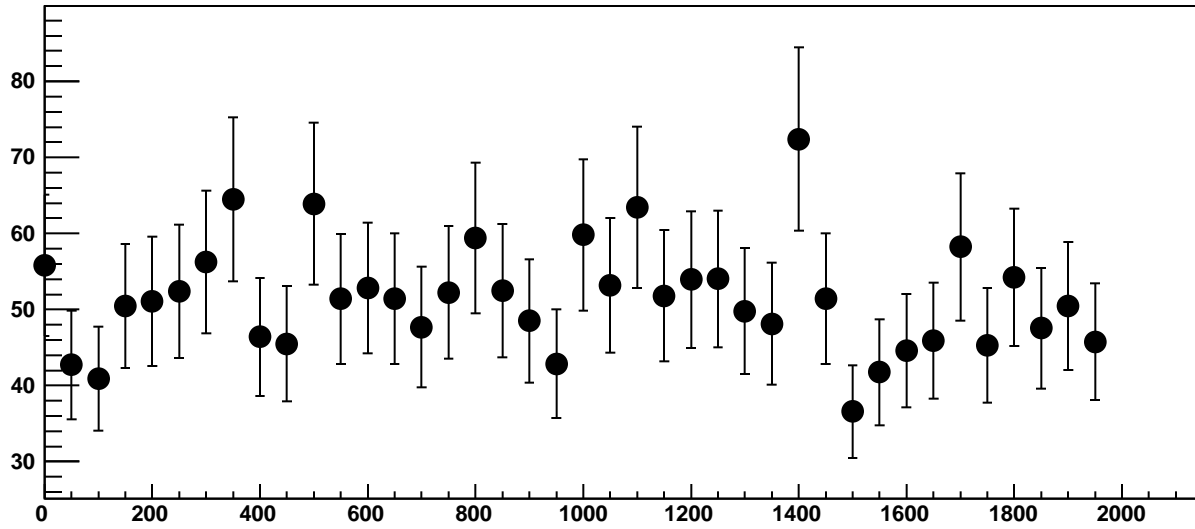
Chip 10, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC



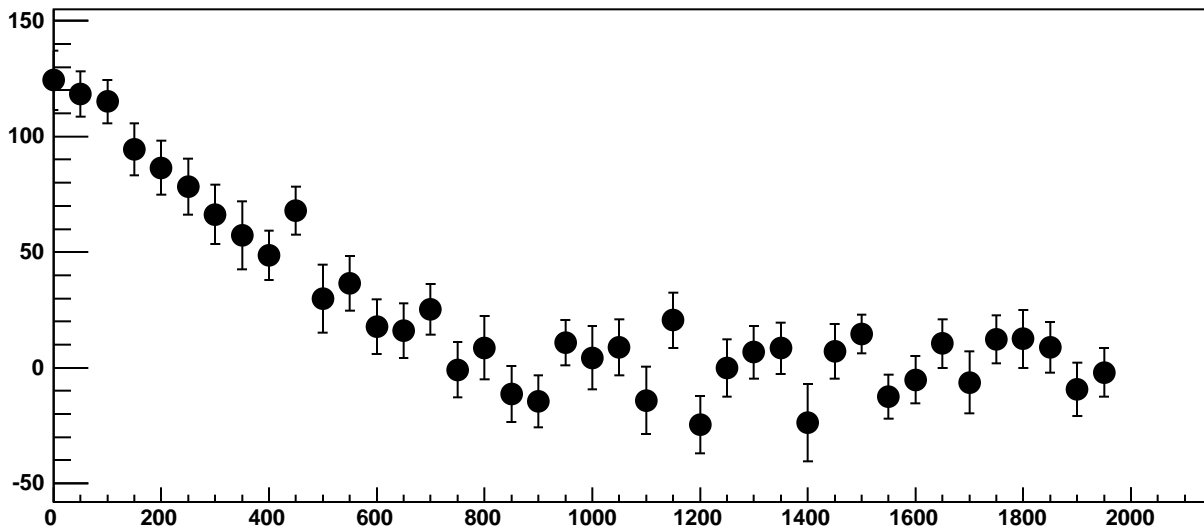
Chip 10, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC



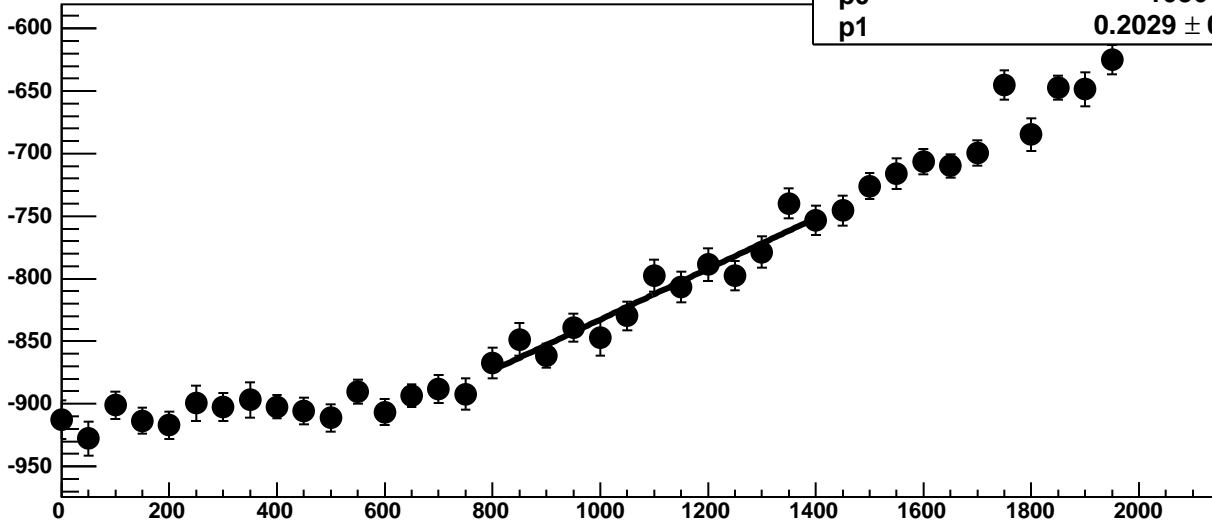
Chip 10, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC



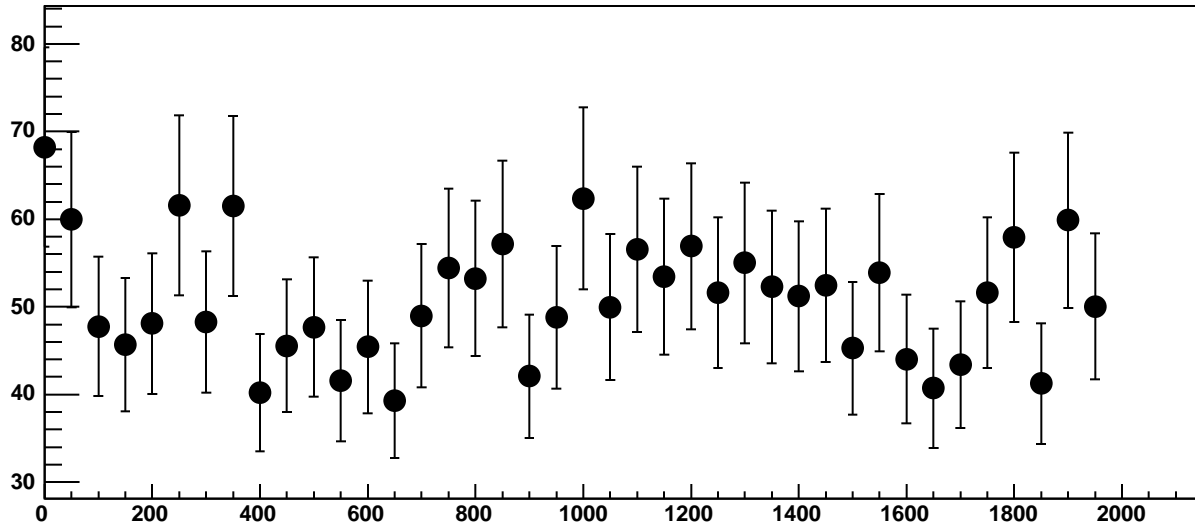
Chip 10, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC



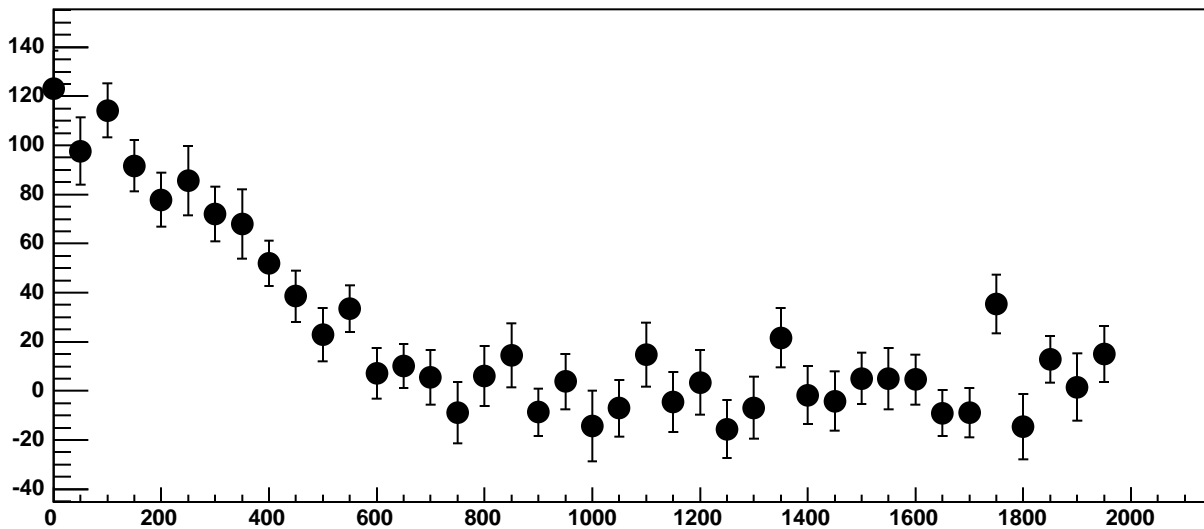
Chip 10, Channel 2, Enable 3, Hold=30, ADC Mean vs DAC



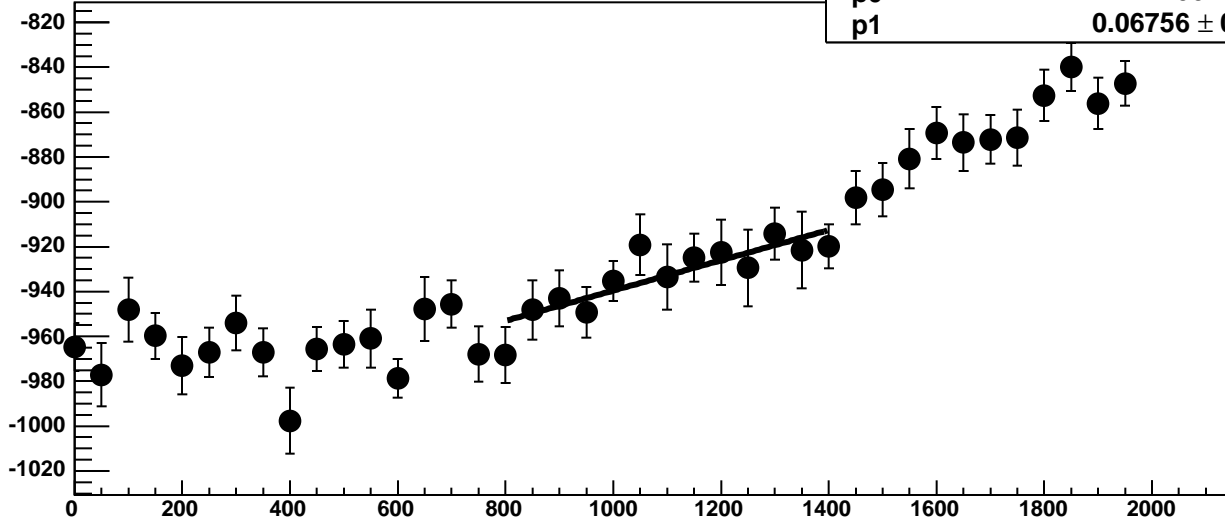
Chip 10, Channel 2, Enable 3, Hold=30, ADC Noise vs DAC



Chip 10, Channel 2, Enable 3, Hold=30, ADC Residuals vs DAC

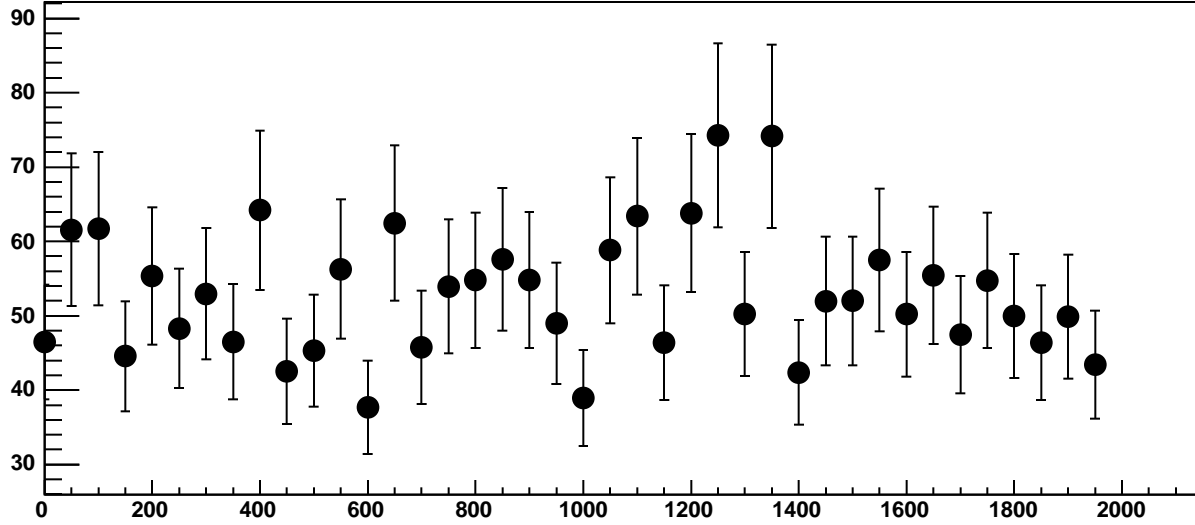


Chip 10, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC

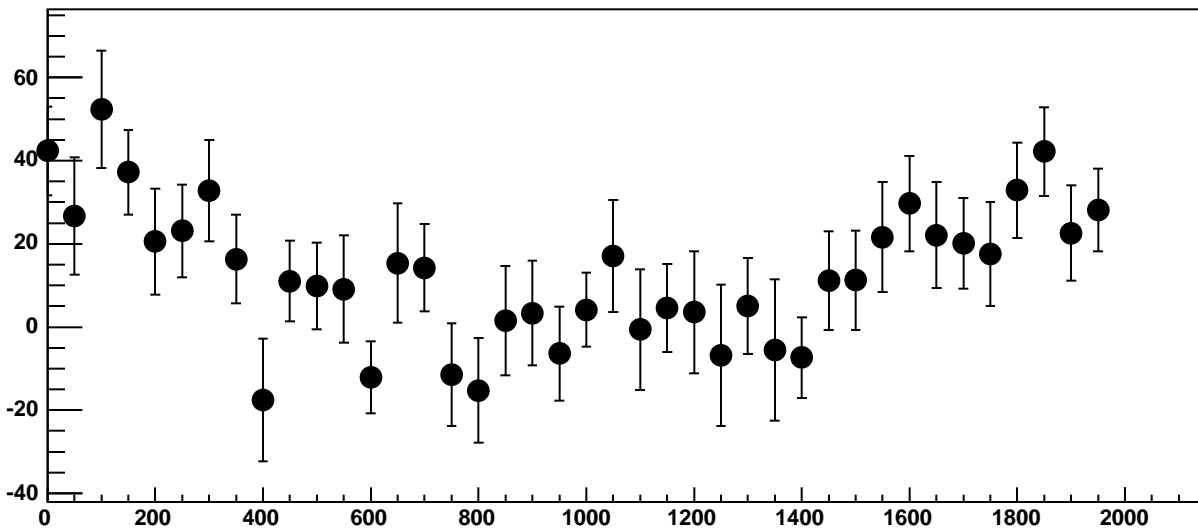


$\chi^2 / \text{ndf}$  4.969 / 11  
p0  $-1007 \pm 19.94$   
p1  $0.06756 \pm 0.01793$

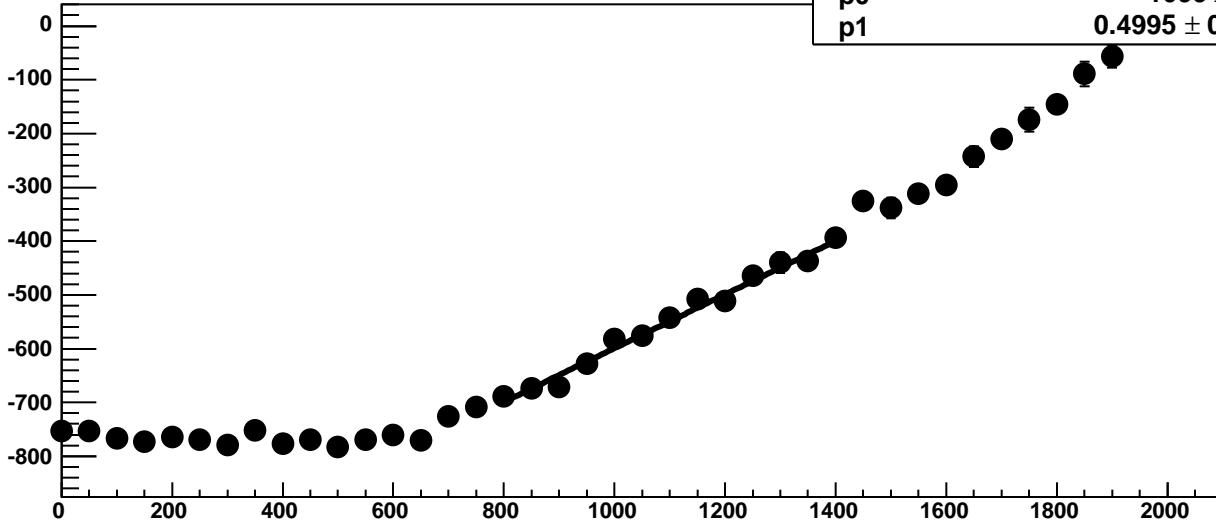
Chip 10, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC



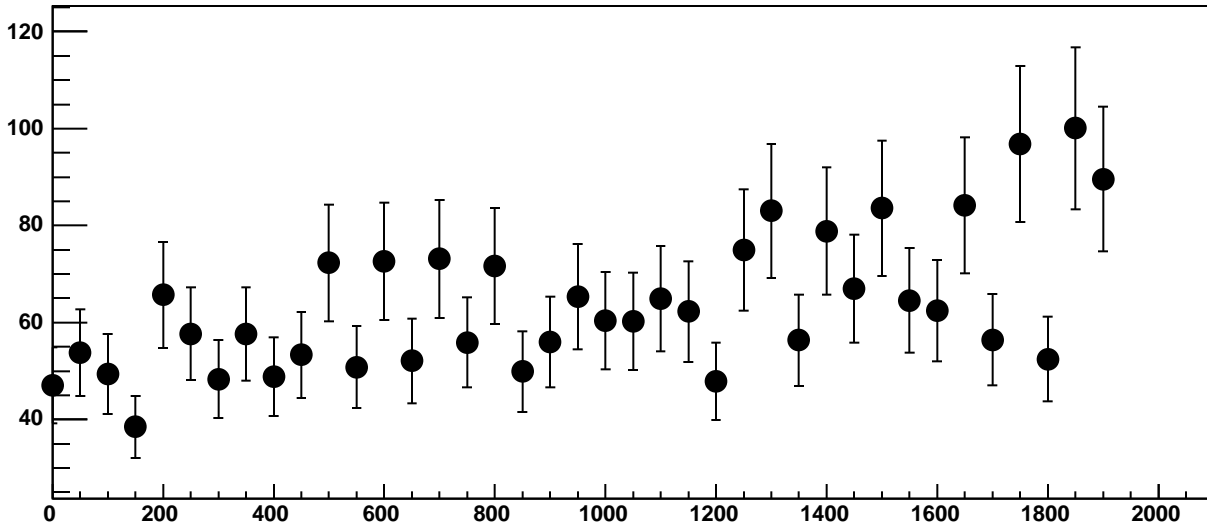
Chip 10, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC



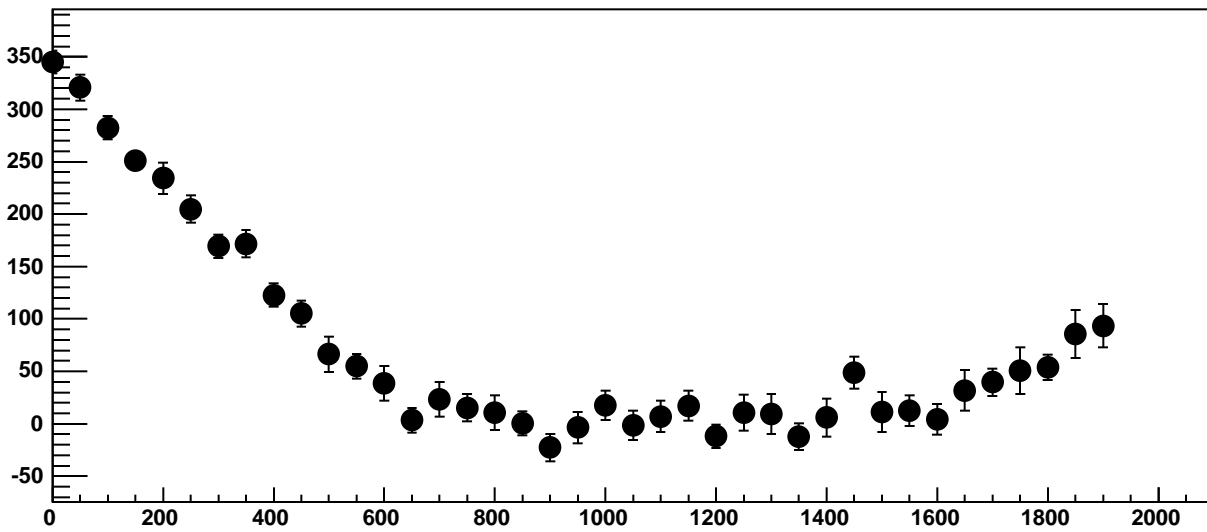
Chip 10, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC



Chip 10, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC

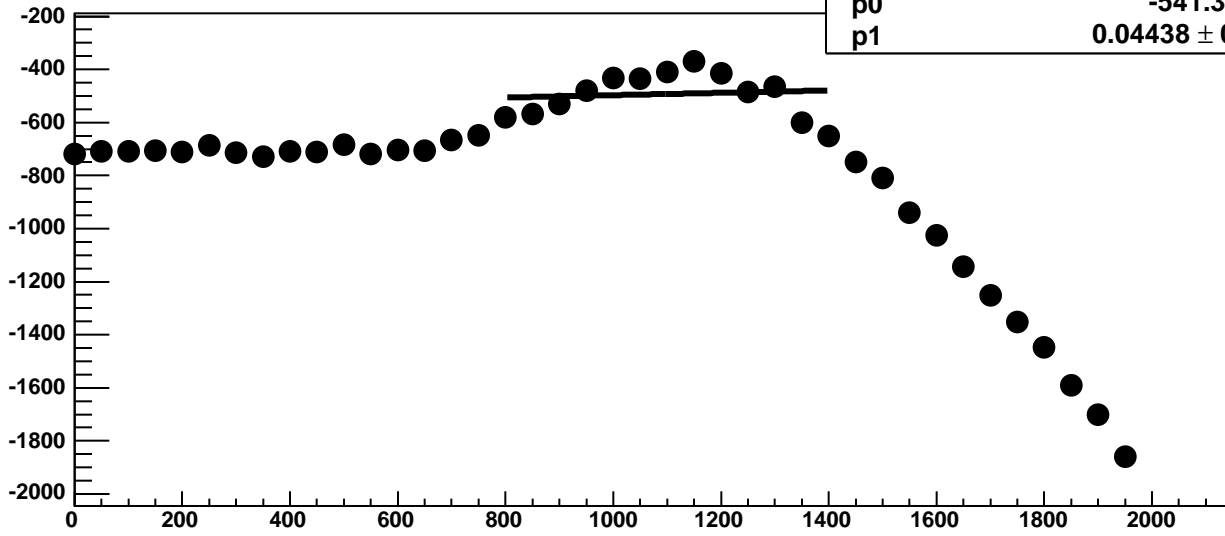


Chip 10, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC





Chip 10, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

140.6 / 11

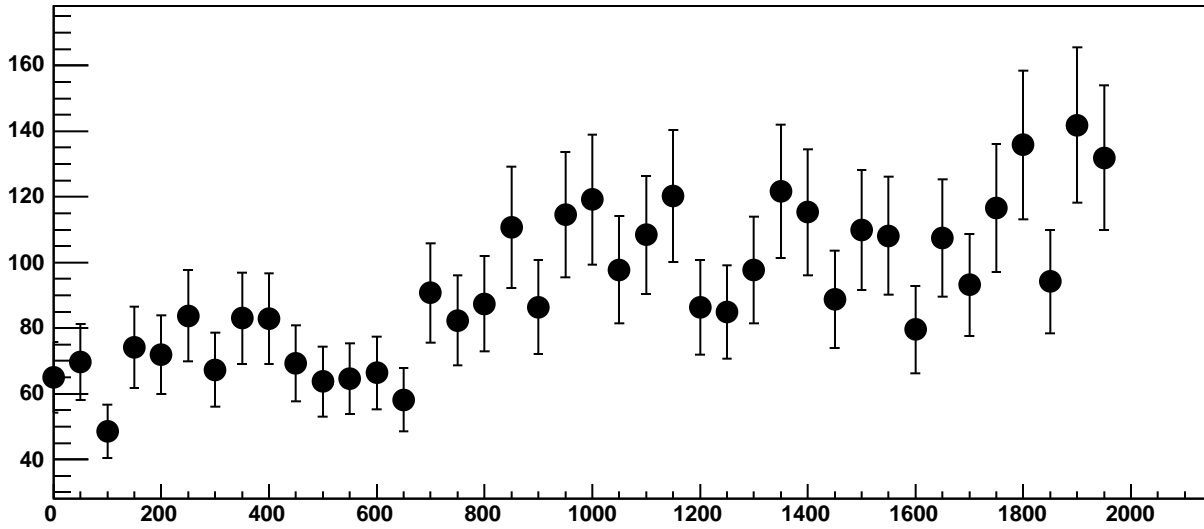
p0

$-541.3 \pm 38.01$

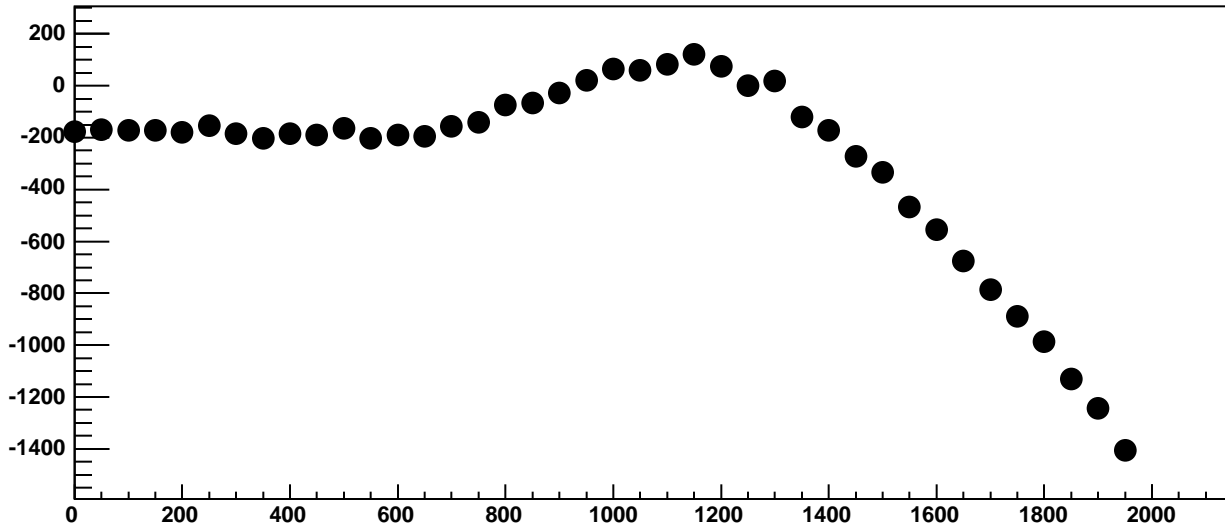
p1

$0.04438 \pm 0.03435$

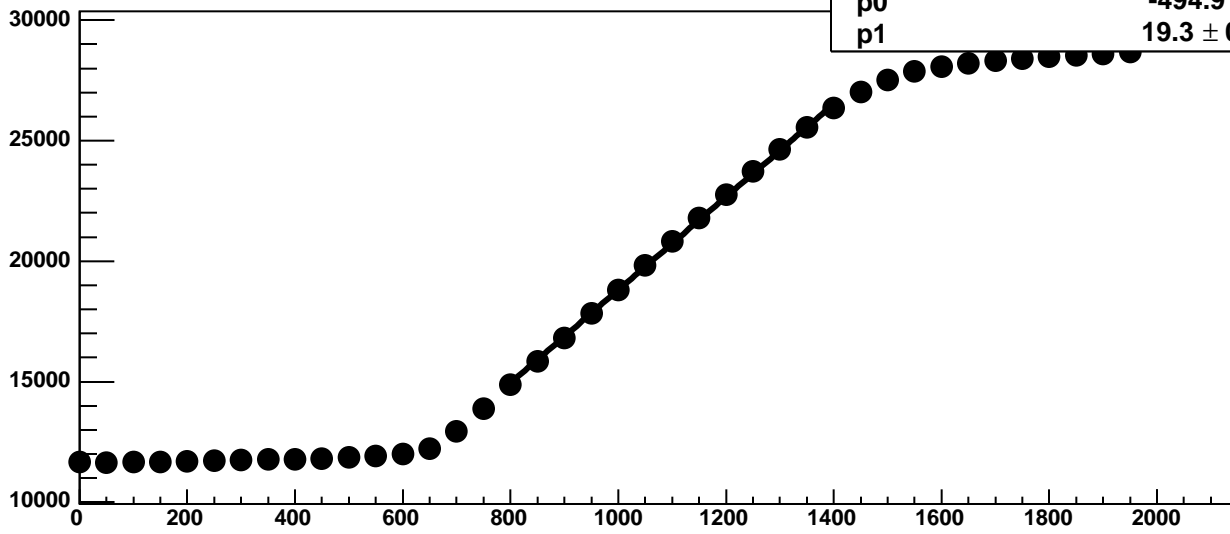
Chip 10, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC



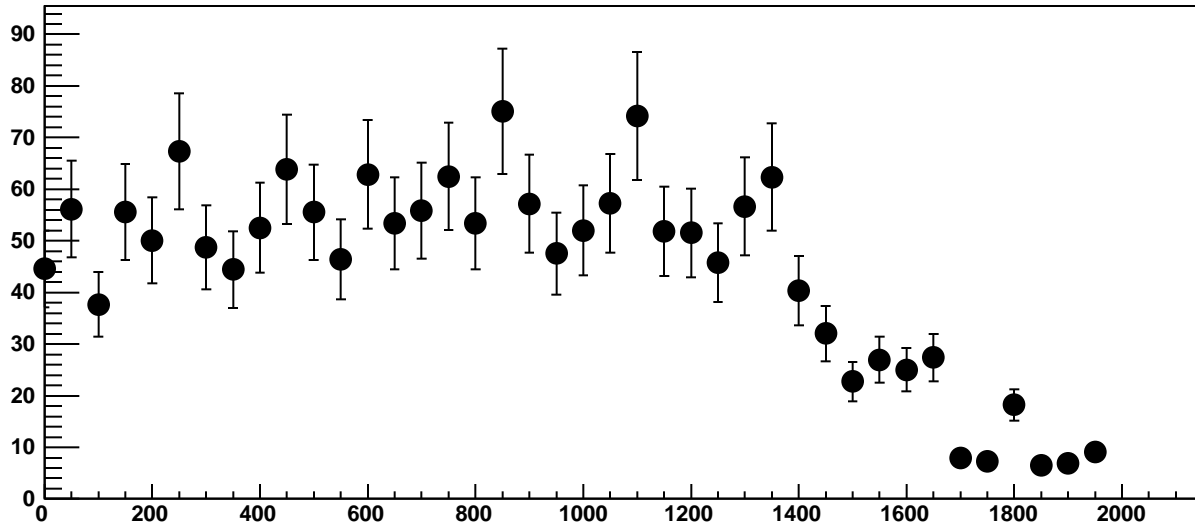
Chip 10, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC



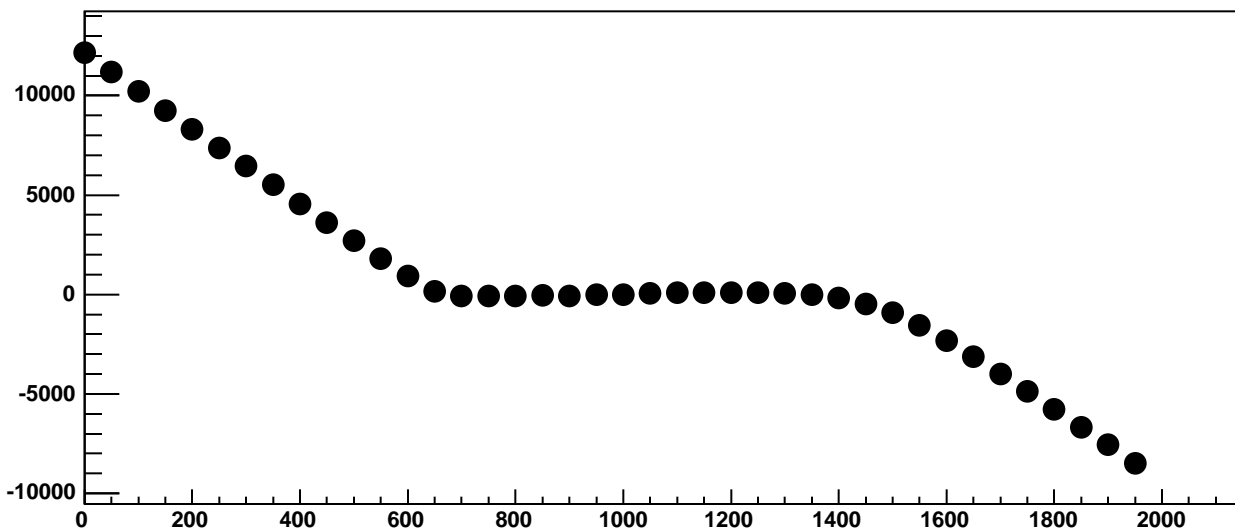
Chip 10, Channel 3, Enable 1!, Hold=30, ADC Mean vs DAC



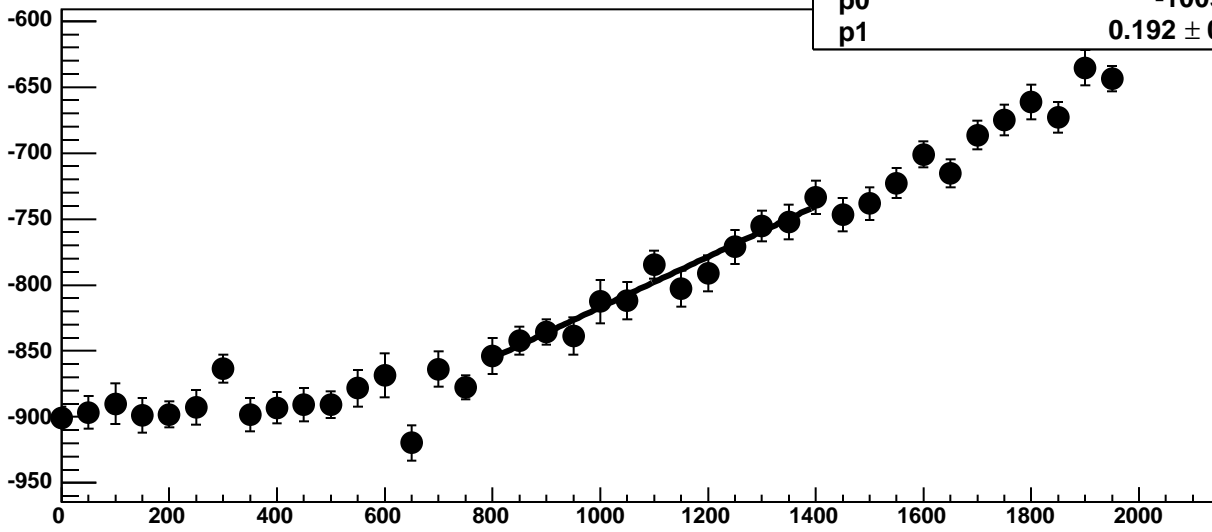
Chip 10, Channel 3, Enable 1!, Hold=30, ADC Noise vs DAC



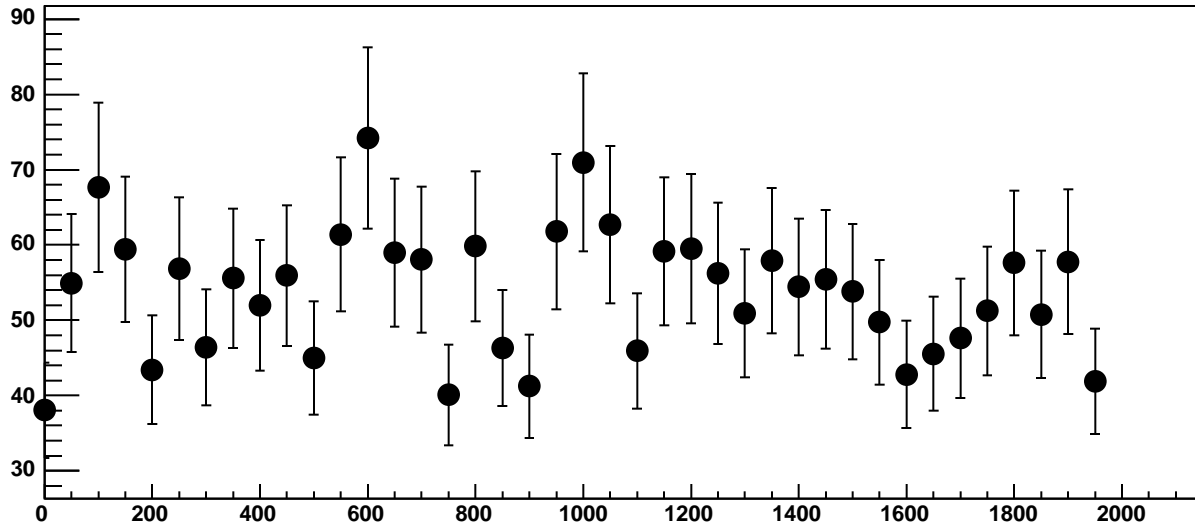
Chip 10, Channel 3, Enable 1!, Hold=30, ADC Residuals vs DAC



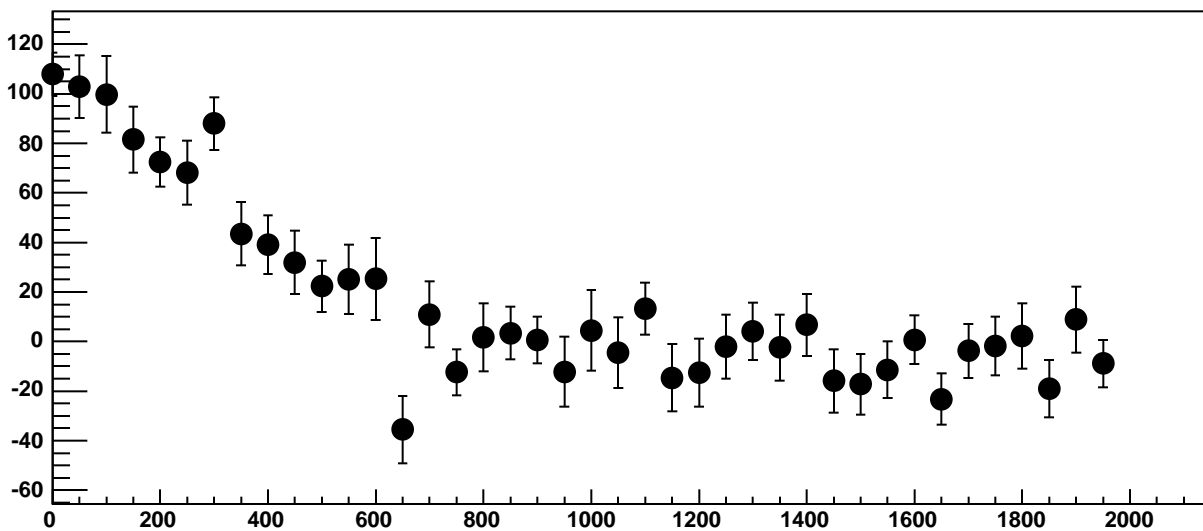
Chip 10, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC



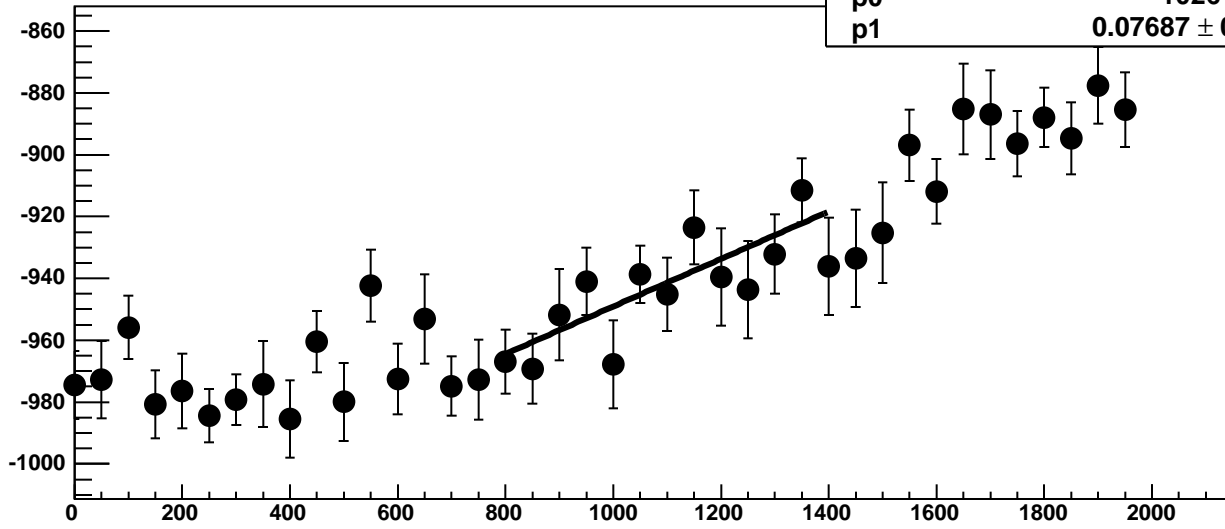
Chip 10, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC



Chip 10, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC

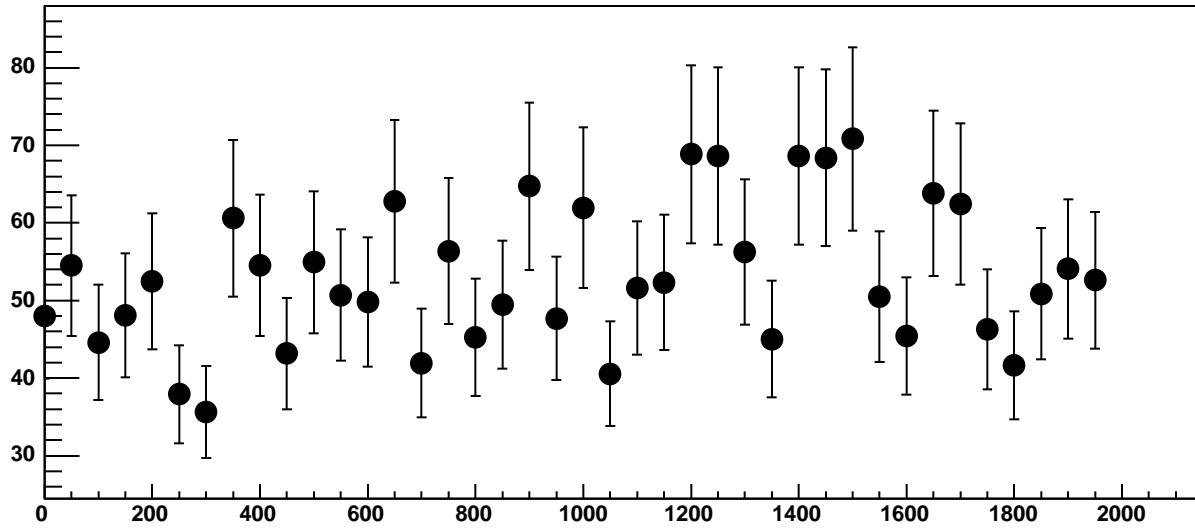


Chip 10, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC

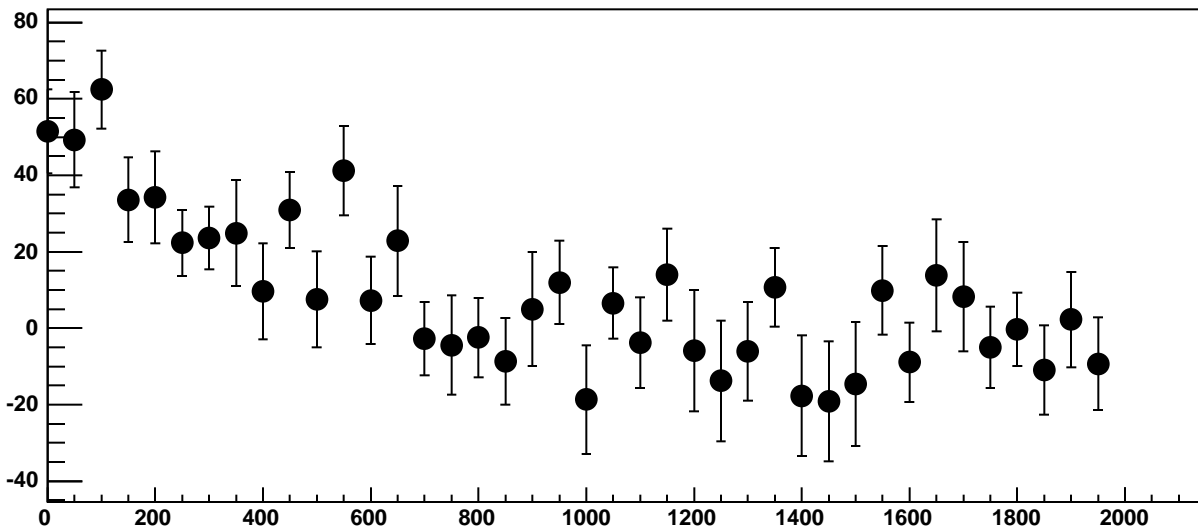


$\chi^2 / \text{ndf}$  9.101 / 11  
p0  $-1026 \pm 19.77$   
p1  $0.07687 \pm 0.01806$

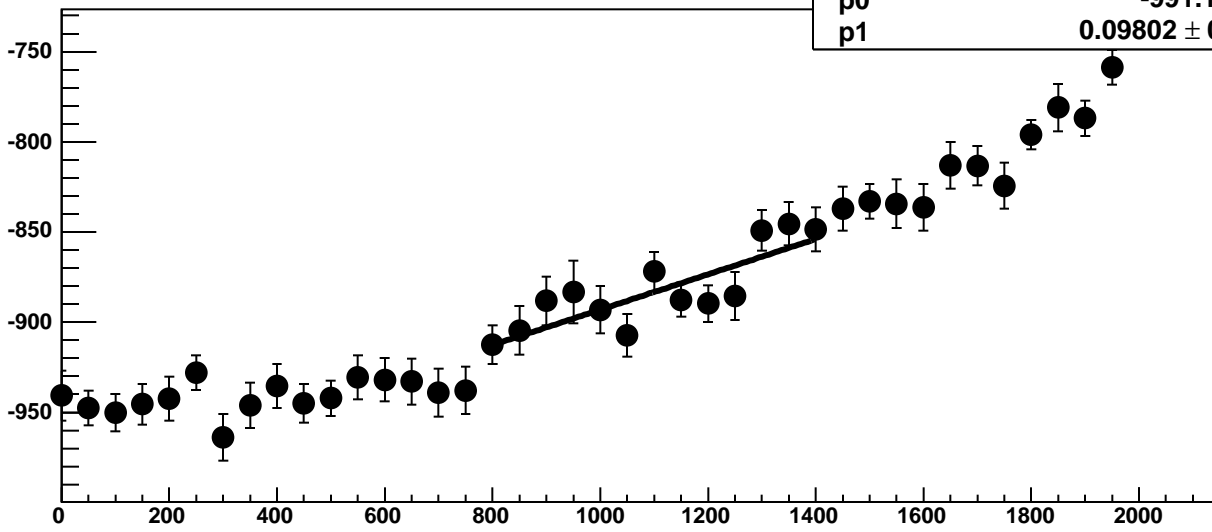
Chip 10, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC



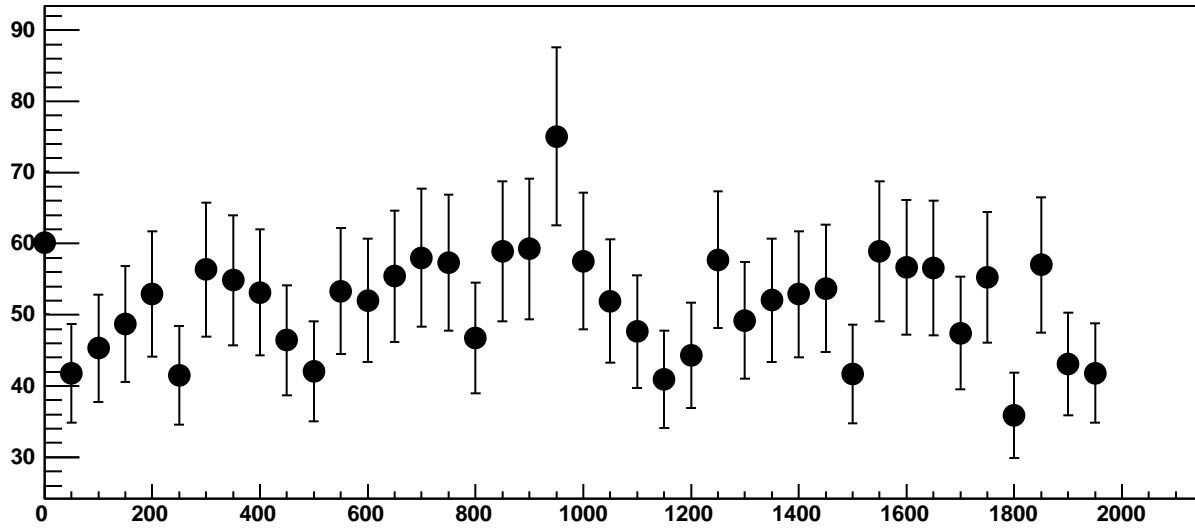
Chip 10, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC



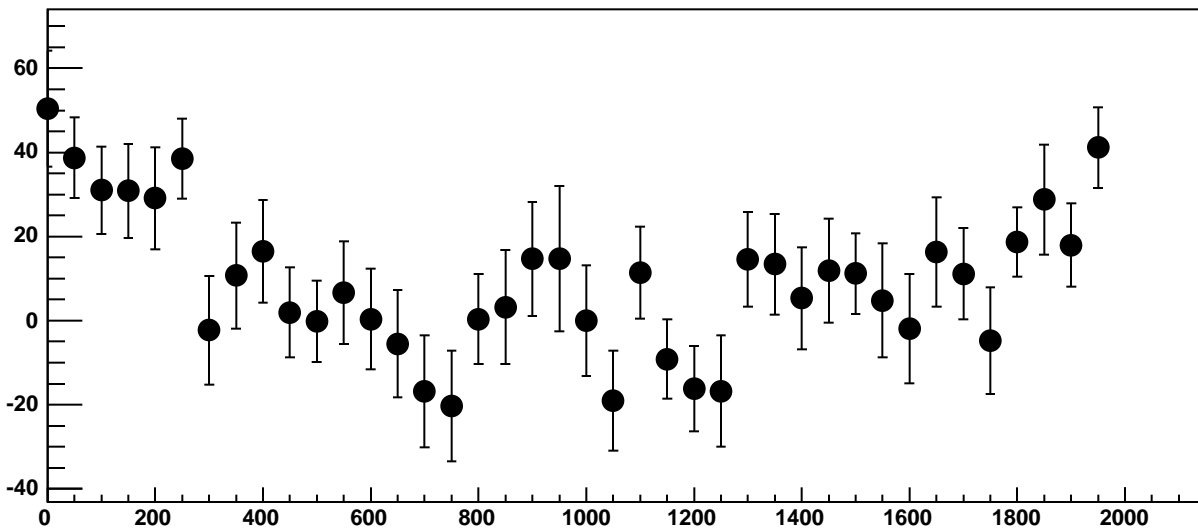
Chip 10, Channel 3, Enable 4, Hold=30, ADC Mean vs DAC



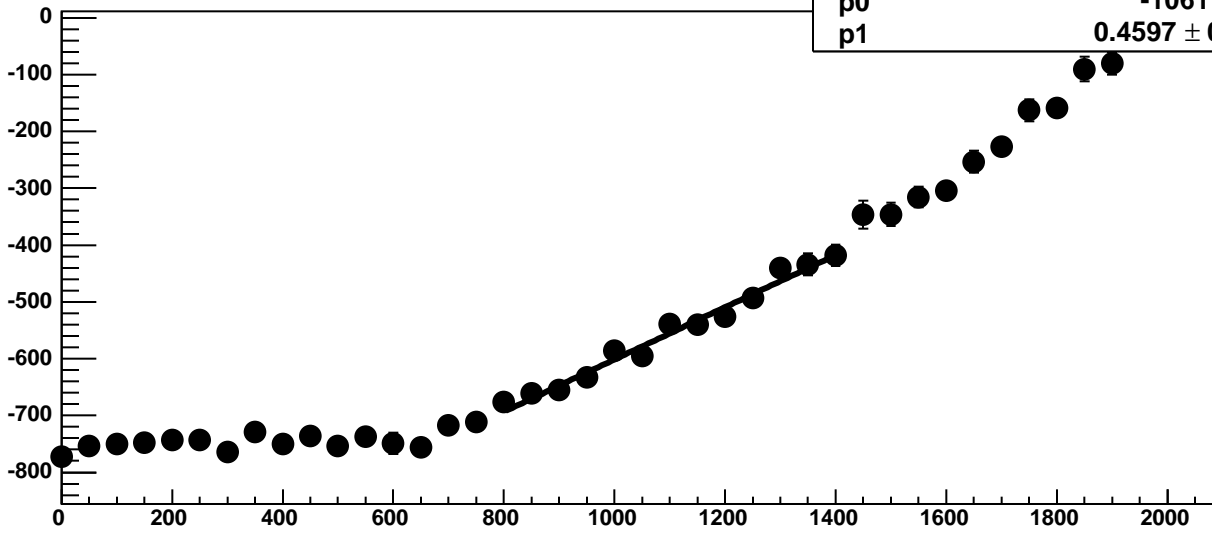
Chip 10, Channel 3, Enable 4, Hold=30, ADC Noise vs DAC



Chip 10, Channel 3, Enable 4, Hold=30, ADC Residuals vs DAC

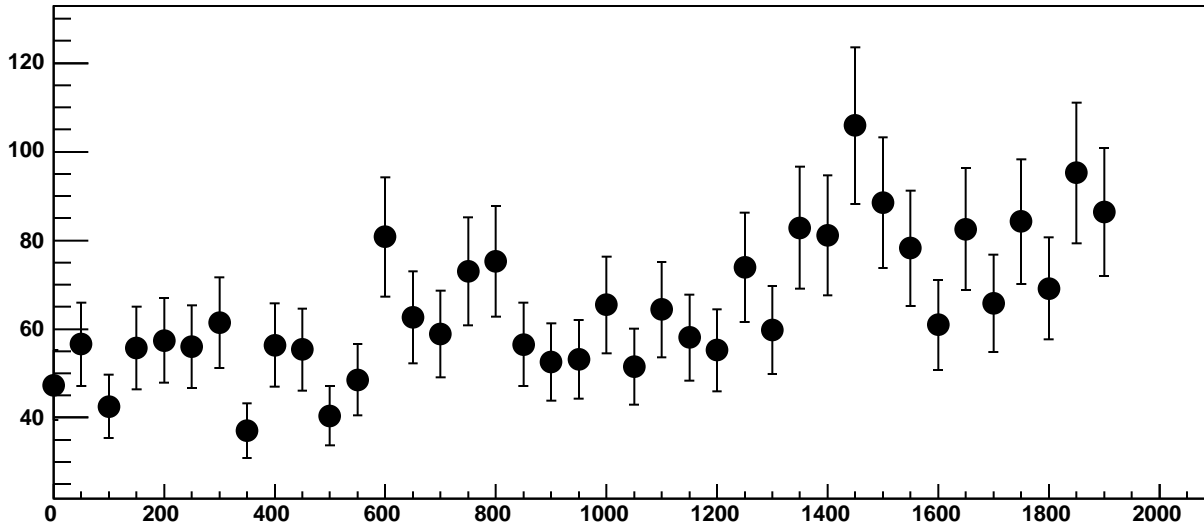


Chip 10, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC

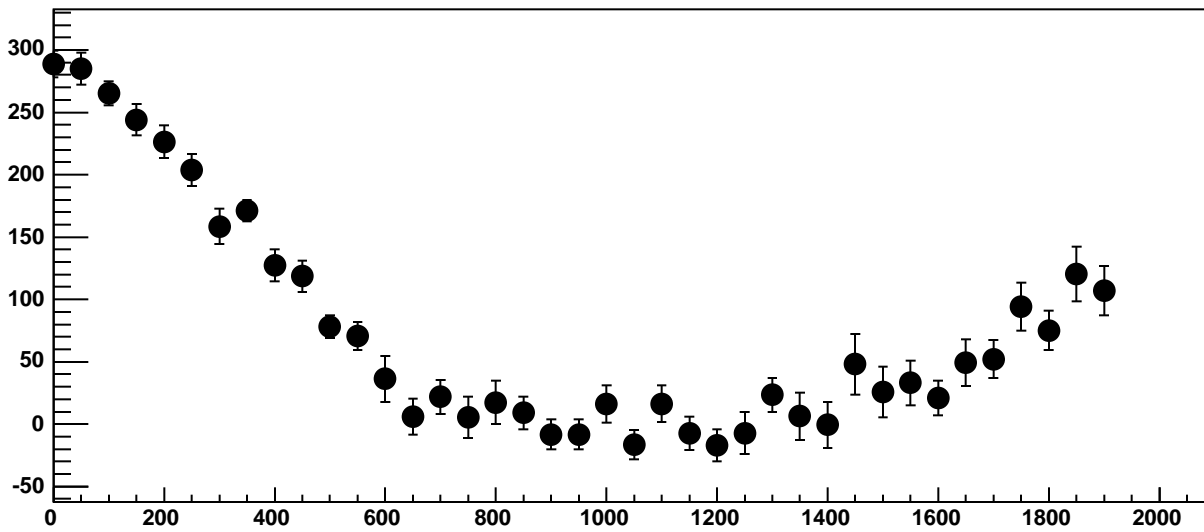


$\chi^2 / \text{ndf}$  12.03 / 11  
p0  $-1061 \pm 24.84$   
p1  $0.4597 \pm 0.02282$

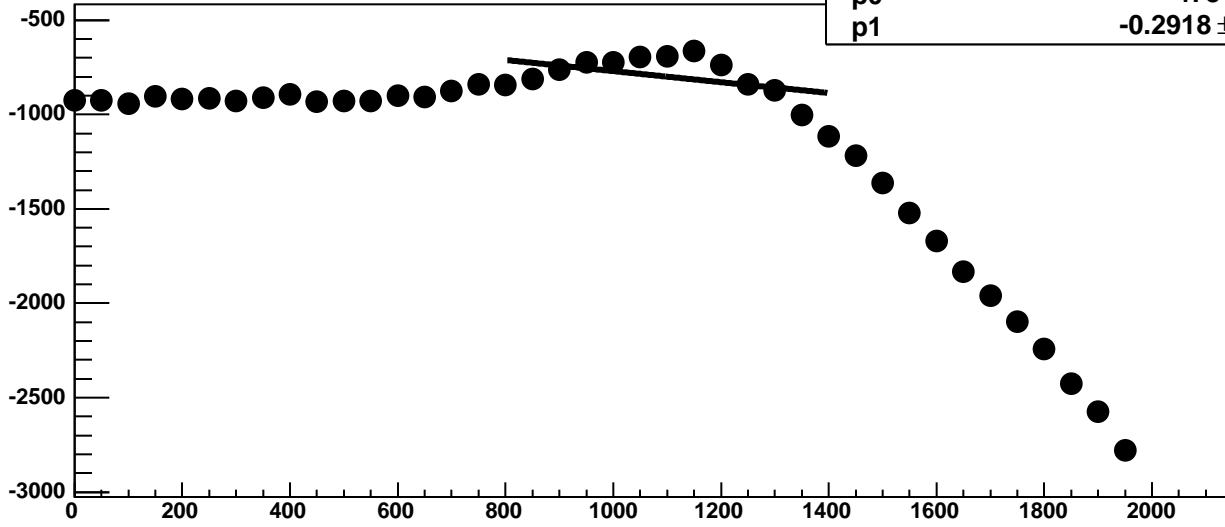
Chip 10, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC



Chip 10, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 10, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

254 / 11

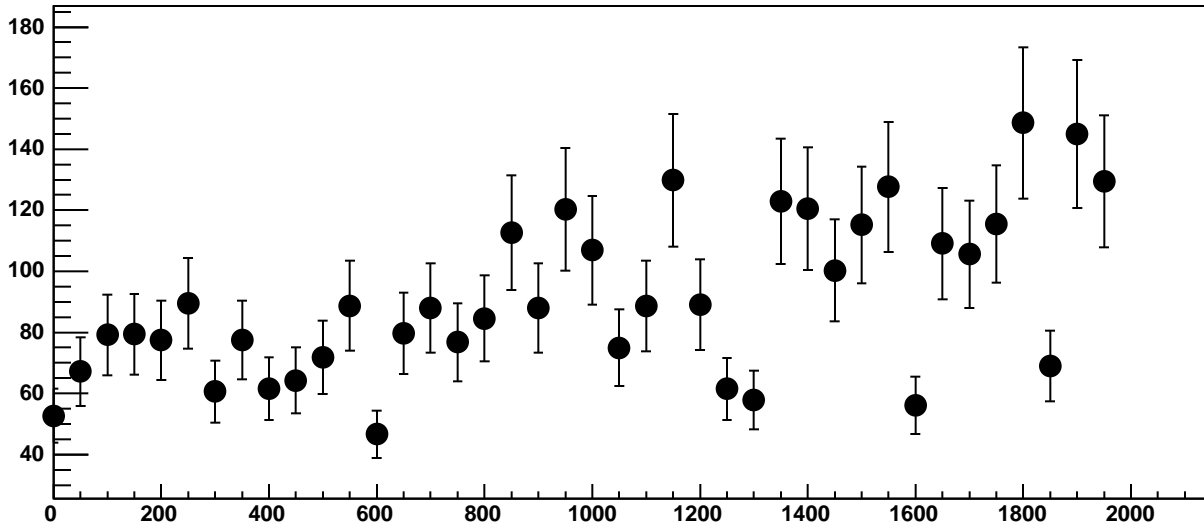
p0

$-478 \pm 35.32$

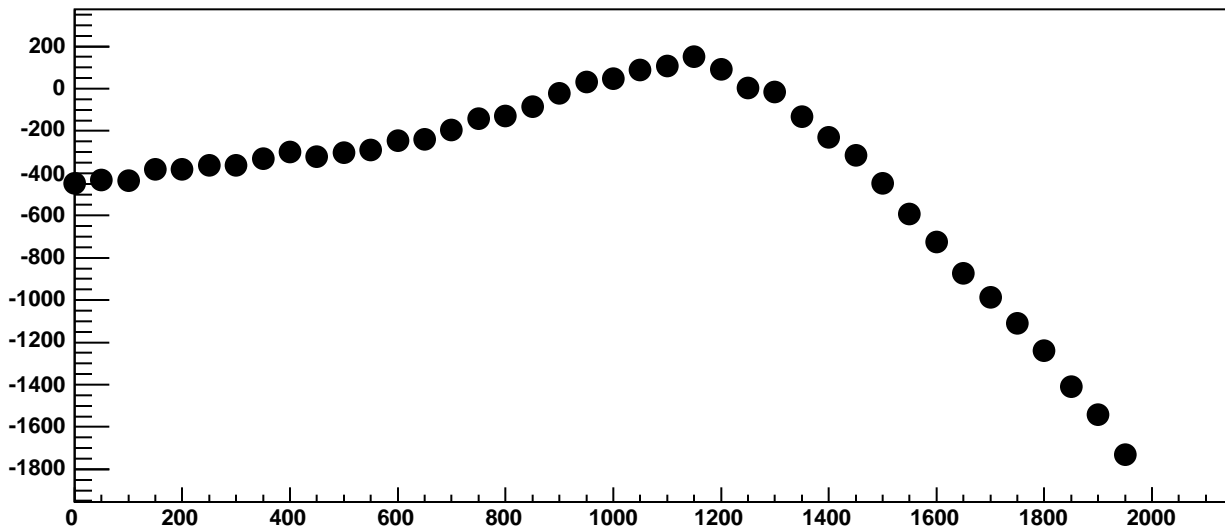
p1

$-0.2918 \pm 0.0311$

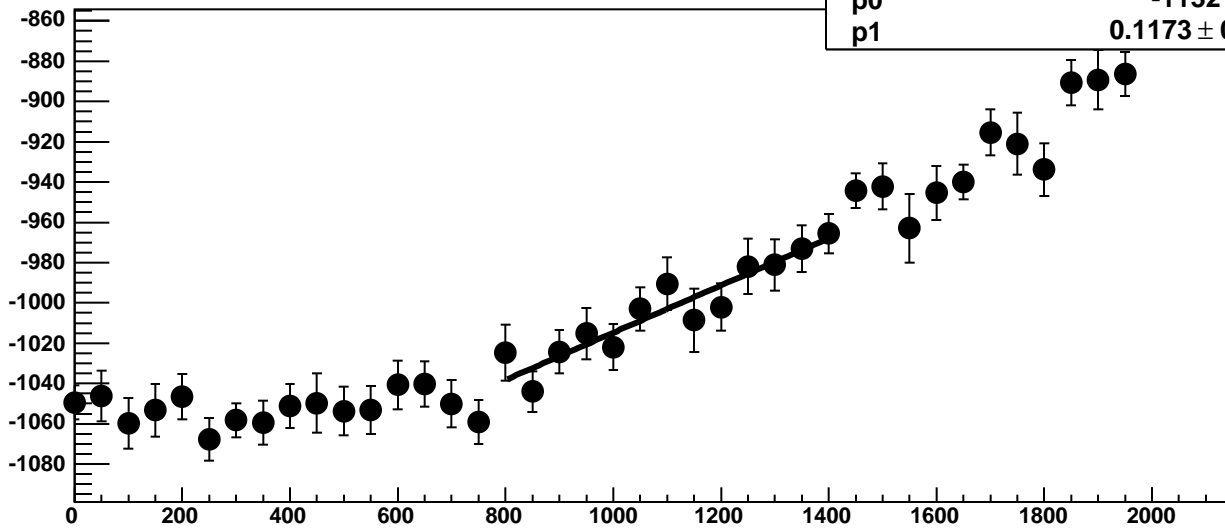
Chip 10, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC



Chip 10, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC

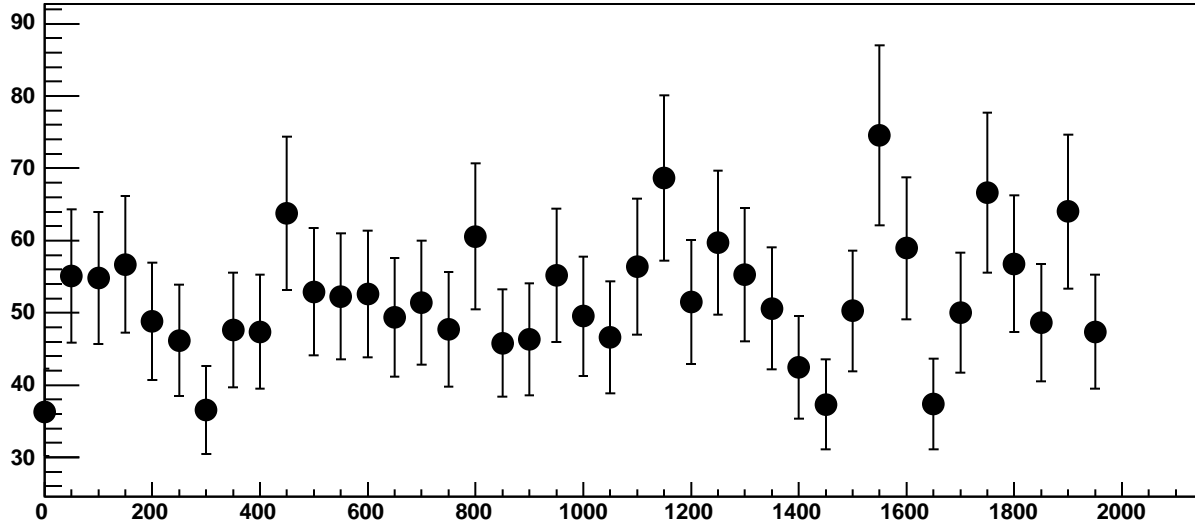


Chip 10, Channel 4, Enable 1, Hold=30, ADC Mean vs DAC

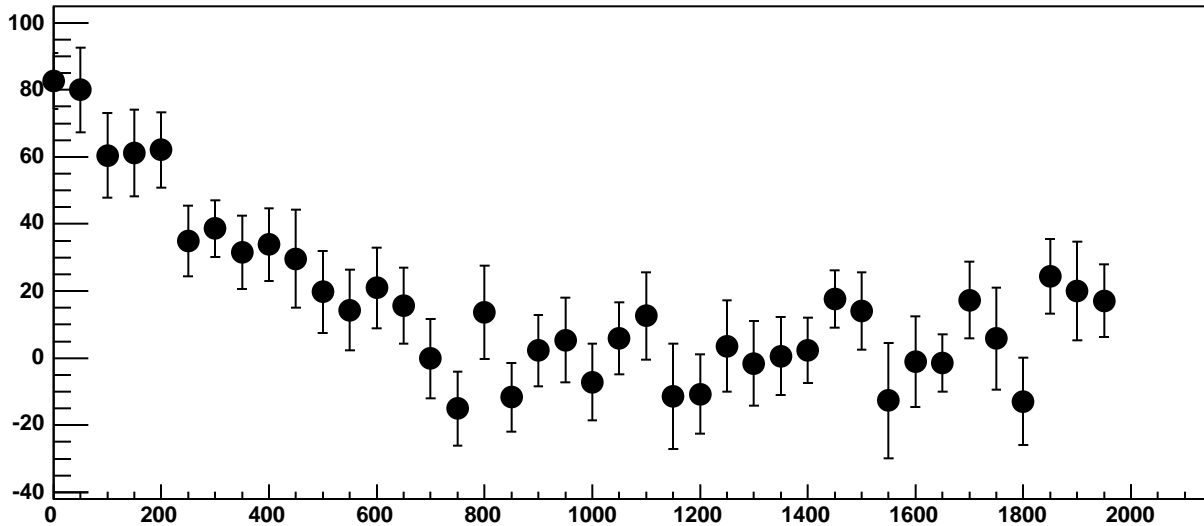


$\chi^2 / \text{ndf}$  5.632 / 11  
p0 -1132 ± 18.86  
p1 0.1173 ± 0.01686

Chip 10, Channel 4, Enable 1, Hold=30, ADC Noise vs DAC

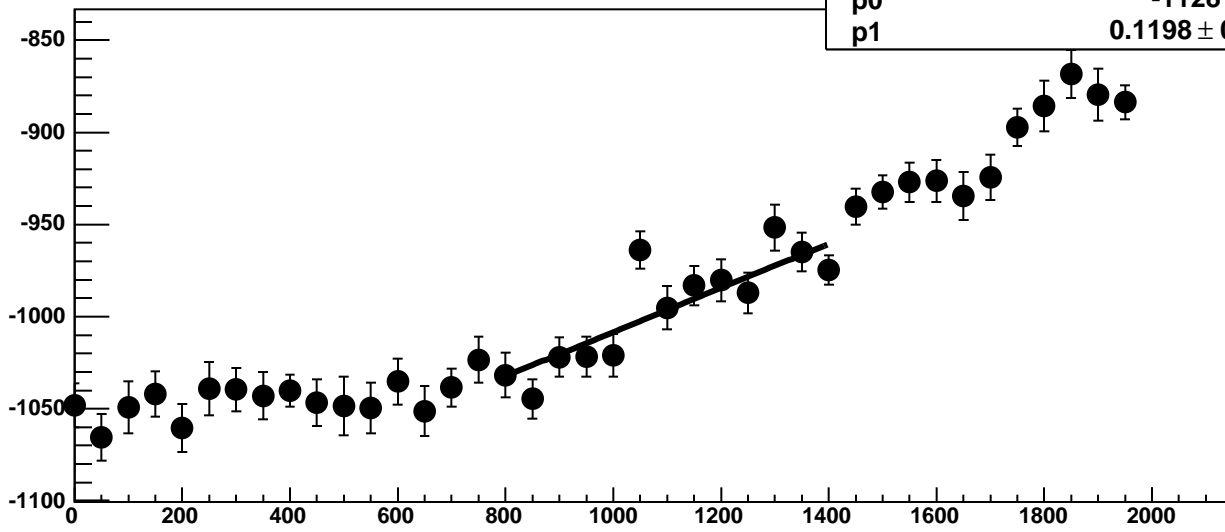


Chip 10, Channel 4, Enable 1, Hold=30, ADC Residuals vs DAC

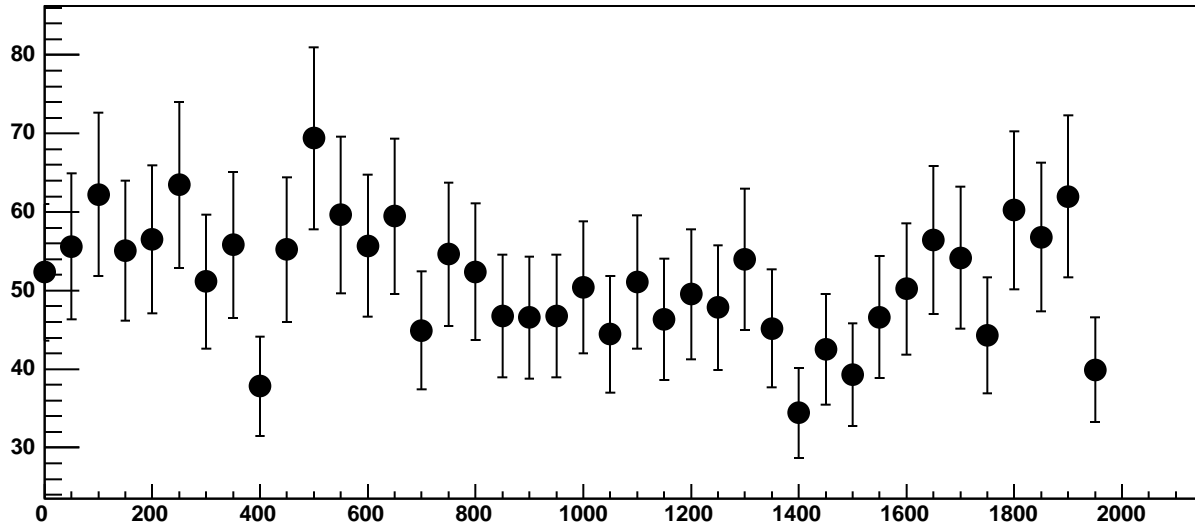




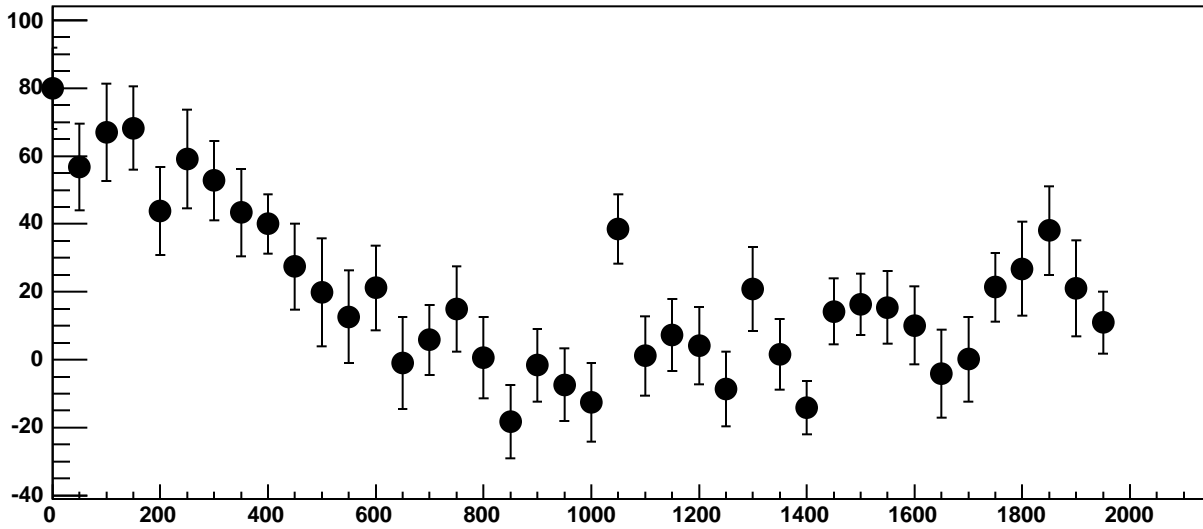
Chip 10, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC



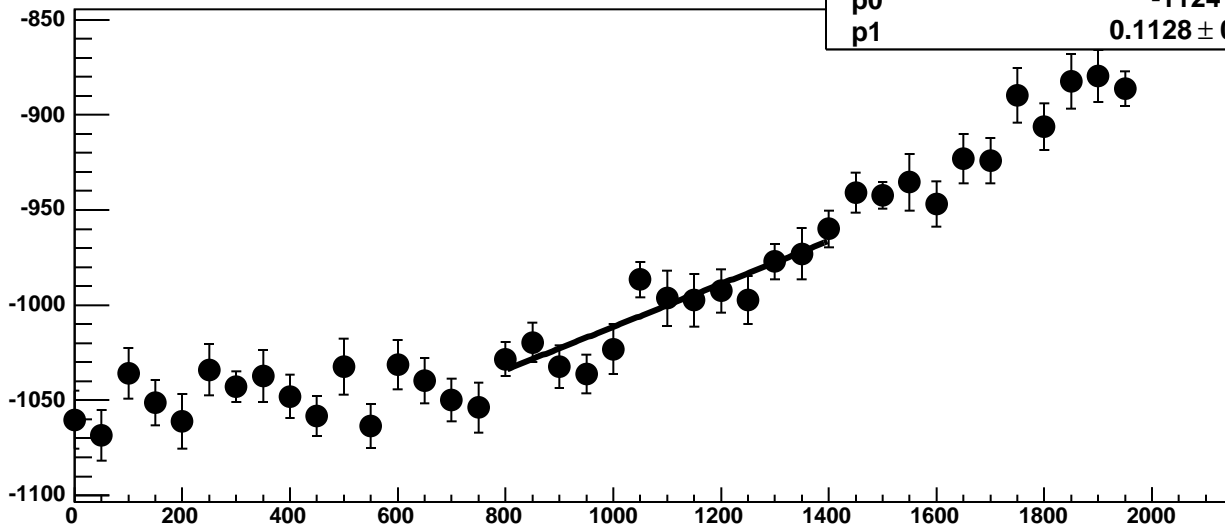
Chip 10, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC



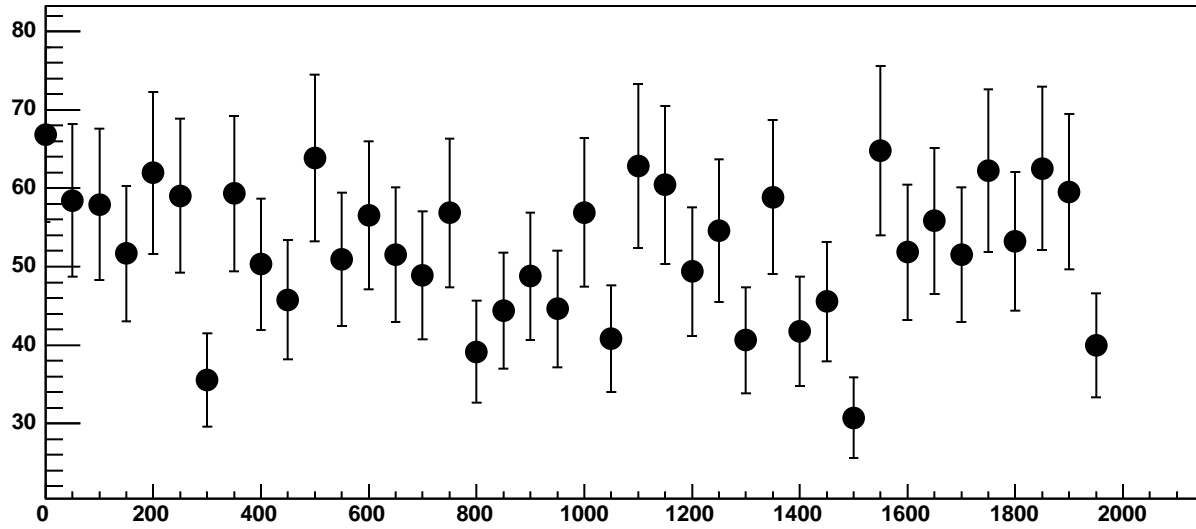
Chip 10, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC



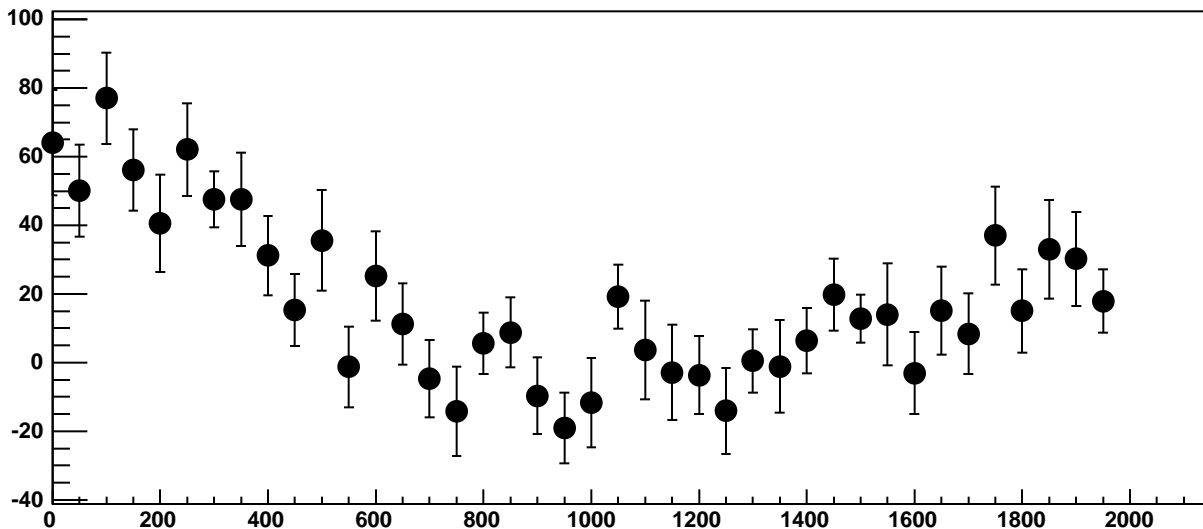
Chip 10, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC



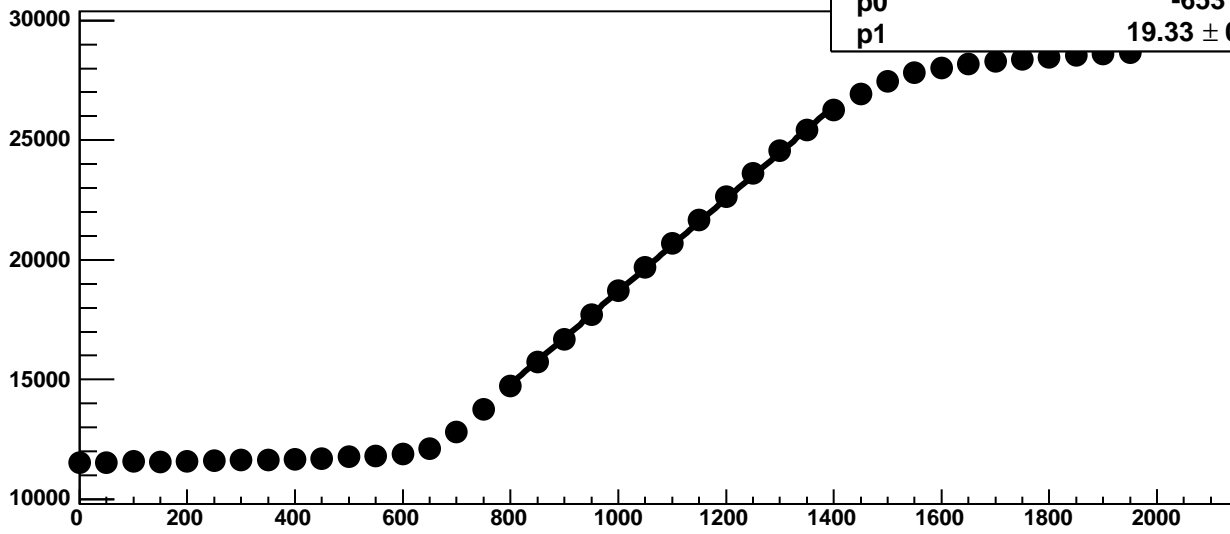
Chip 10, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC



Chip 10, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC

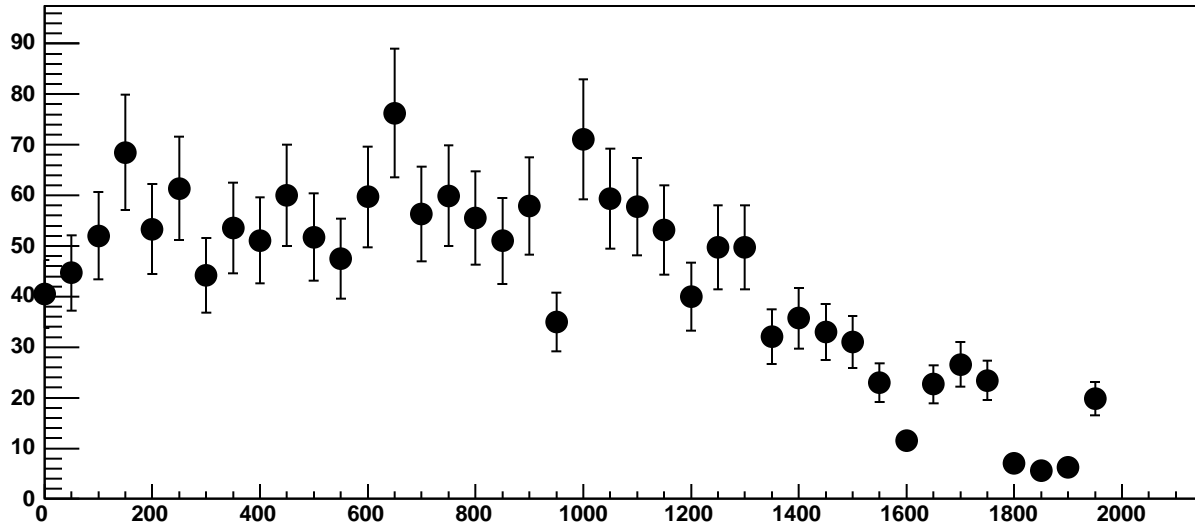


Chip 10, Channel 4, Enable 4!, Hold=30, ADC Mean vs DAC

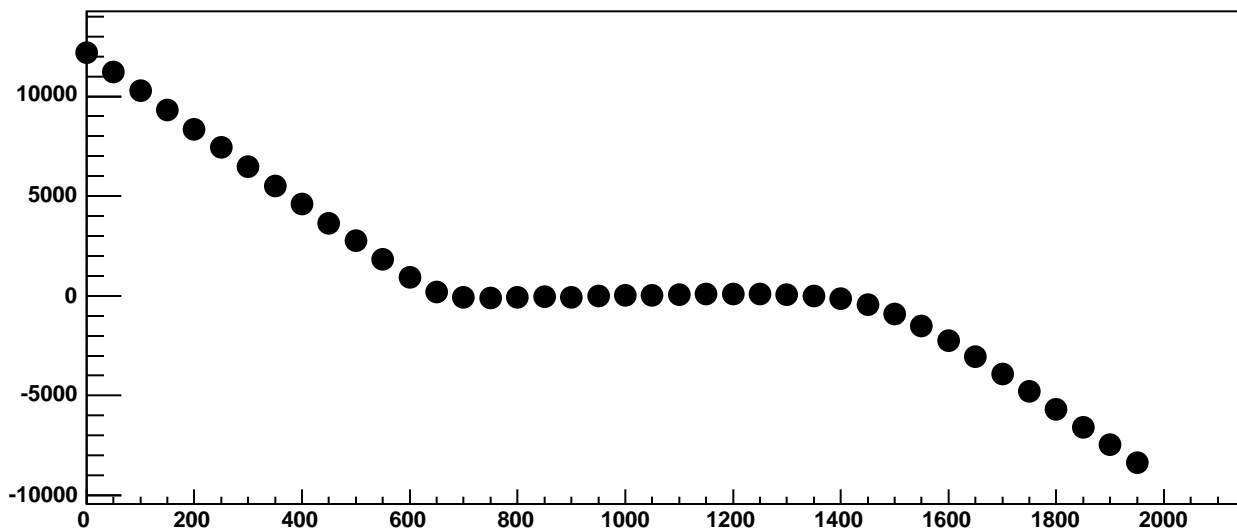


$\chi^2 / \text{ndf}$  756.9 / 11  
p0  $-653 \pm 17.37$   
p1  $19.33 \pm 0.01493$

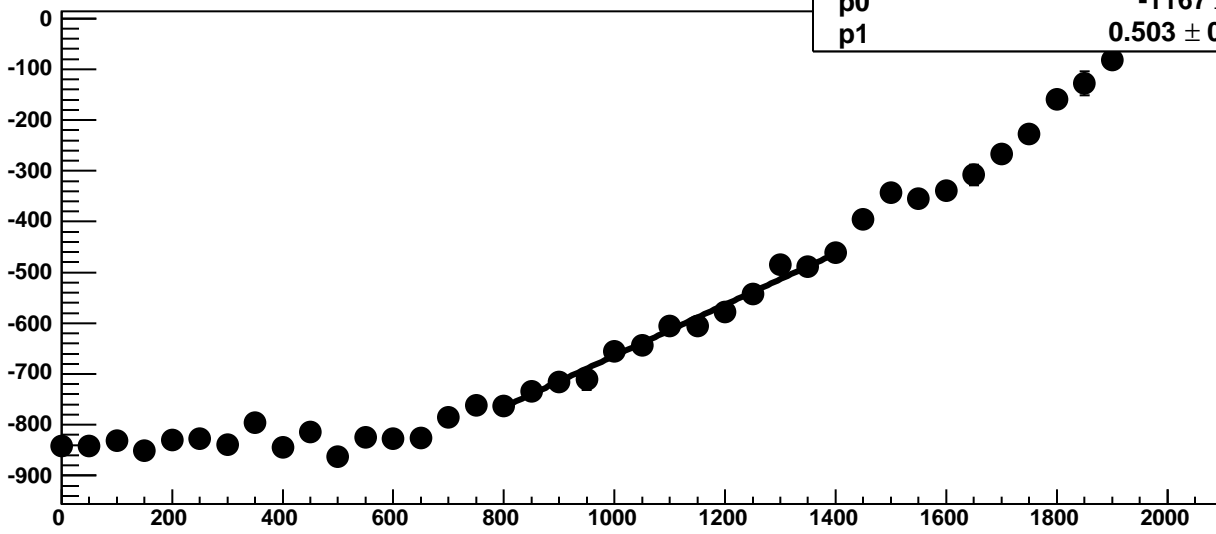
Chip 10, Channel 4, Enable 4!, Hold=30, ADC Noise vs DAC



Chip 10, Channel 4, Enable 4!, Hold=30, ADC Residuals vs DAC

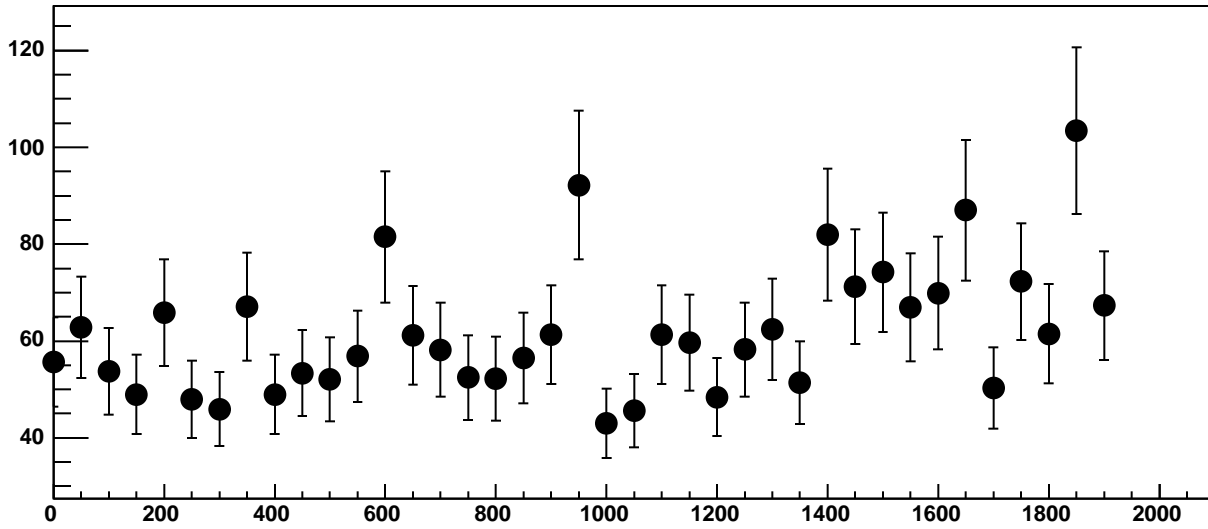


Chip 10, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC

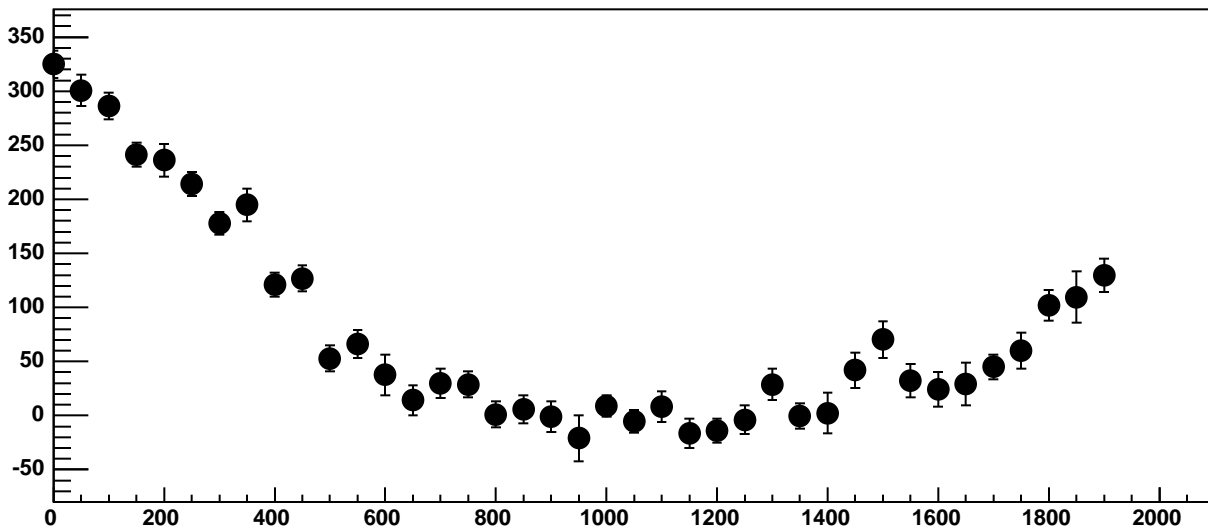


$\chi^2 / \text{ndf}$  9.705 / 11  
p0  $-1167 \pm 22.12$   
p1  $0.503 \pm 0.02005$

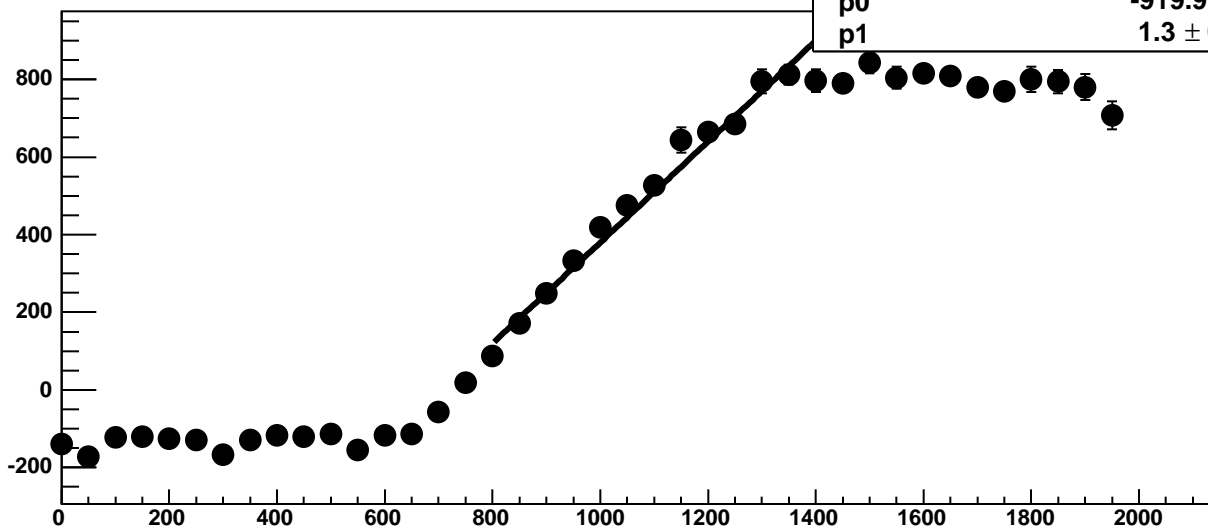
Chip 10, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC



Chip 10, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC

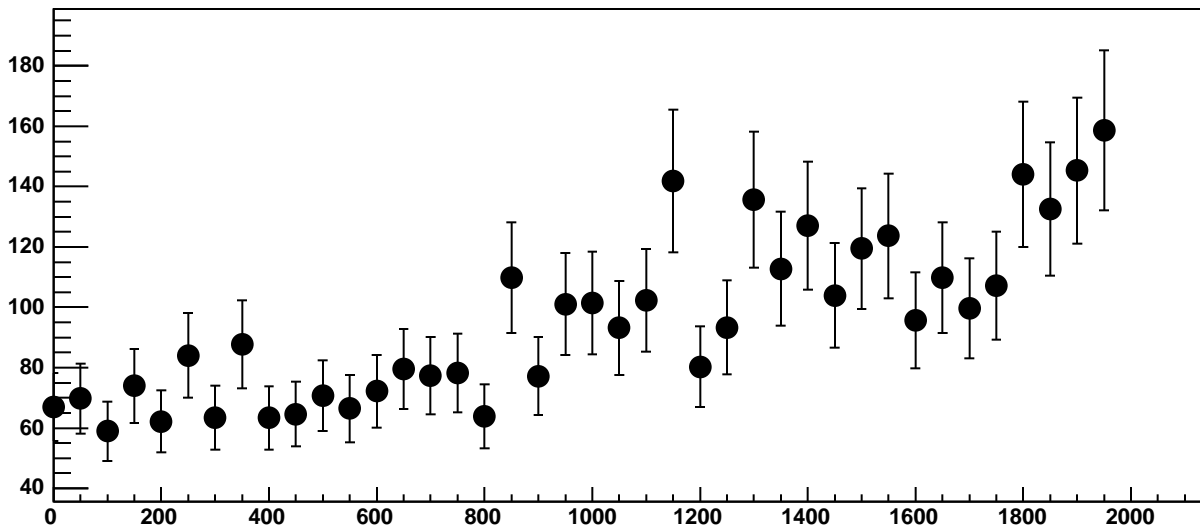


Chip 10, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC

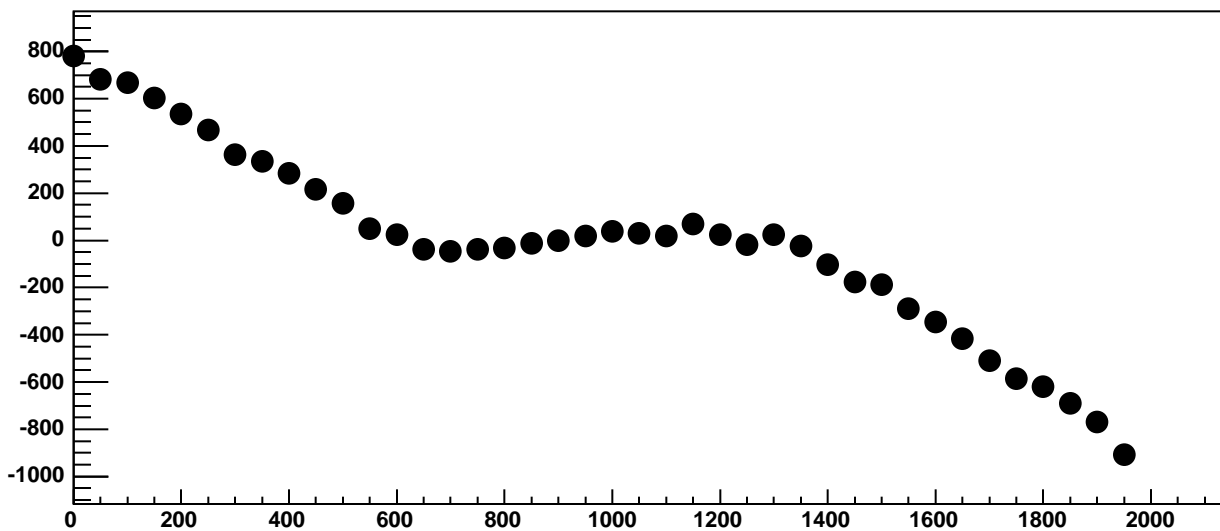


$\chi^2 / \text{ndf}$  32.04 / 11  
p0  $-919.9 \pm 34.38$   
p1  $1.3 \pm 0.03229$

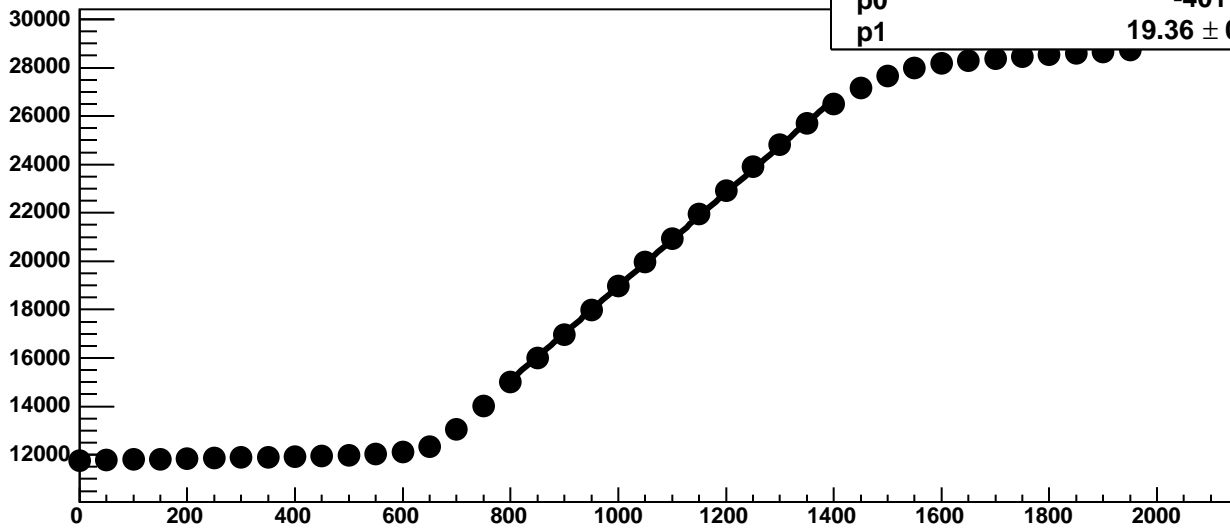
Chip 10, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC



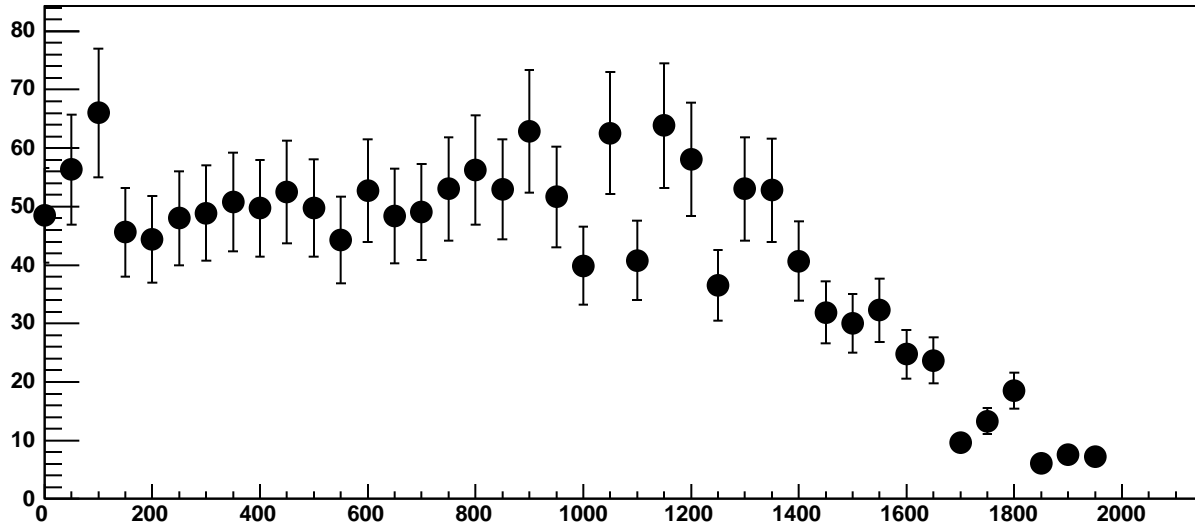
Chip 10, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC



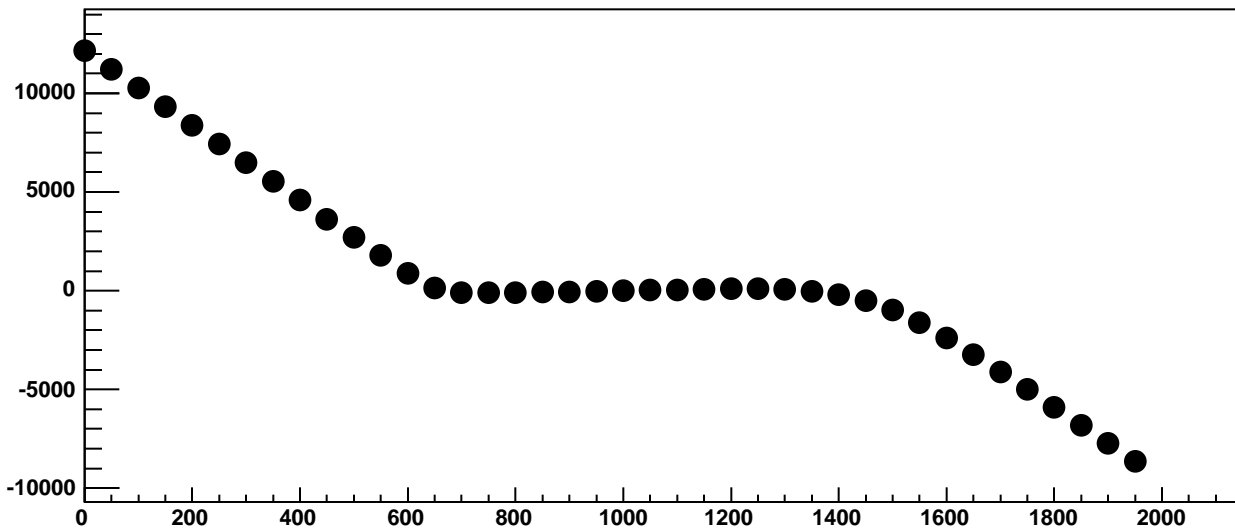
Chip 10, Channel 5, Enable 1!, Hold=30, ADC Mean vs DAC



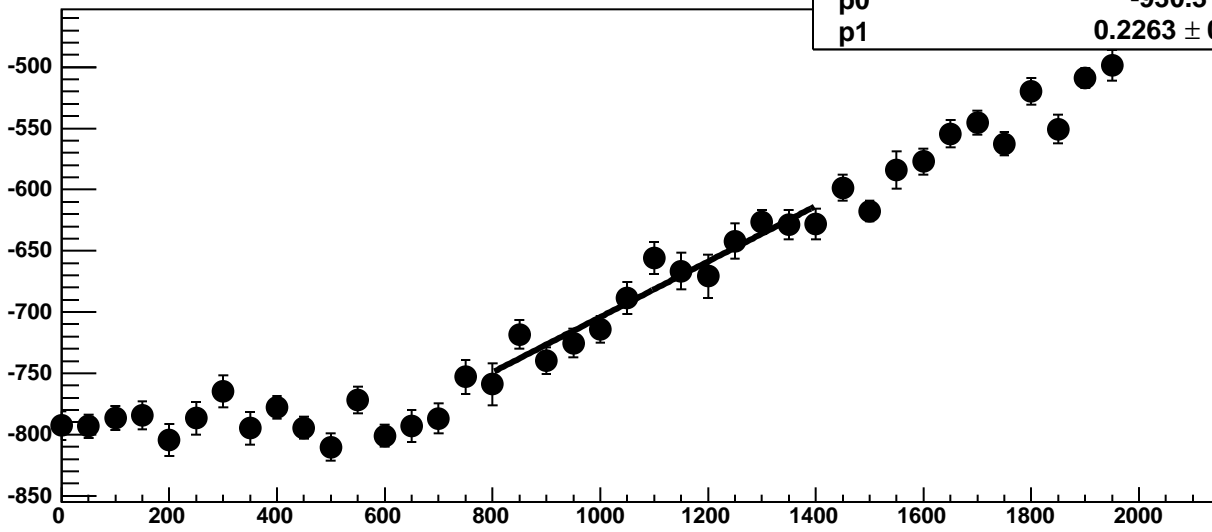
Chip 10, Channel 5, Enable 1!, Hold=30, ADC Noise vs DAC



Chip 10, Channel 5, Enable 1!, Hold=30, ADC Residuals vs DAC

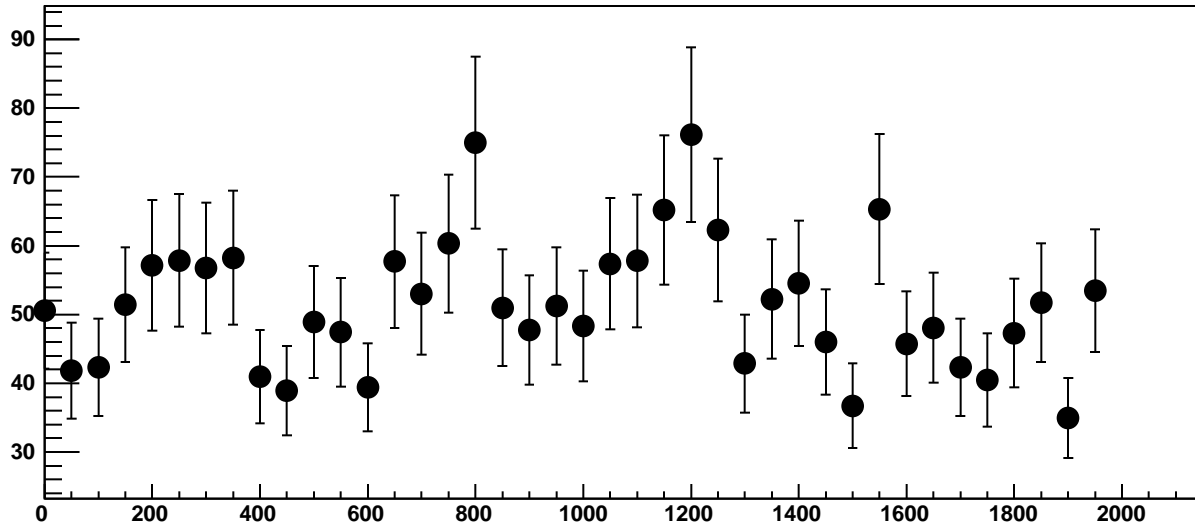


Chip 10, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC

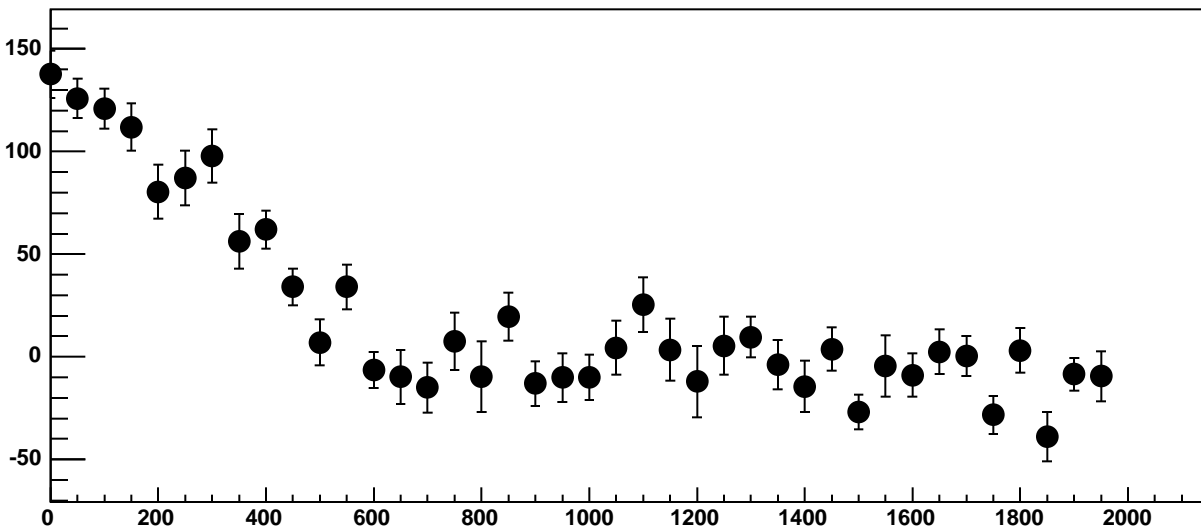


$\chi^2 / \text{ndf}$  12.96 / 11  
p0  $-930.3 \pm 20.69$   
p1  $0.2263 \pm 0.01848$

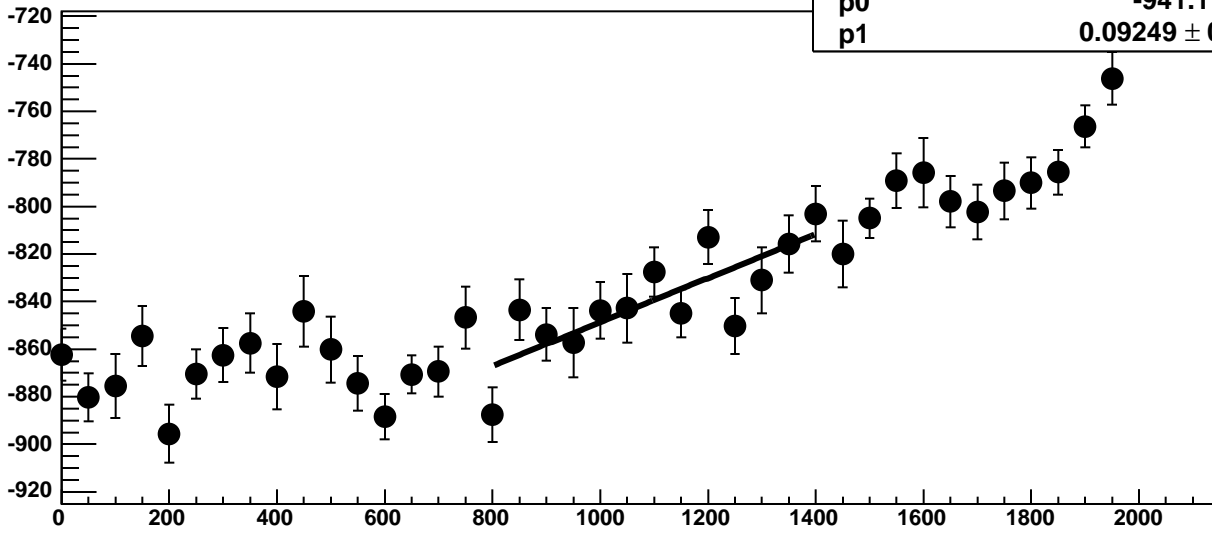
Chip 10, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC



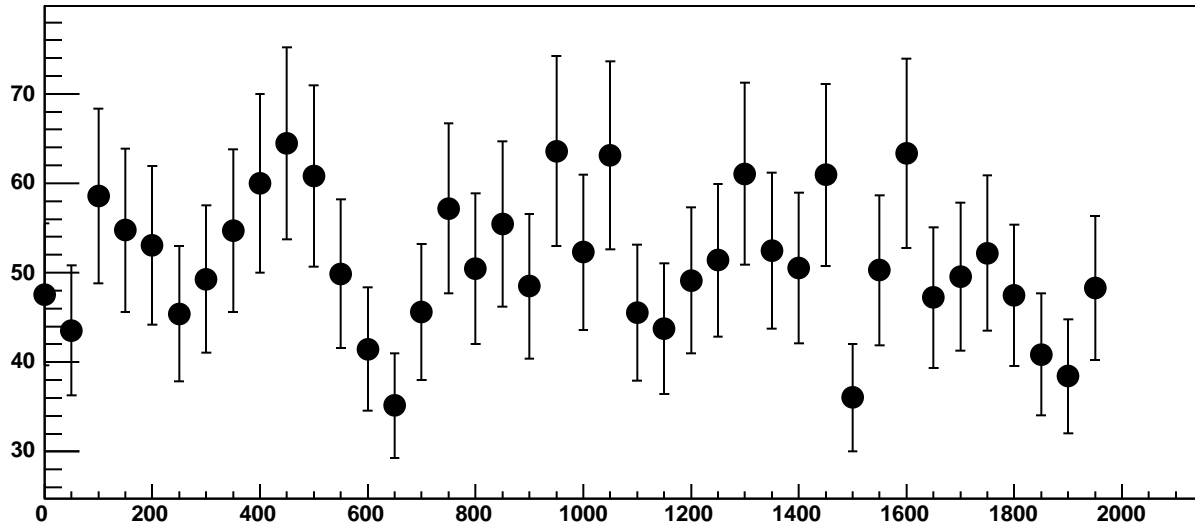
Chip 10, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC



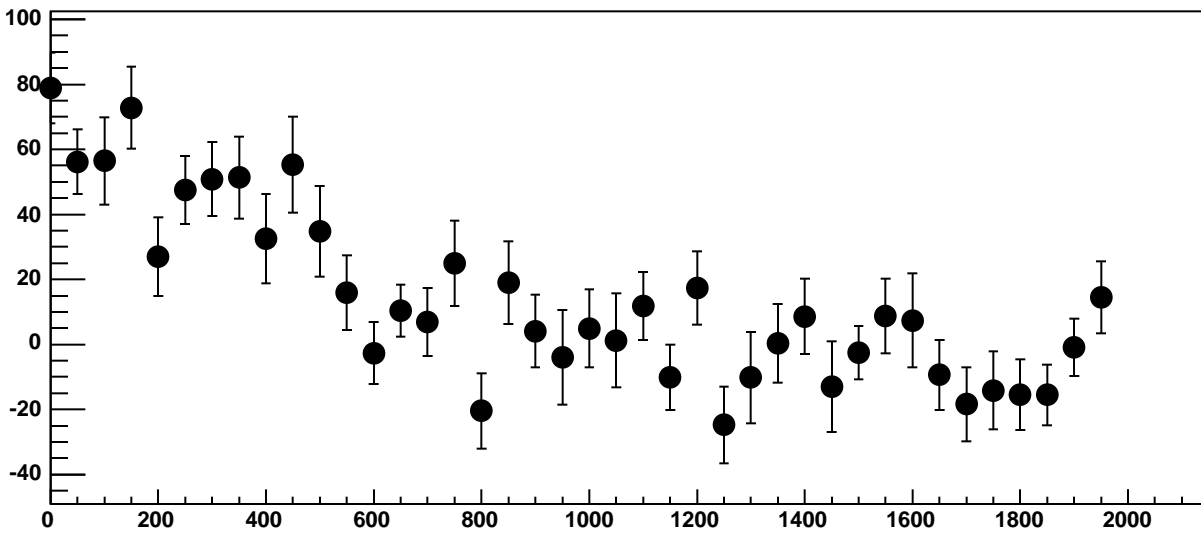
Chip 10, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC



Chip 10, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC

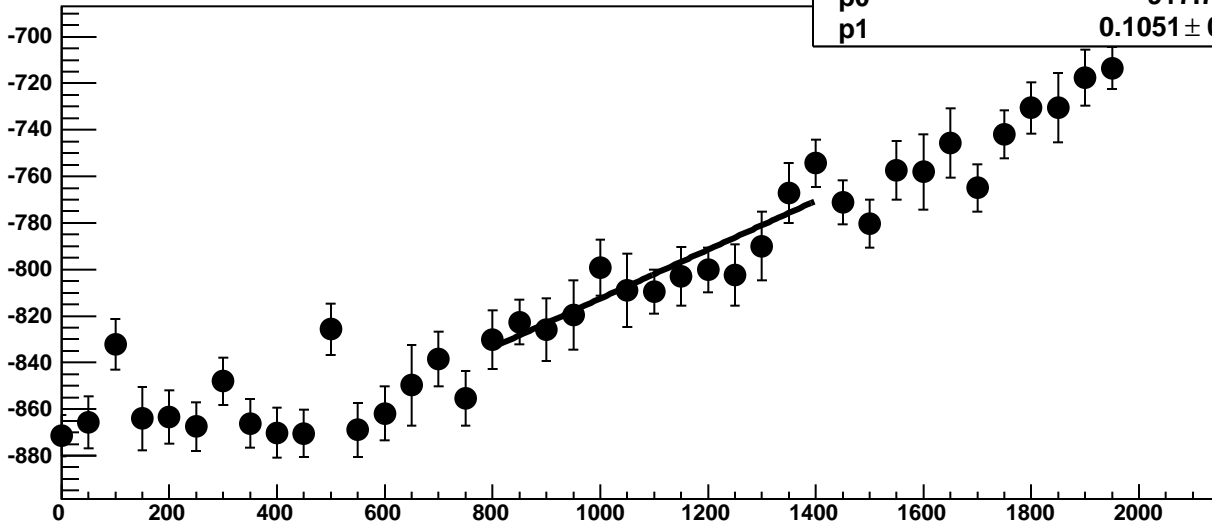


Chip 10, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC

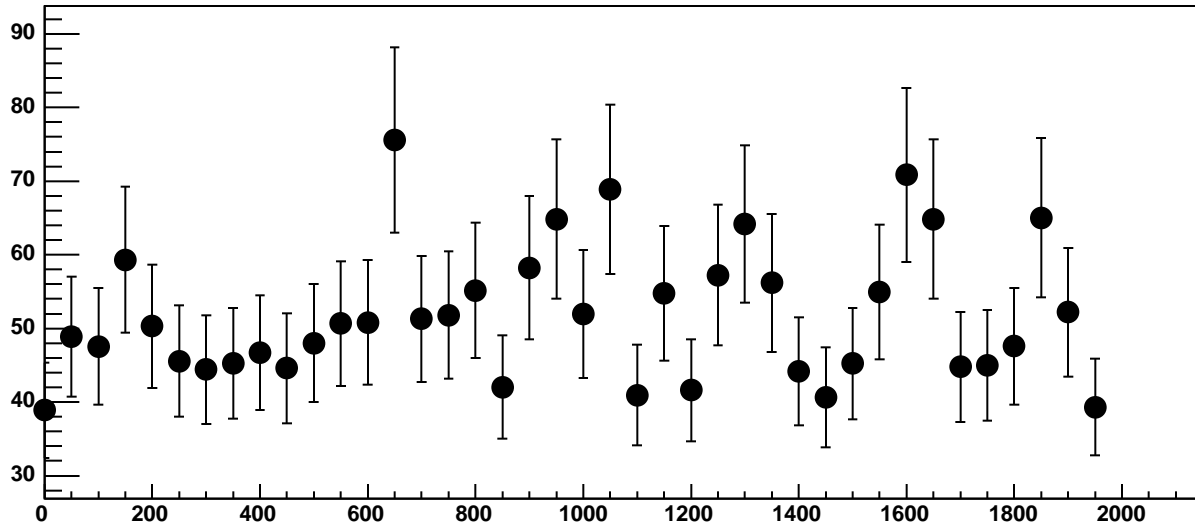




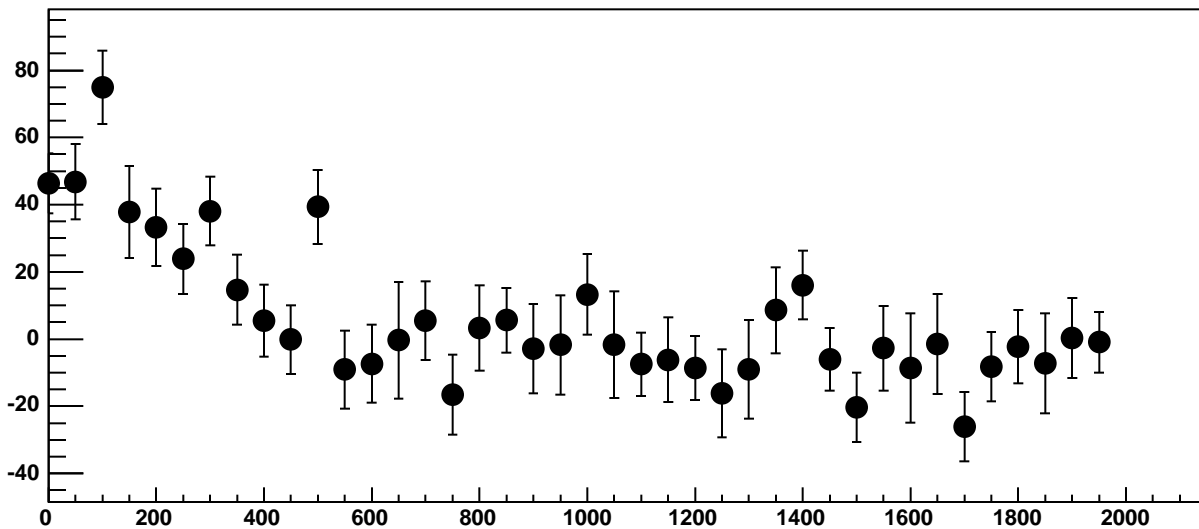
Chip 10, Channel 5, Enable 4, Hold=30, ADC Mean vs DAC



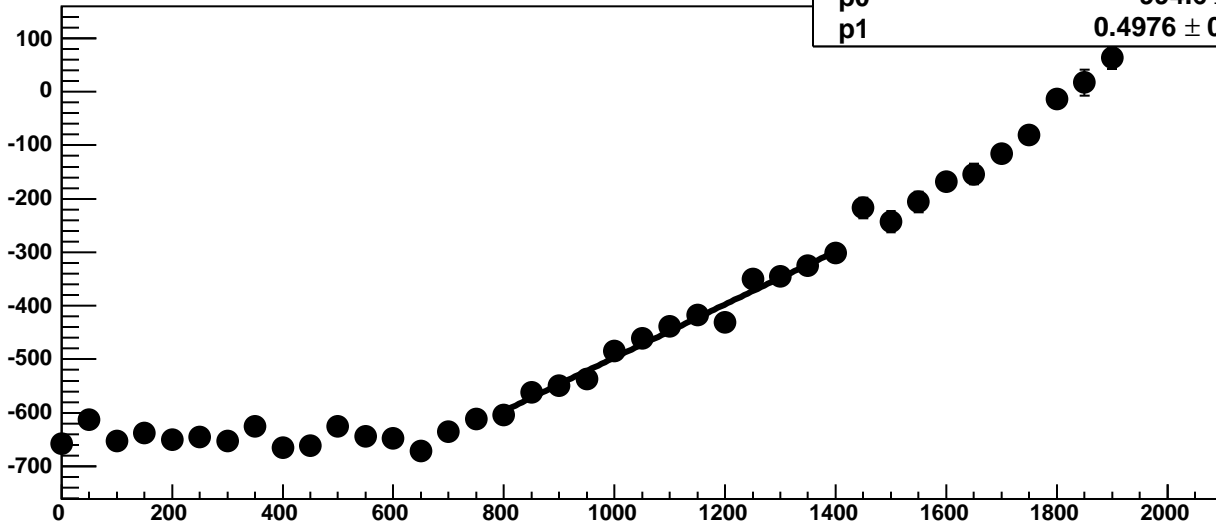
Chip 10, Channel 5, Enable 4, Hold=30, ADC Noise vs DAC



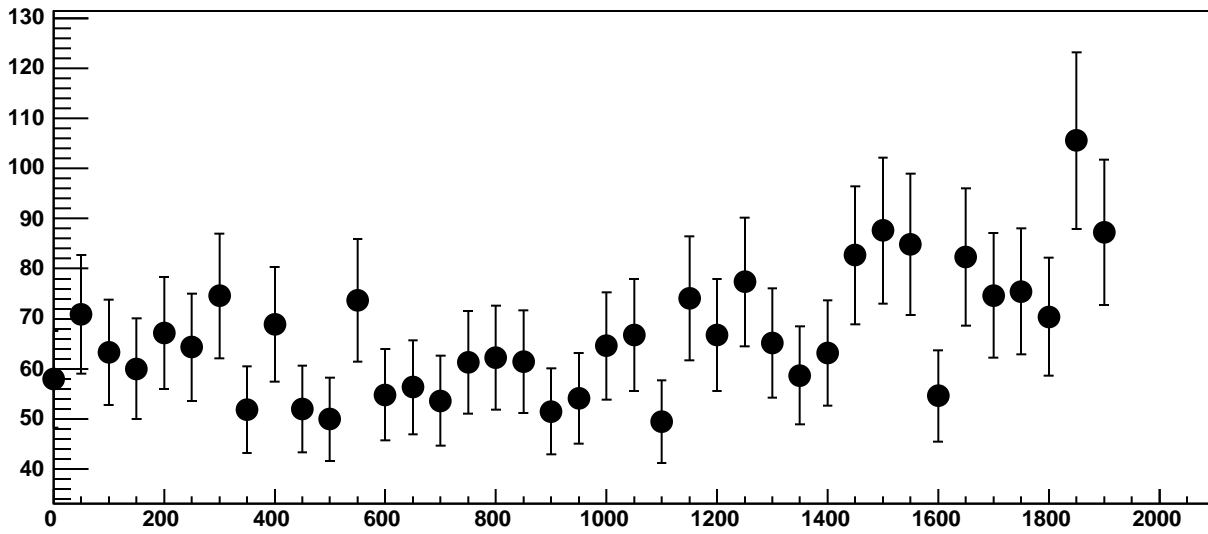
Chip 10, Channel 5, Enable 4, Hold=30, ADC Residuals vs DAC



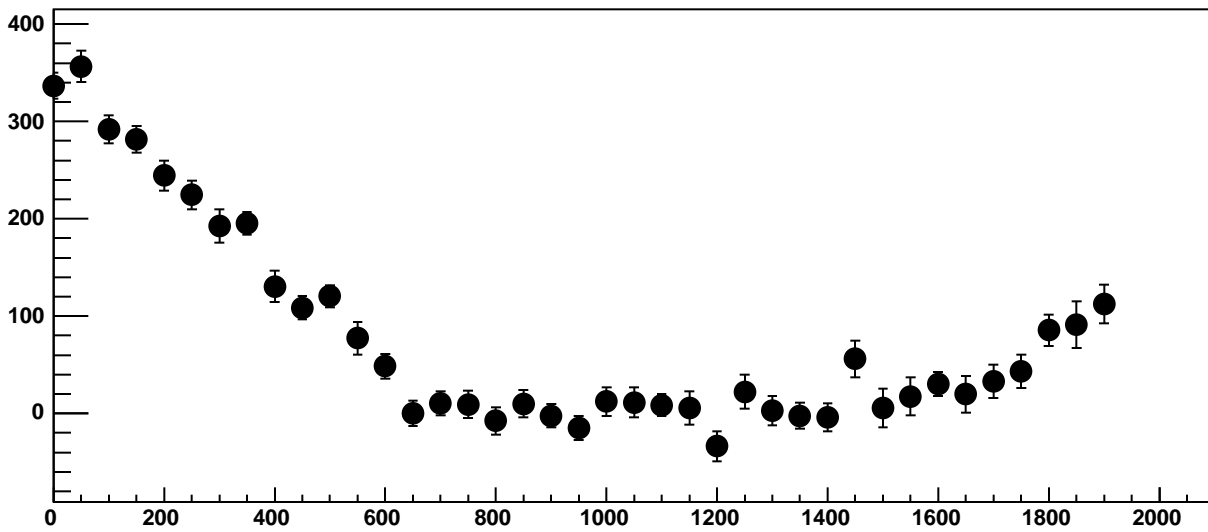
Chip 10, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC



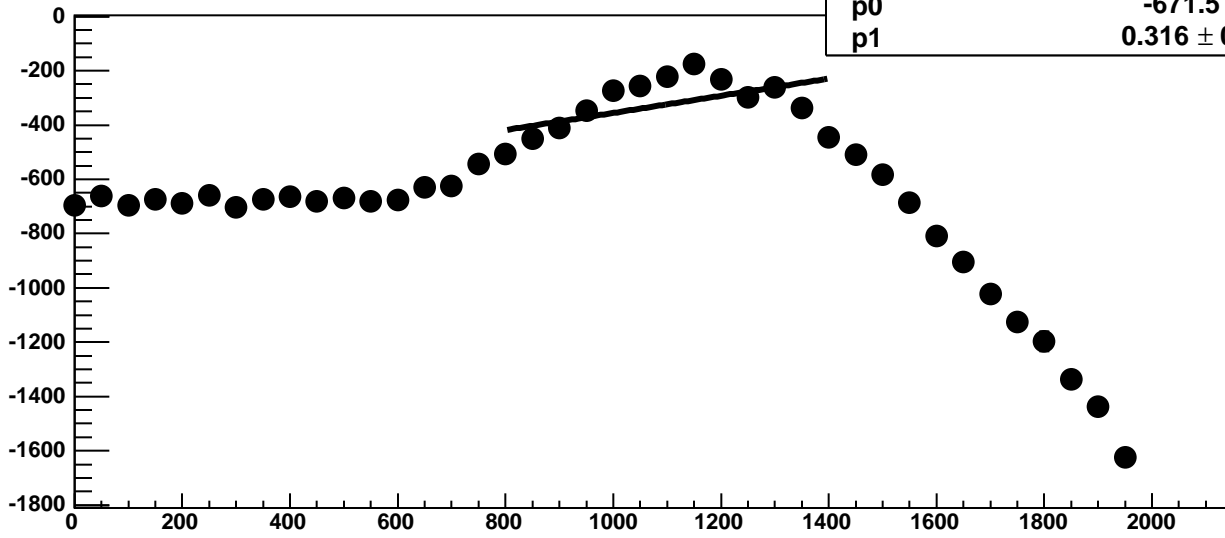
Chip 10, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC



Chip 10, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 10, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

186.2 / 11

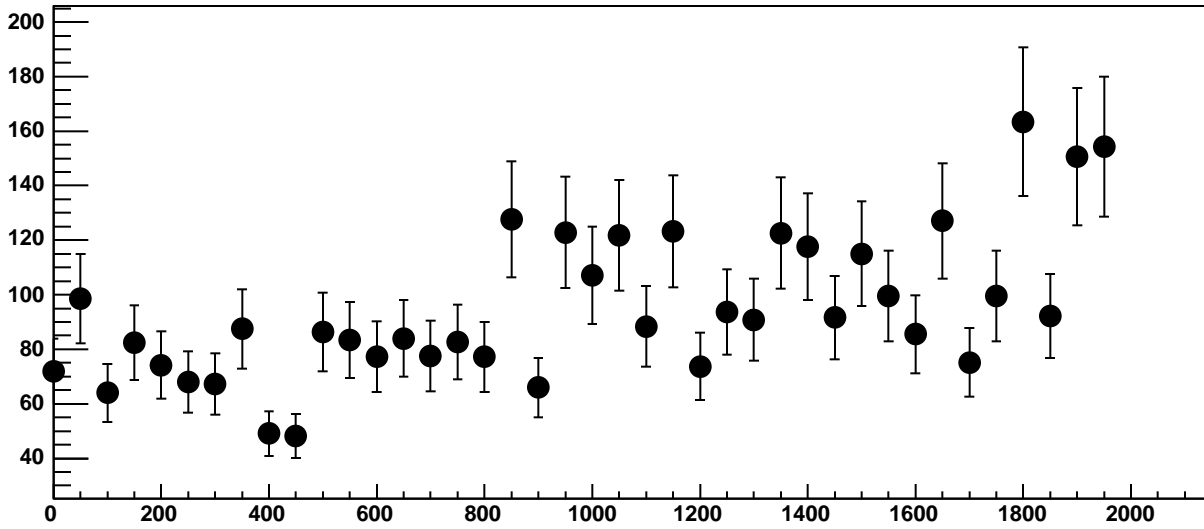
p0

$-671.5 \pm 35.48$

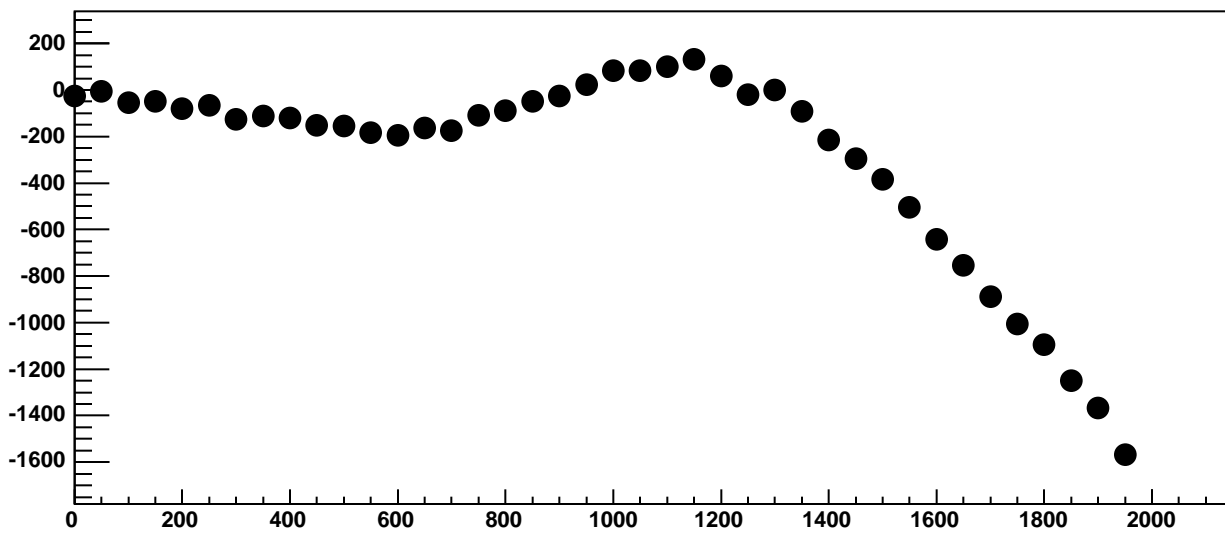
p1

$0.316 \pm 0.03244$

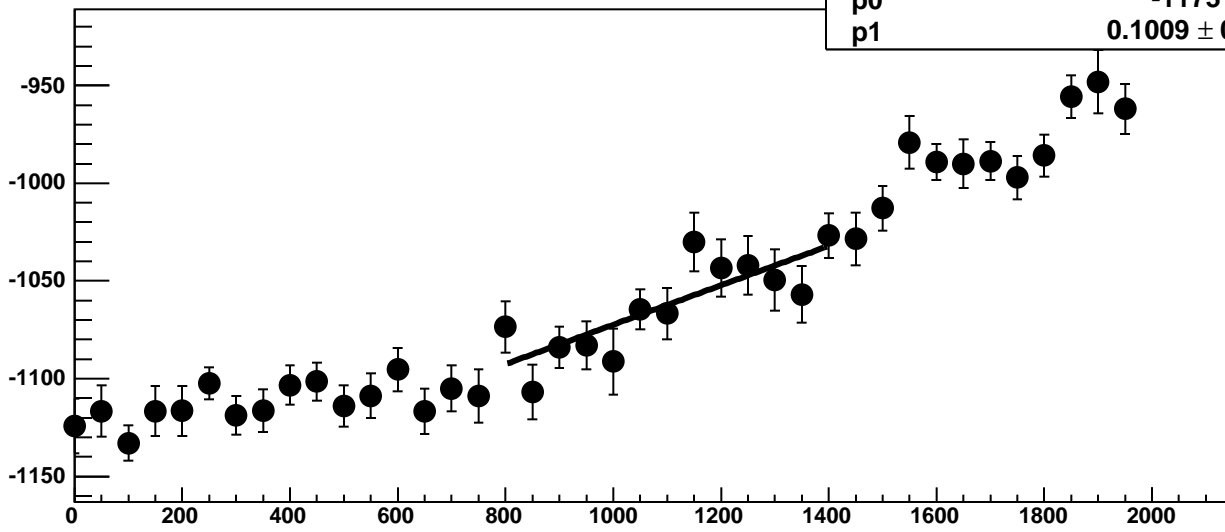
Chip 10, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC



Chip 10, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC

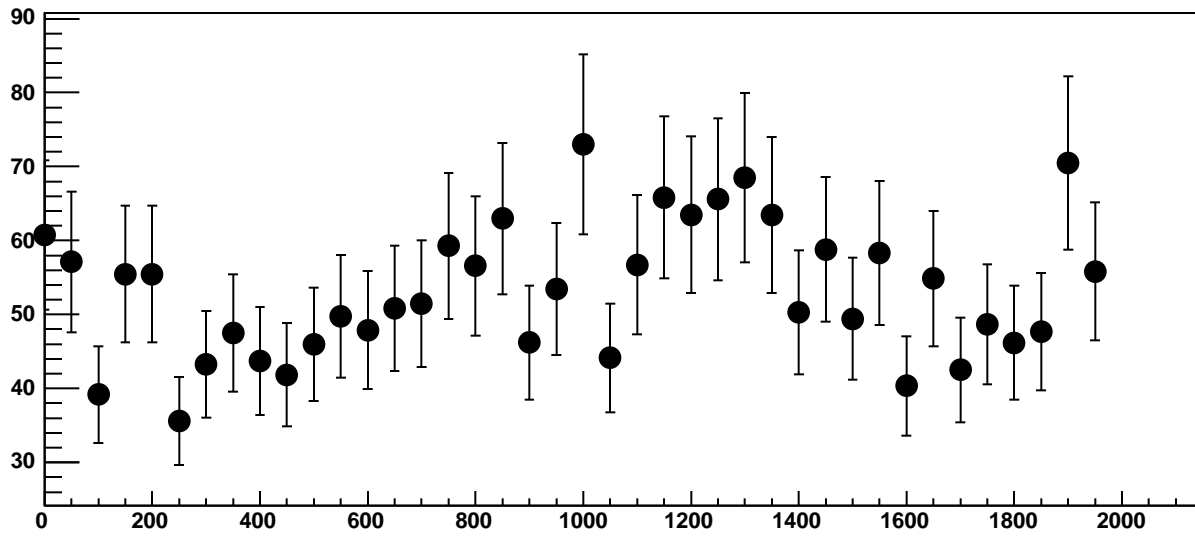


Chip 10, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC

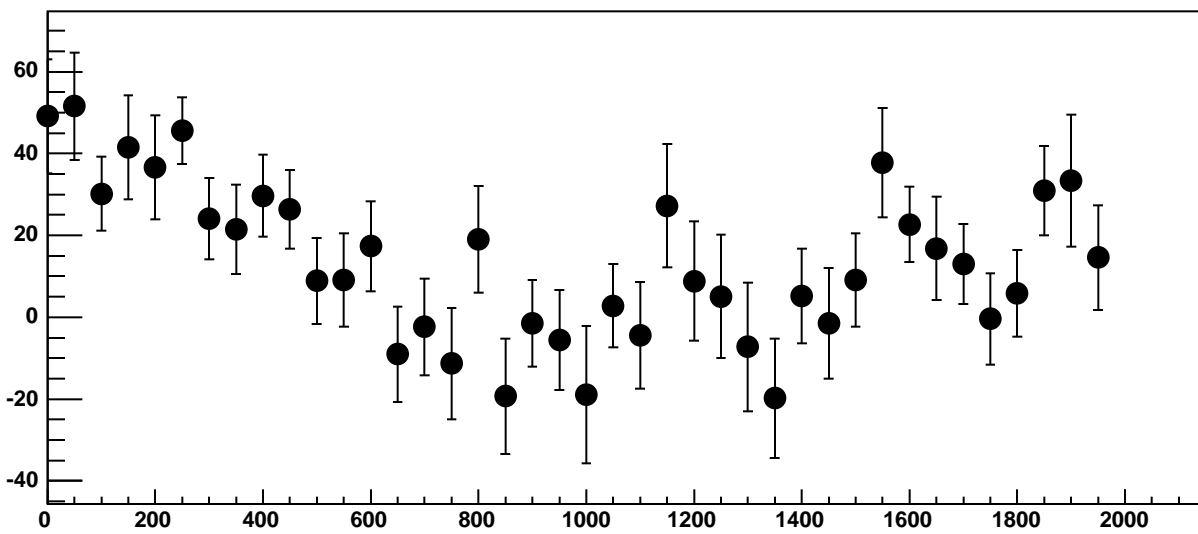


$\chi^2 / \text{ndf}$  11.71 / 11  
p0  $-1173 \pm 21.25$   
p1  $0.1009 \pm 0.01928$

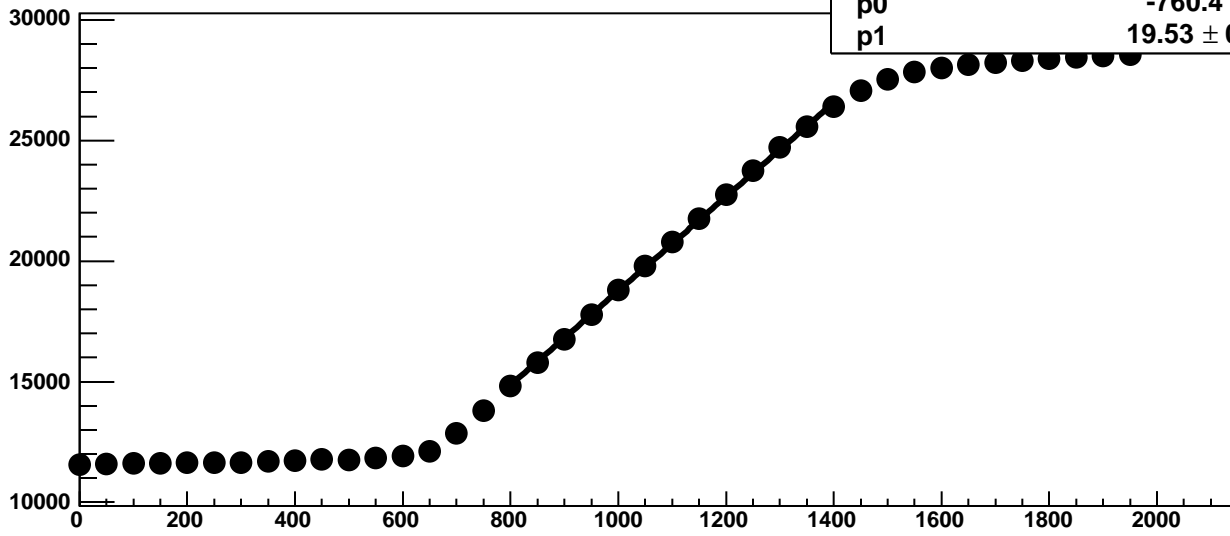
Chip 10, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC



Chip 10, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC

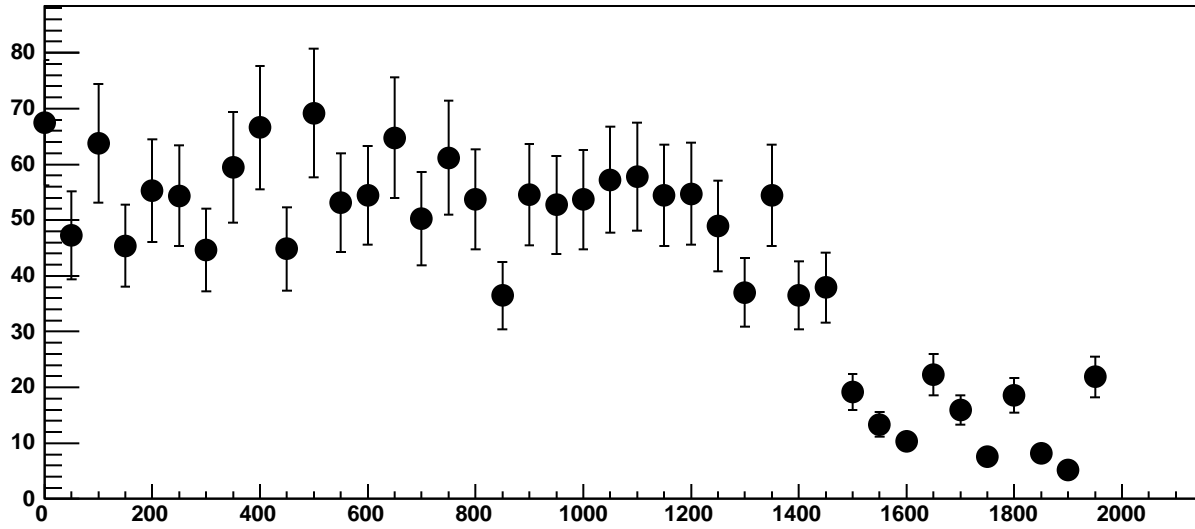


Chip 10, Channel 6, Enable 2!, Hold=30, ADC Mean vs DAC

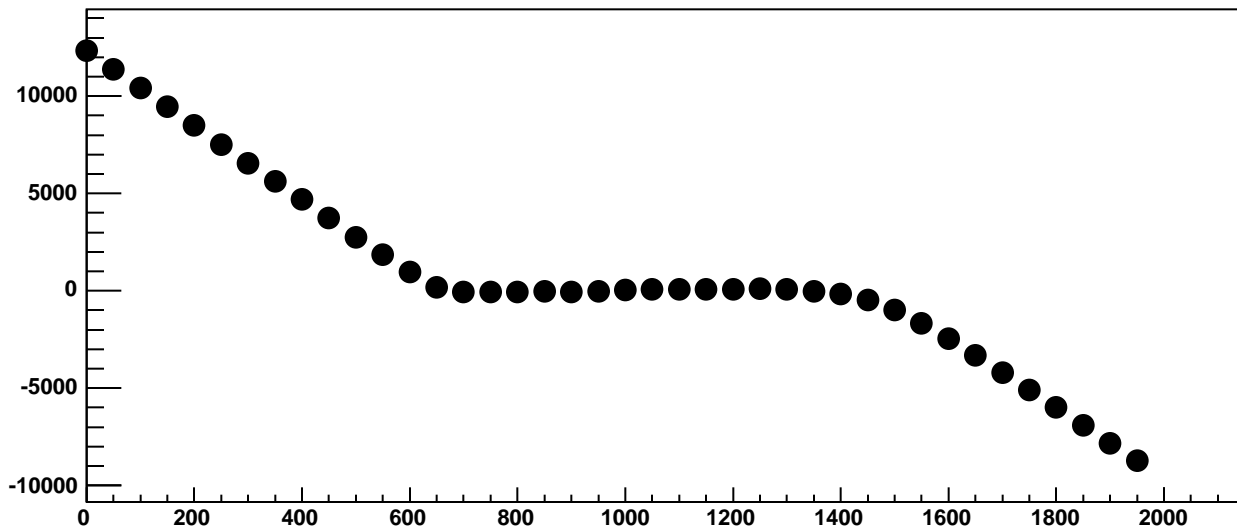


$\chi^2 / \text{ndf}$	761.4 / 11
p0	-760.4 ± 17.08
p1	19.53 ± 0.01503

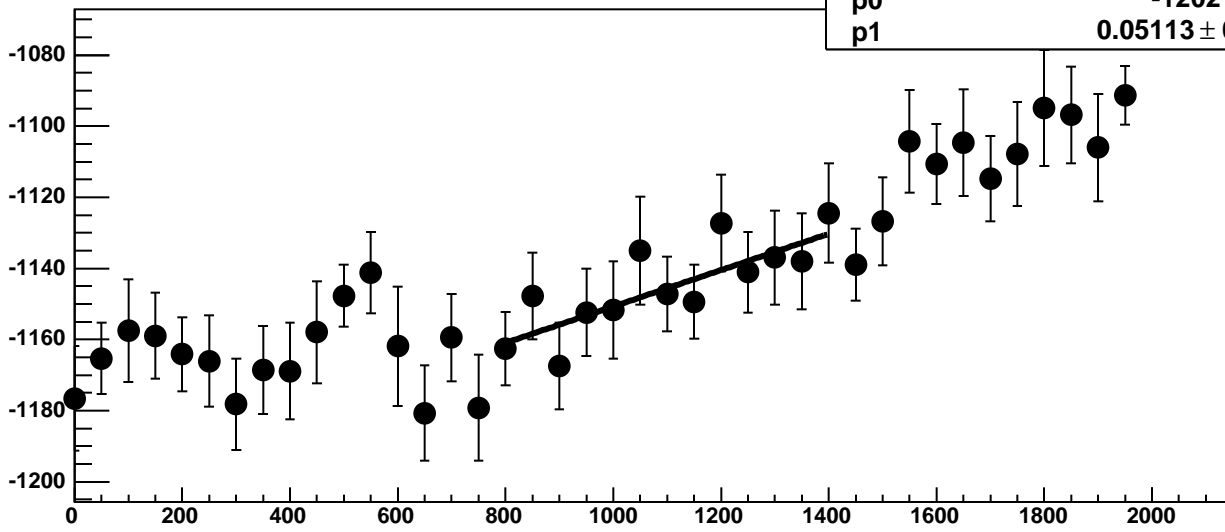
Chip 10, Channel 6, Enable 2!, Hold=30, ADC Noise vs DAC



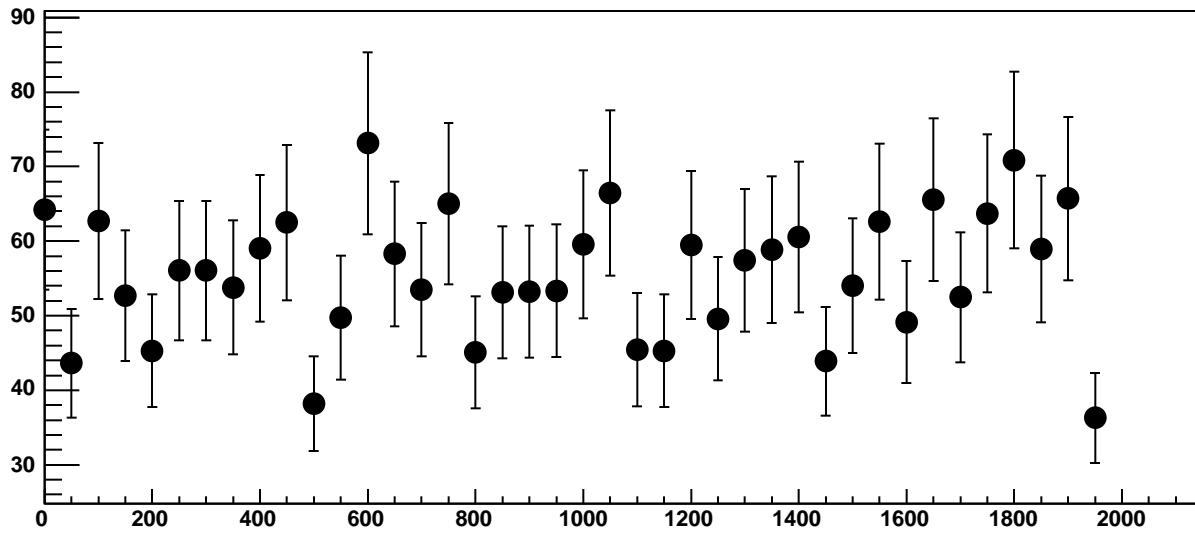
Chip 10, Channel 6, Enable 2!, Hold=30, ADC Residuals vs DAC



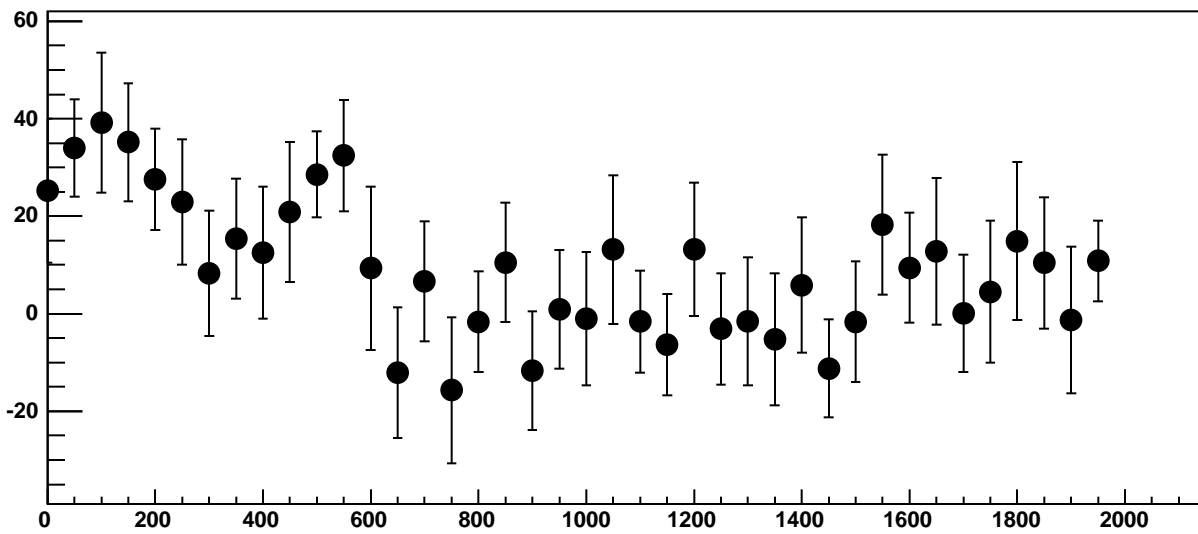
Chip 10, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC



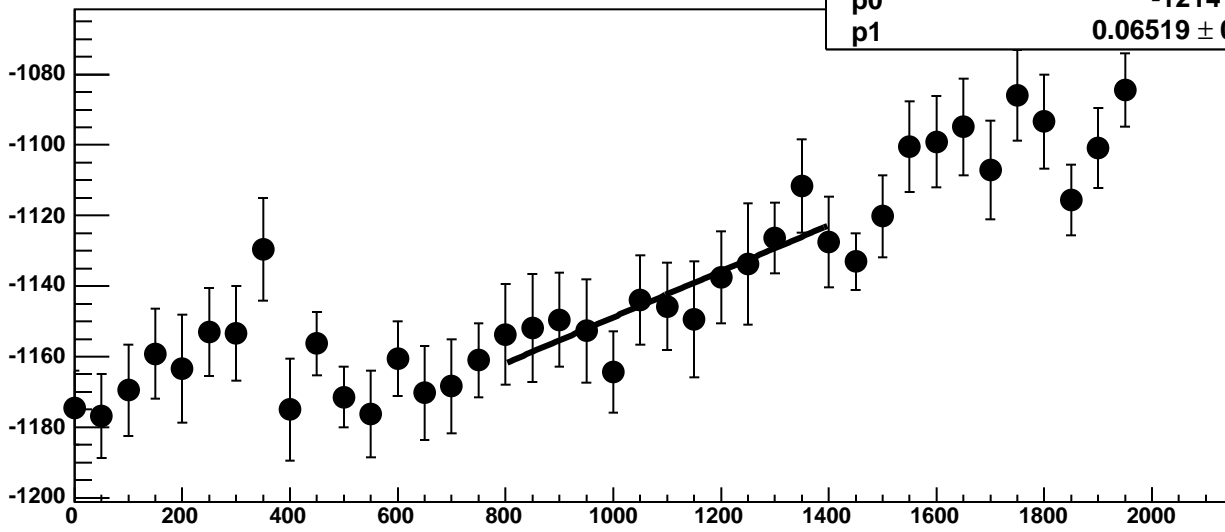
Chip 10, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC



Chip 10, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC

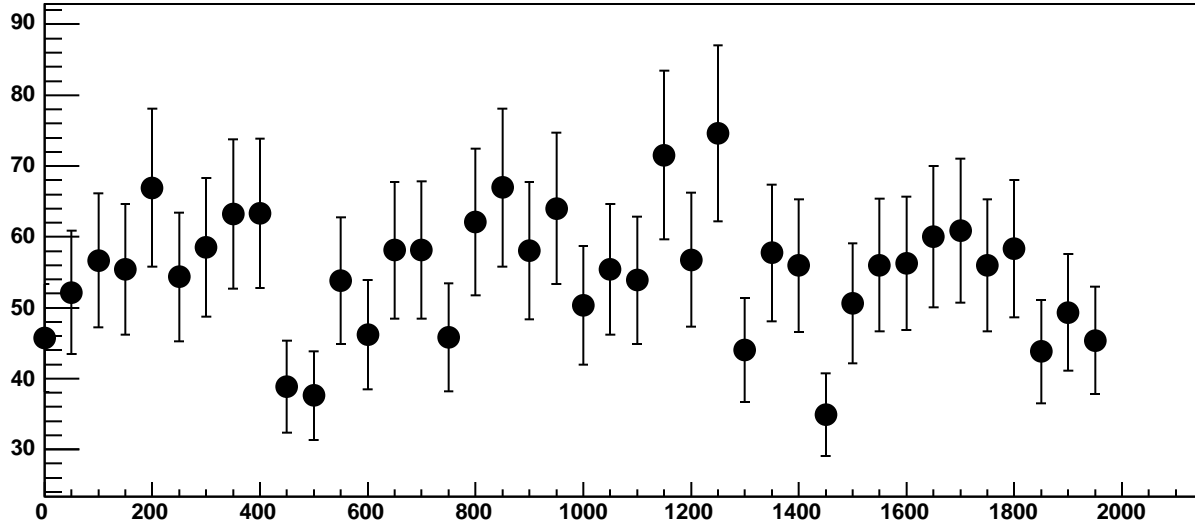


Chip 10, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC

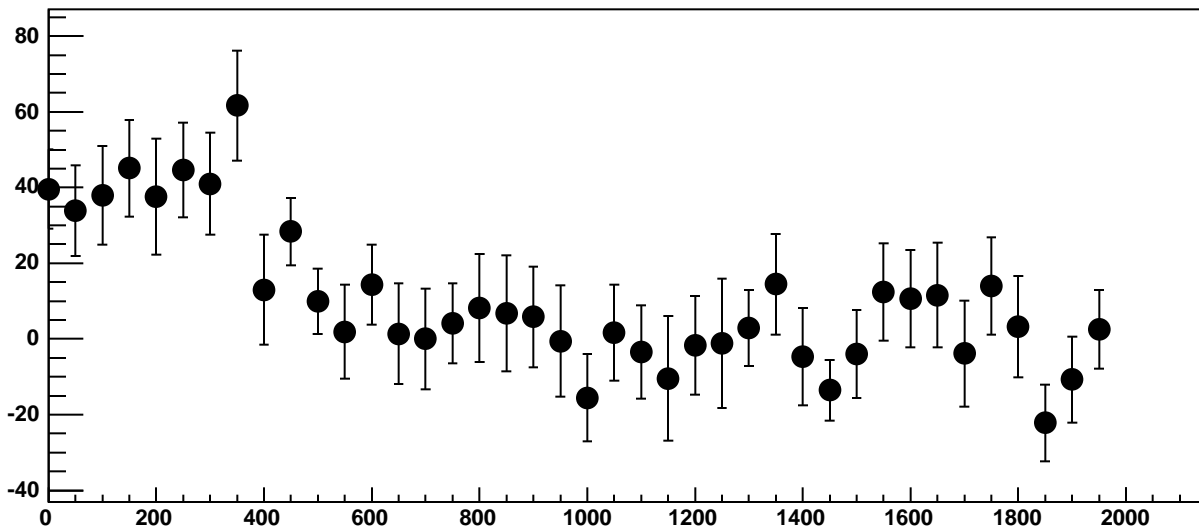


$\chi^2 / \text{ndf}$  4.447 / 11  
p0  $-1214 \pm 22.36$   
p1  $0.06519 \pm 0.01979$

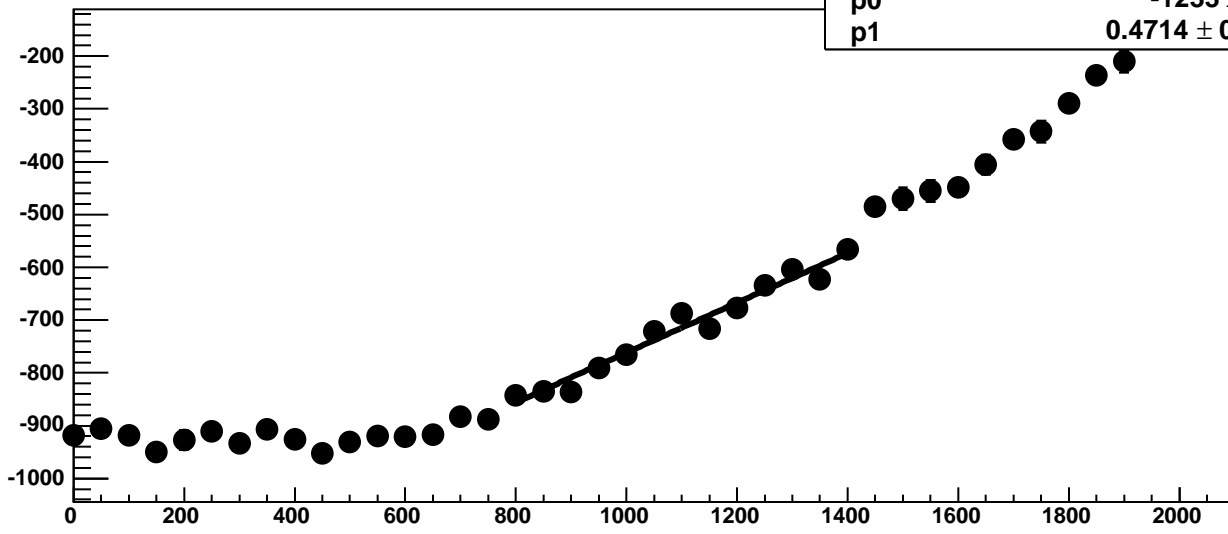
Chip 10, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC



Chip 10, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC

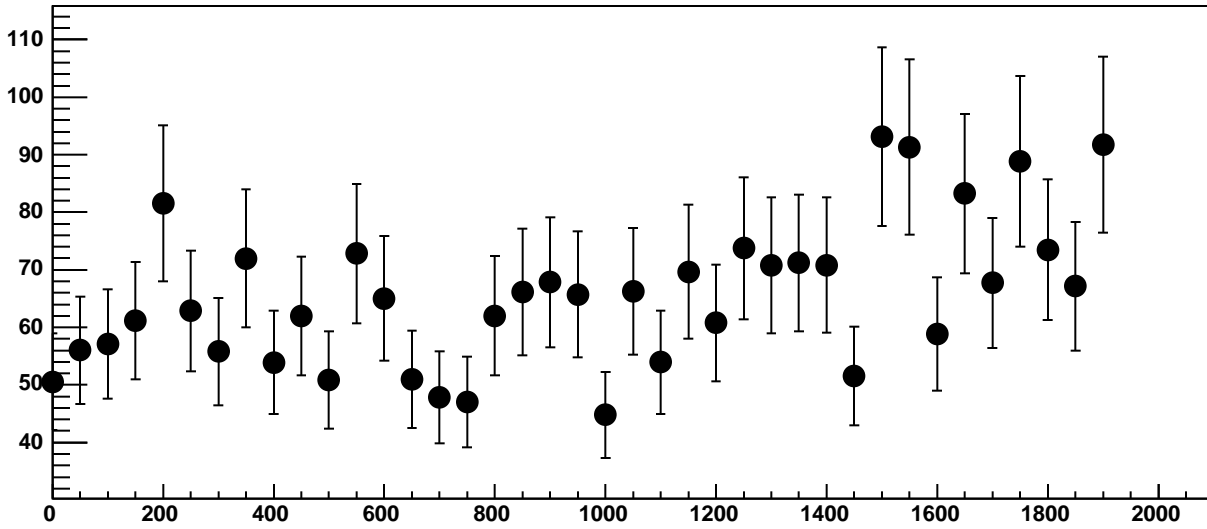


Chip 10, Channel 6, Enable 5, Hold=30, ADC Mean vs DAC

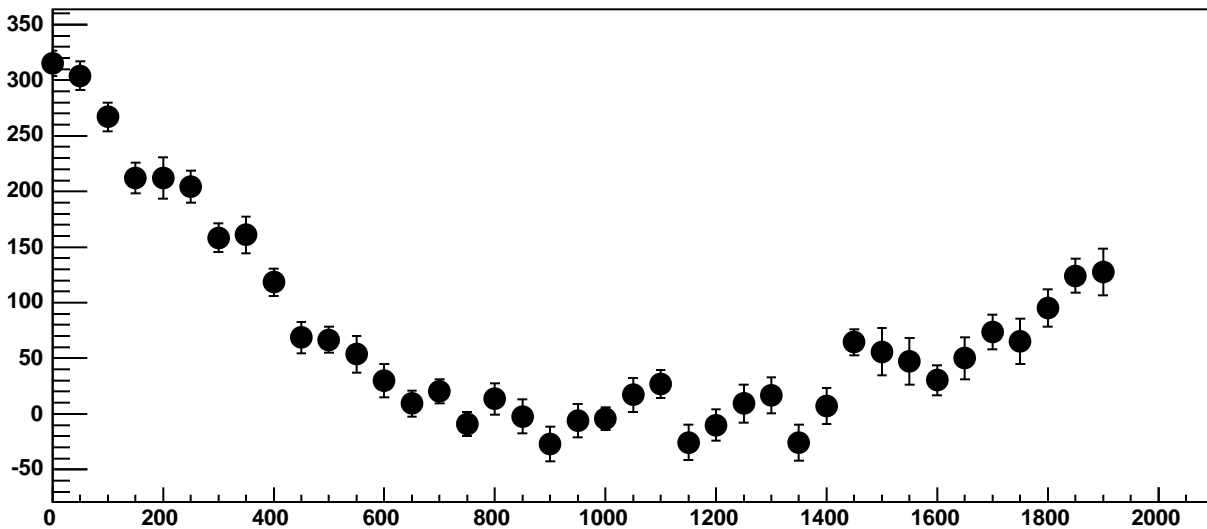


$\chi^2 / \text{ndf}$  17.32 / 11  
p0  $-1233 \pm 24.94$   
p1  $0.4714 \pm 0.02276$

Chip 10, Channel 6, Enable 5, Hold=30, ADC Noise vs DAC

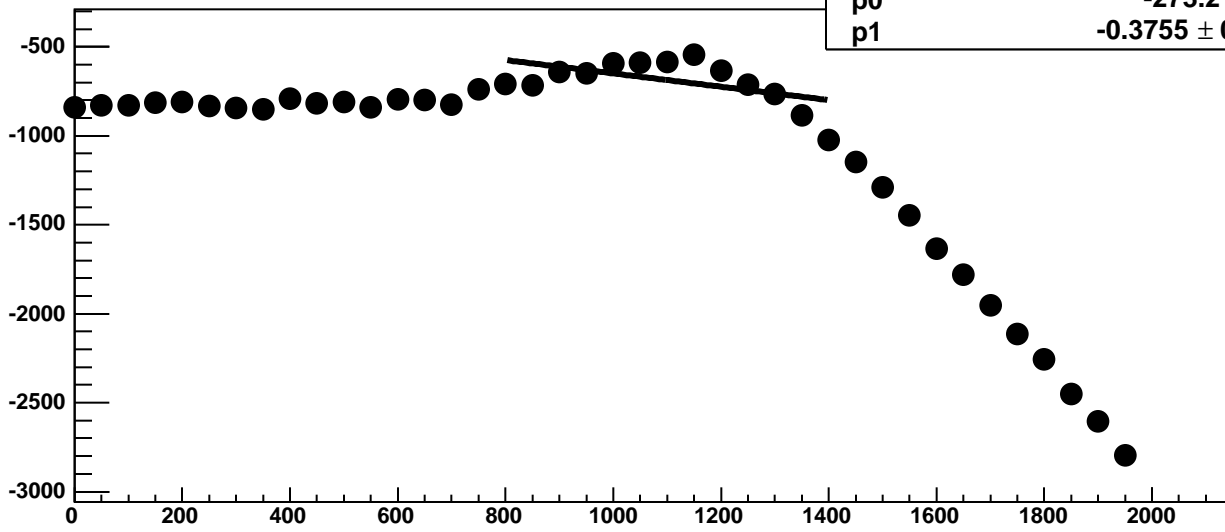


Chip 10, Channel 6, Enable 5, Hold=30, ADC Residuals vs DAC





Chip 10, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

297.4 / 11

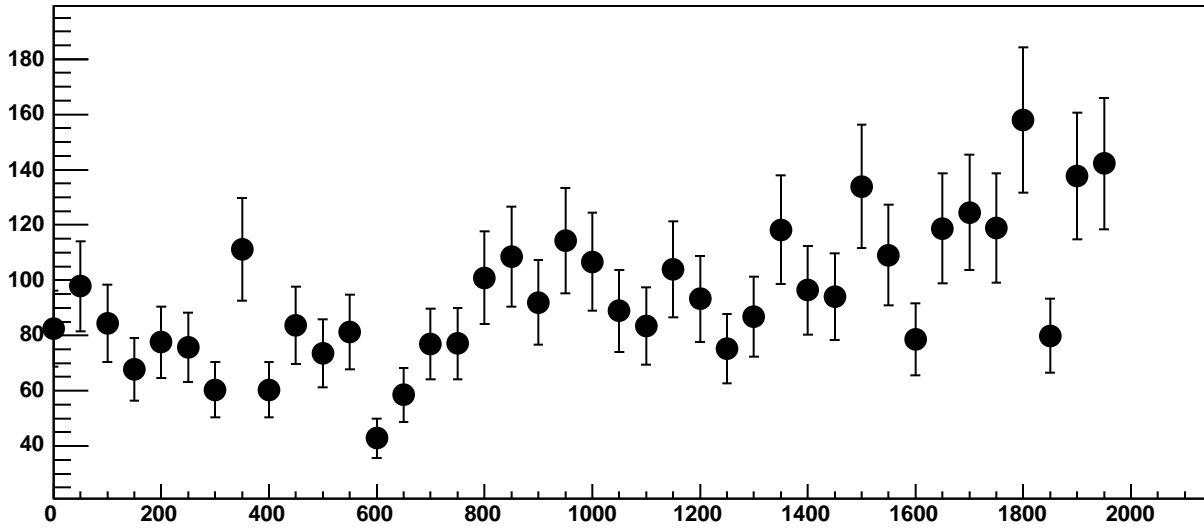
p0

$-273.2 \pm 37.97$

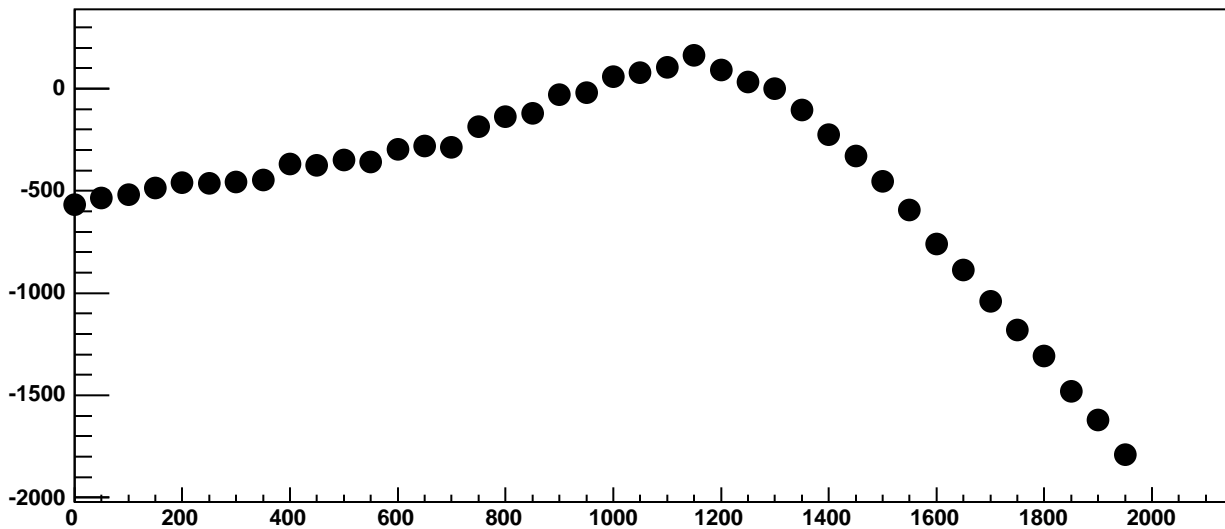
p1

$-0.3755 \pm 0.03369$

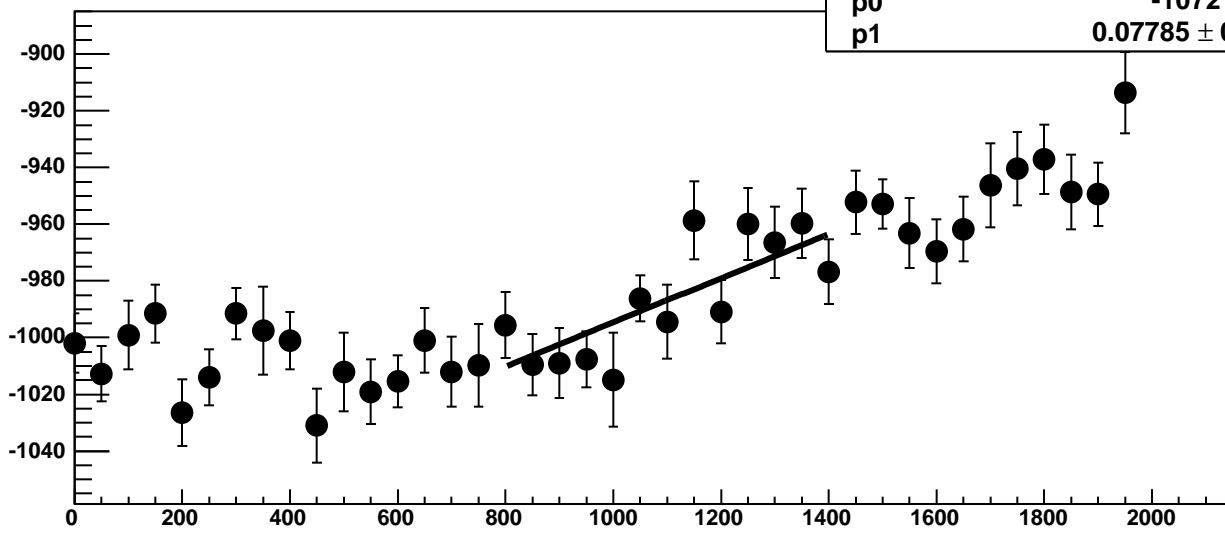
Chip 10, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC



Chip 10, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 10, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

12.52 / 11

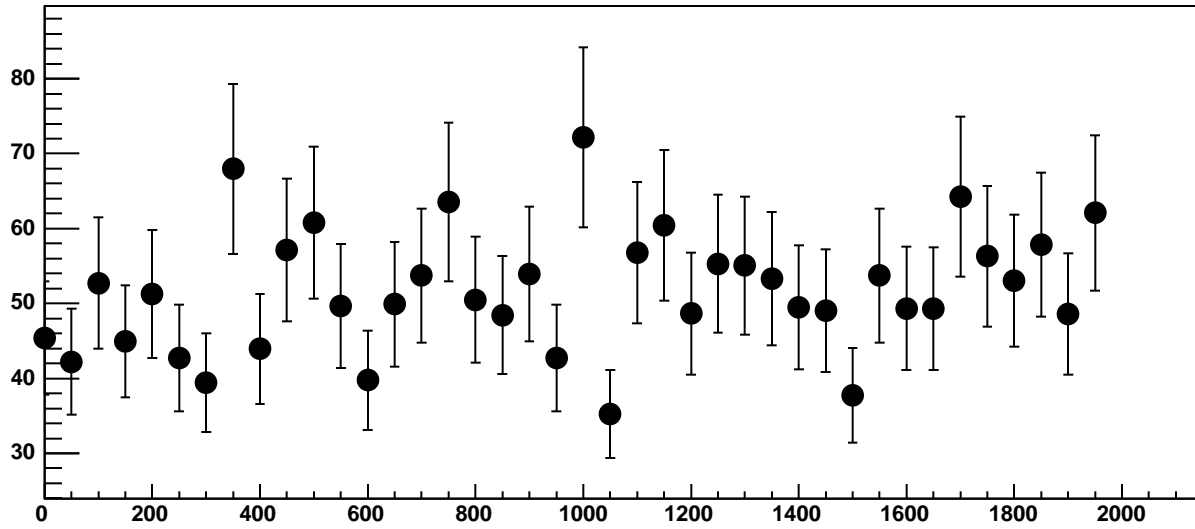
p0

$-1072 \pm 19.05$

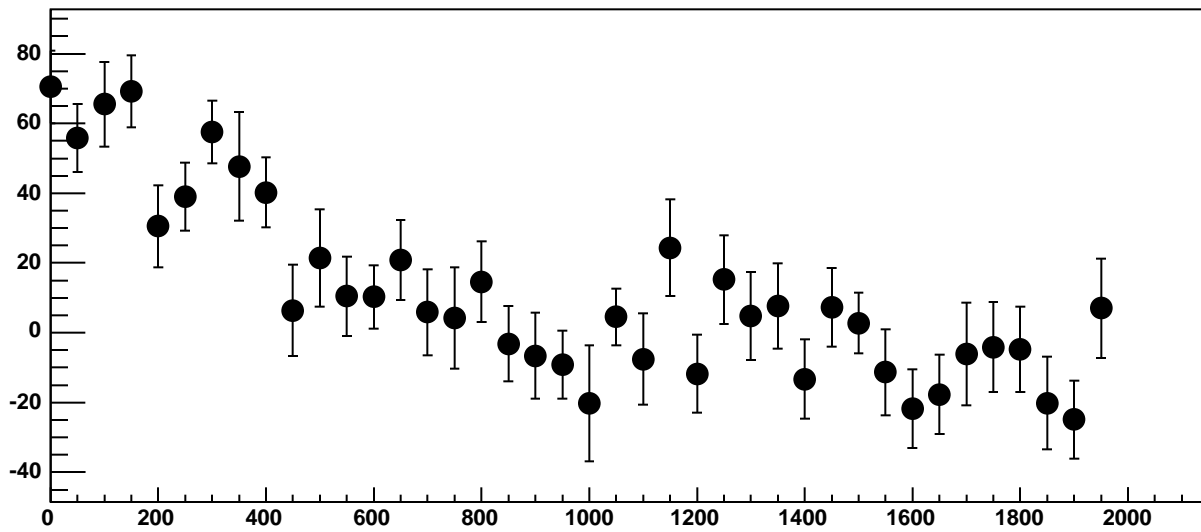
p1

$0.07785 \pm 0.01725$

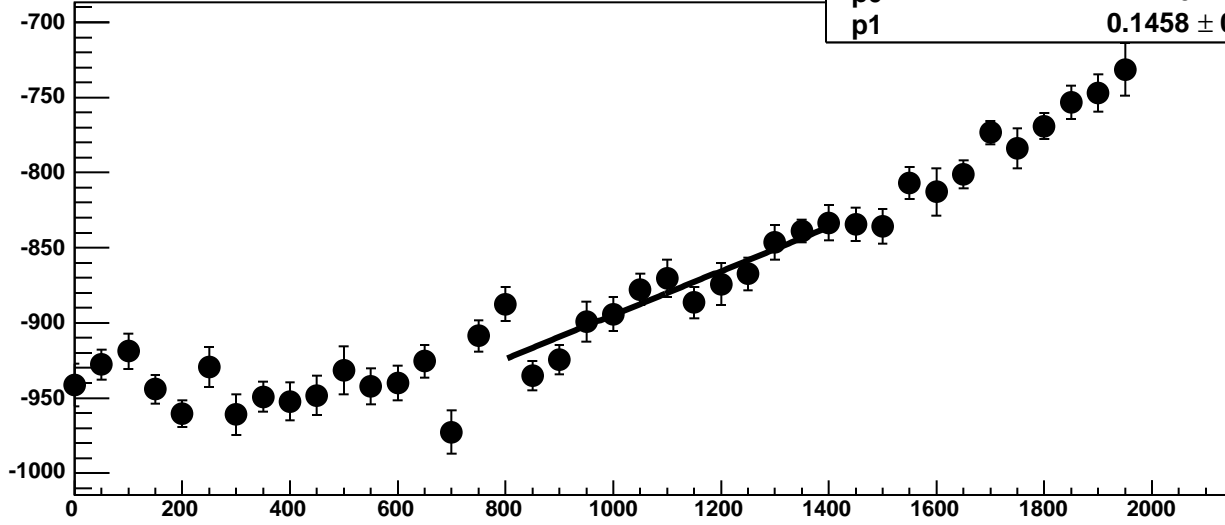
Chip 10, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC



Chip 10, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC



Chip 10, Channel 7, Enable 2, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

21.03 / 11

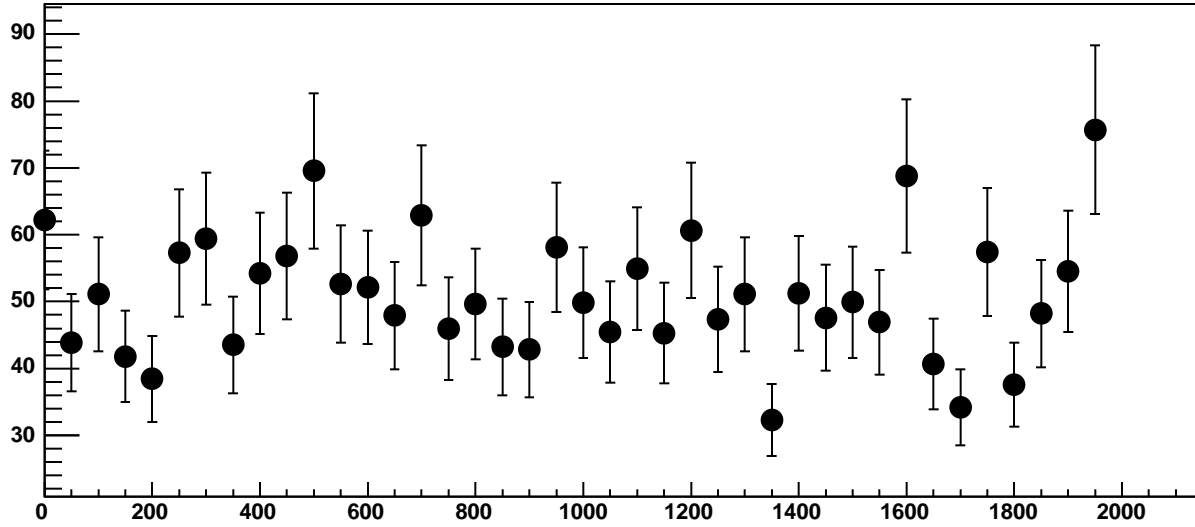
p0

$-1041 \pm 17.23$

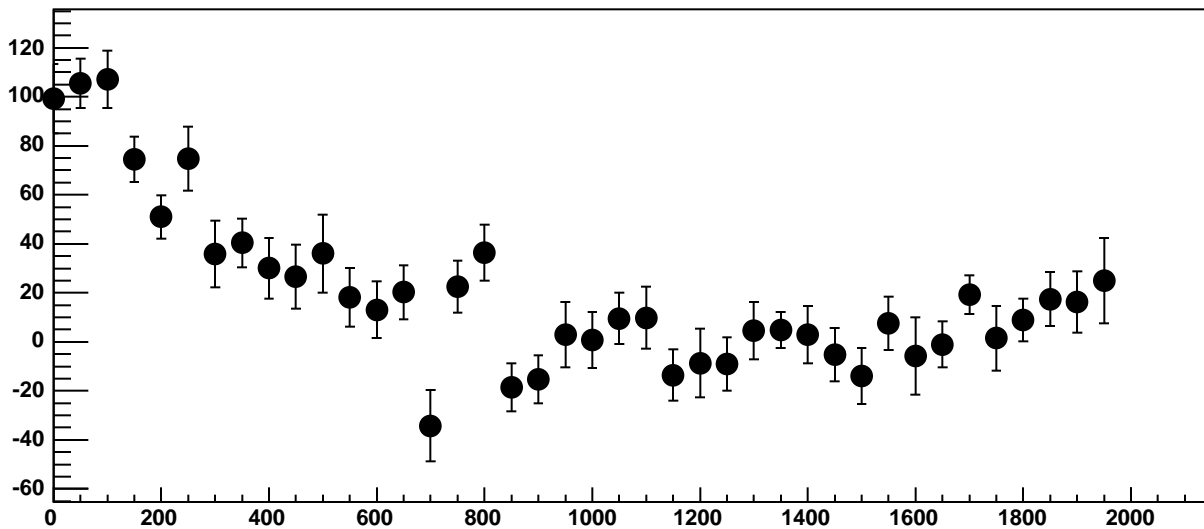
p1

$0.1458 \pm 0.01525$

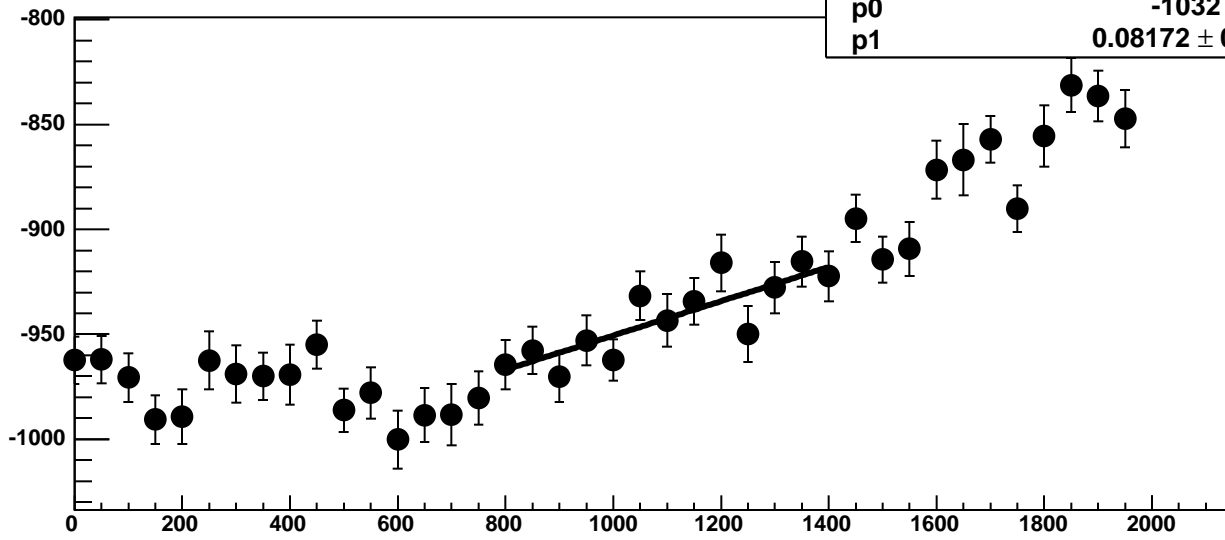
Chip 10, Channel 7, Enable 2, Hold=30, ADC Noise vs DAC



Chip 10, Channel 7, Enable 2, Hold=30, ADC Residuals vs DAC

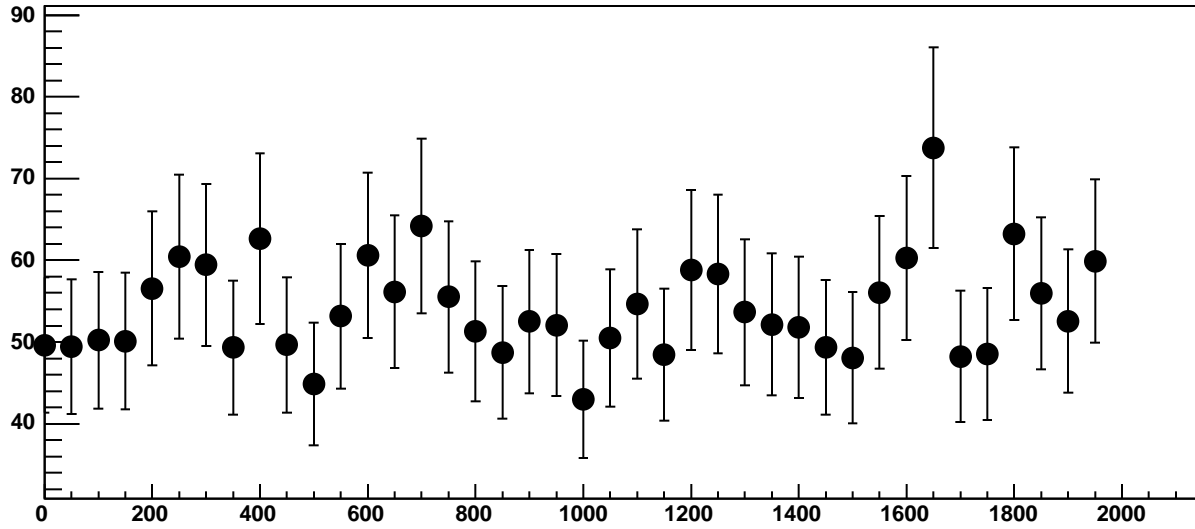


Chip 10, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC

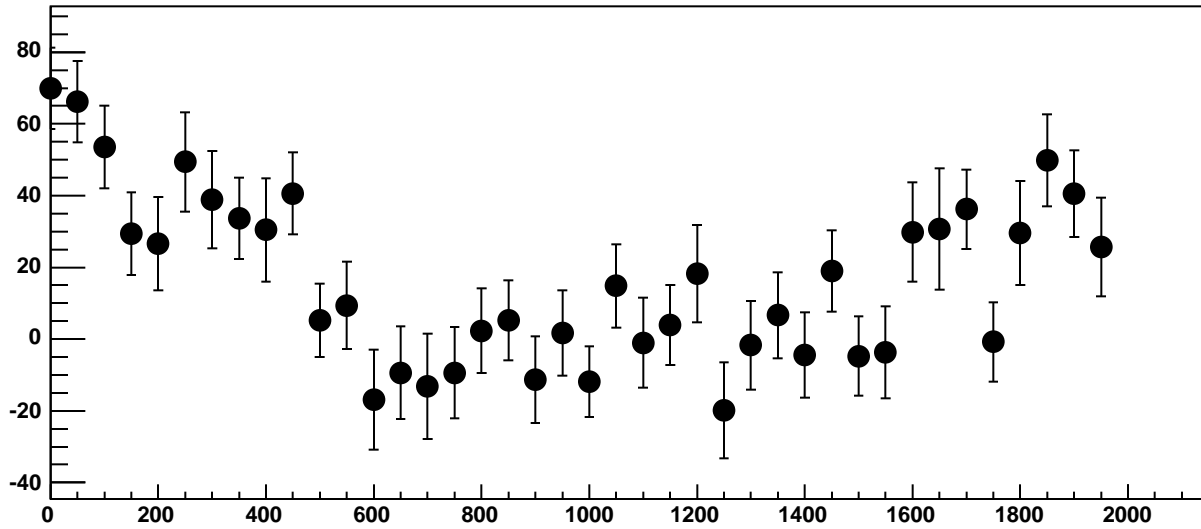


$\chi^2 / \text{ndf}$  8.848 / 11  
p0  $-1032 \pm 19.44$   
p1  $0.08172 \pm 0.01759$

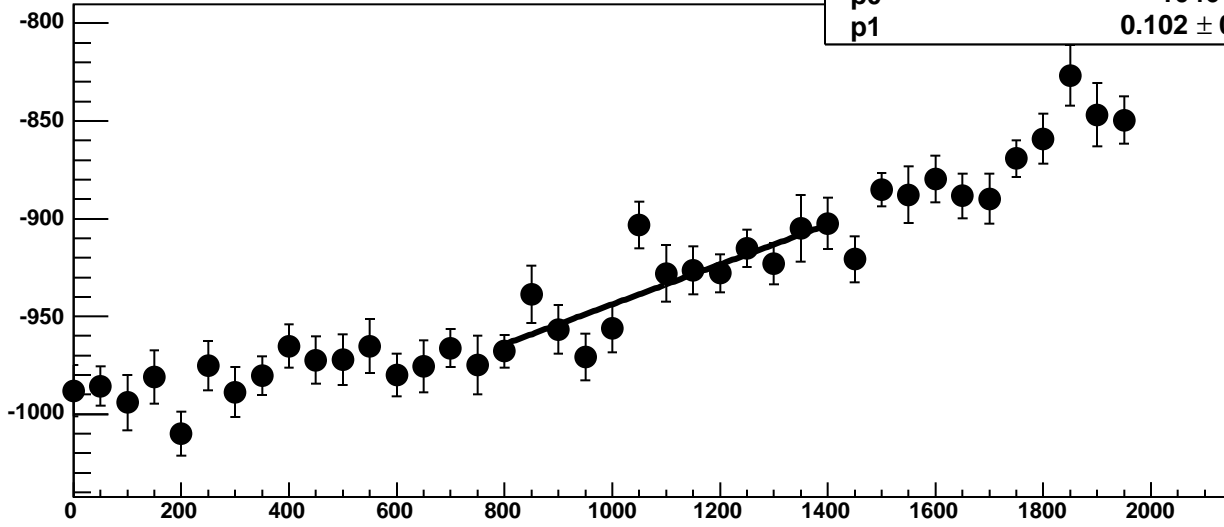
Chip 10, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC



Chip 10, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC

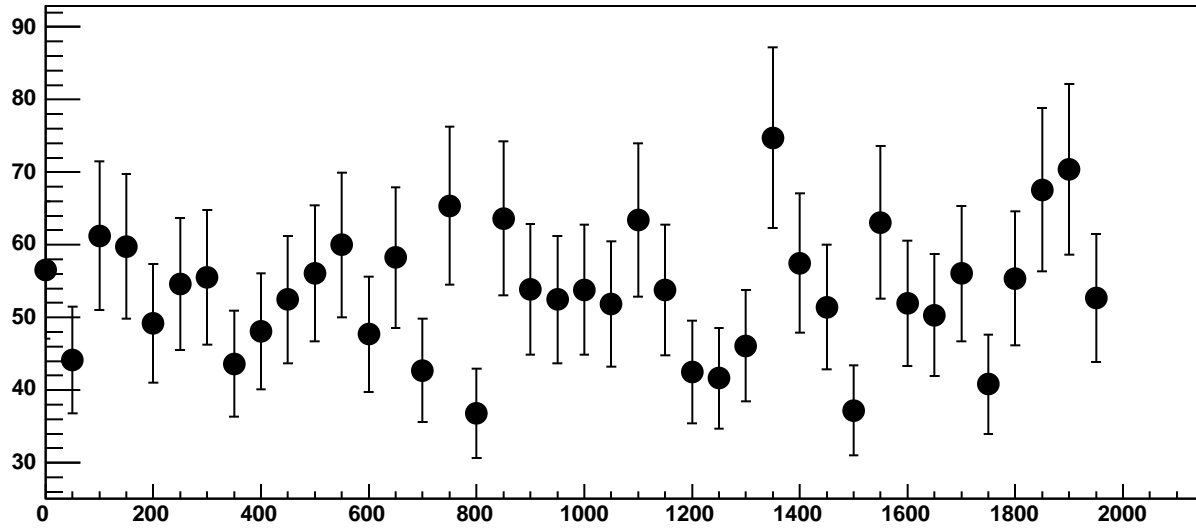


Chip 10, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC

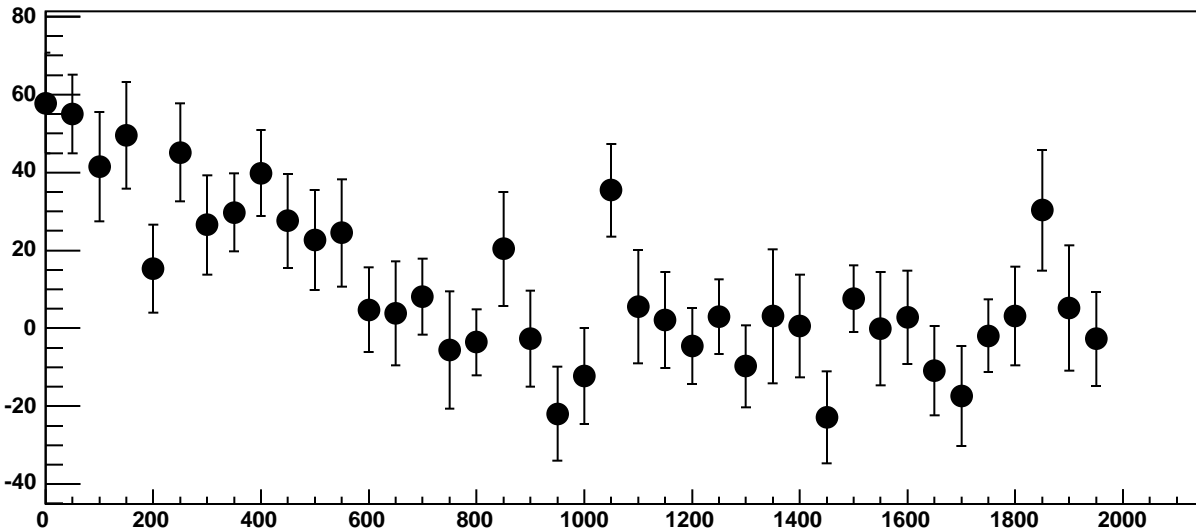


$\chi^2 / \text{ndf}$  16.71 / 11  
p0  $-1046 \pm 18.65$   
p1  $0.102 \pm 0.01692$

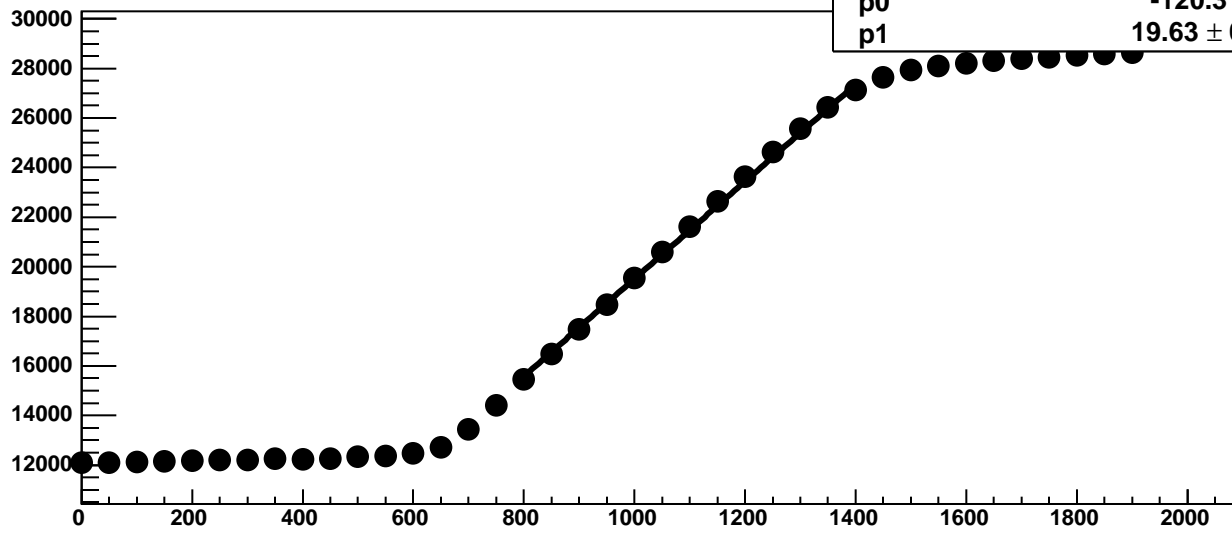
Chip 10, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC



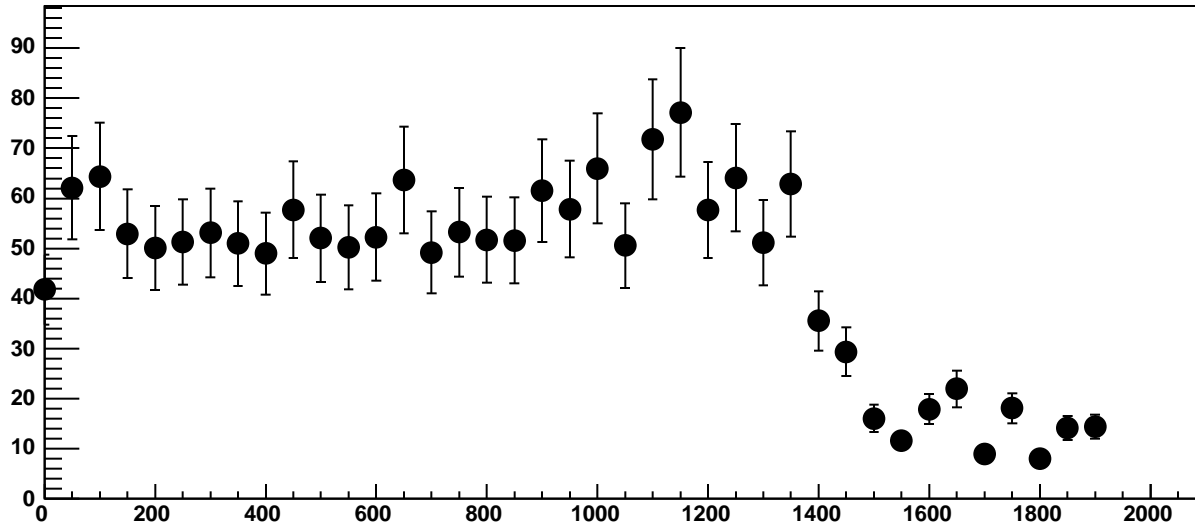
Chip 10, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC



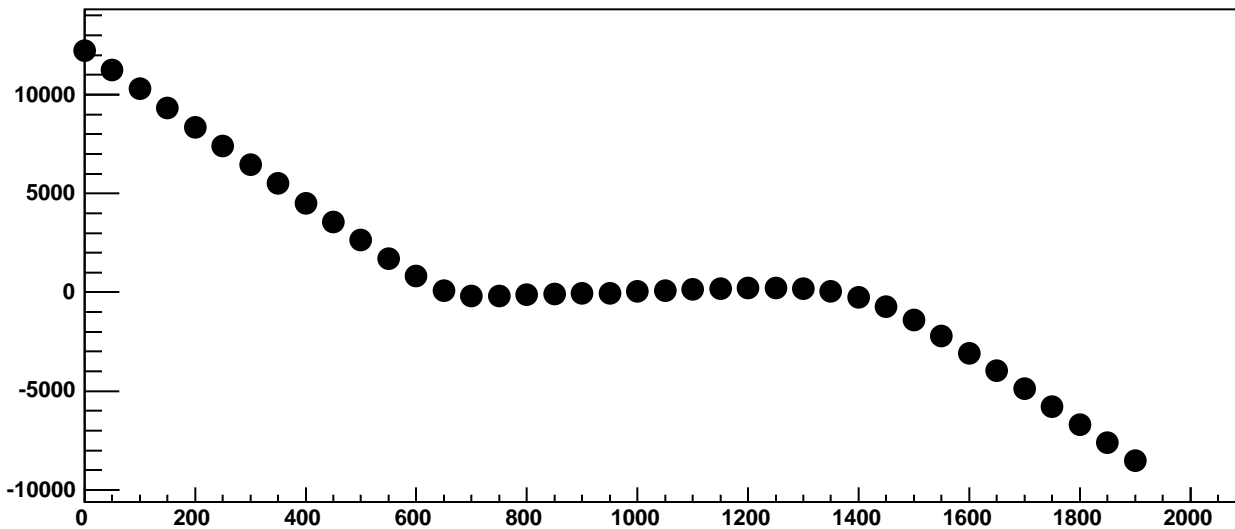
Chip 10, Channel 7, Enable 5!, Hold=30, ADC Mean vs DAC



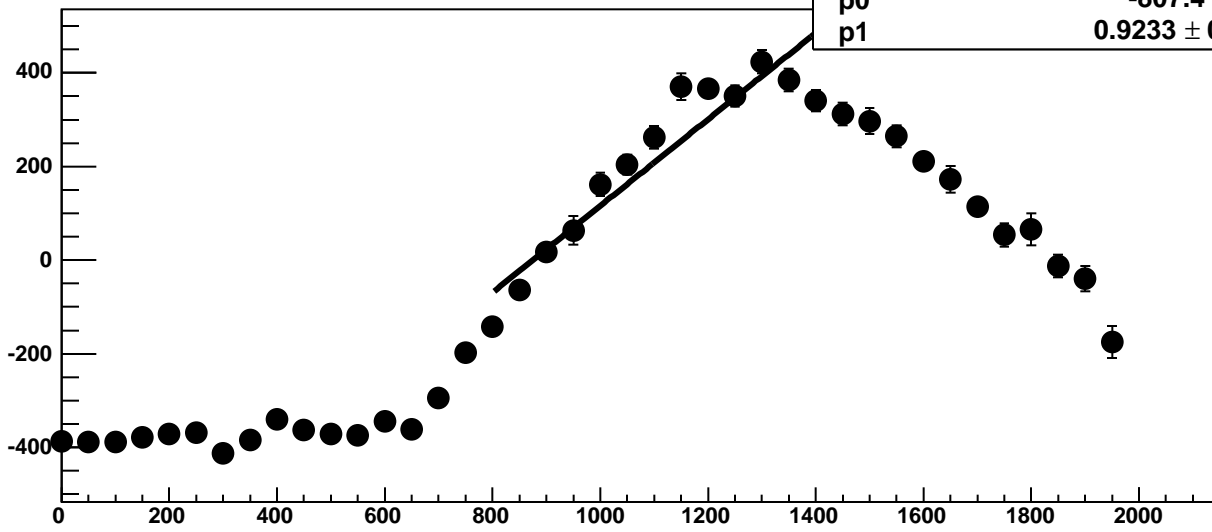
Chip 10, Channel 7, Enable 5!, Hold=30, ADC Noise vs DAC



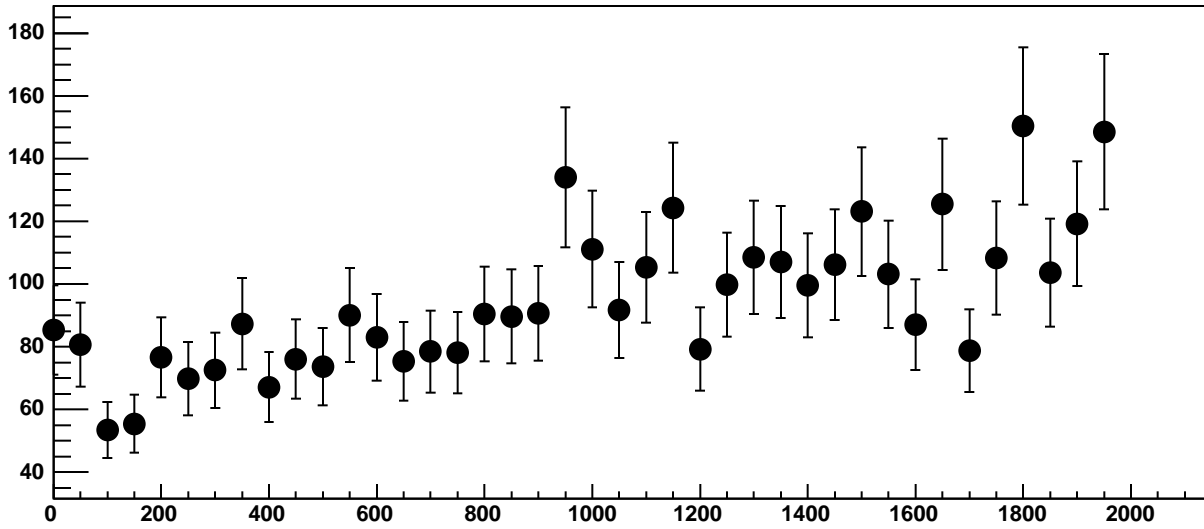
Chip 10, Channel 7, Enable 5!, Hold=30, ADC Residuals vs DAC



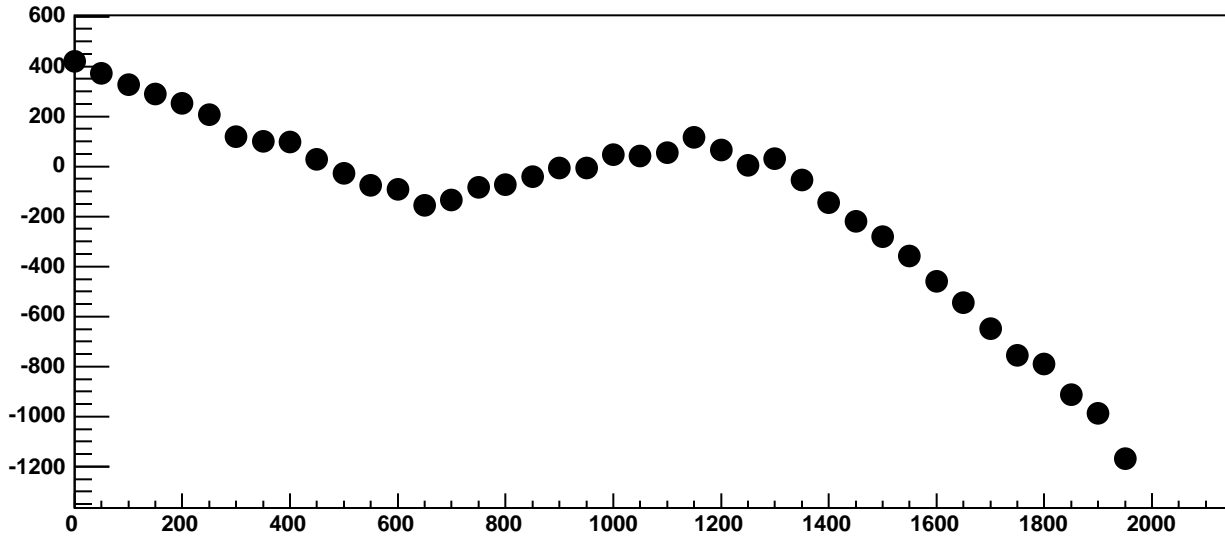
Chip 10, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC



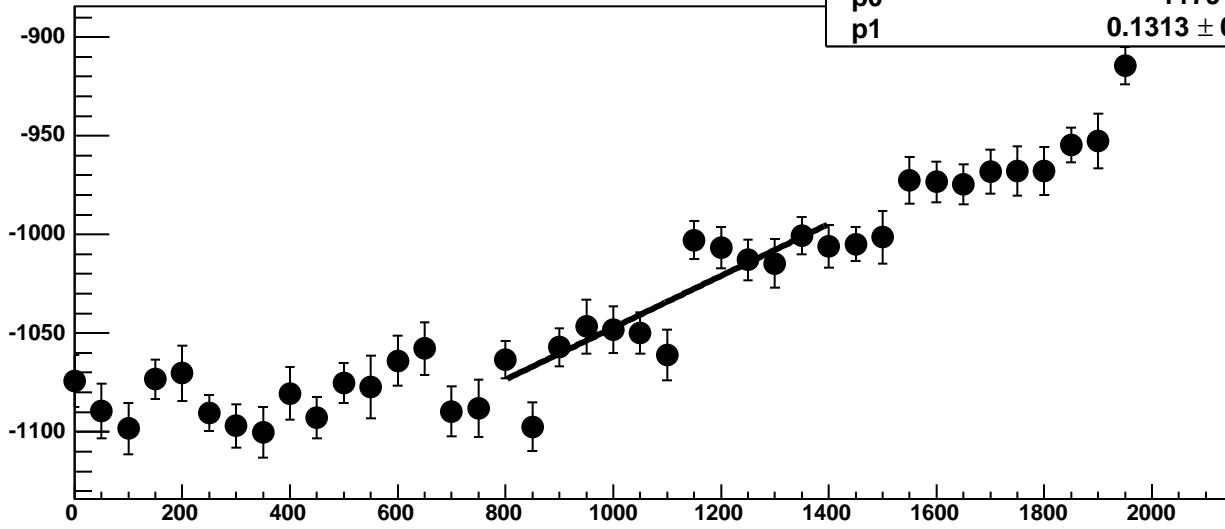
Chip 10, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC



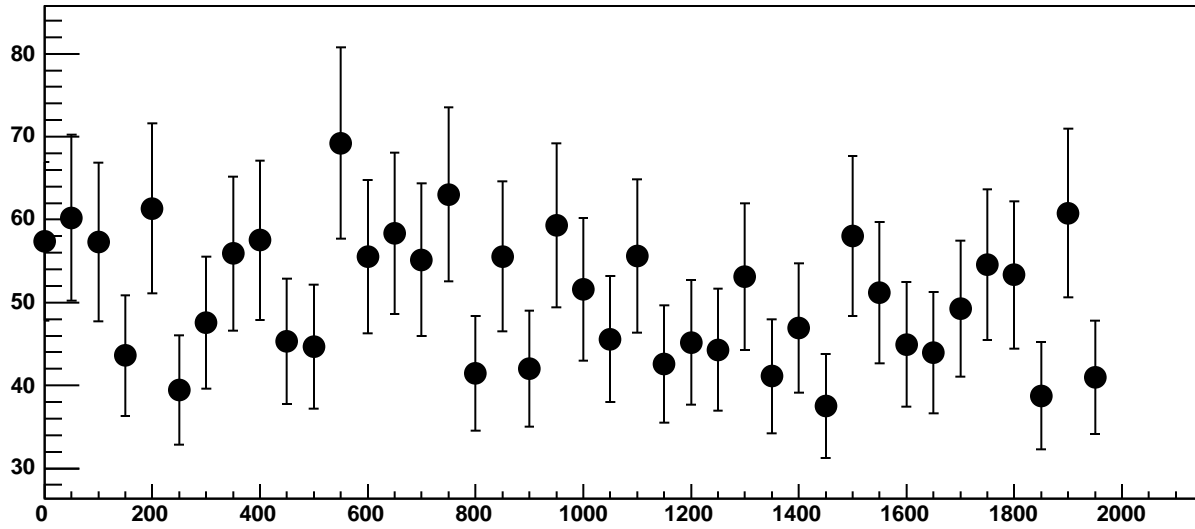
Chip 10, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC



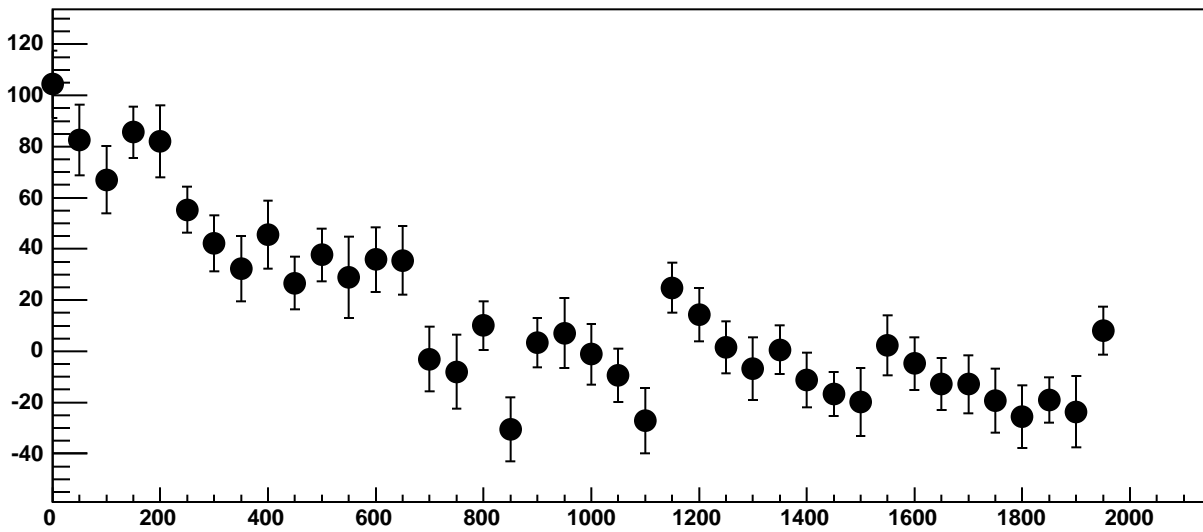
Chip 10, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC



Chip 10, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC

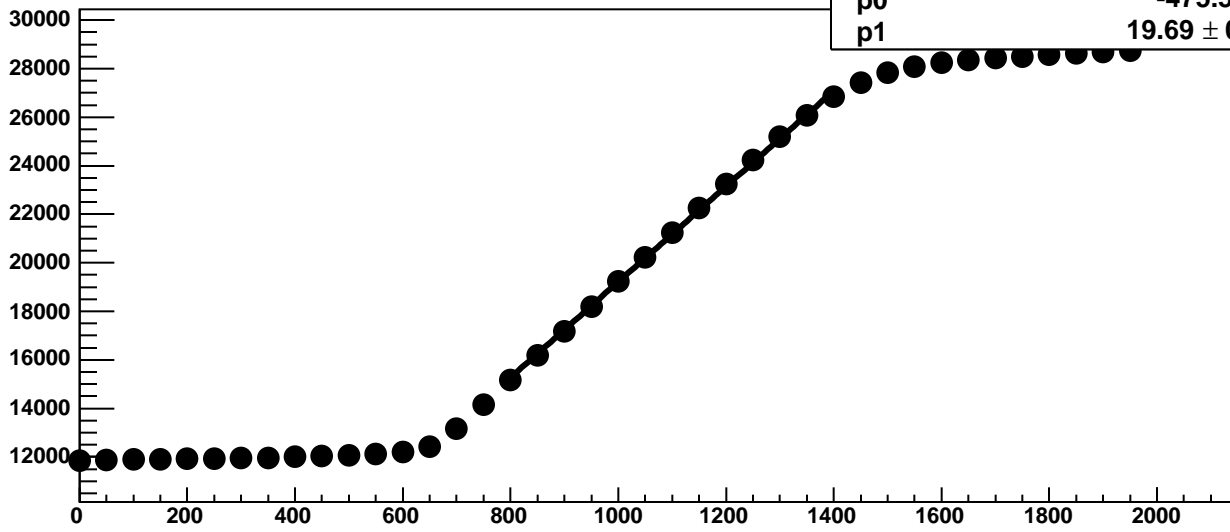


Chip 10, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC

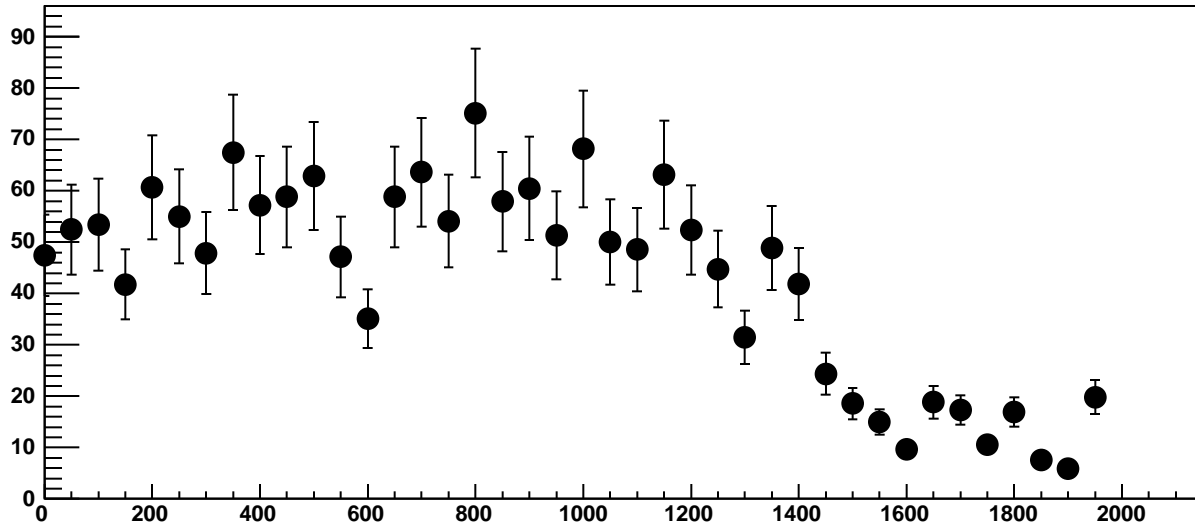




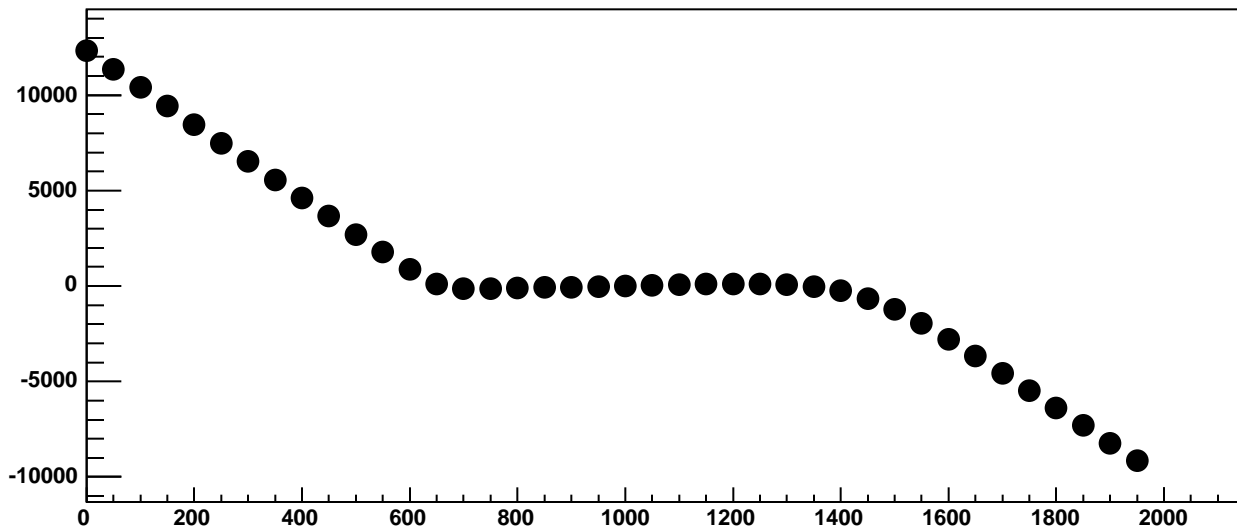
Chip 10, Channel 8, Enable 2!, Hold=30, ADC Mean vs DAC



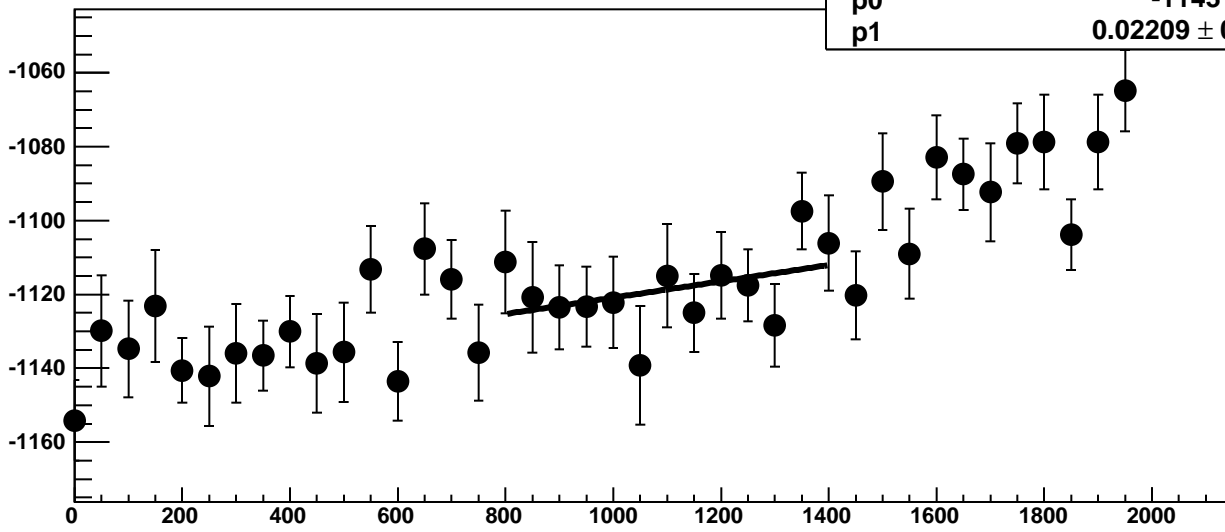
Chip 10, Channel 8, Enable 2!, Hold=30, ADC Noise vs DAC



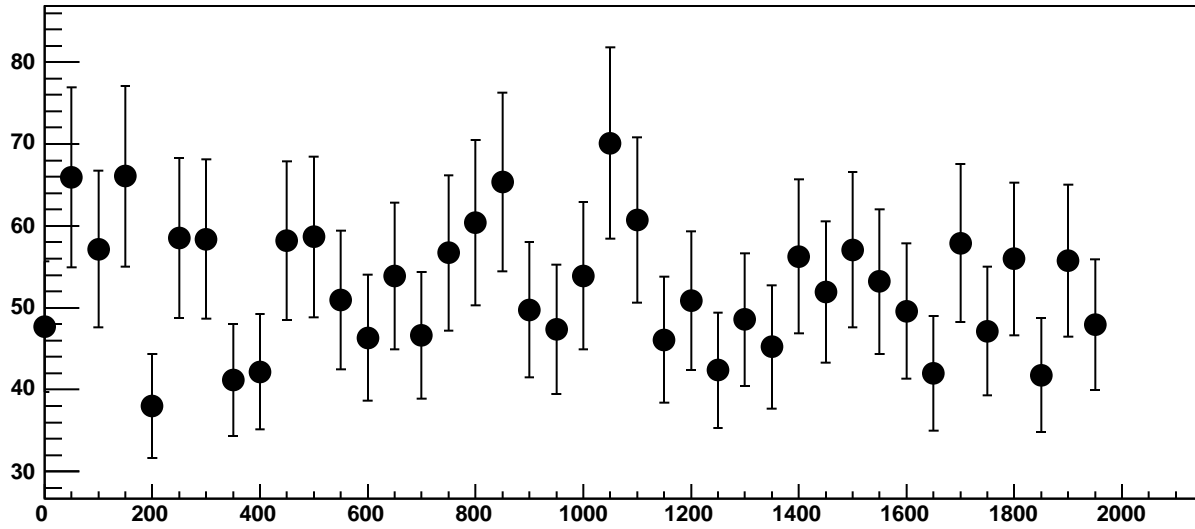
Chip 10, Channel 8, Enable 2!, Hold=30, ADC Residuals vs DAC



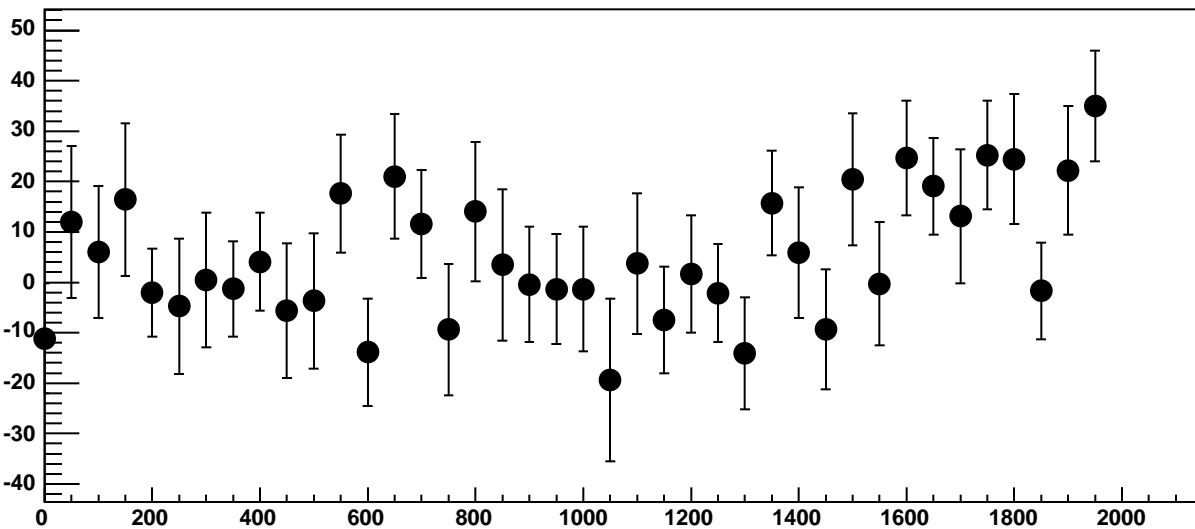
Chip 10, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC



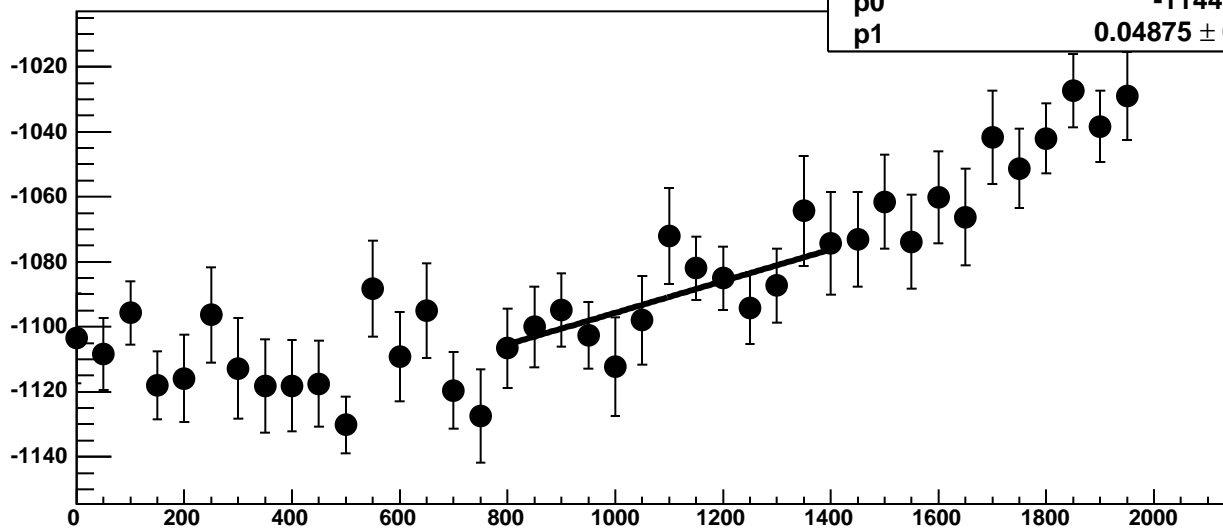
Chip 10, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC



Chip 10, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC

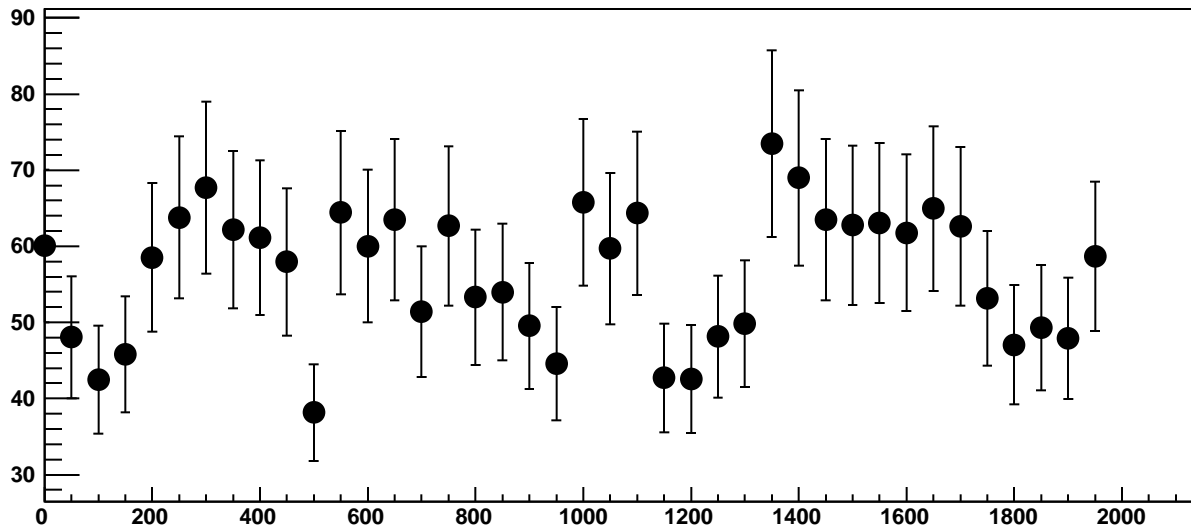


Chip 10, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC

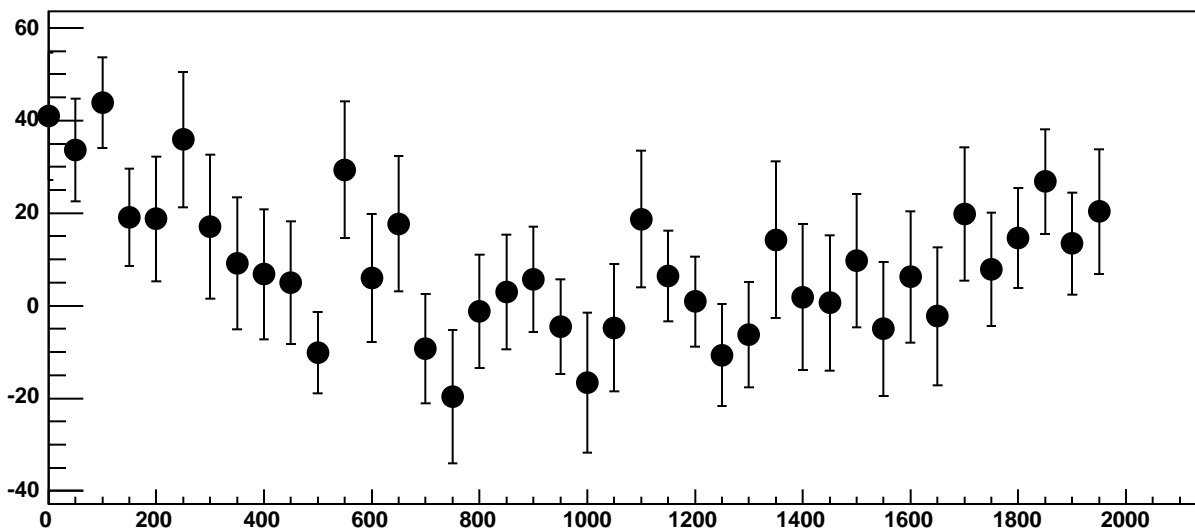


$\chi^2 / \text{ndf}$  5.839 / 11  
p0  $-1144 \pm 20.93$   
p1  $0.04875 \pm 0.01896$

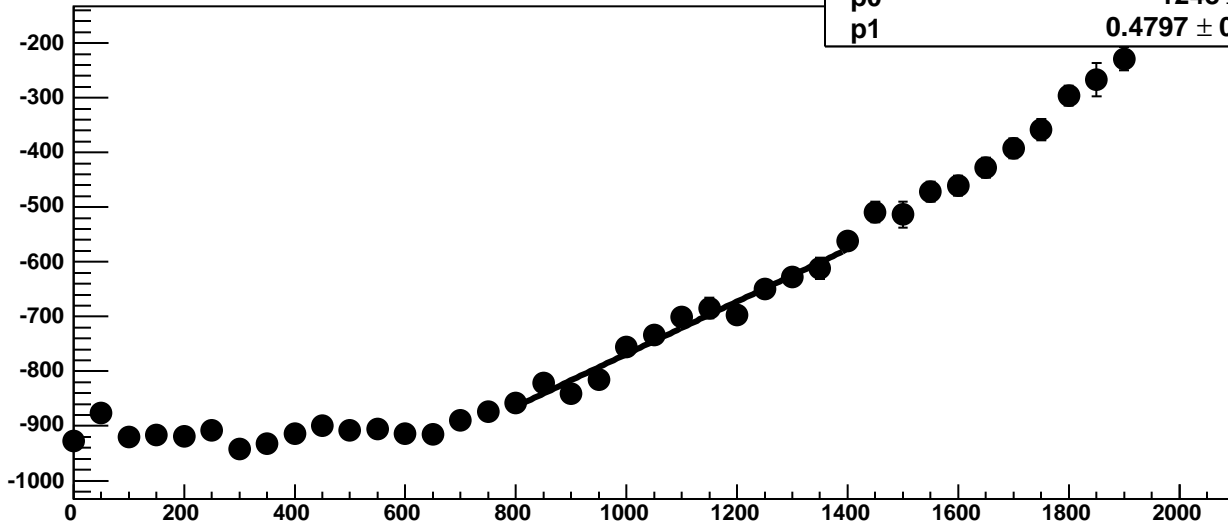
Chip 10, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC



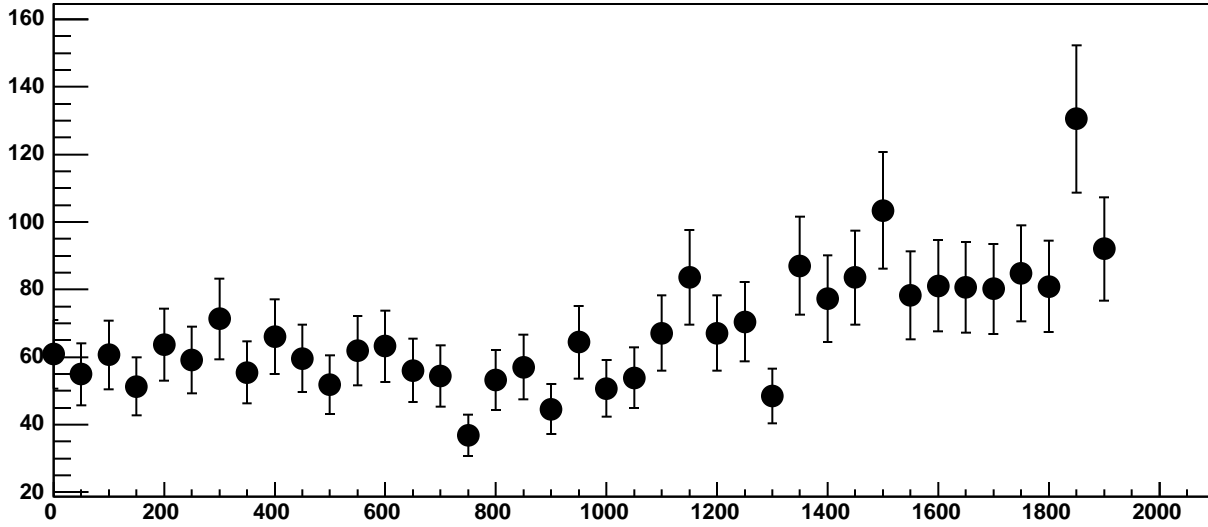
Chip 10, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC



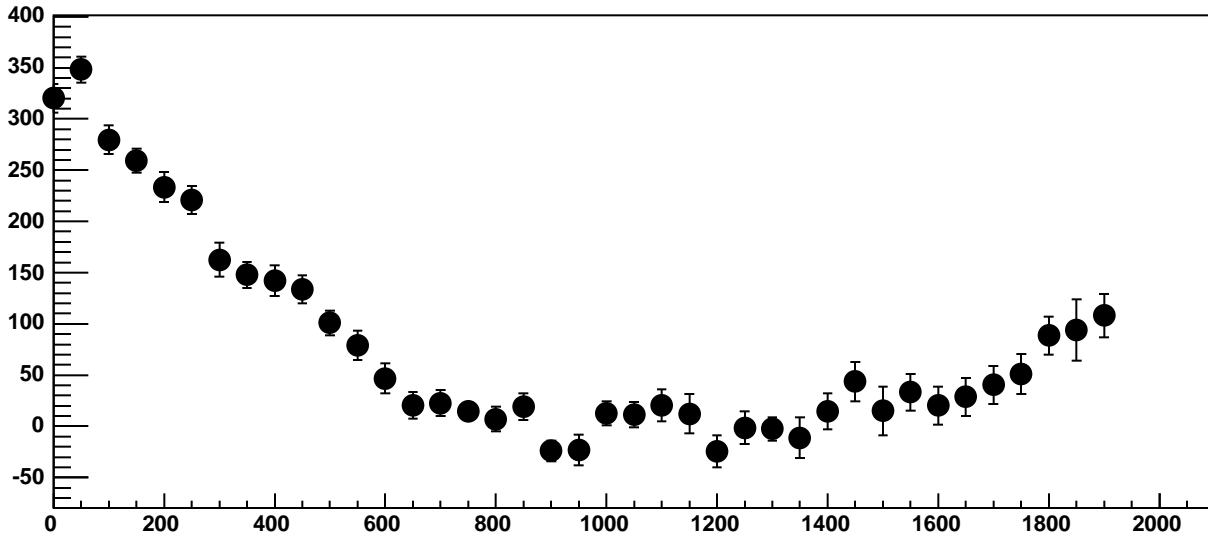
Chip 10, Channel 8, Enable 5, Hold=30, ADC Mean vs DAC



Chip 10, Channel 8, Enable 5, Hold=30, ADC Noise vs DAC



Chip 10, Channel 8, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 10, Channel 9, Enable 0!, Hold=30, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

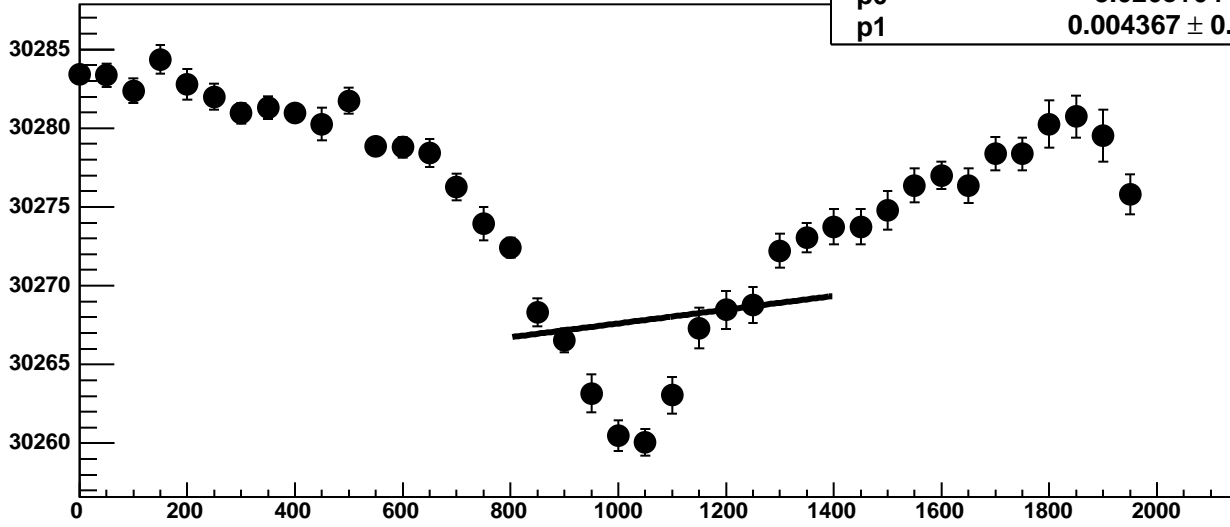
293.2 / 11

p0

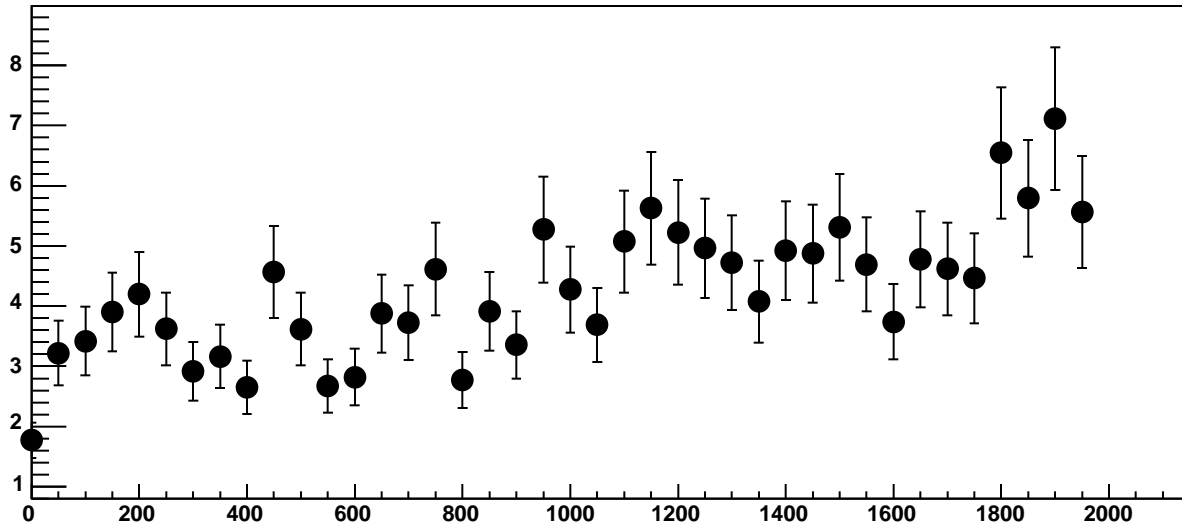
$3.026\text{e}+04 \pm 1.426$

p1

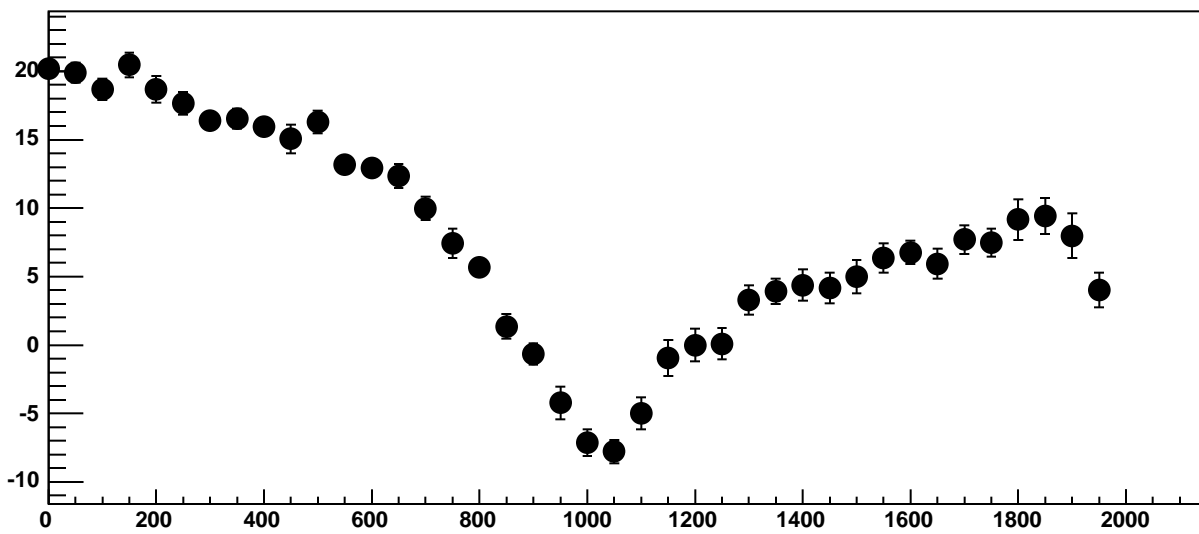
$0.004367 \pm 0.001339$



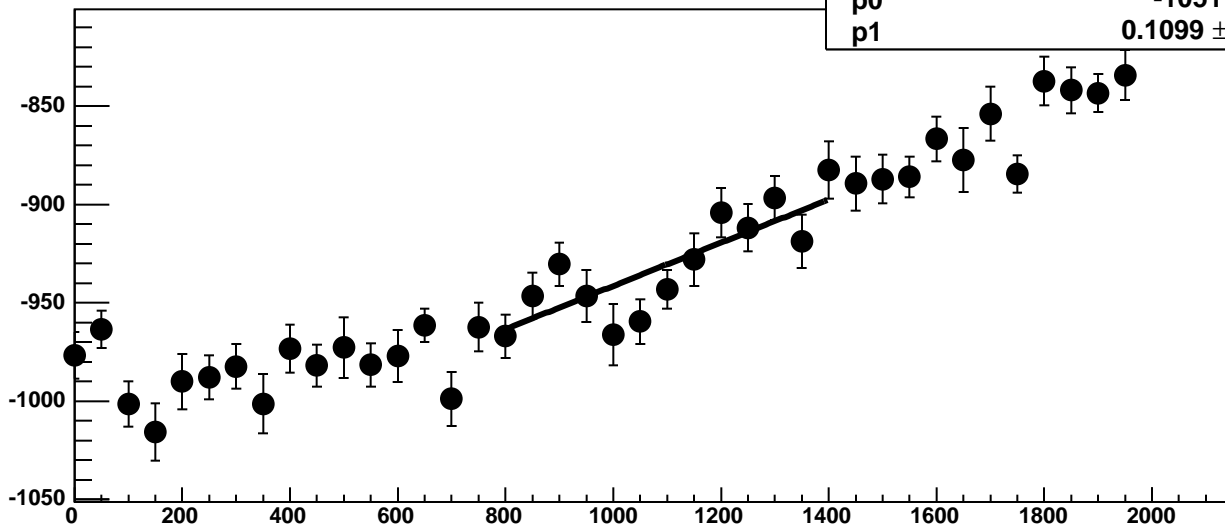
Chip 10, Channel 9, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 10, Channel 9, Enable 0!, Hold=30, ADC Residuals vs DAC

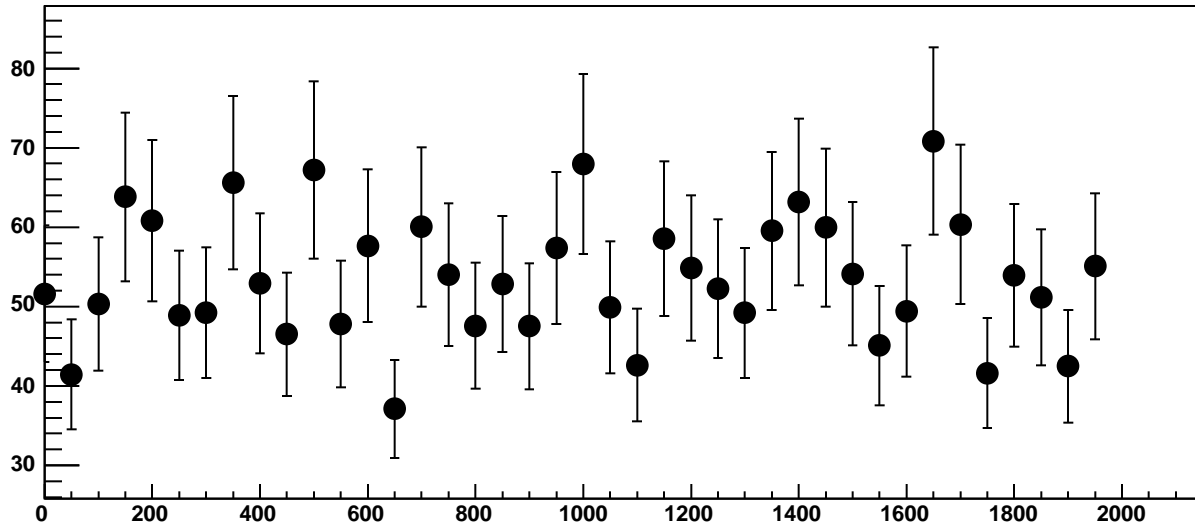


Chip 10, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC

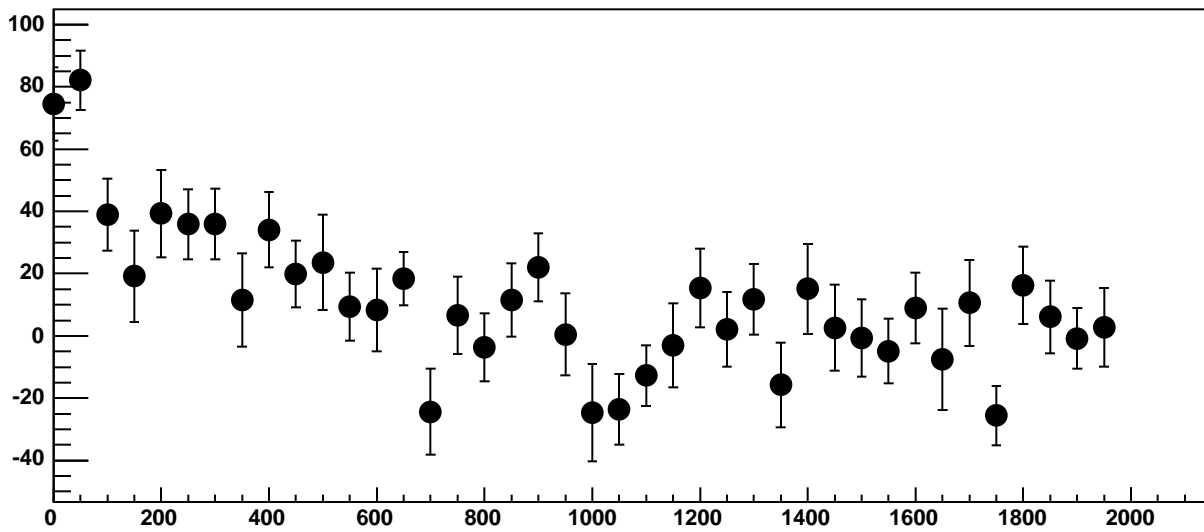


$\chi^2 / \text{ndf}$  18.64 / 11  
p0  $-1051 \pm 20.02$   
p1  $0.1099 \pm 0.0182$

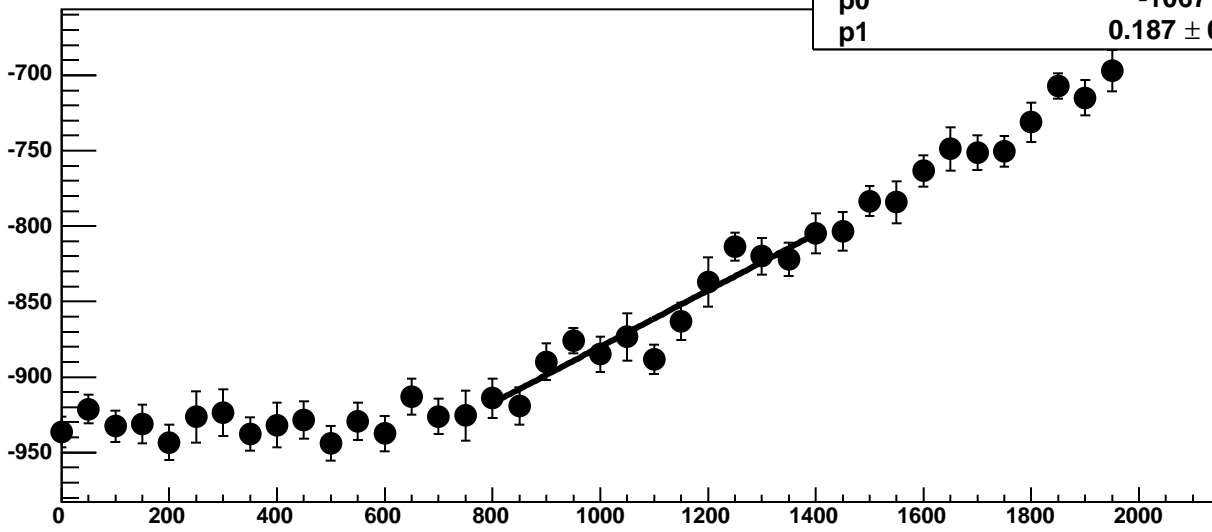
Chip 10, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC



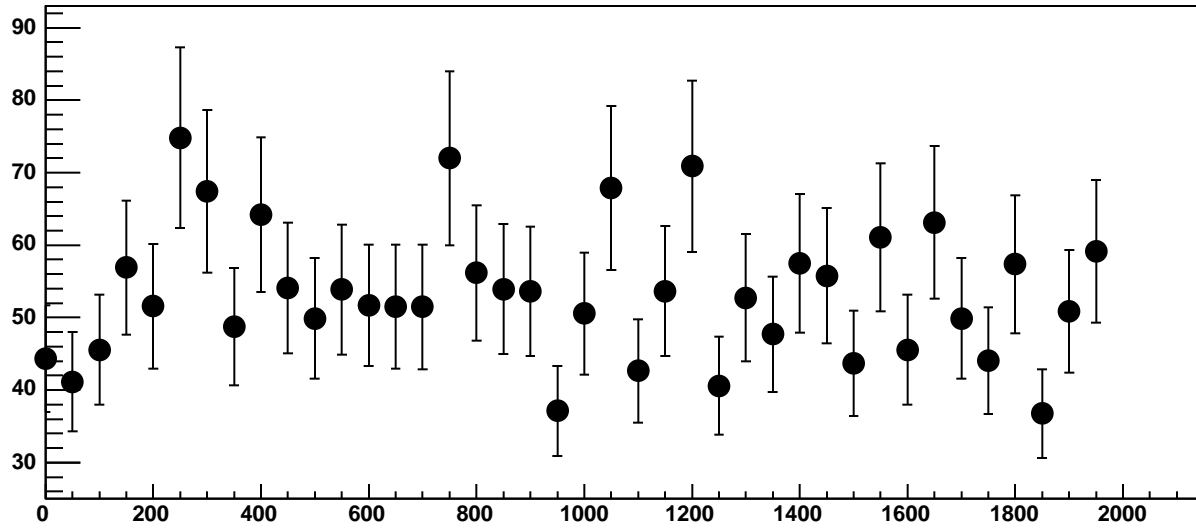
Chip 10, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC



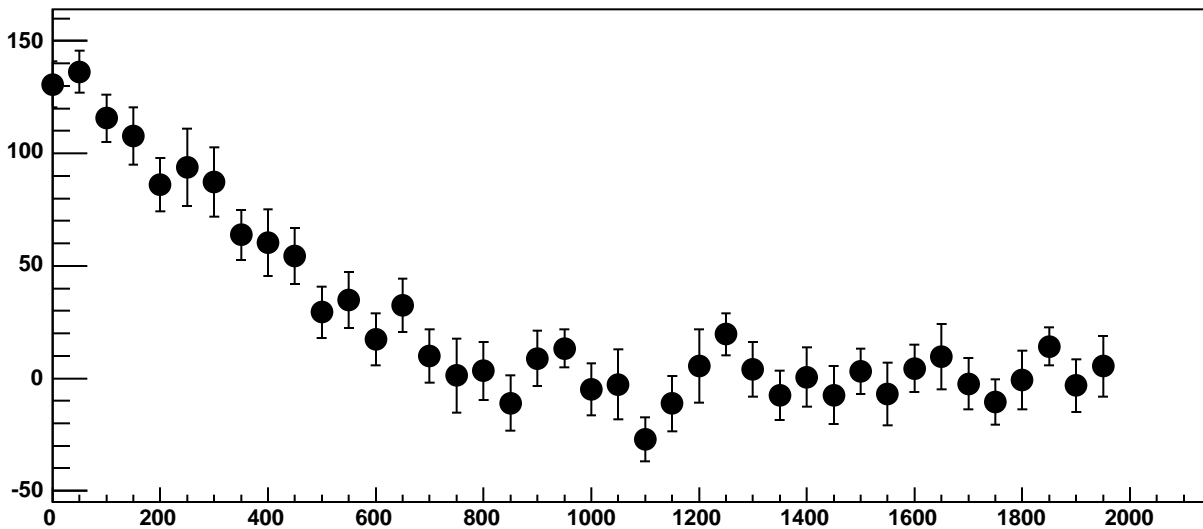
Chip 10, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC



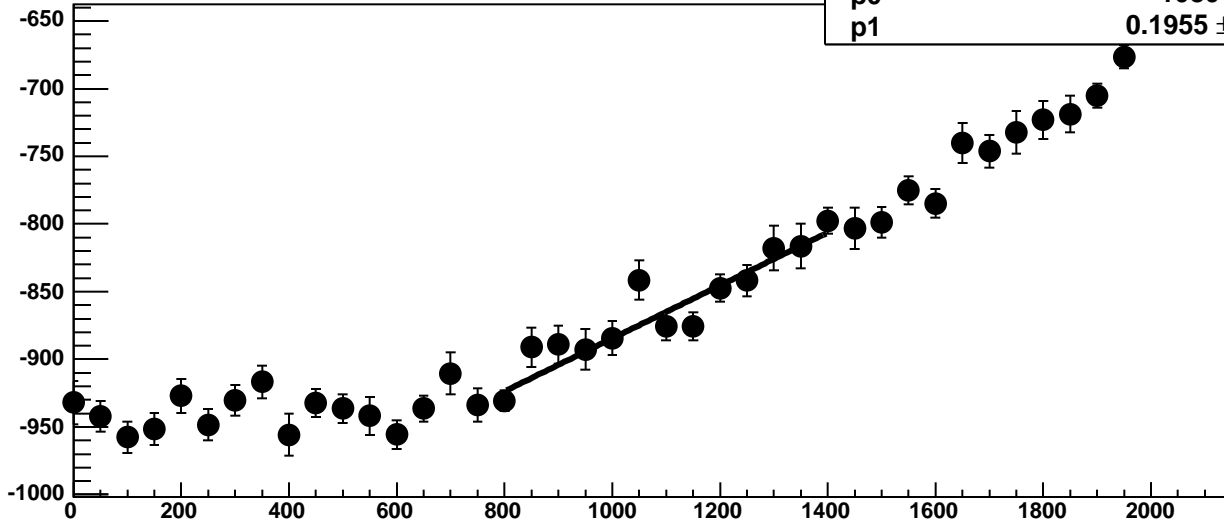
Chip 10, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC



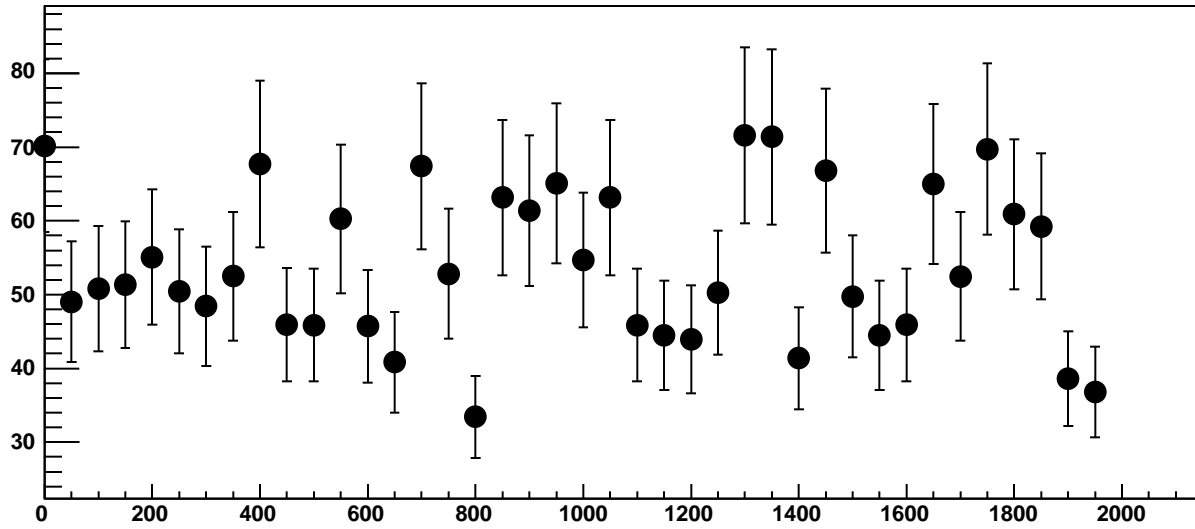
Chip 10, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC



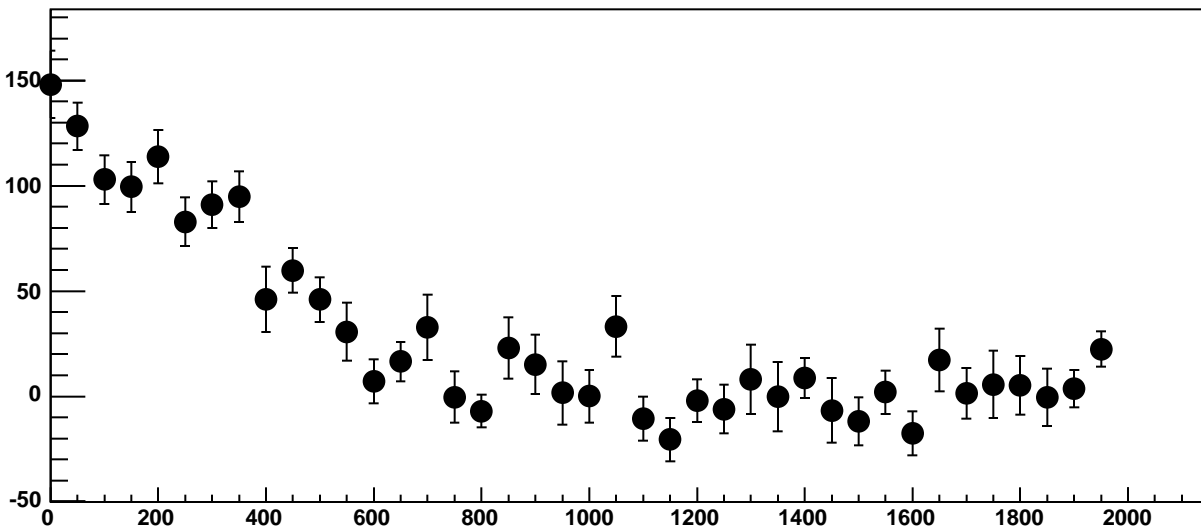
Chip 10, Channel 9, Enable 3, Hold=30, ADC Mean vs DAC



Chip 10, Channel 9, Enable 3, Hold=30, ADC Noise vs DAC

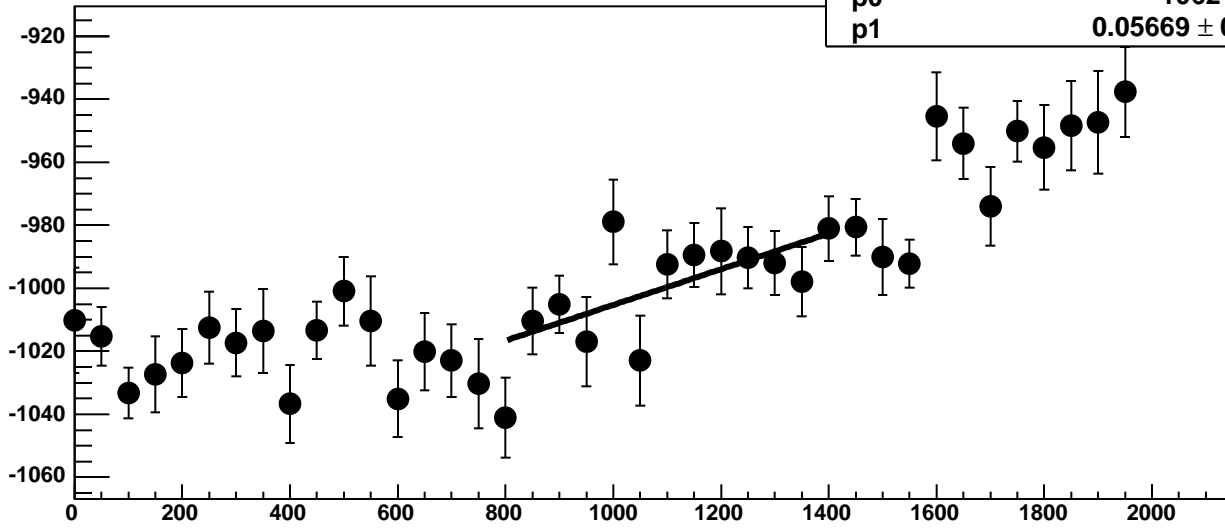


Chip 10, Channel 9, Enable 3, Hold=30, ADC Residuals vs DAC



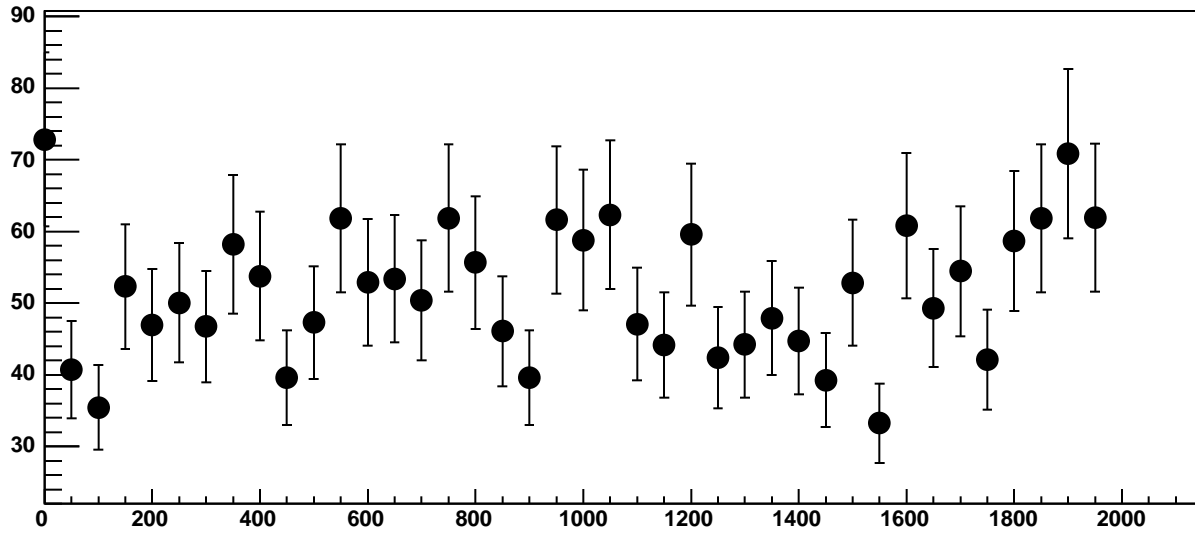


Chip 10, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC

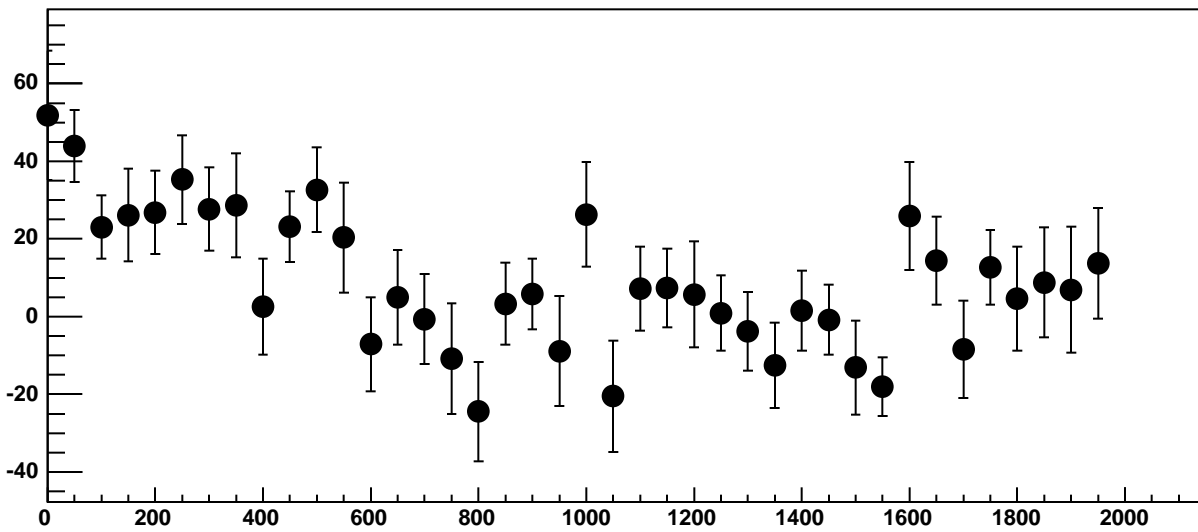


$\chi^2 / \text{ndf}$  13.05 / 11  
p0  $-1062 \pm 18.25$   
p1  $0.05669 \pm 0.01615$

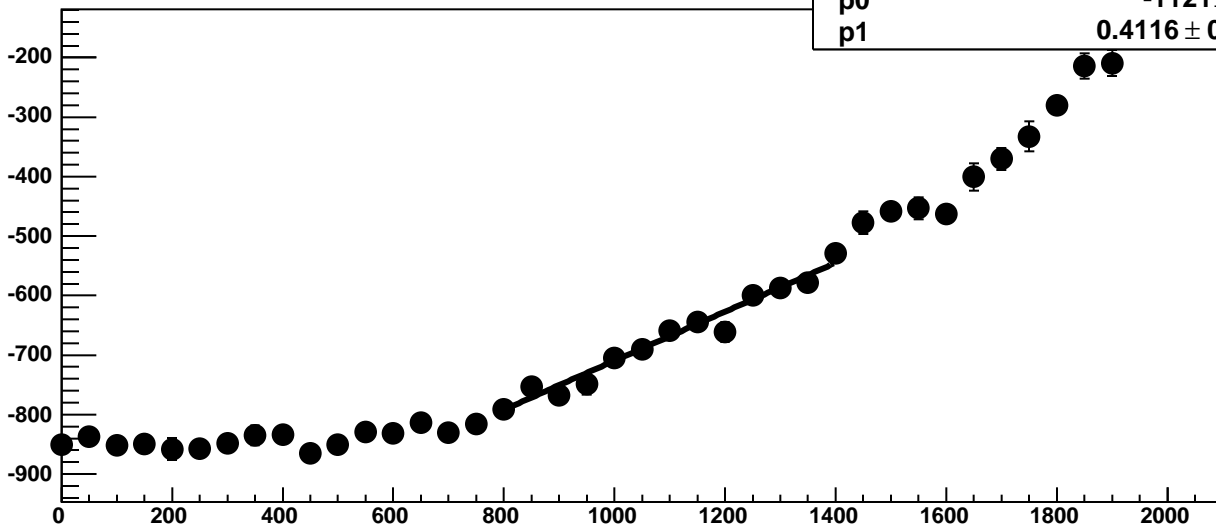
Chip 10, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC



Chip 10, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC

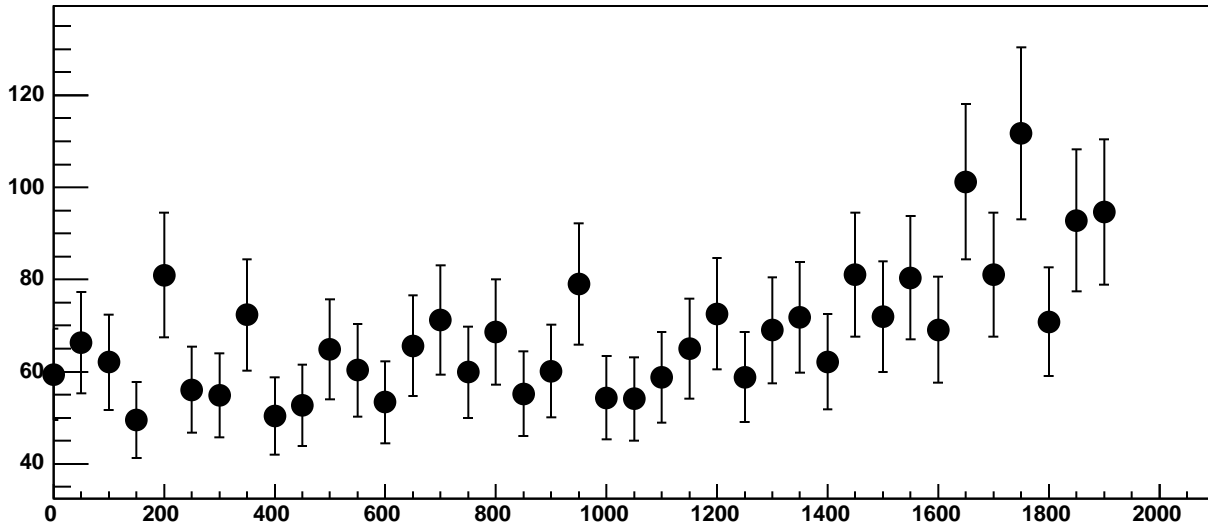


Chip 10, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC

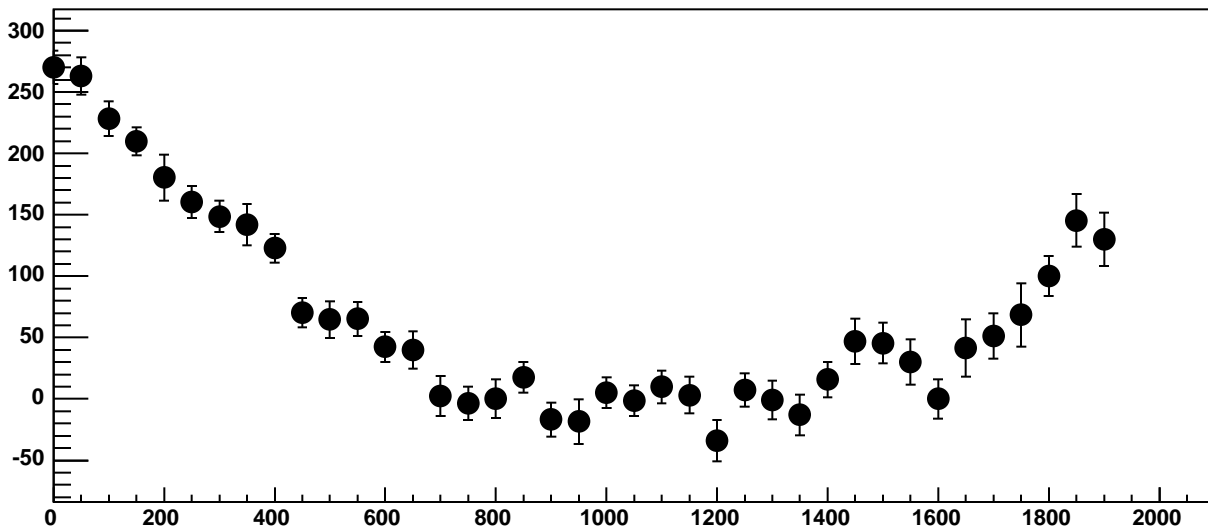


$\chi^2 / \text{ndf}$  11.43 / 11  
p0 -1121 ± 24.06  
p1 0.4116 ± 0.02177

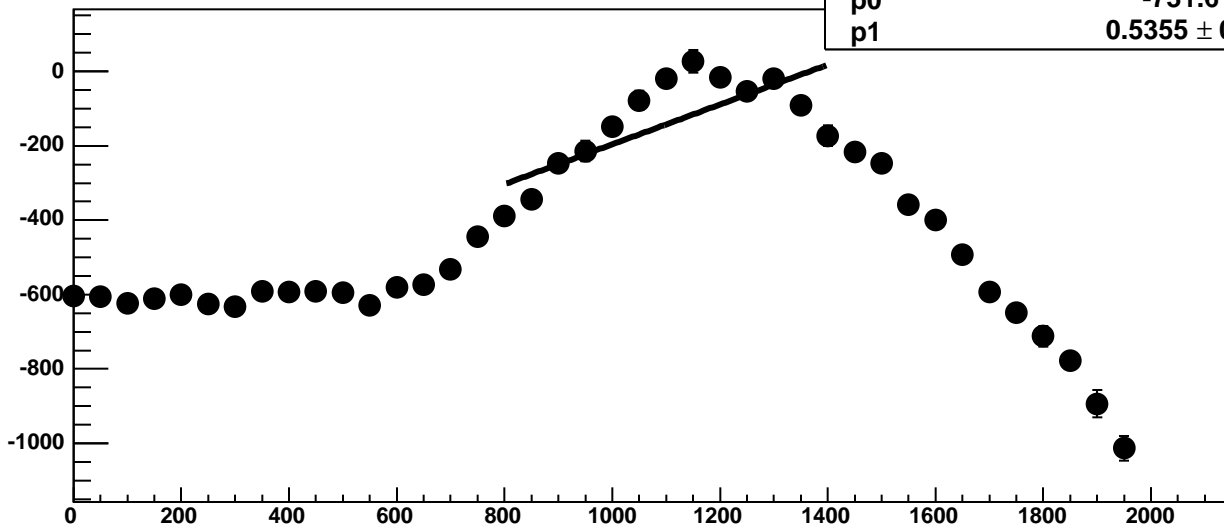
Chip 10, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC



Chip 10, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 10, Channel 10, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

189.5 / 11

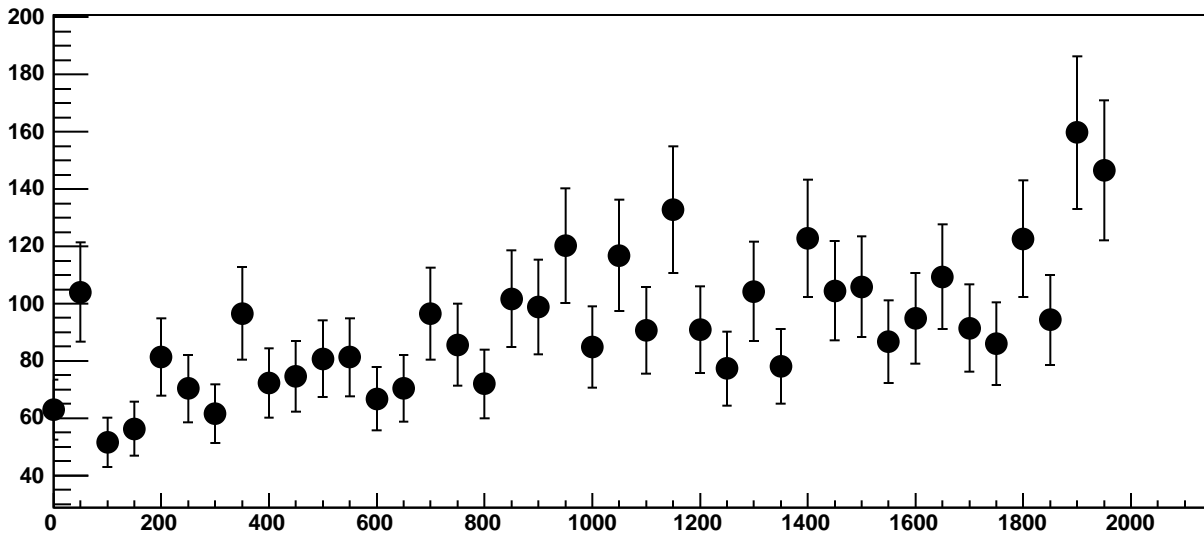
p0

$-731.6 \pm 34.16$

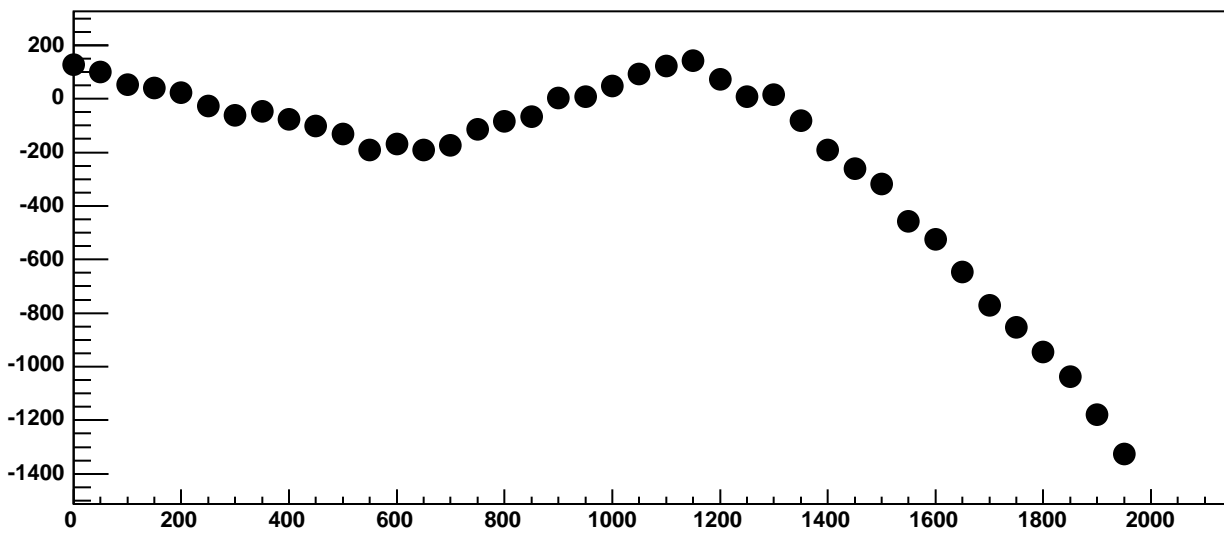
p1

$0.5355 \pm 0.03079$

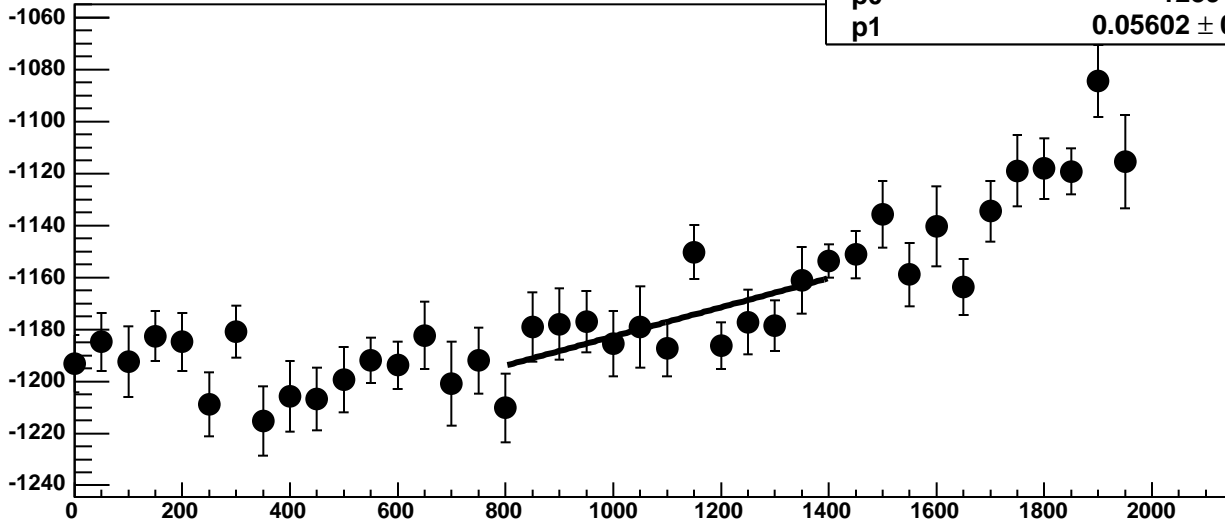
Chip 10, Channel 10, Enable 0, Hold=30, ADC Noise vs DAC



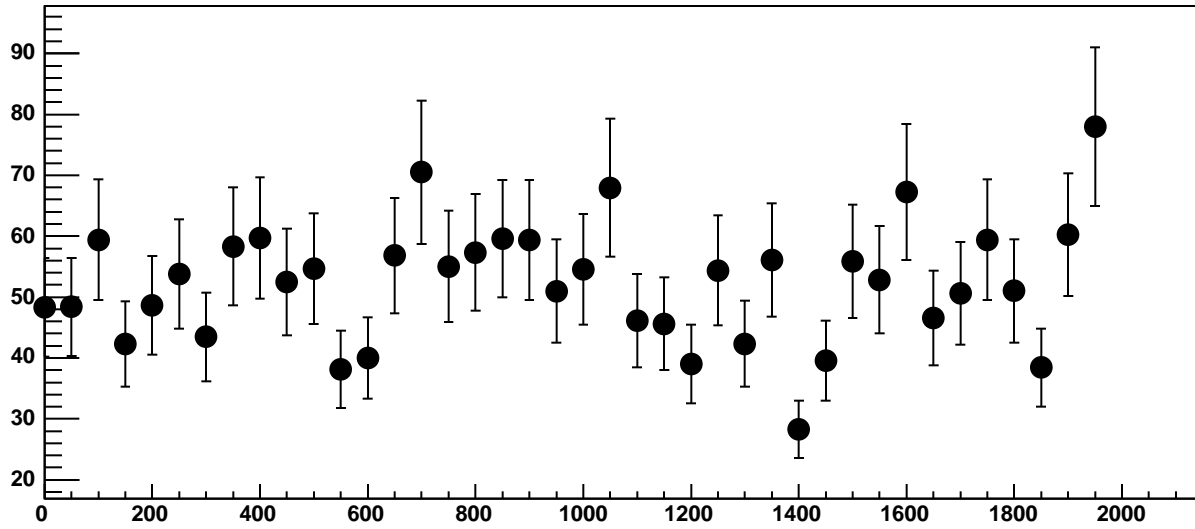
Chip 10, Channel 10, Enable 0, Hold=30, ADC Residuals vs DAC



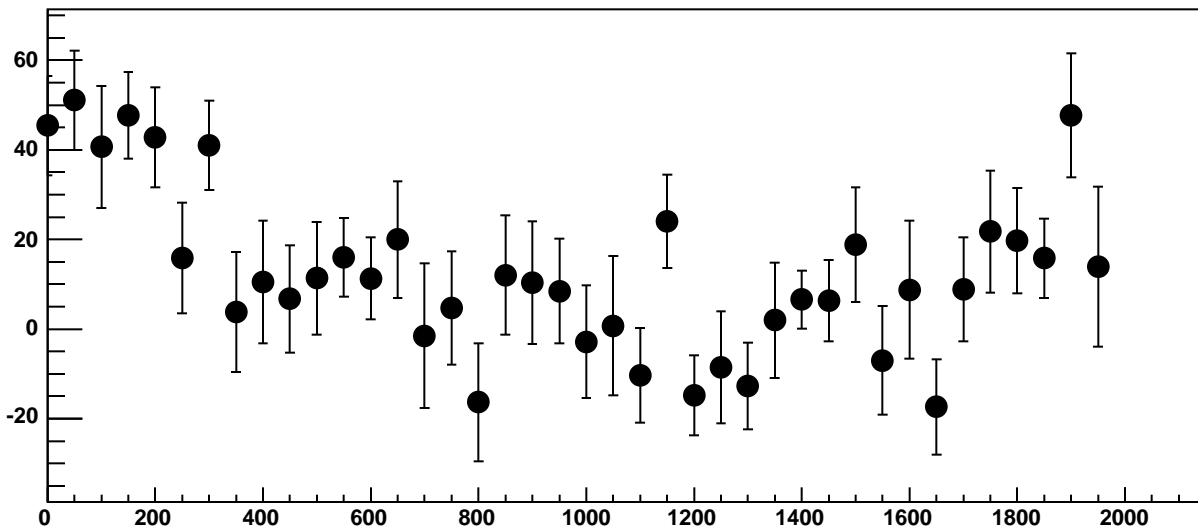
Chip 10, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC



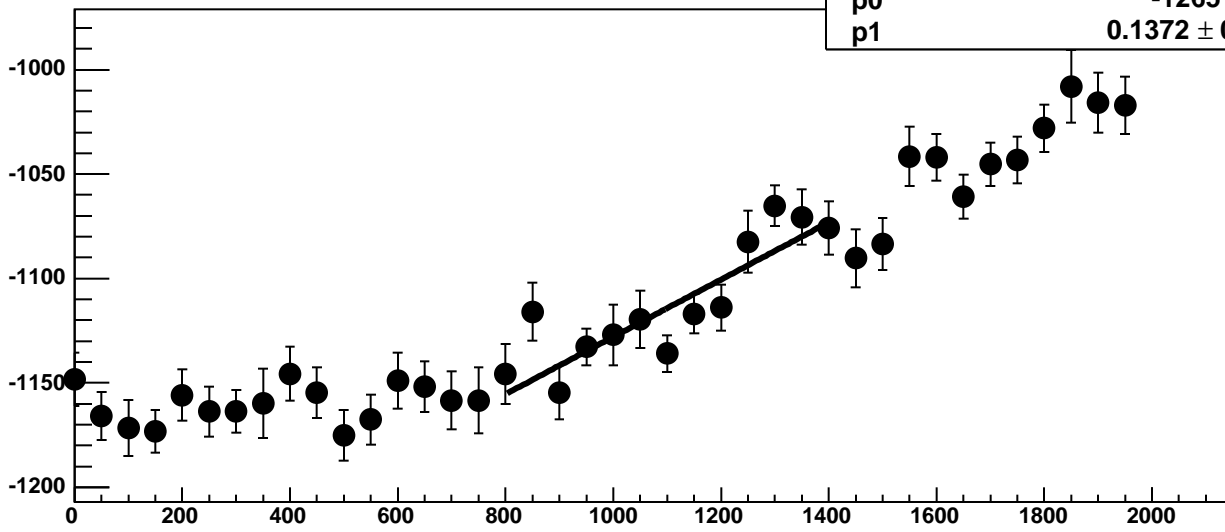
Chip 10, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC



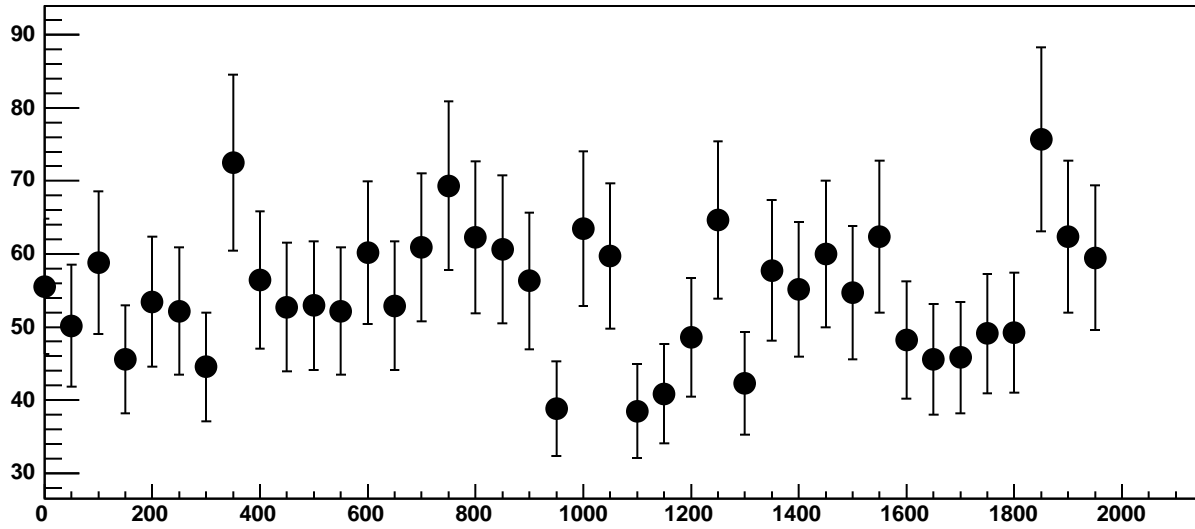
Chip 10, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC



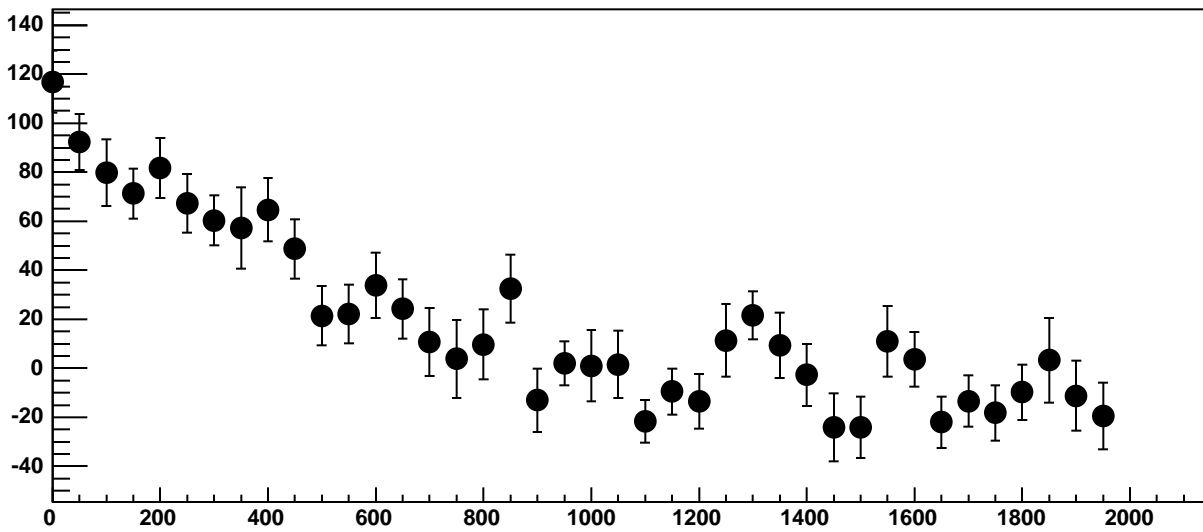
Chip 10, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC



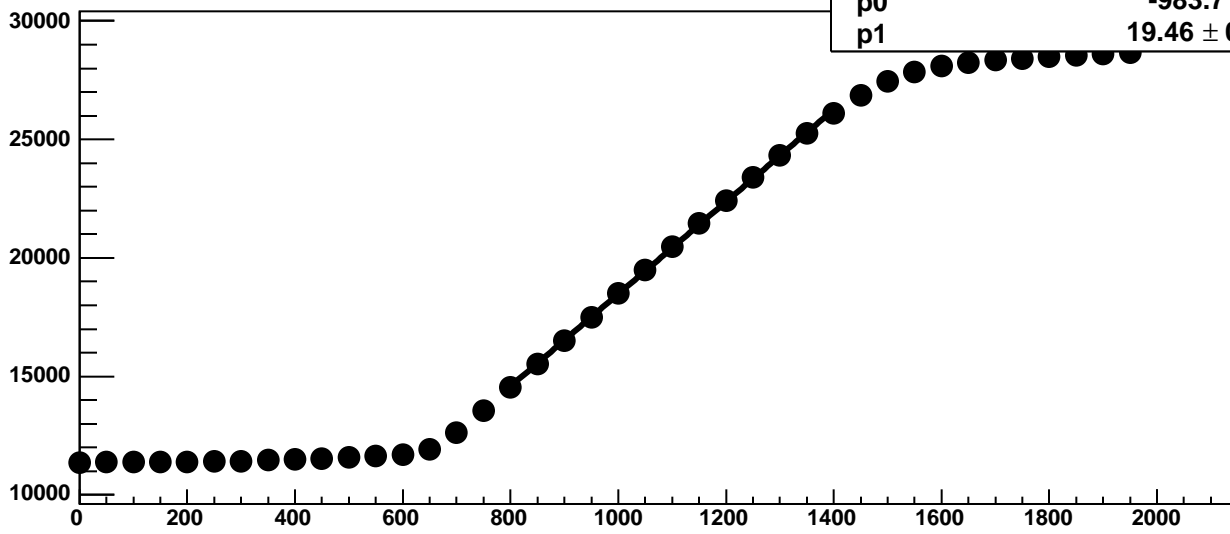
Chip 10, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC



Chip 10, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC

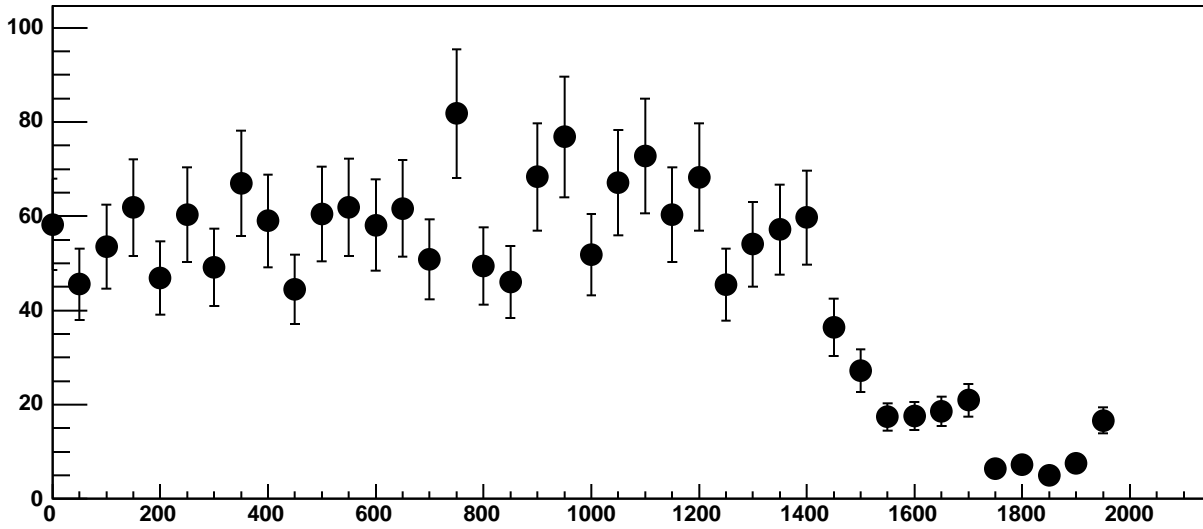


Chip 10, Channel 10, Enable 3!, Hold=30, ADC Mean vs DAC

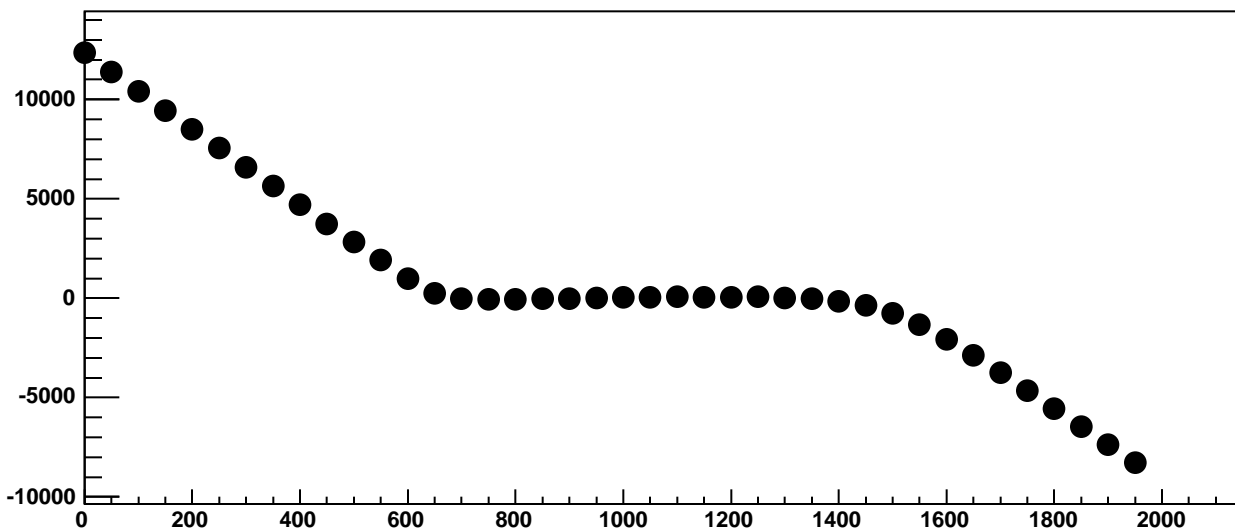


$\chi^2 / \text{ndf}$  254.4 / 11  
p0  $-983.7 \pm 20.53$   
p1  $19.46 \pm 0.01845$

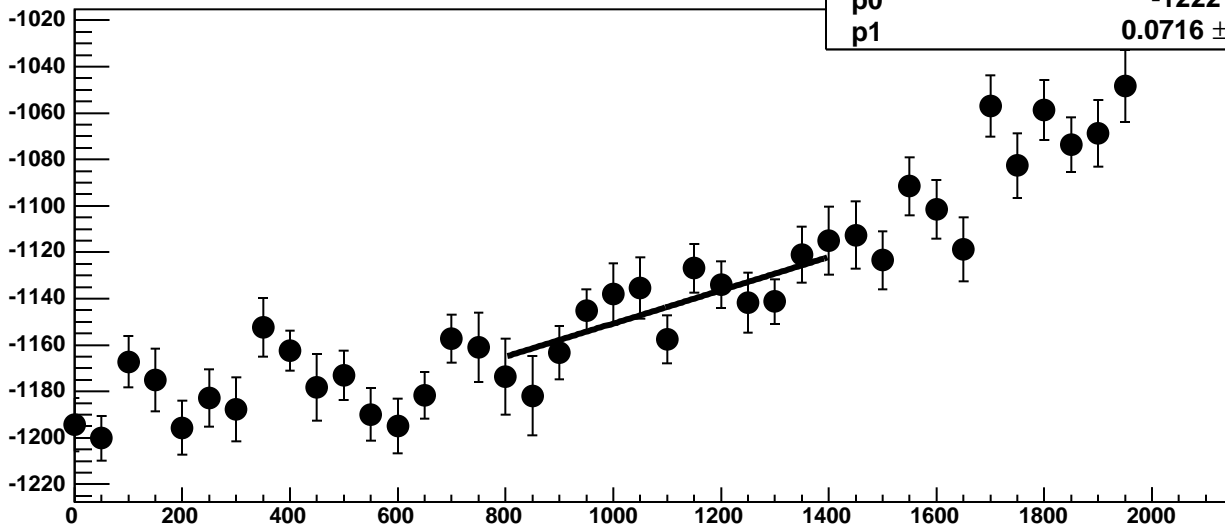
Chip 10, Channel 10, Enable 3!, Hold=30, ADC Noise vs DAC



Chip 10, Channel 10, Enable 3!, Hold=30, ADC Residuals vs DAC

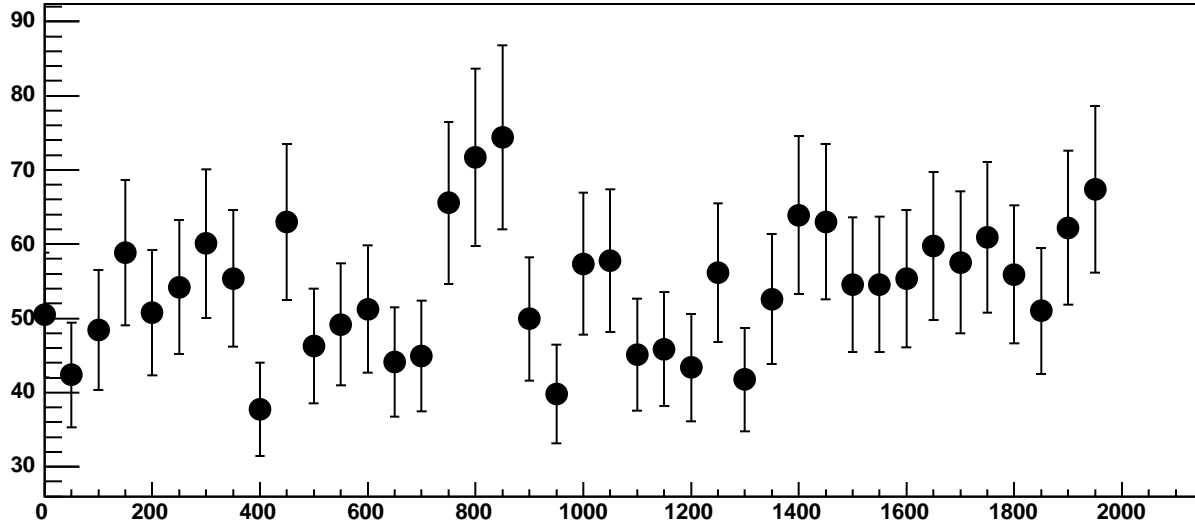


Chip 10, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC

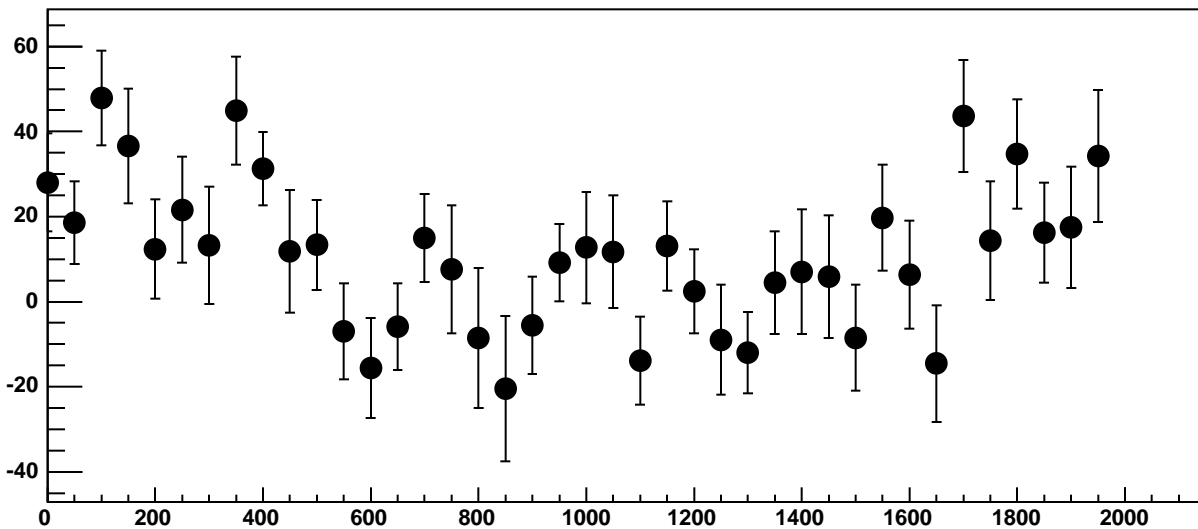


$\chi^2 / \text{ndf}$  10.48 / 11  
p0  $-1222 \pm 21.67$   
p1  $0.0716 \pm 0.0192$

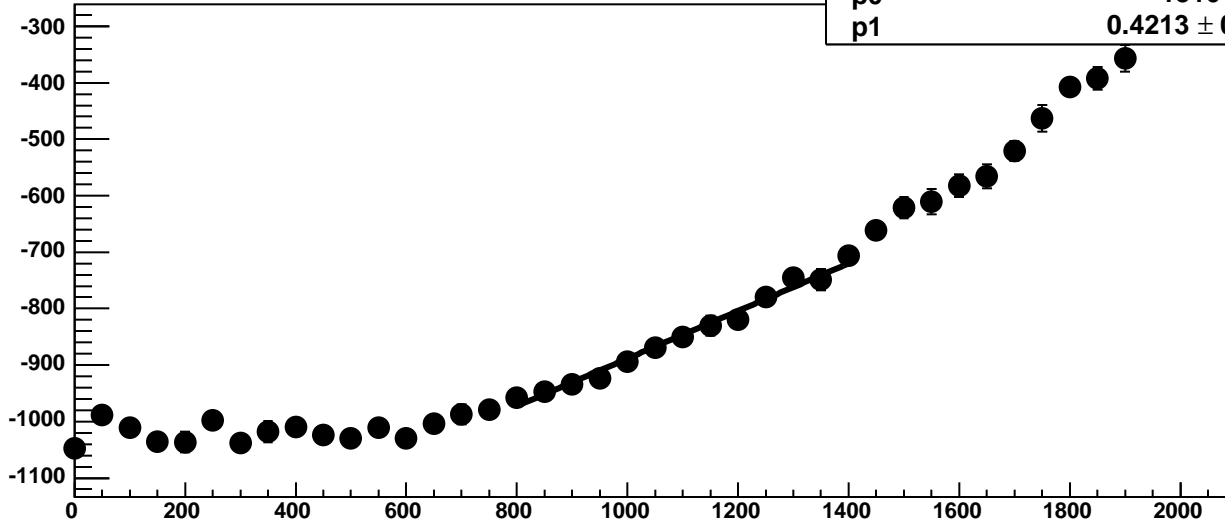
Chip 10, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC



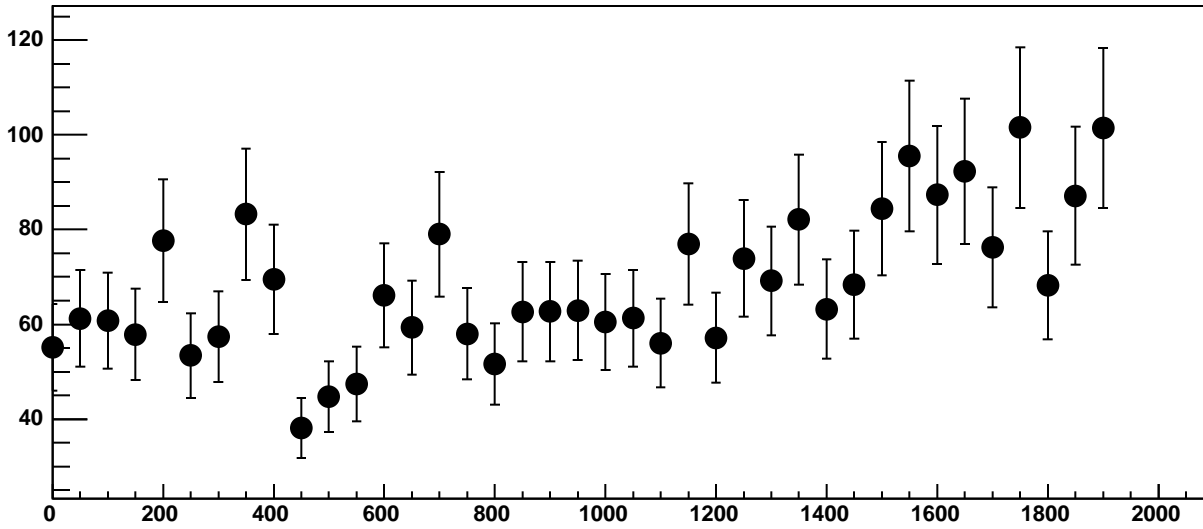
Chip 10, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC



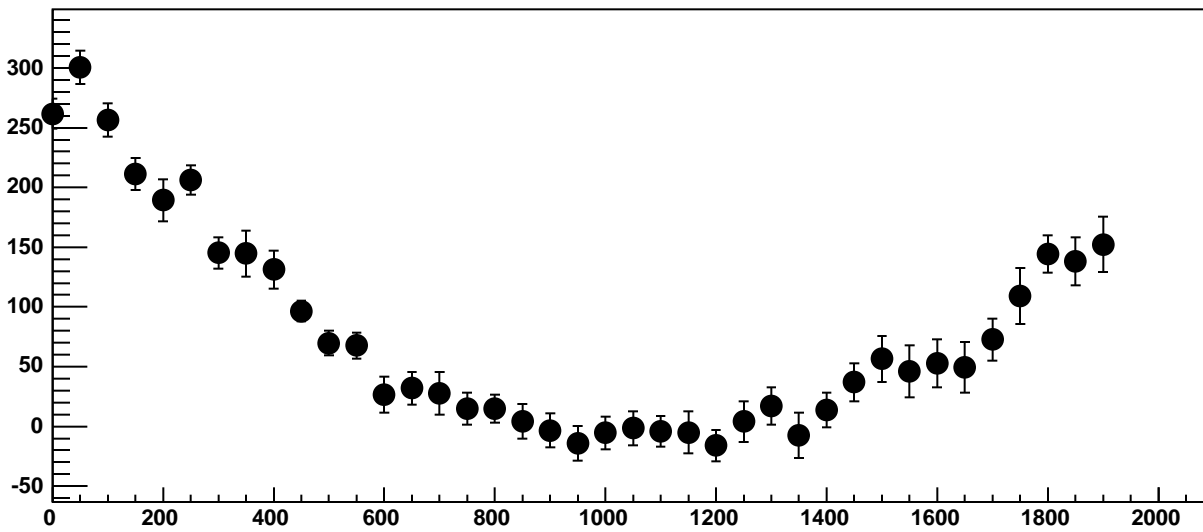
Chip 10, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC



Chip 10, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC



Chip 10, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC





Chip 10, Channel 11, Enable 0!, Hold=30, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

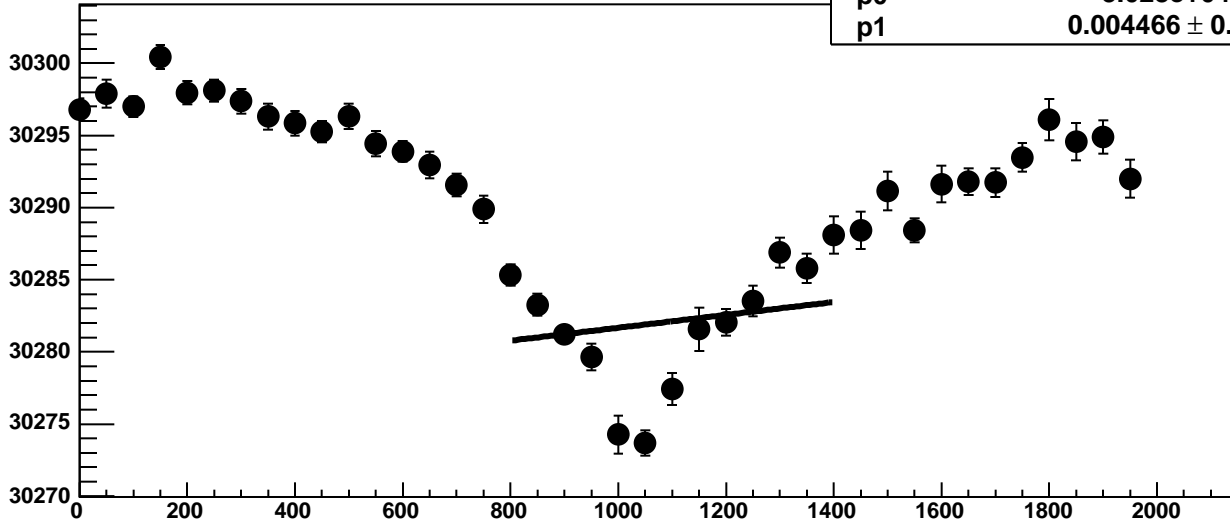
219.2 / 11

p0

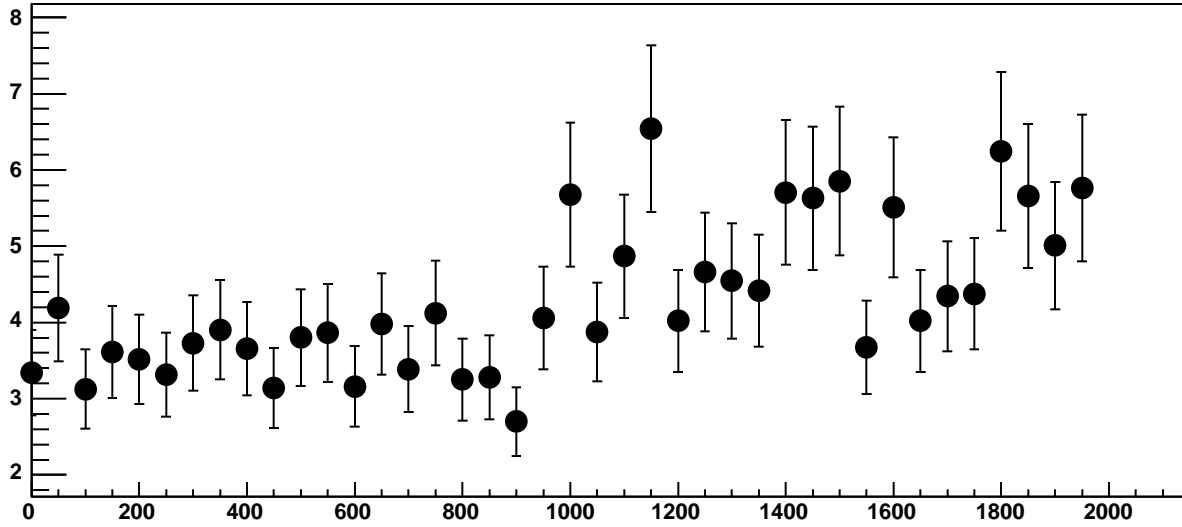
$3.028\text{e}+04 \pm 1.44$

p1

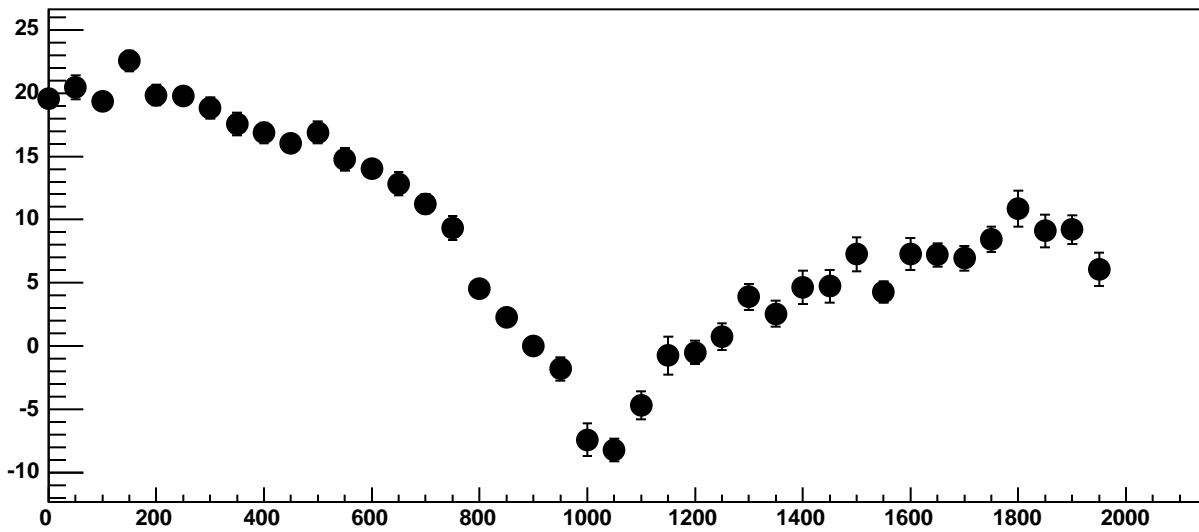
$0.004466 \pm 0.001364$



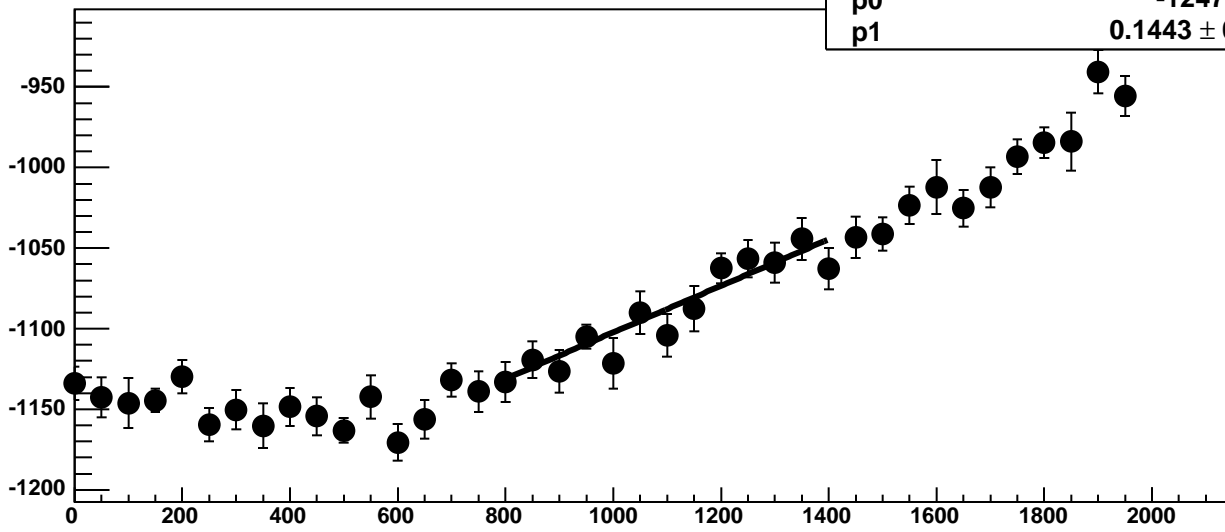
Chip 10, Channel 11, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 10, Channel 11, Enable 0!, Hold=30, ADC Residuals vs DAC

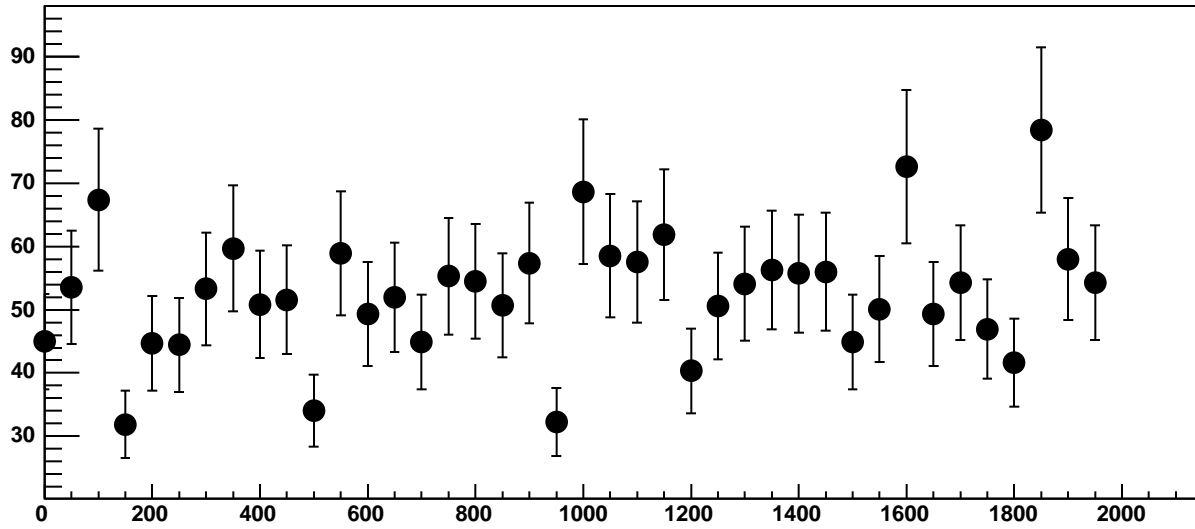


Chip 10, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC

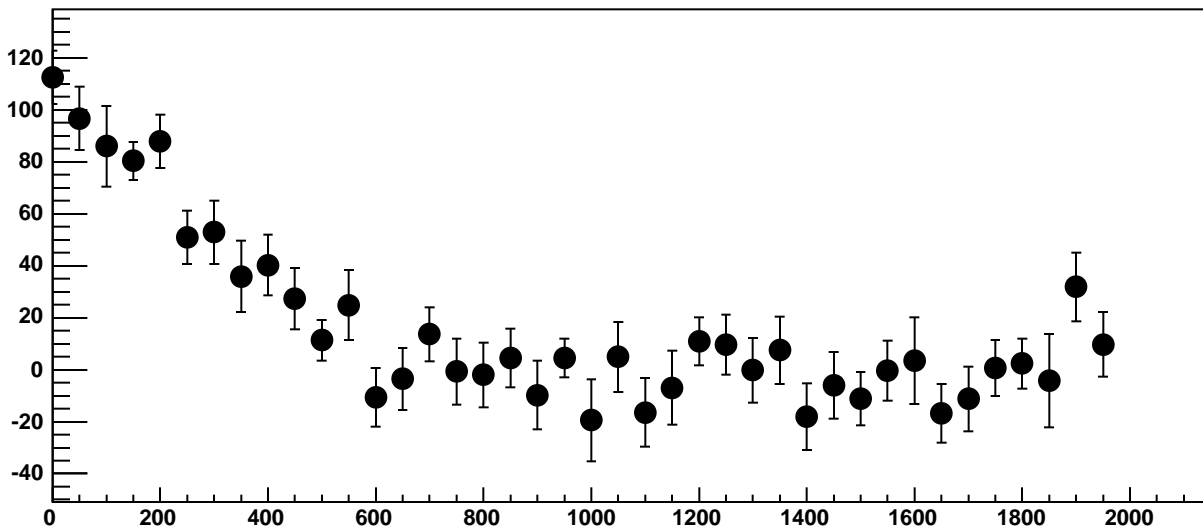


$\chi^2 / \text{ndf}$  8.963 / 11  
p0 -1247 ± 19.41  
p1 0.1443 ± 0.01761

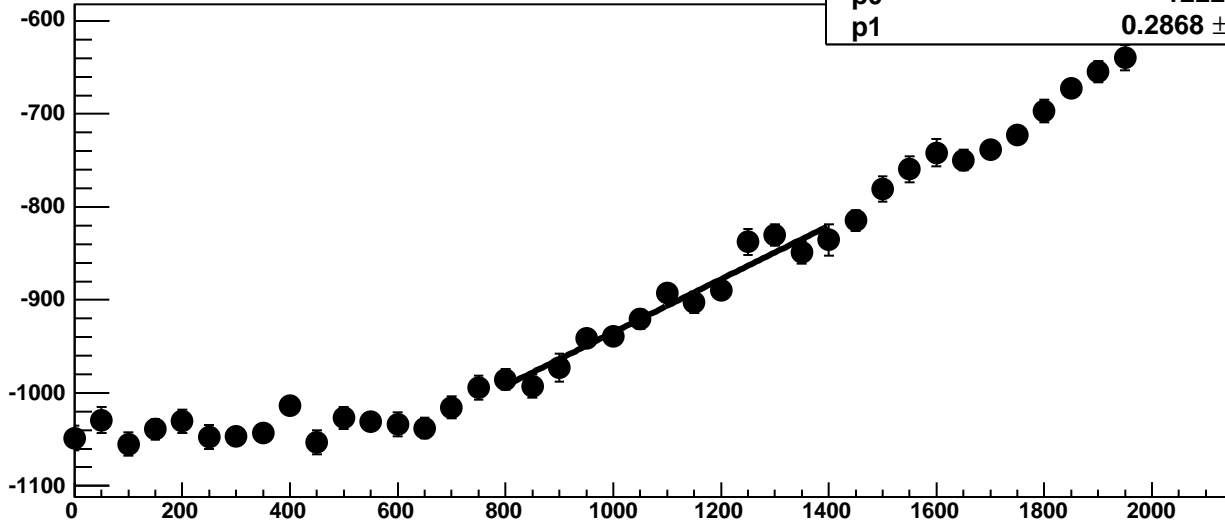
Chip 10, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC



Chip 10, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC

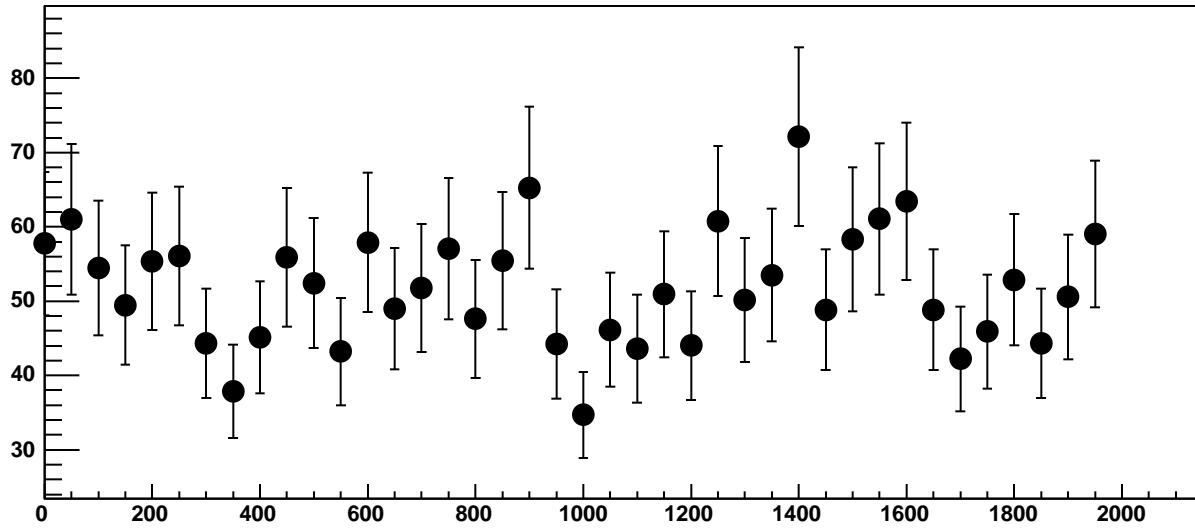


Chip 10, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC

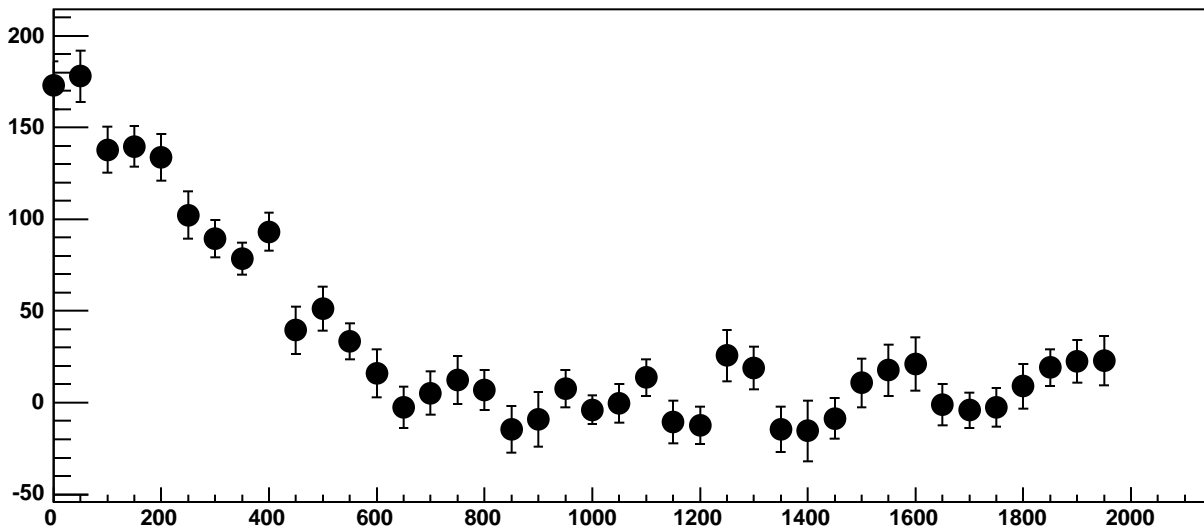


$\chi^2 / \text{ndf}$  15.4 / 11  
p0  $-1222 \pm 20.11$   
p1  $0.2868 \pm 0.0184$

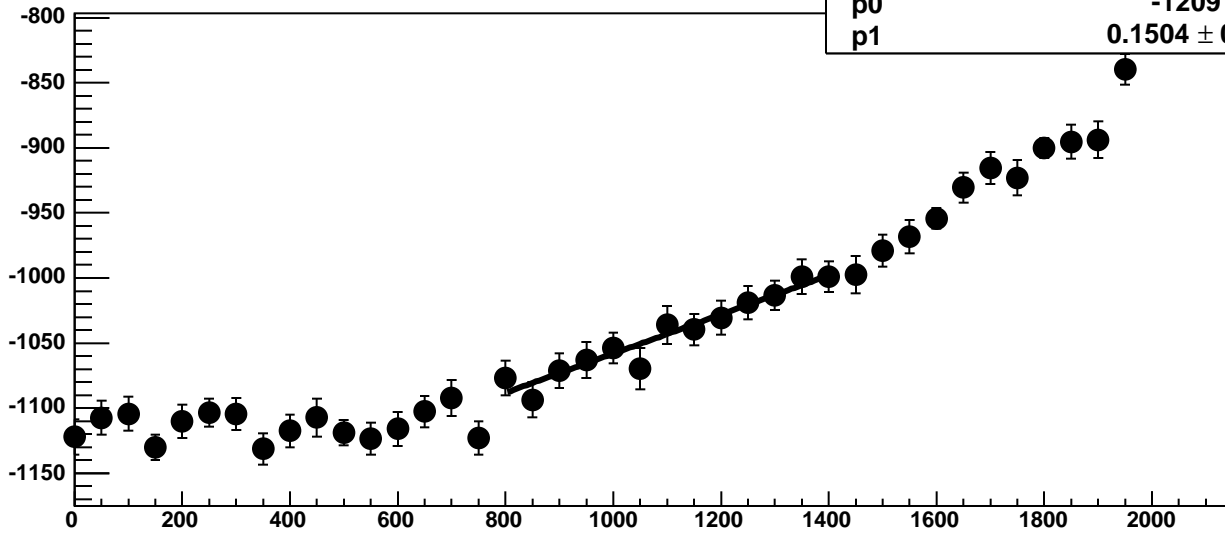
Chip 10, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC



Chip 10, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC

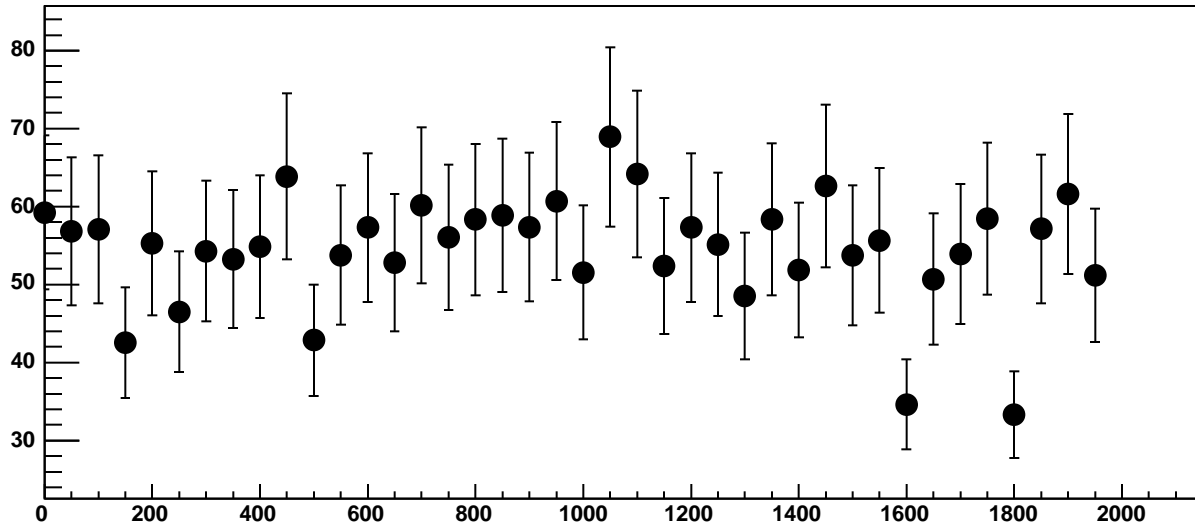


Chip 10, Channel 11, Enable 3, Hold=30, ADC Mean vs DAC

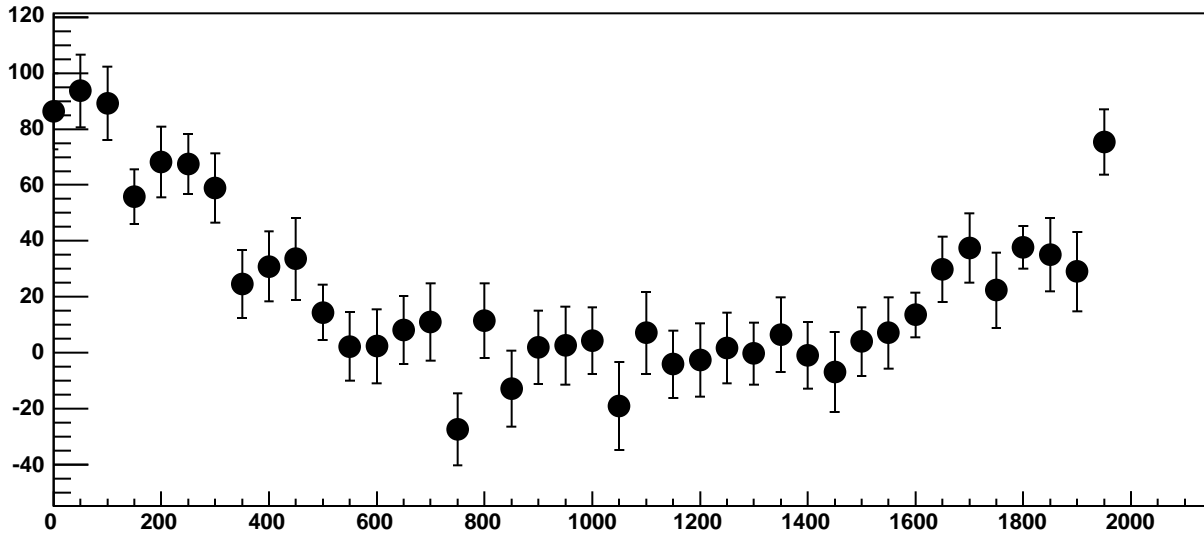


$\chi^2 / \text{ndf}$  3.933 / 11  
p0  $-1209 \pm 21.43$   
p1  $0.1504 \pm 0.01896$

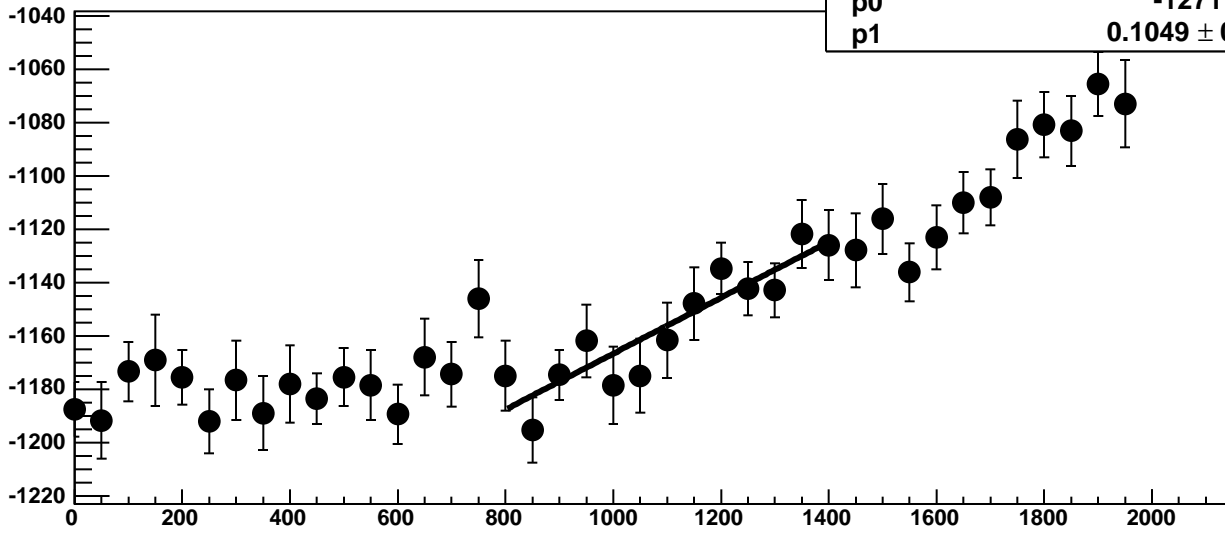
Chip 10, Channel 11, Enable 3, Hold=30, ADC Noise vs DAC



Chip 10, Channel 11, Enable 3, Hold=30, ADC Residuals vs DAC

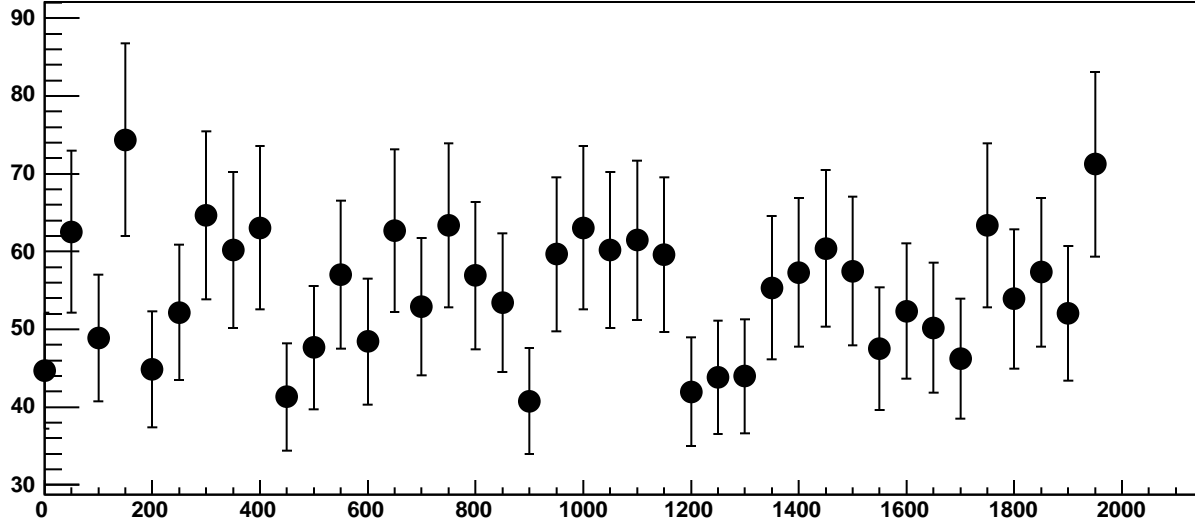


Chip 10, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC

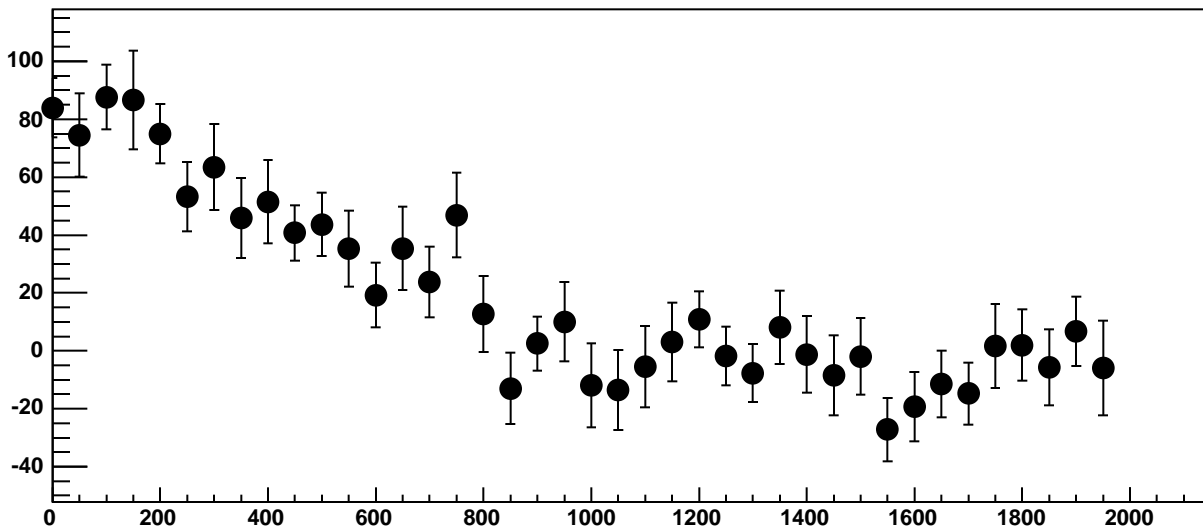


$\chi^2 / \text{ndf}$  6.831 / 11  
p0  $-1271 \pm 19.82$   
p1  $0.1049 \pm 0.01762$

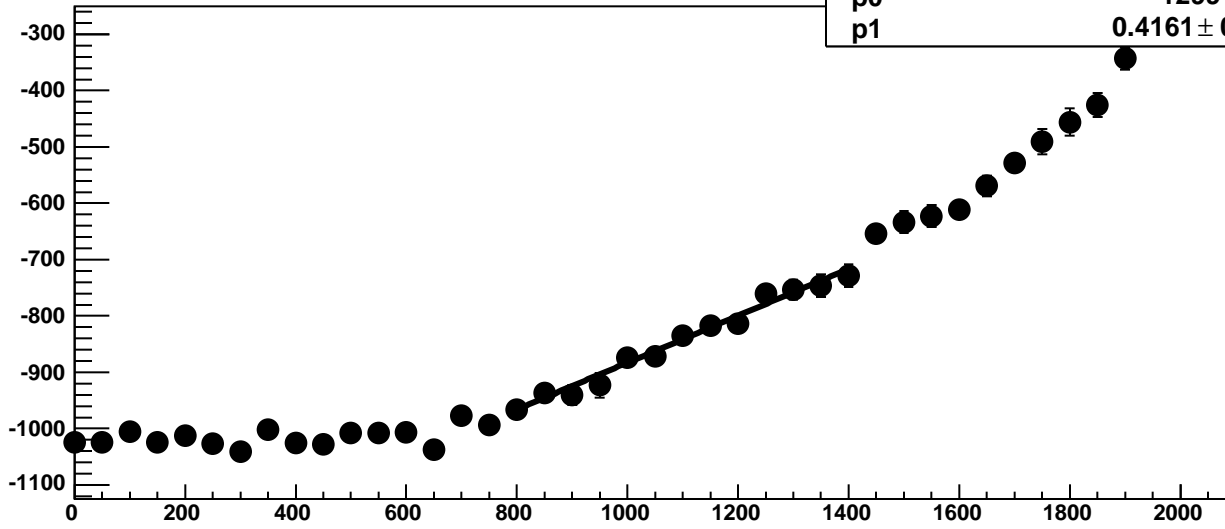
Chip 10, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC



Chip 10, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC

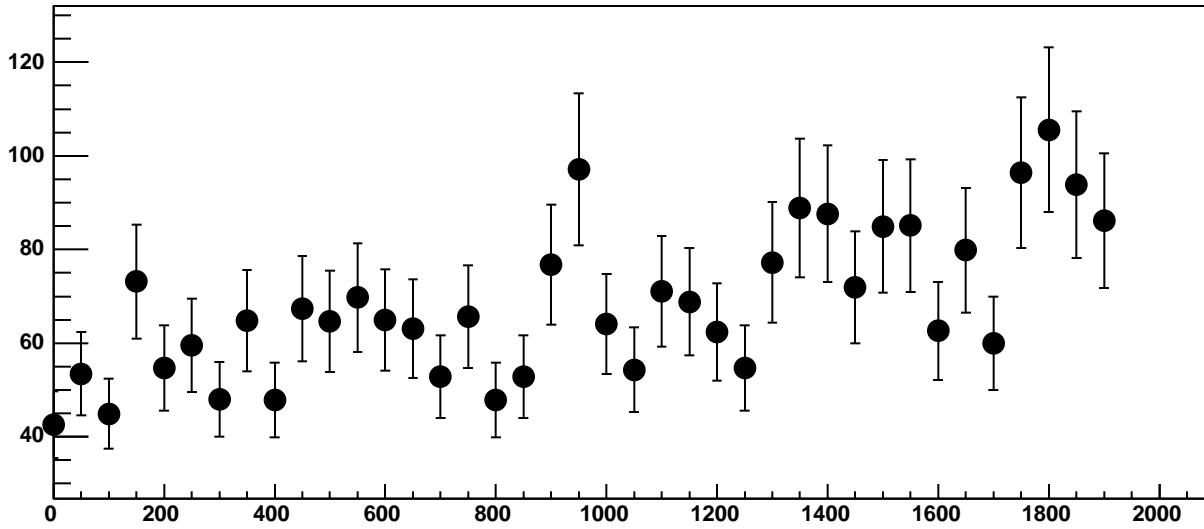


Chip 10, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC

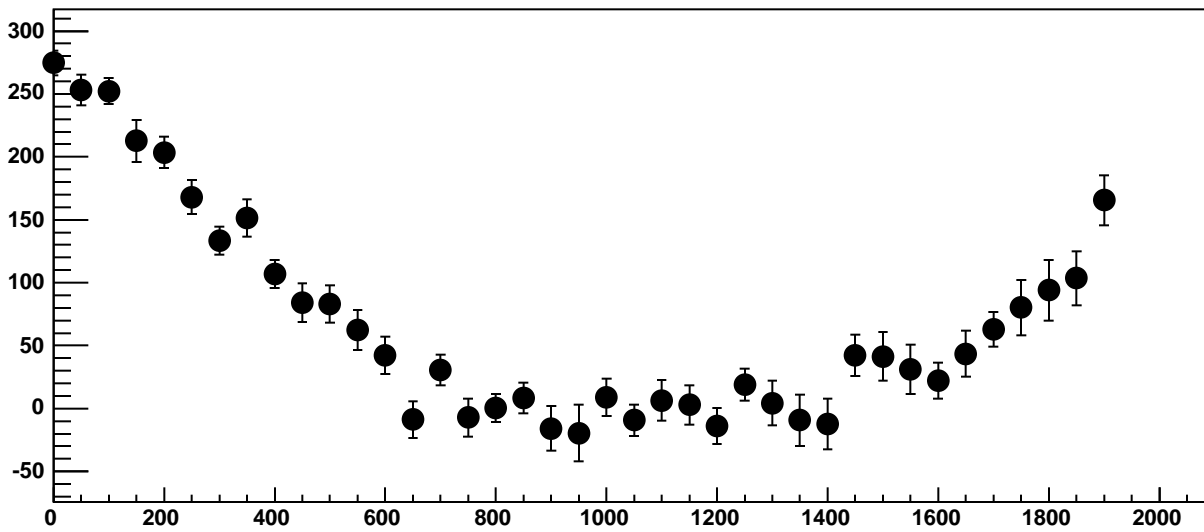


$\chi^2 / \text{ndf}$  7.033 / 11  
p0  $-1299 \pm 23.97$   
p1  $0.4161 \pm 0.02226$

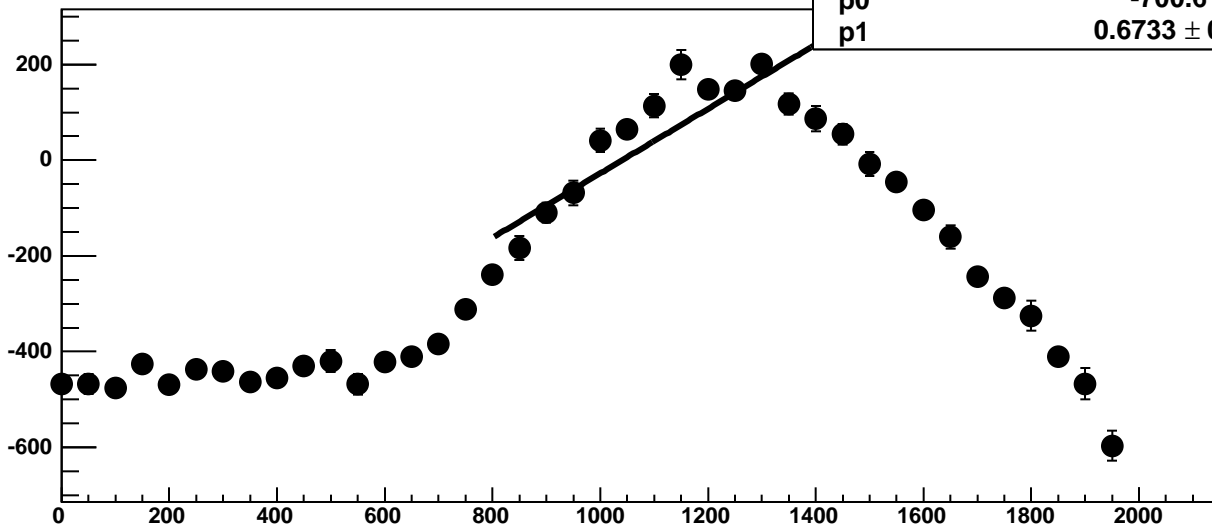
Chip 10, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC



Chip 10, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 10, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

120.1 / 11

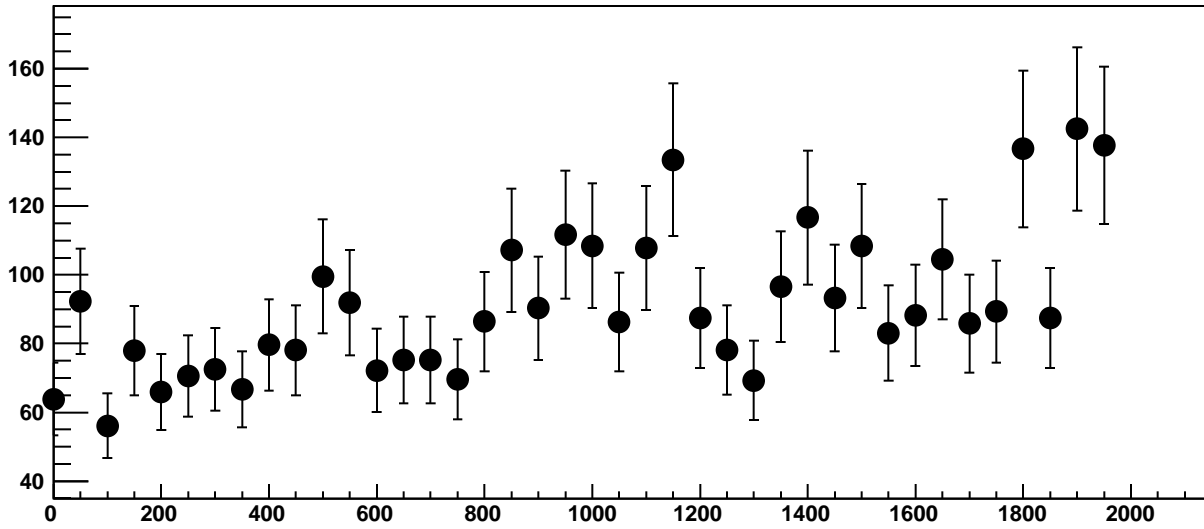
p0

$-700.6 \pm 35.88$

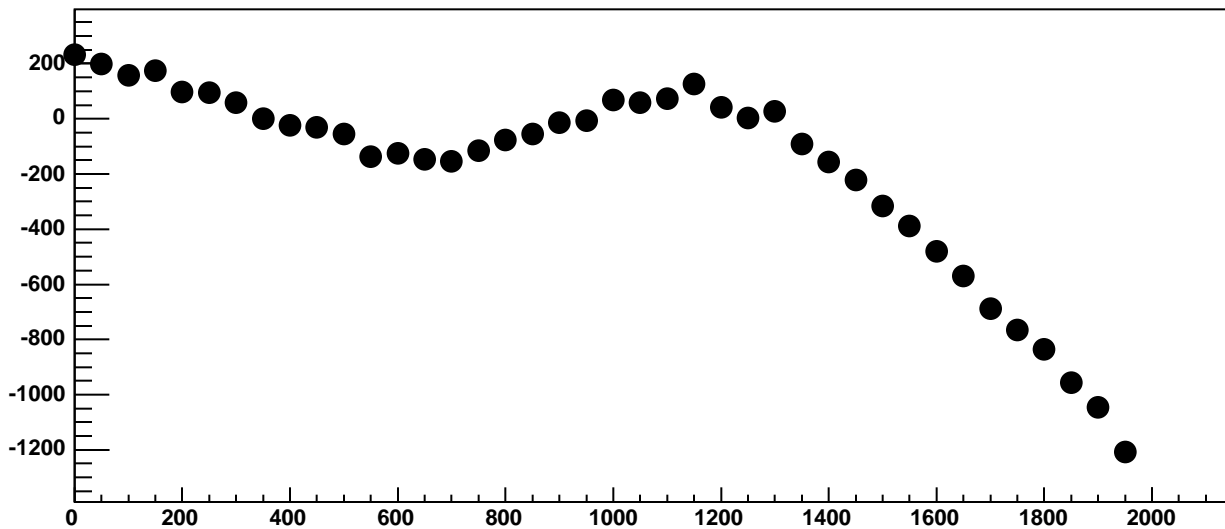
p1

$0.6733 \pm 0.03182$

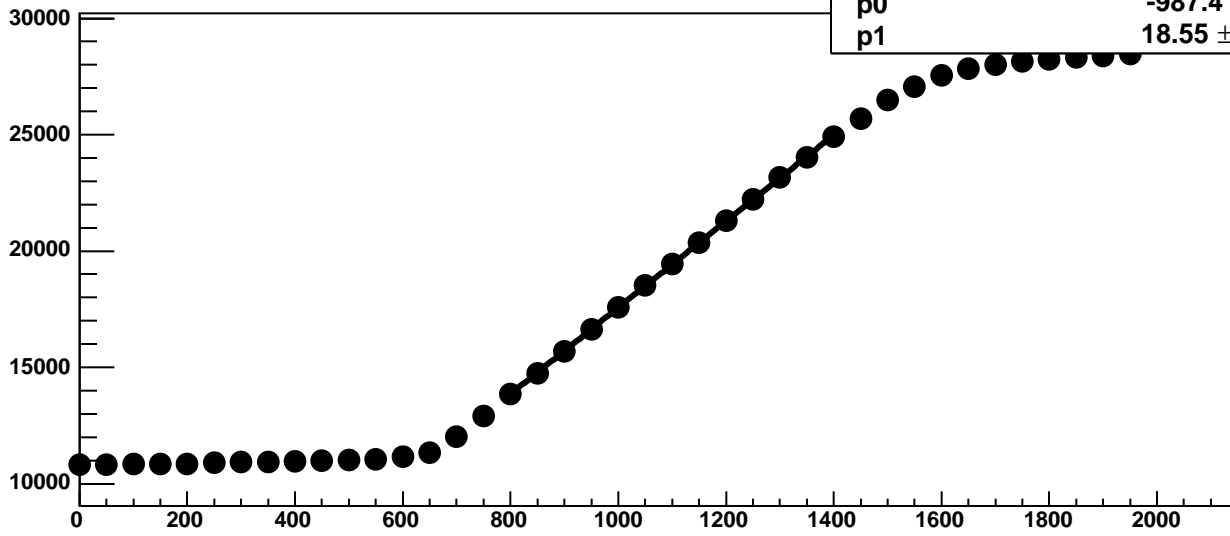
Chip 10, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC



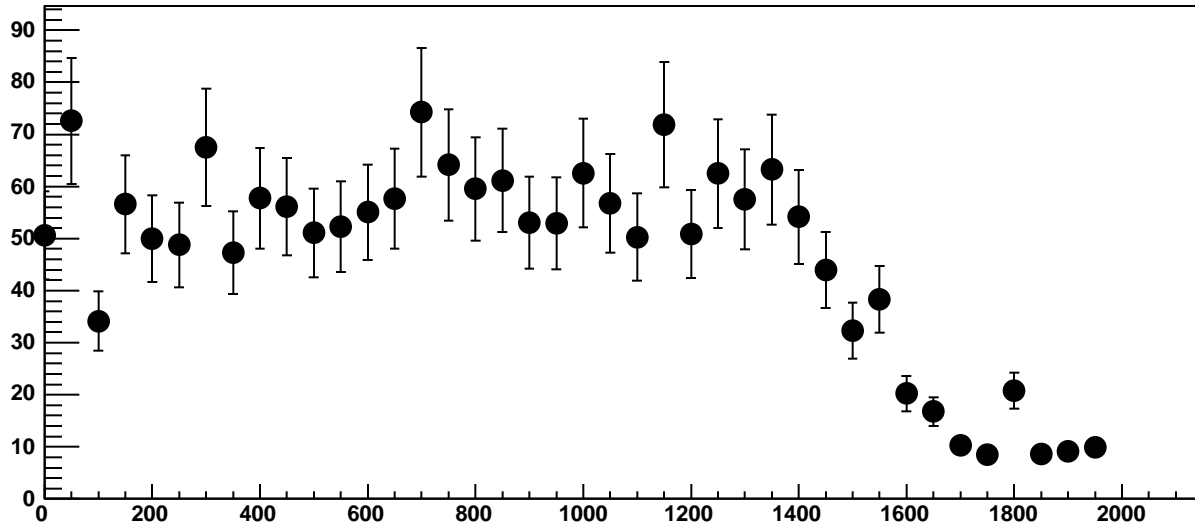
Chip 10, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC



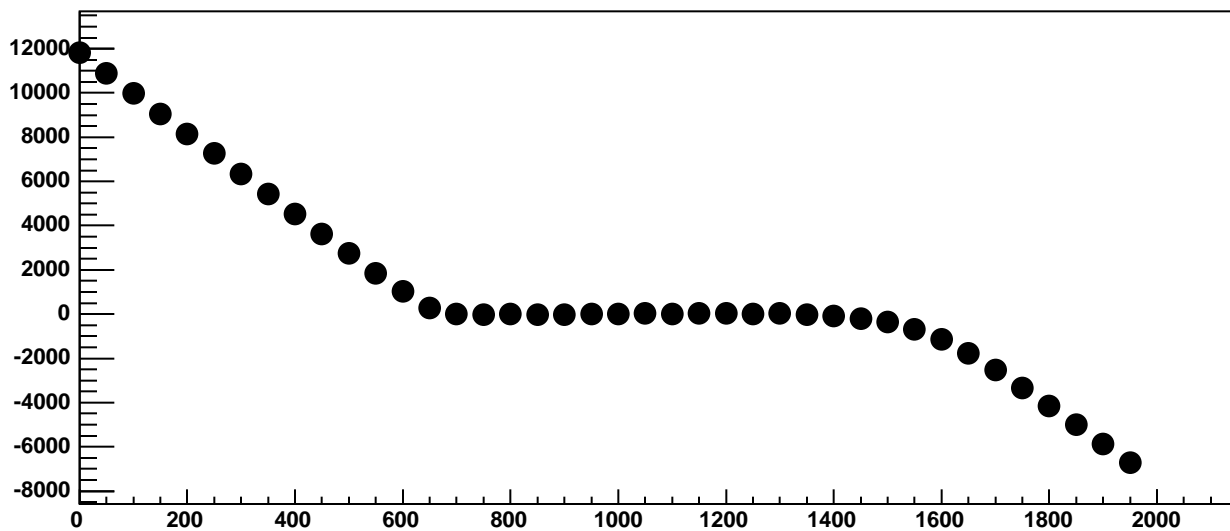
Chip 10, Channel 12, Enable 1!, Hold=30, ADC Mean vs DAC



Chip 10, Channel 12, Enable 1!, Hold=30, ADC Noise vs DAC

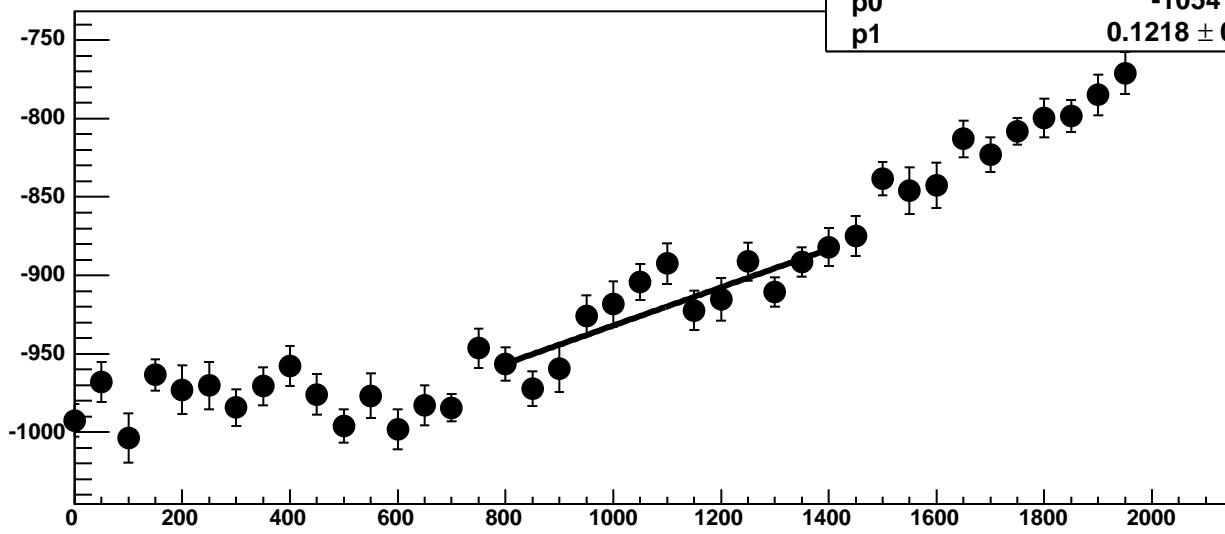


Chip 10, Channel 12, Enable 1!, Hold=30, ADC Residuals vs DAC

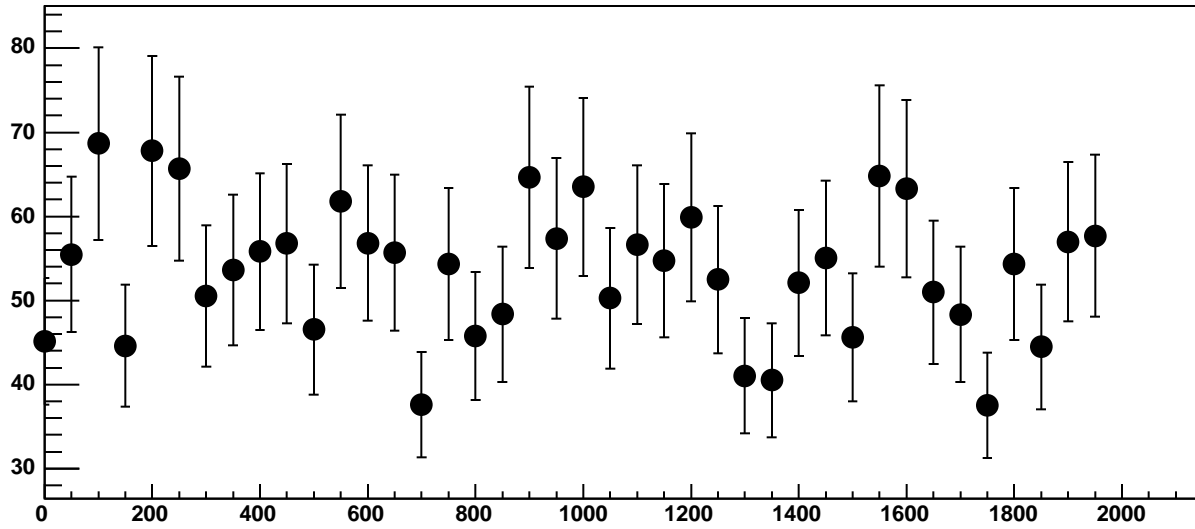




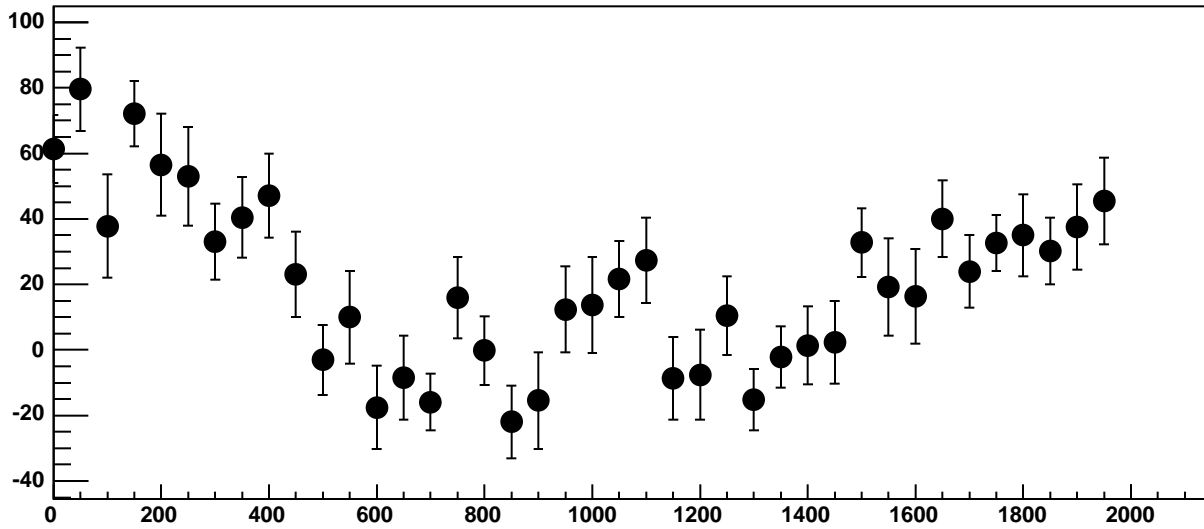
Chip 10, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC



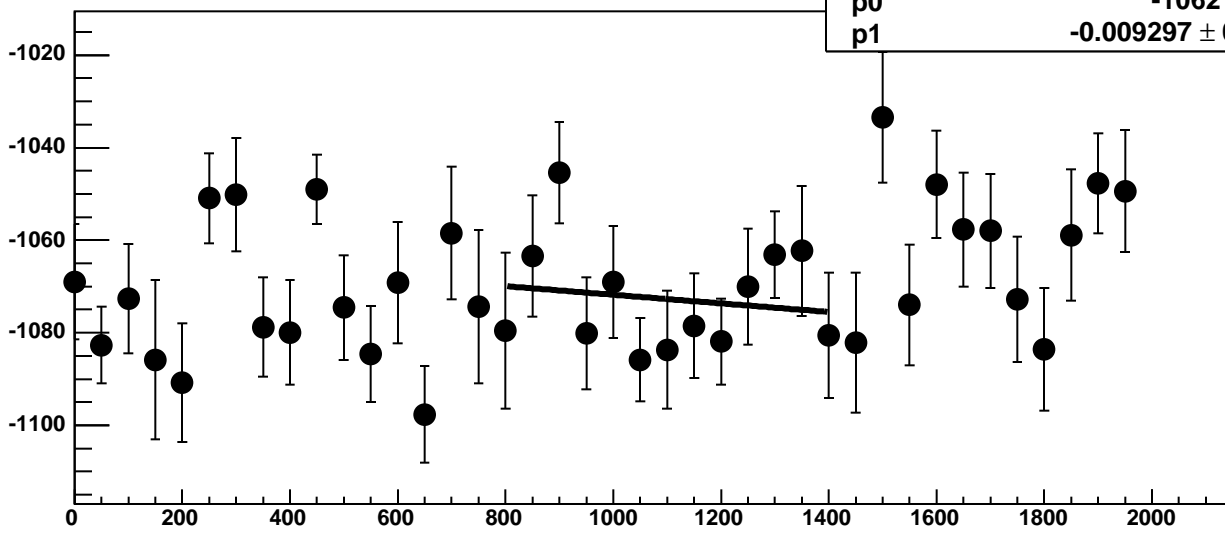
Chip 10, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC



Chip 10, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC

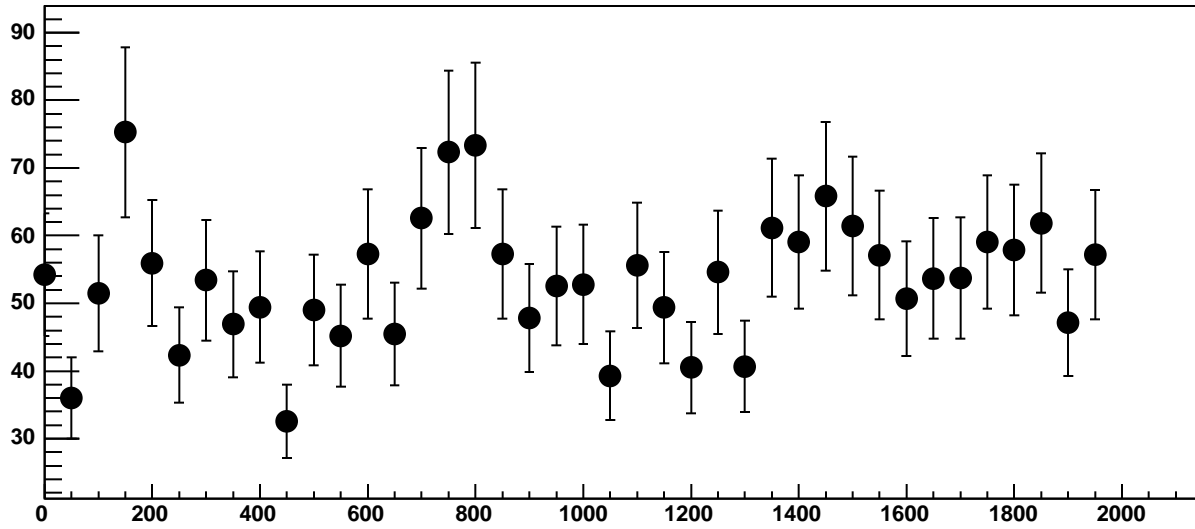


Chip 10, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC

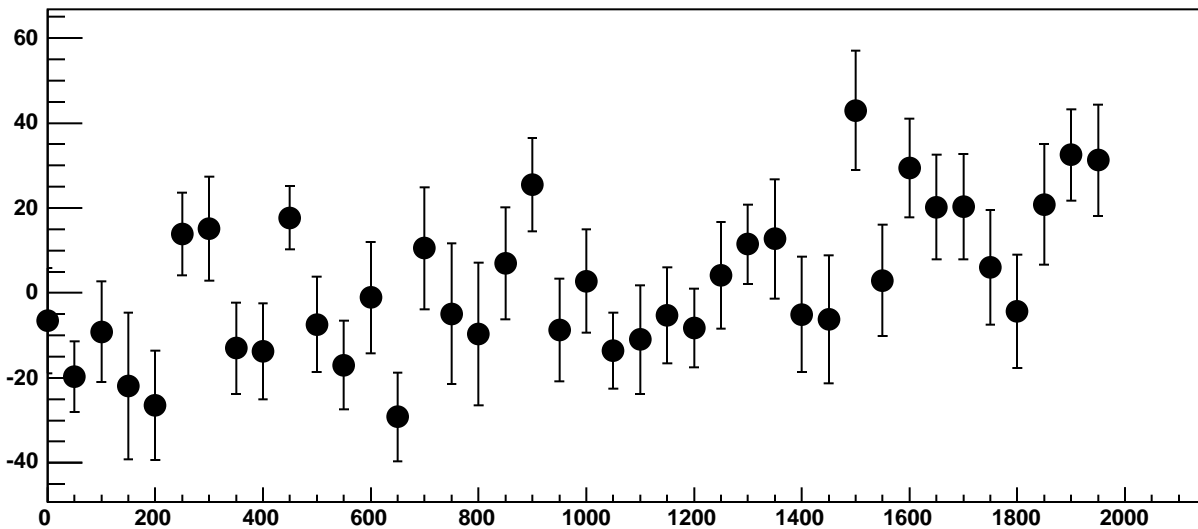


$\chi^2 / \text{ndf}$  13.16 / 11  
p0  $-1062 \pm 21.28$   
p1  $-0.009297 \pm 0.01891$

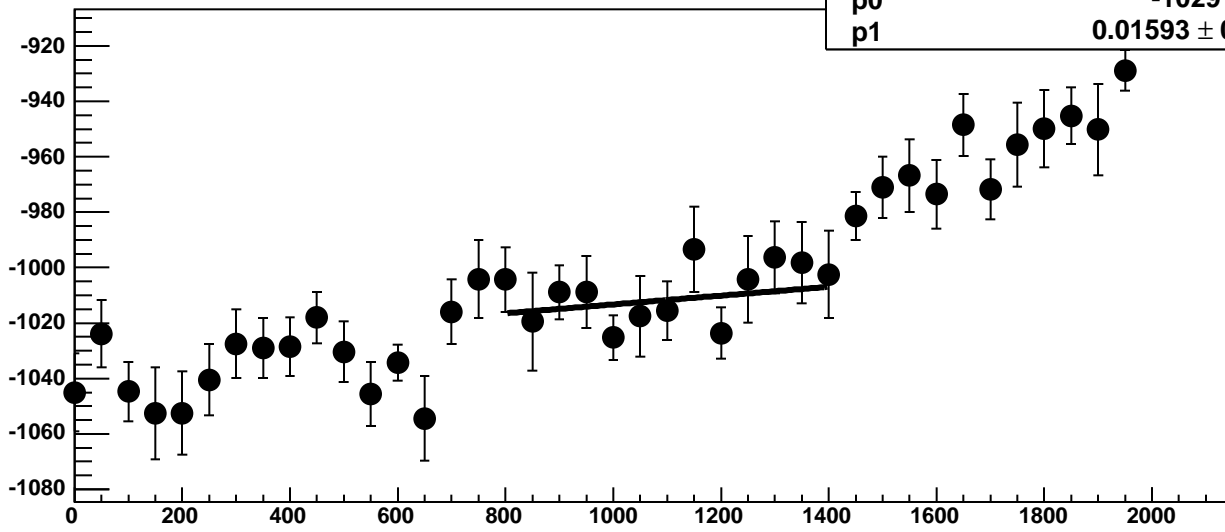
Chip 10, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC



Chip 10, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC

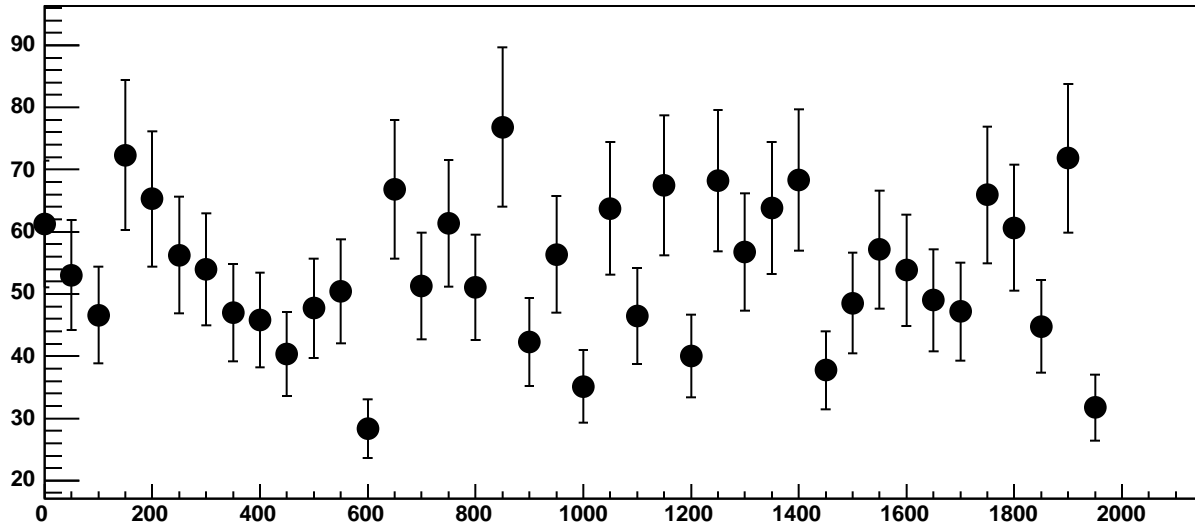


Chip 10, Channel 12, Enable 4, Hold=30, ADC Mean vs DAC

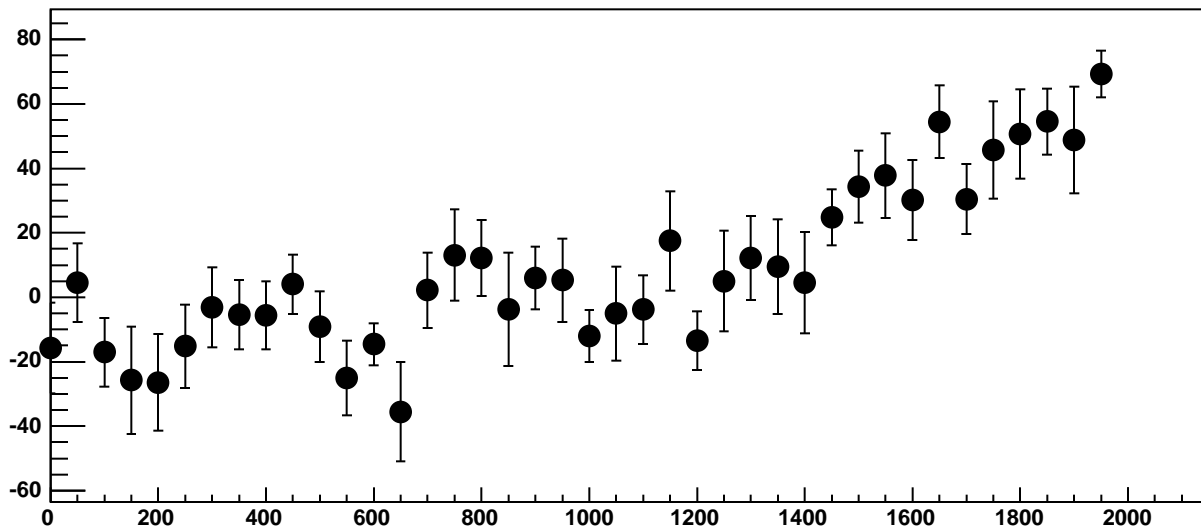


$\chi^2 / \text{ndf}$  9.045 / 11  
p0  $-1029 \pm 21.19$   
p1  $0.01593 \pm 0.01945$

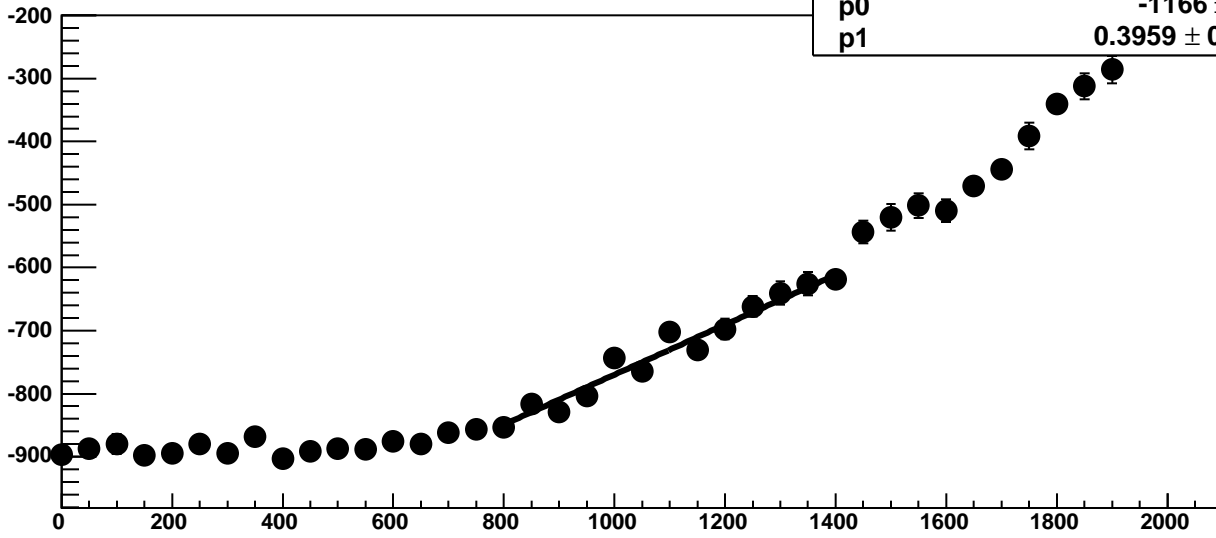
Chip 10, Channel 12, Enable 4, Hold=30, ADC Noise vs DAC



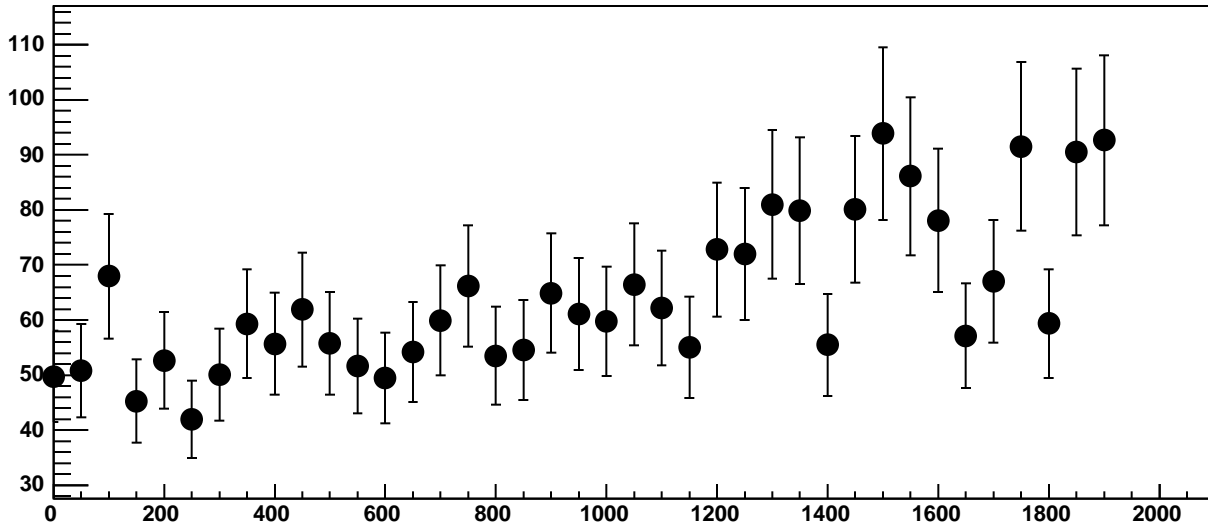
Chip 10, Channel 12, Enable 4, Hold=30, ADC Residuals vs DAC



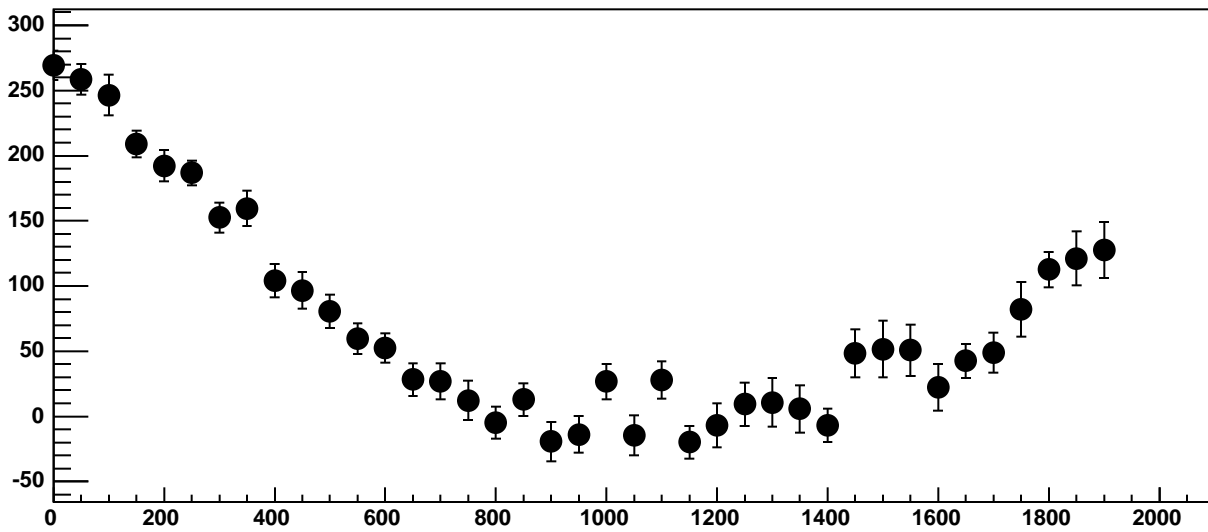
Chip 10, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC



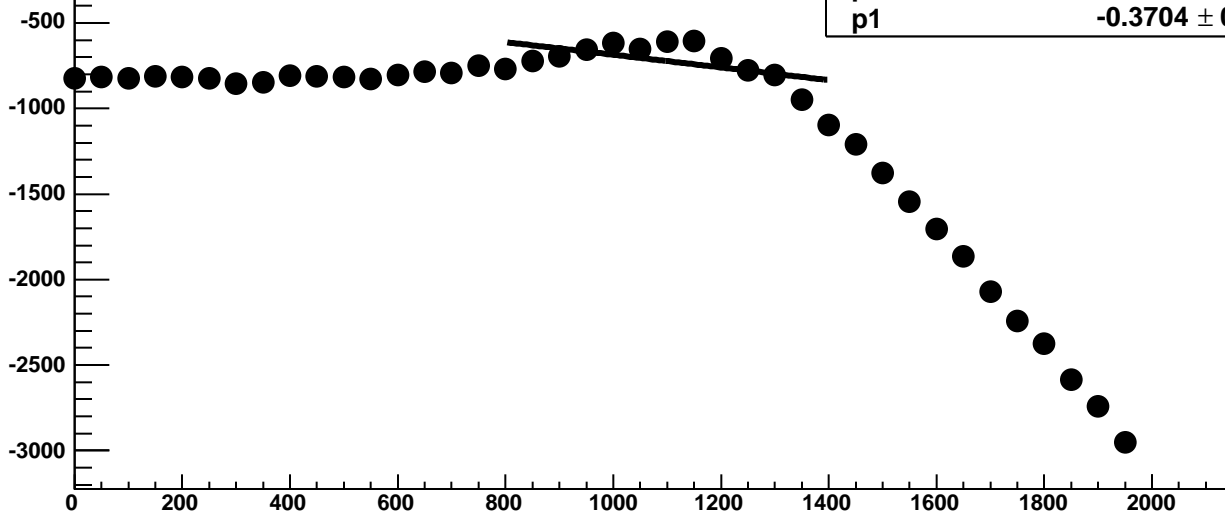
Chip 10, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC



Chip 10, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC

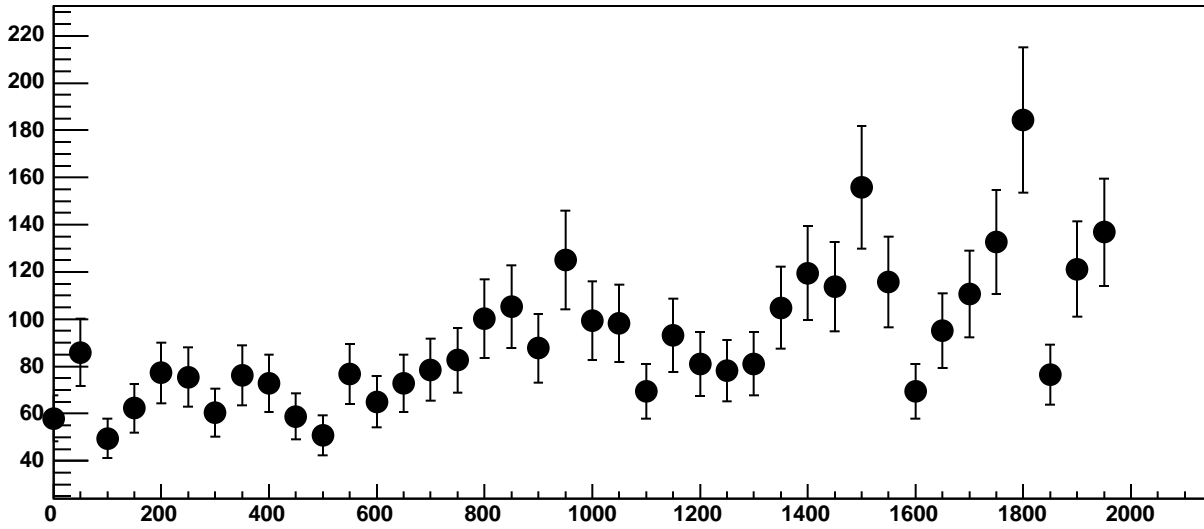


Chip 10, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC

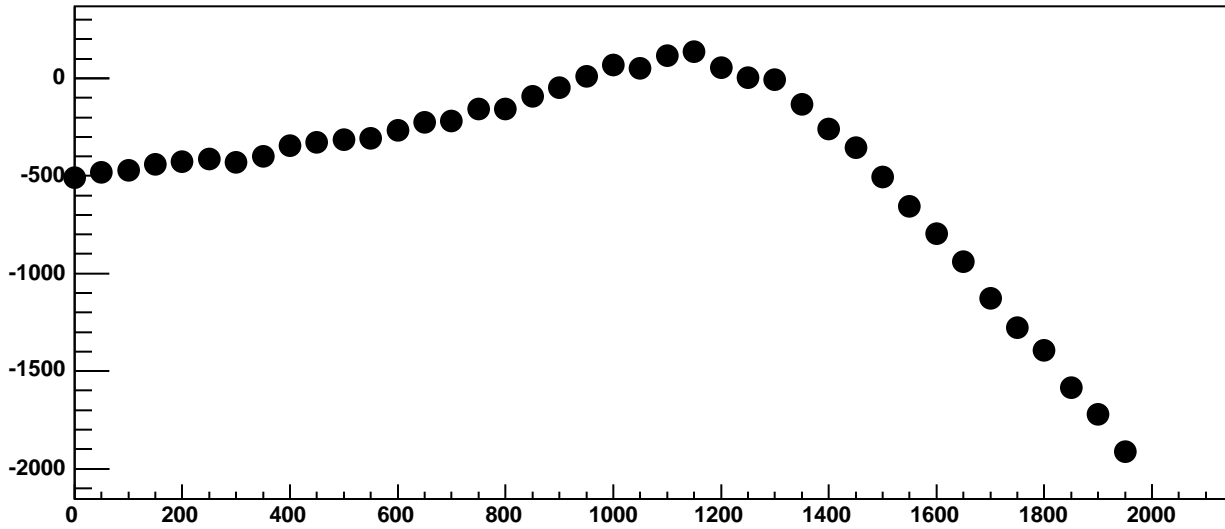


$\chi^2 / \text{ndf}$  303.2 / 11  
p0  $-315.8 \pm 38.05$   
p1  $-0.3704 \pm 0.03386$

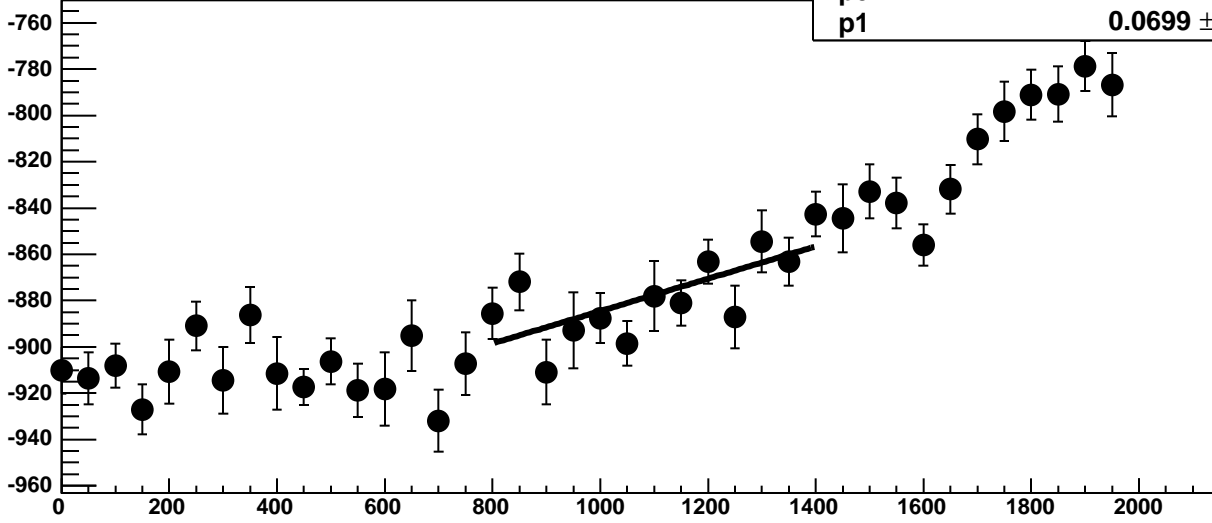
Chip 10, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC



Chip 10, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC



Chip 10, Channel 13, Enable 1, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

16.26 / 11

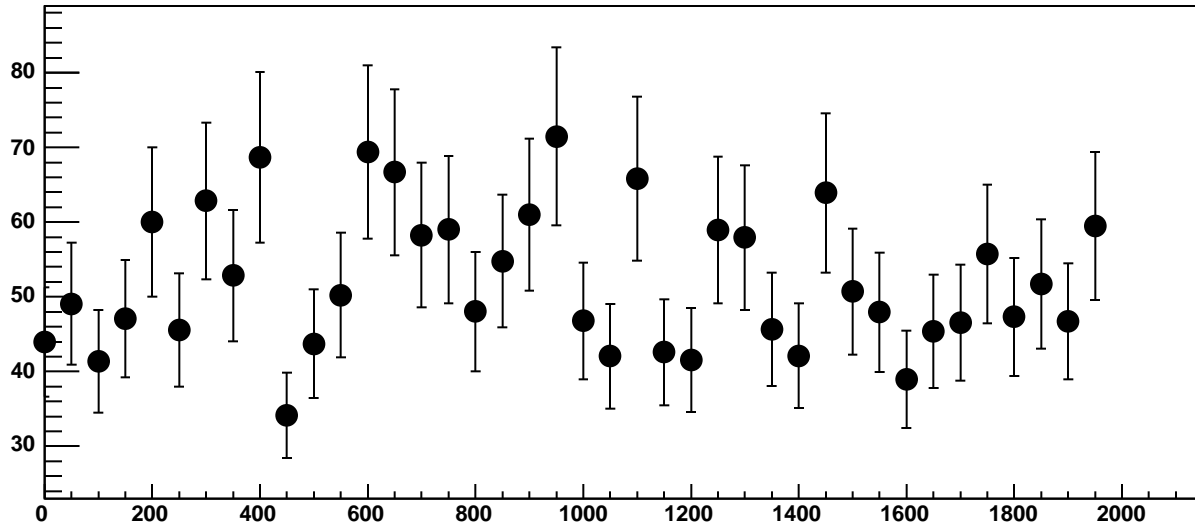
p0

$-954.4 \pm 19.08$

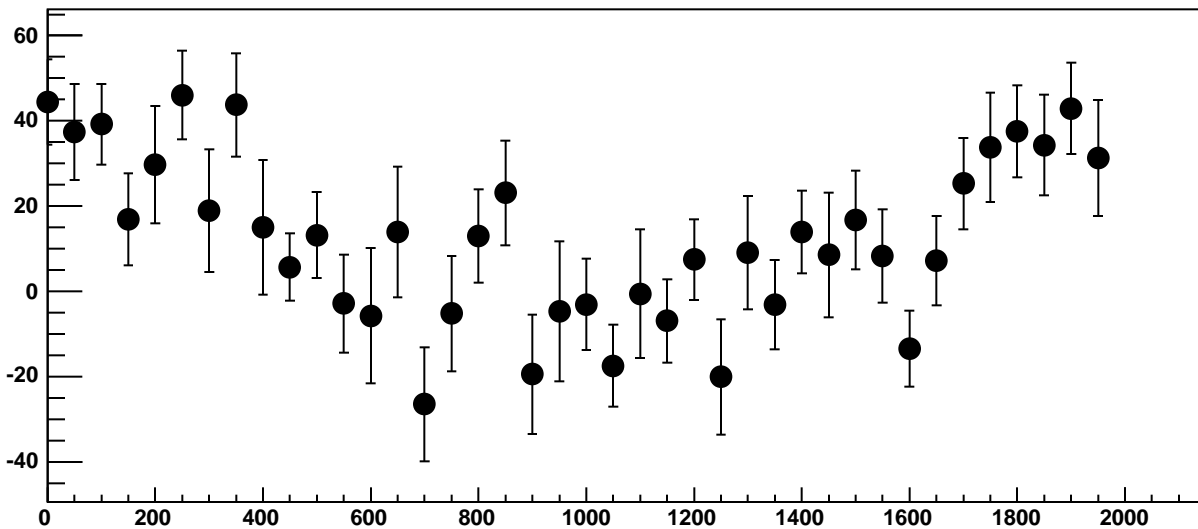
p1

$0.0699 \pm 0.0168$

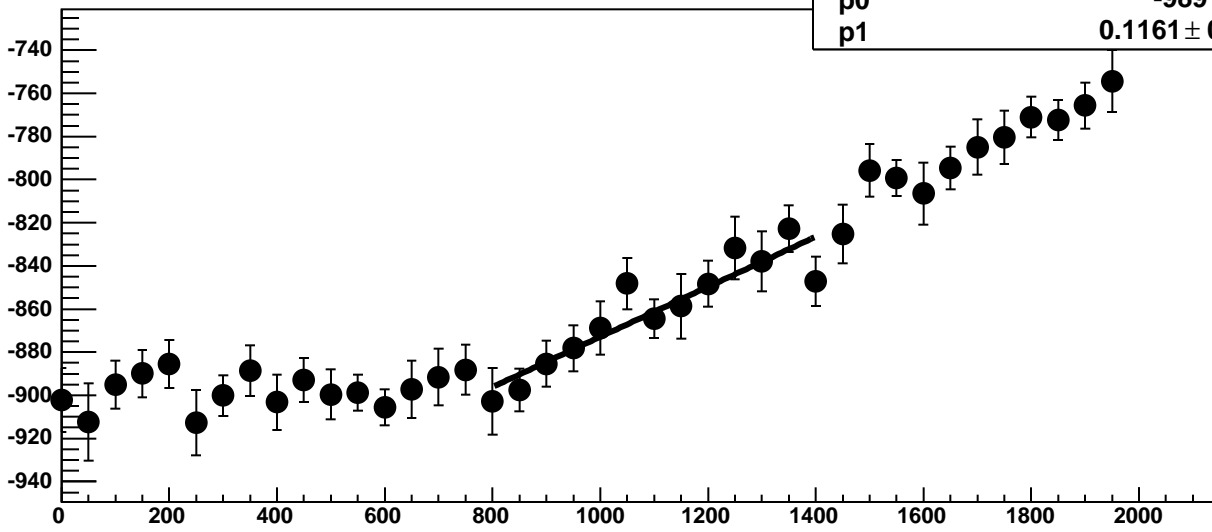
Chip 10, Channel 13, Enable 1, Hold=30, ADC Noise vs DAC



Chip 10, Channel 13, Enable 1, Hold=30, ADC Residuals vs DAC

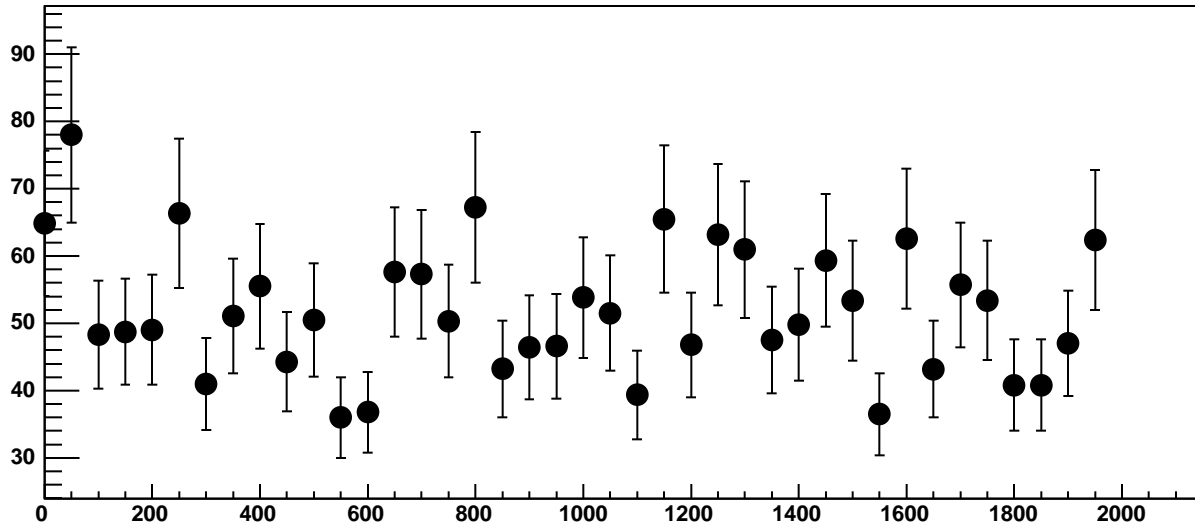


Chip 10, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC

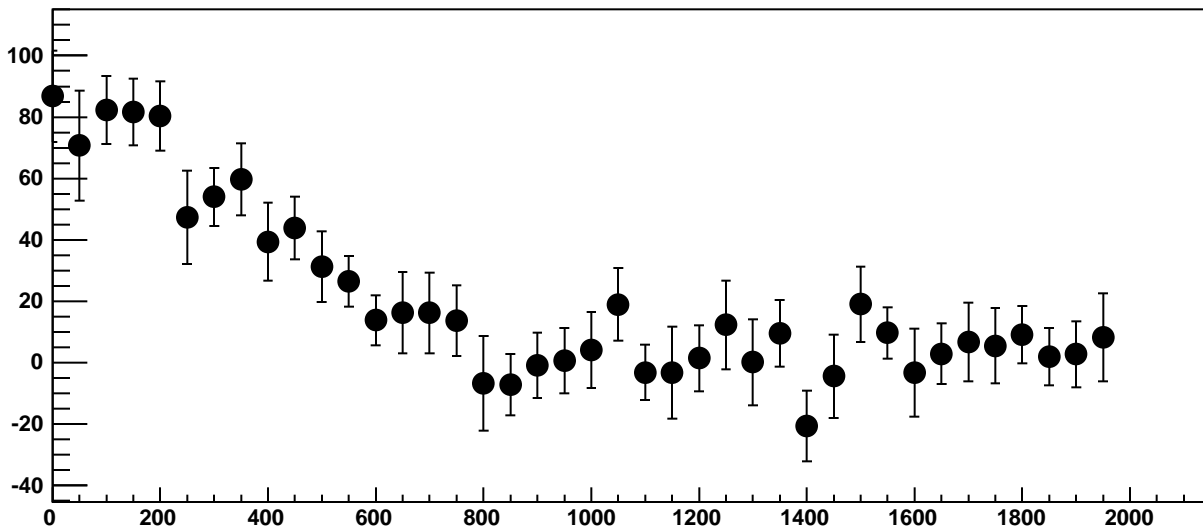


$\chi^2 / \text{ndf}$  8.348 / 11  
p0  $-989 \pm 19.39$   
p1  $0.1161 \pm 0.01749$

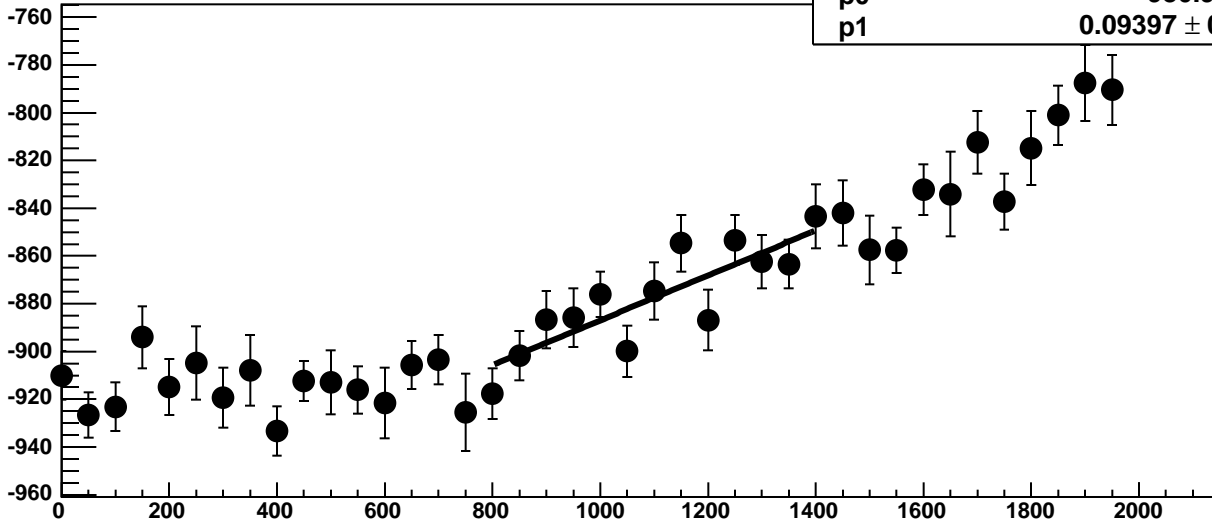
Chip 10, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC



Chip 10, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 10, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

12.79 / 11

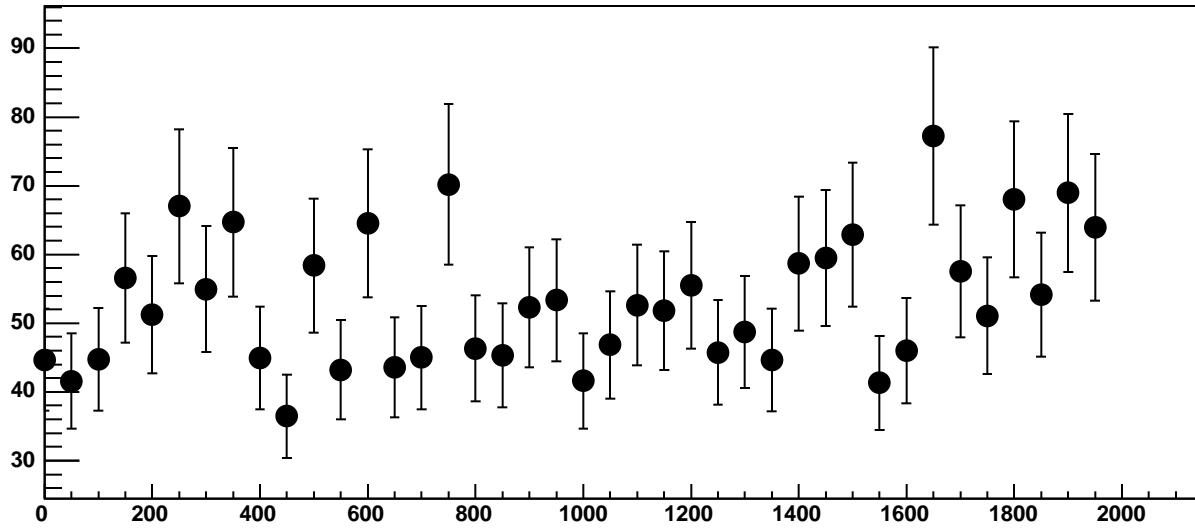
p0

$-980.9 \pm 18.4$

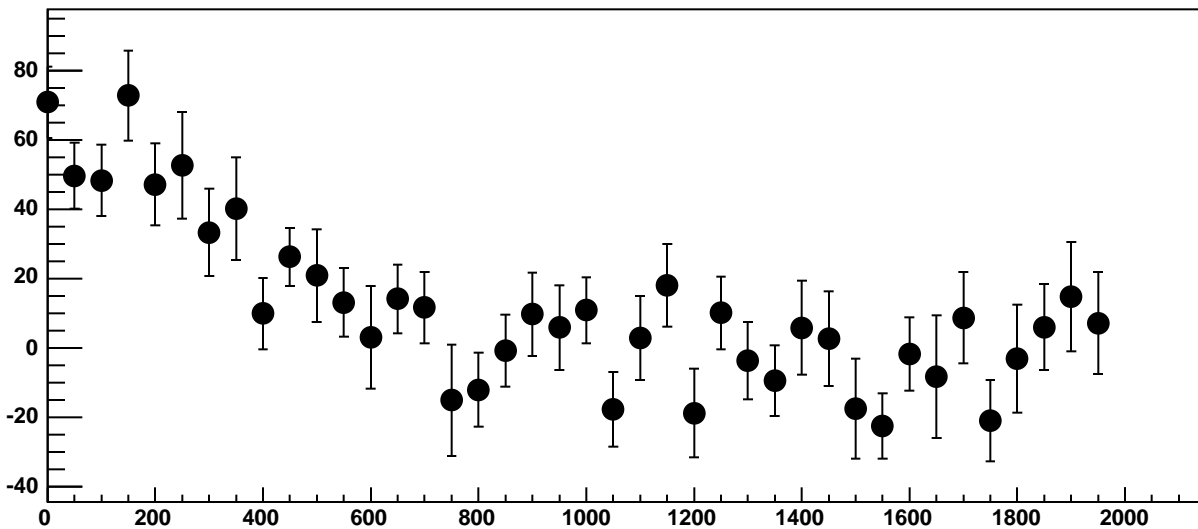
p1

$0.09397 \pm 0.01662$

Chip 10, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC

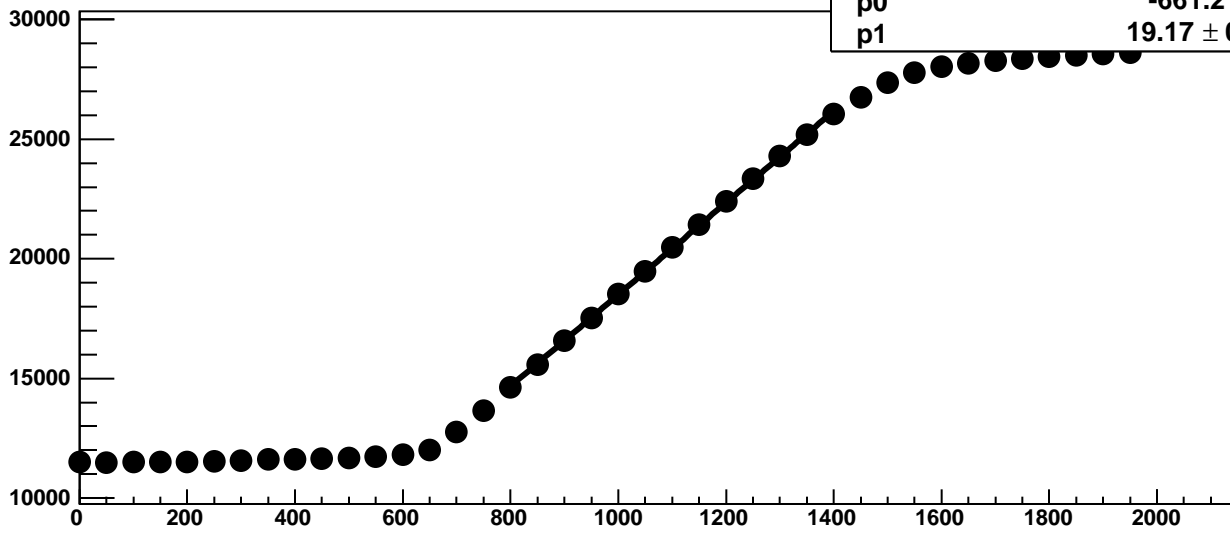


Chip 10, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC

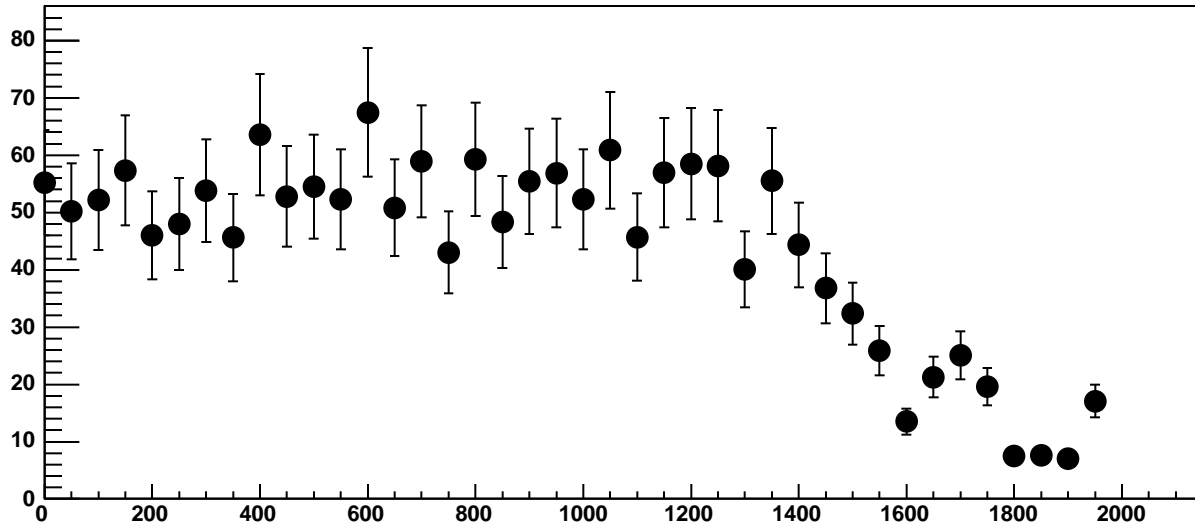




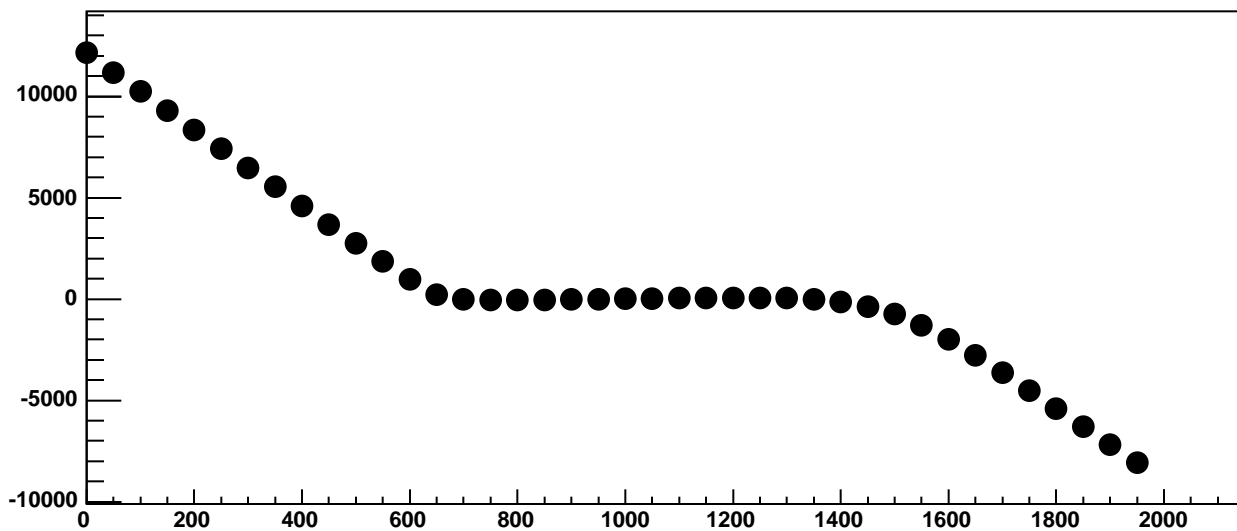
Chip 10, Channel 13, Enable 4!, Hold=30, ADC Mean vs DAC



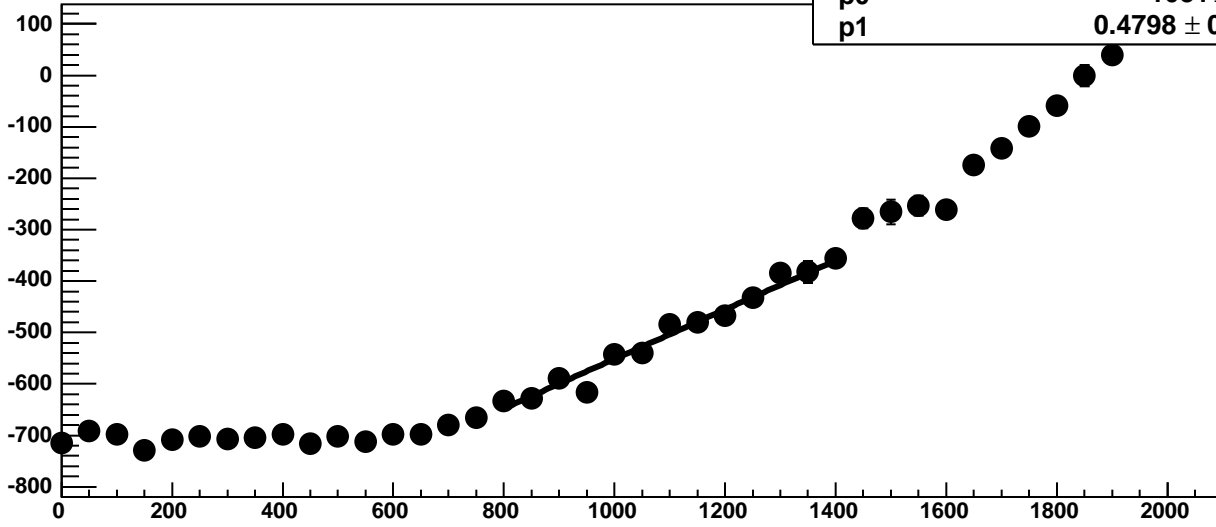
Chip 10, Channel 13, Enable 4!, Hold=30, ADC Noise vs DAC



Chip 10, Channel 13, Enable 4!, Hold=30, ADC Residuals vs DAC

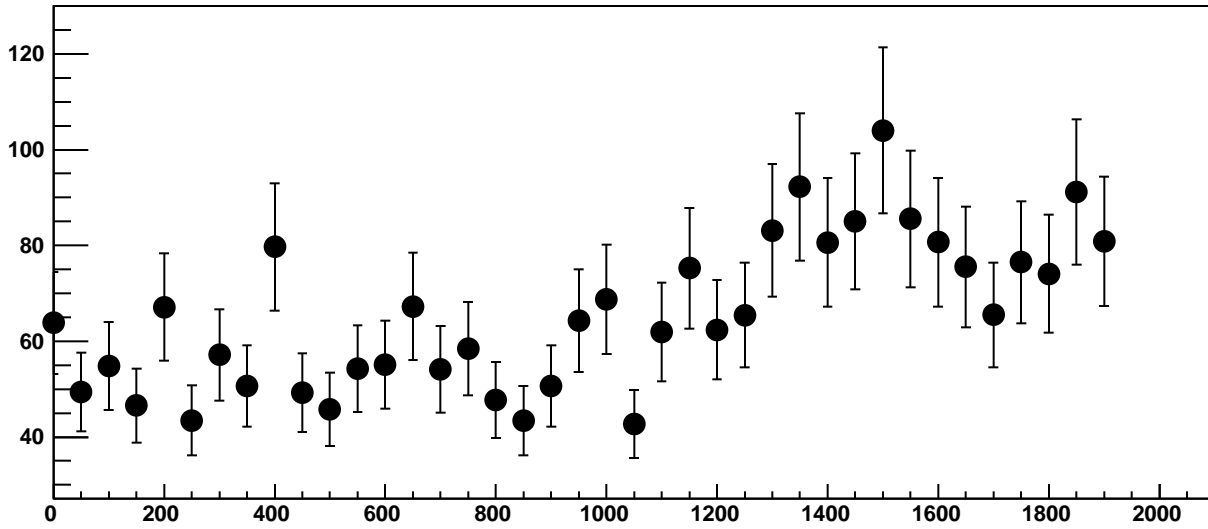


Chip 10, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC

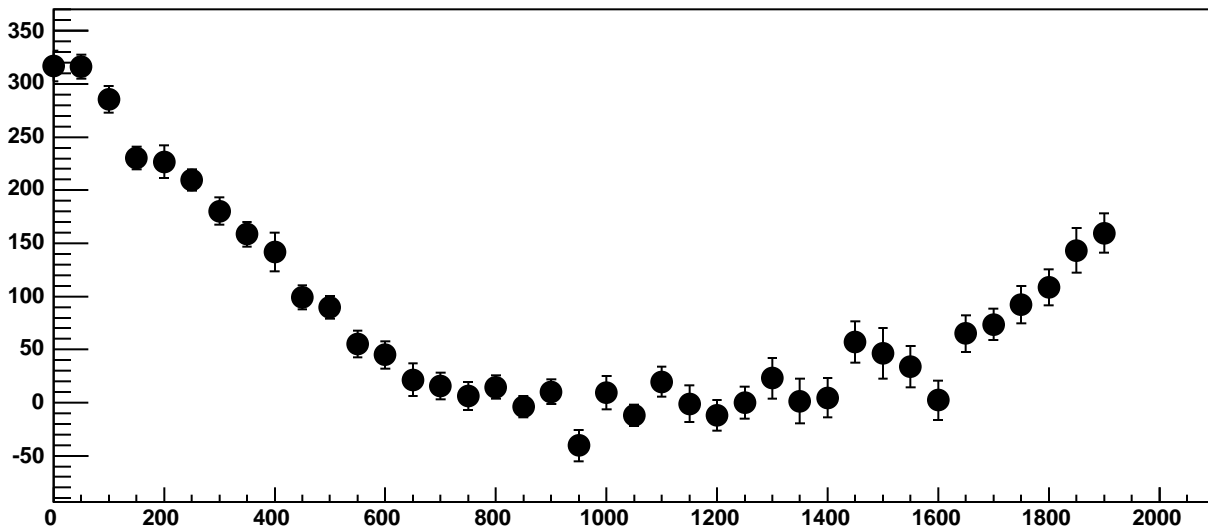


$\chi^2 / \text{ndf}$  16.09 / 11  
p0  $-1031 \pm 22.37$   
p1  $0.4798 \pm 0.02138$

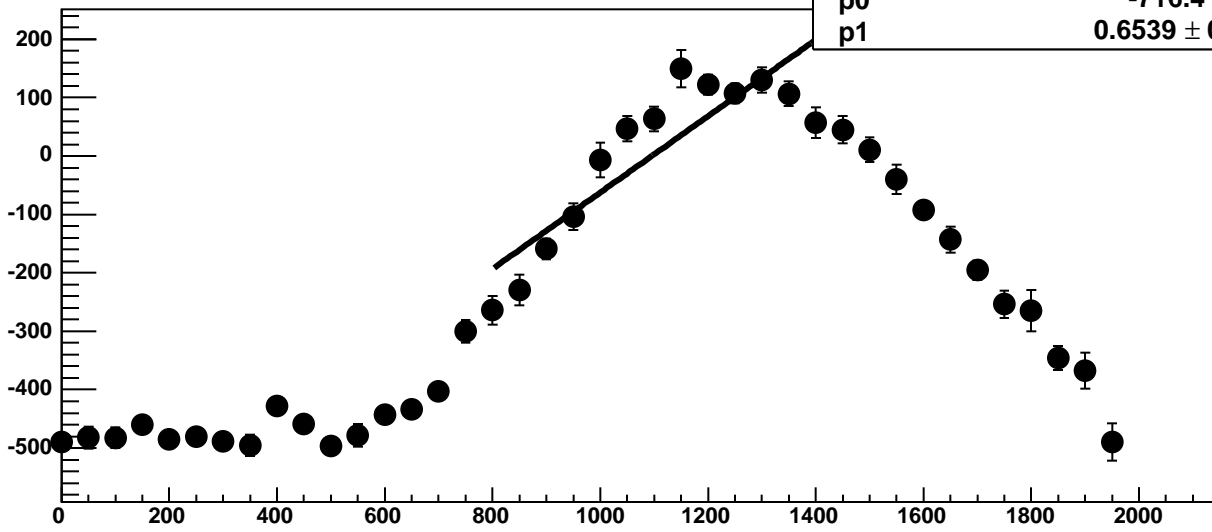
Chip 10, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC



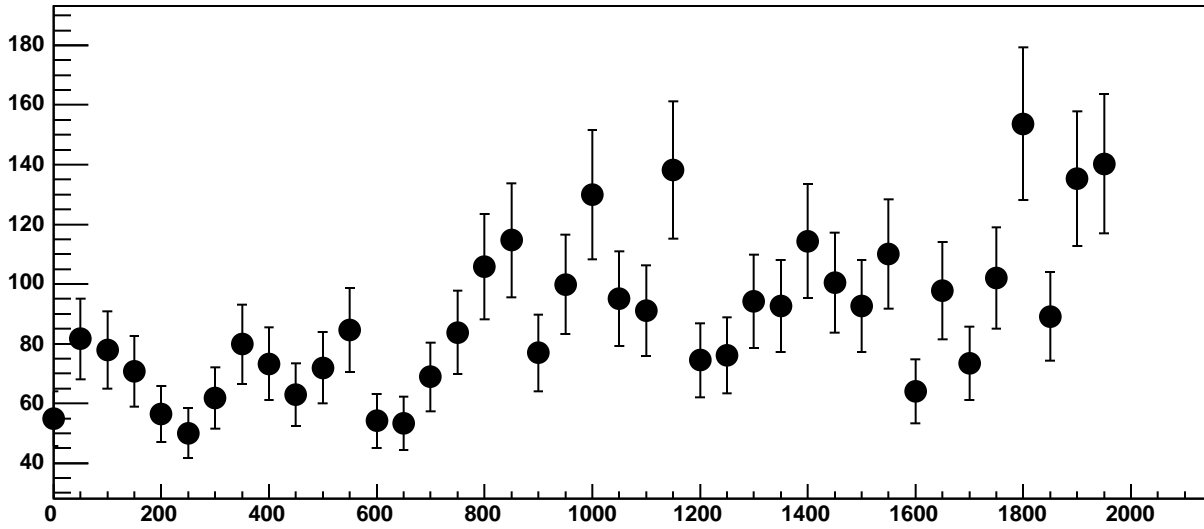
Chip 10, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC



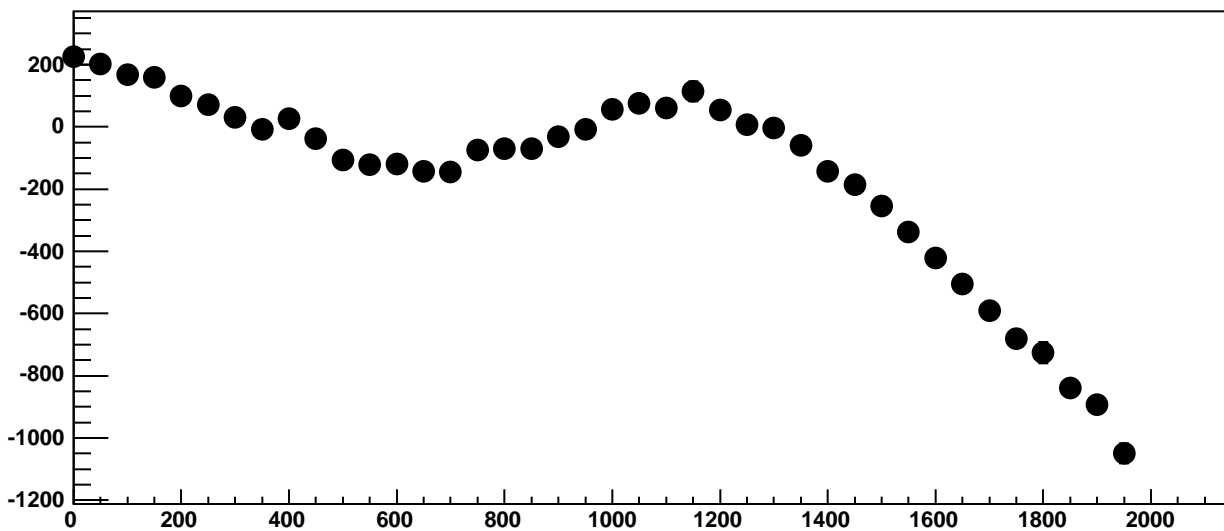
Chip 10, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC



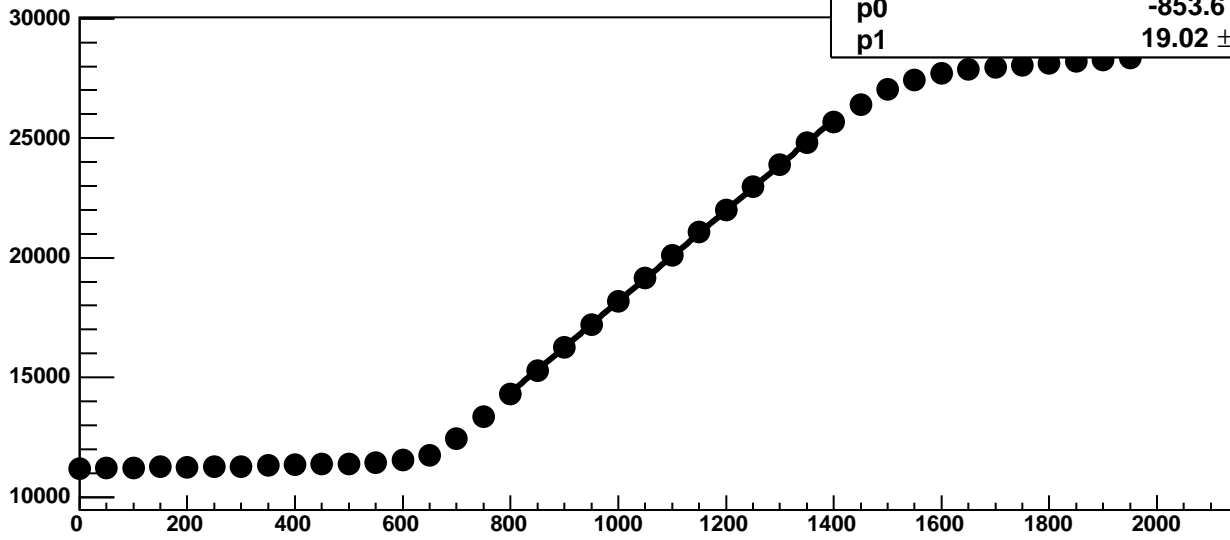
Chip 10, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC



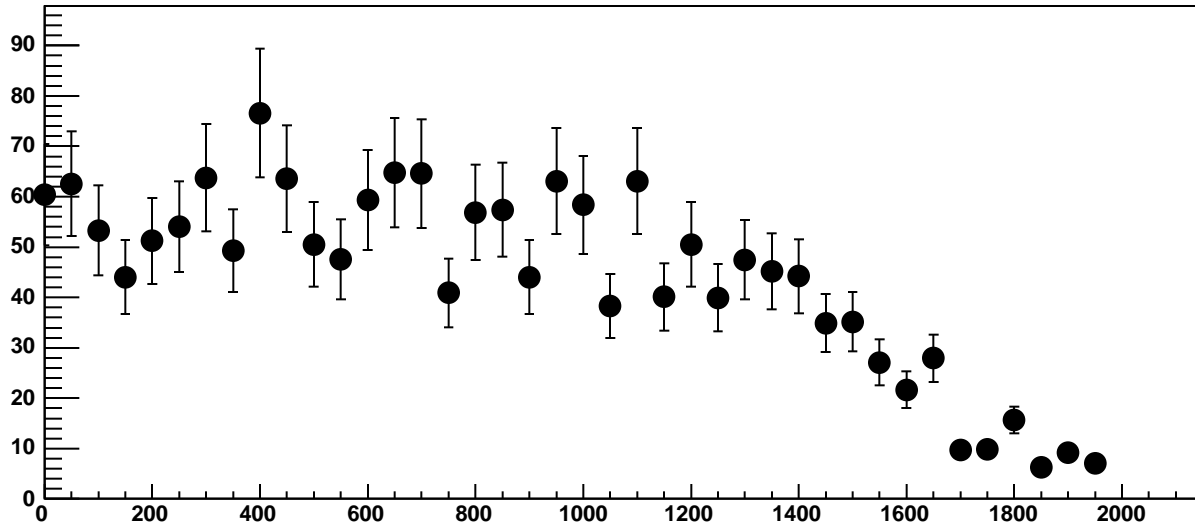
Chip 10, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC



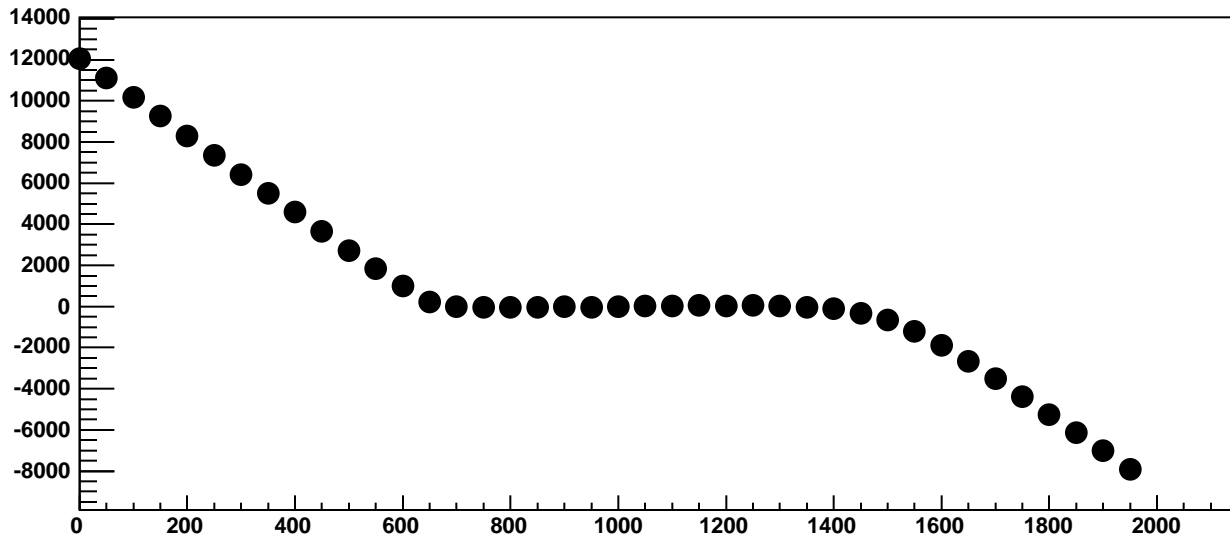
Chip 10, Channel 14, Enable 1!, Hold=30, ADC Mean vs DAC



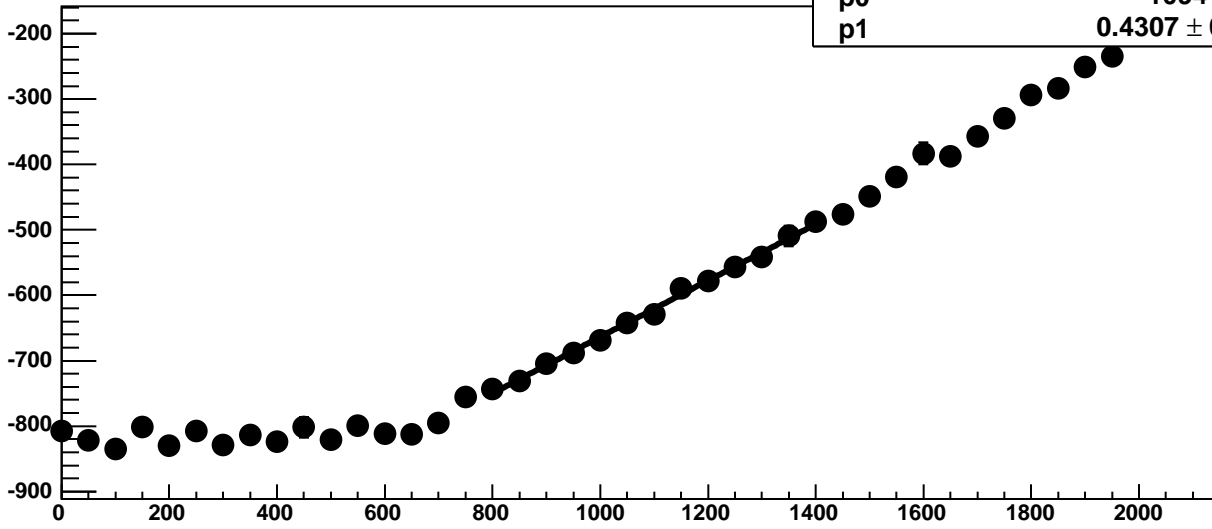
Chip 10, Channel 14, Enable 1!, Hold=30, ADC Noise vs DAC



Chip 10, Channel 14, Enable 1!, Hold=30, ADC Residuals vs DAC

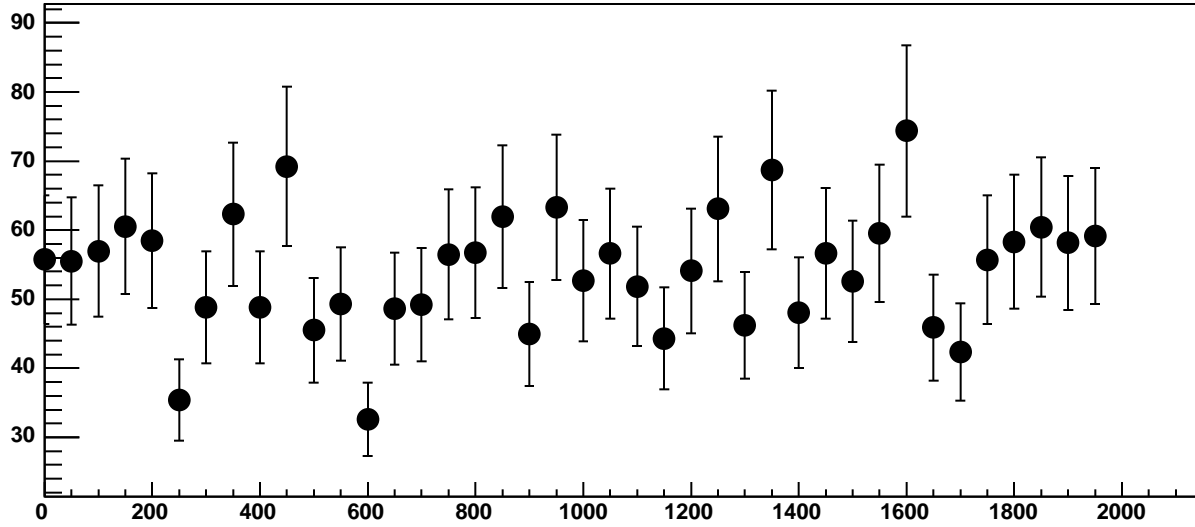


Chip 10, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC

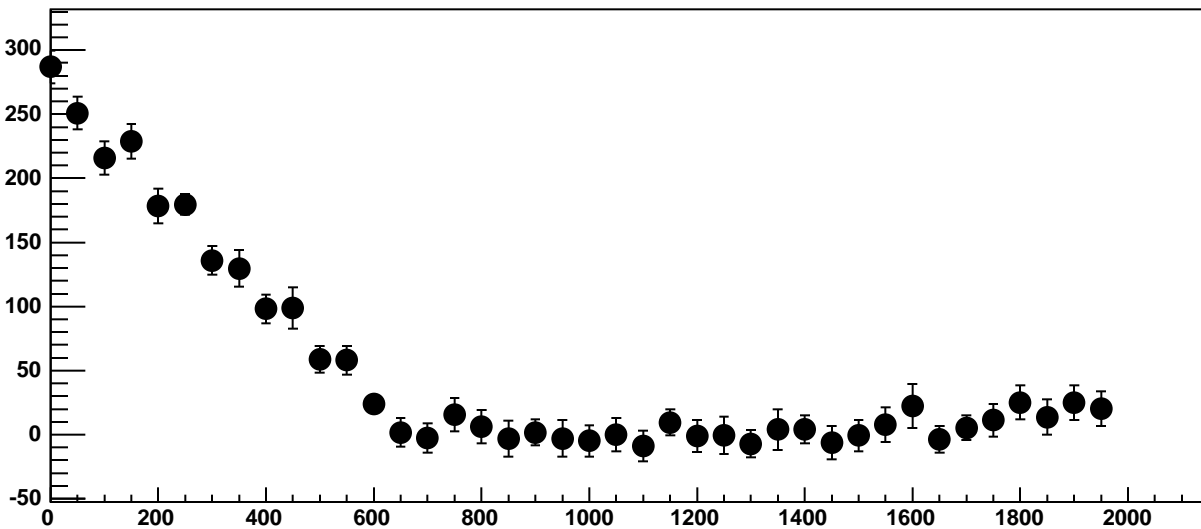


$\chi^2 / \text{ndf}$  2.592 / 11  
p0  $-1094 \pm 20.65$   
p1  $0.4307 \pm 0.01841$

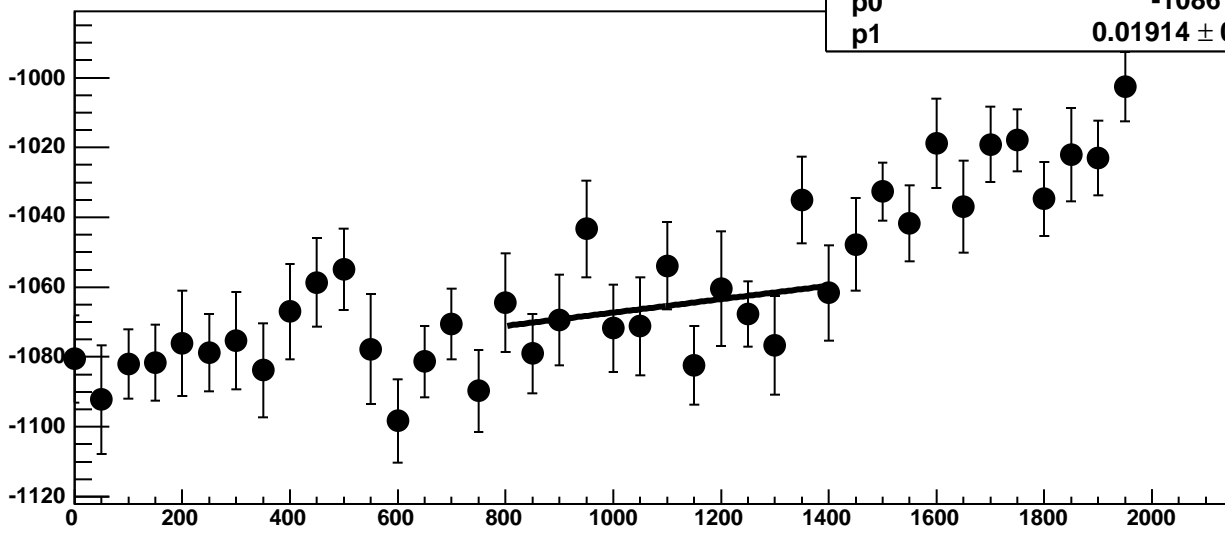
Chip 10, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC



Chip 10, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC

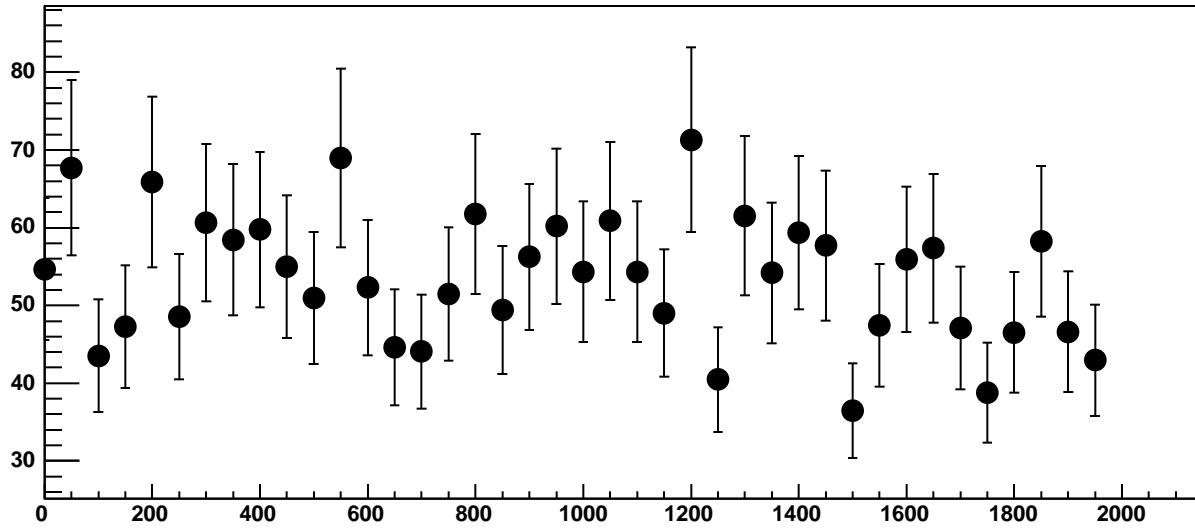


Chip 10, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC

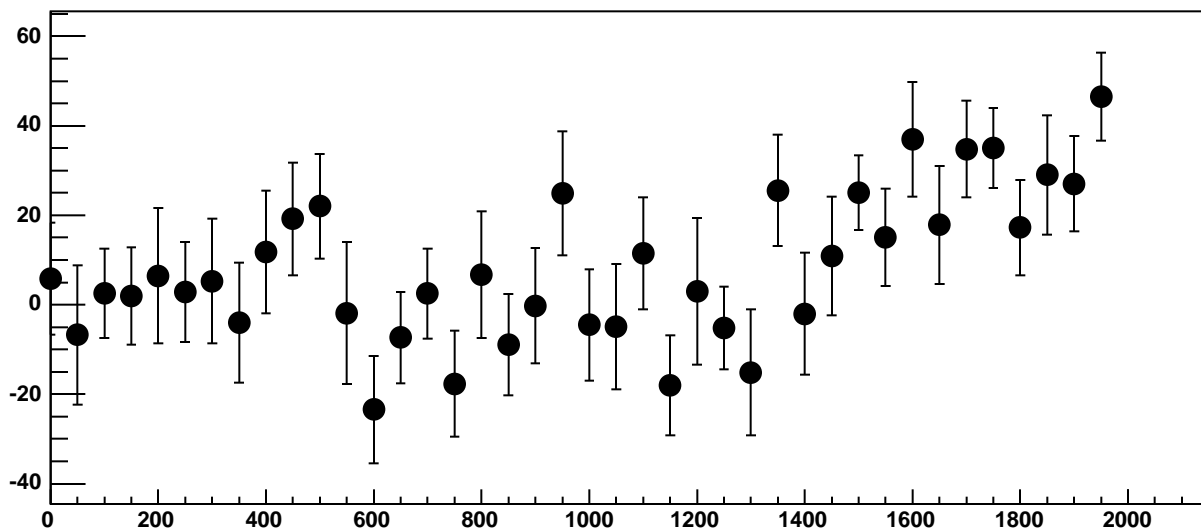


$\chi^2 / \text{ndf}$  13.5 / 11  
p0  $-1086 \pm 21.26$   
p1  $0.01914 \pm 0.01898$

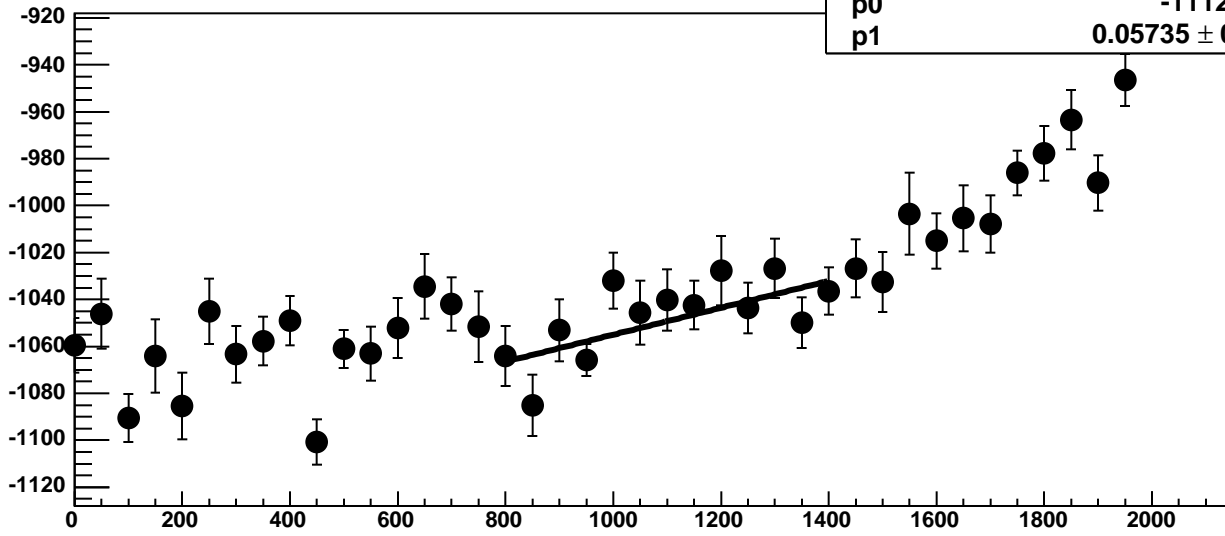
Chip 10, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC



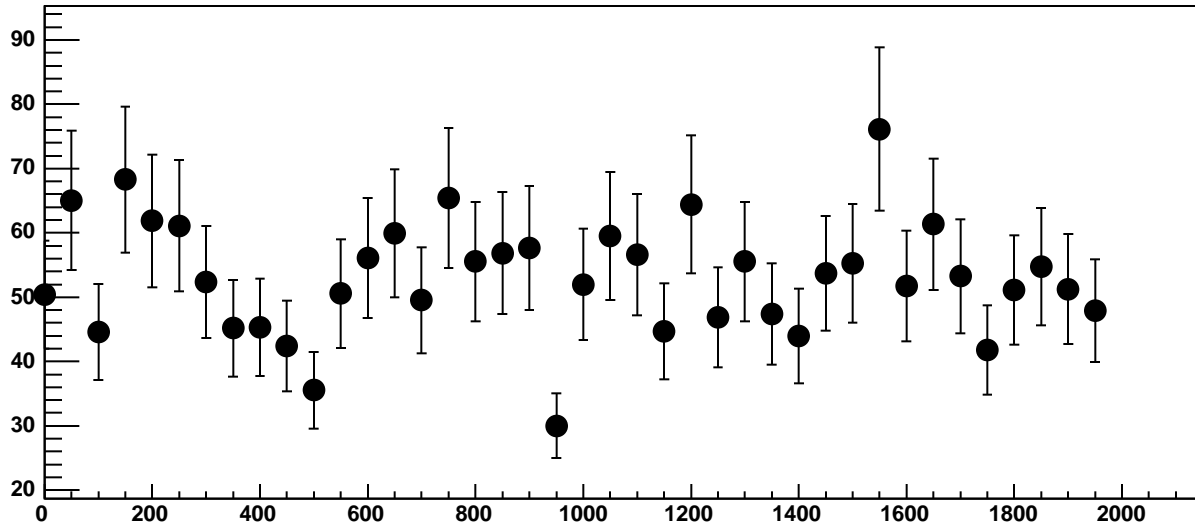
Chip 10, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC



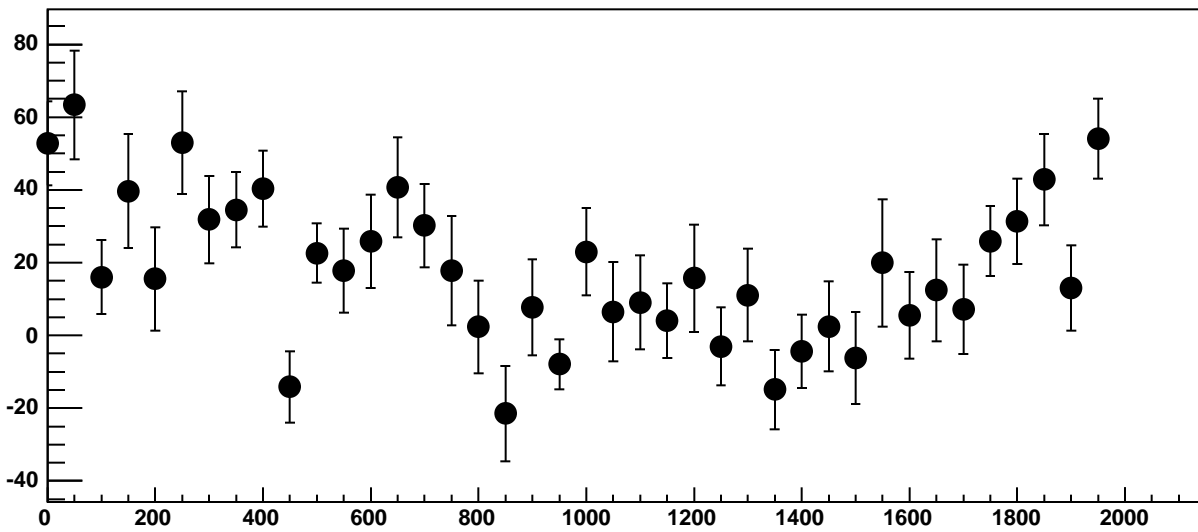
Chip 10, Channel 14, Enable 4, Hold=30, ADC Mean vs DAC



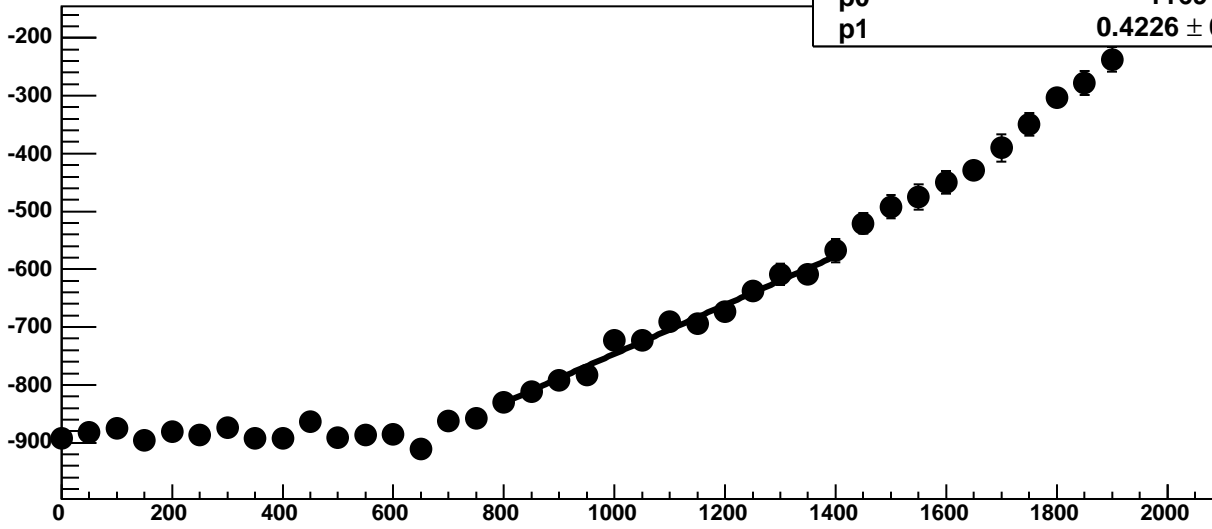
Chip 10, Channel 14, Enable 4, Hold=30, ADC Noise vs DAC



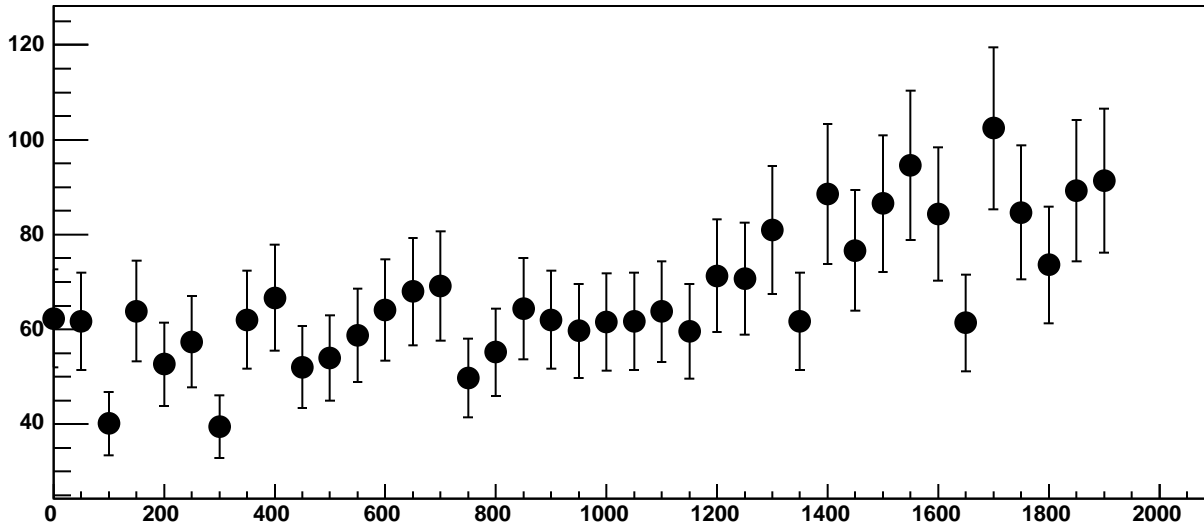
Chip 10, Channel 14, Enable 4, Hold=30, ADC Residuals vs DAC



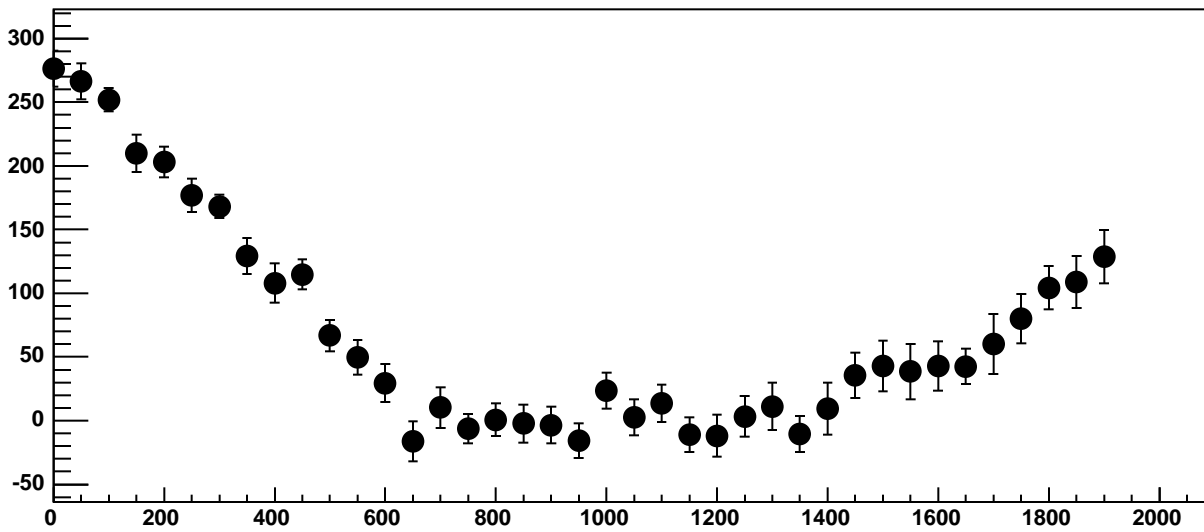
Chip 10, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC



Chip 10, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC

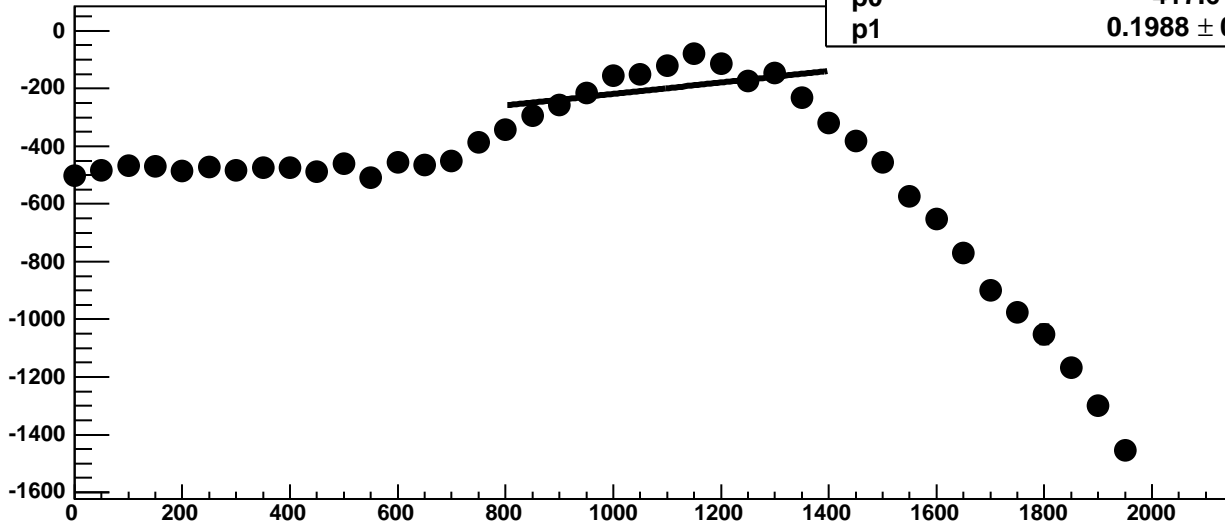


Chip 10, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC





Chip 10, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

173.6 / 11

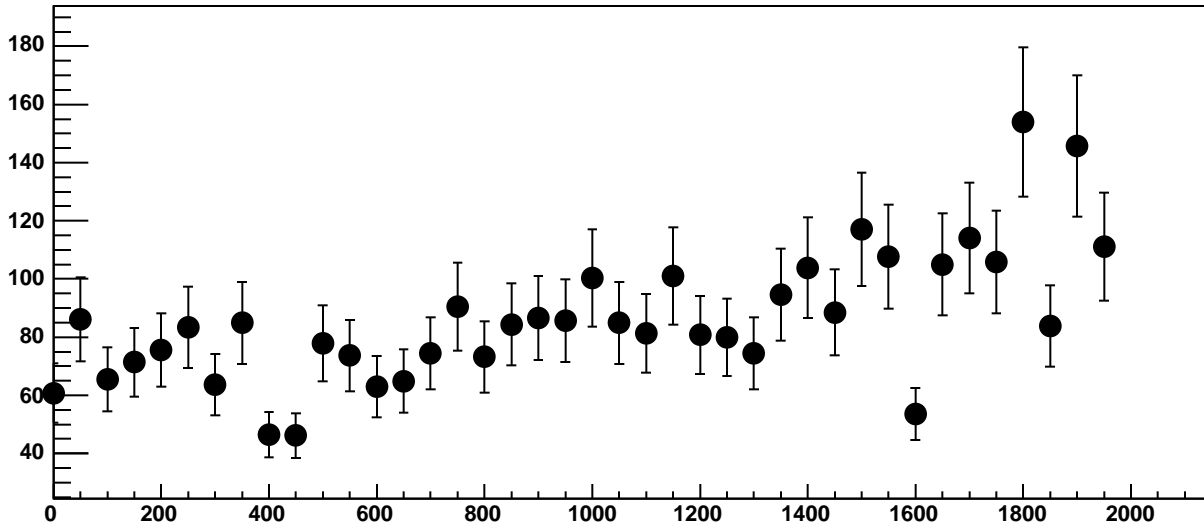
p0

$-417.6 \pm 31.96$

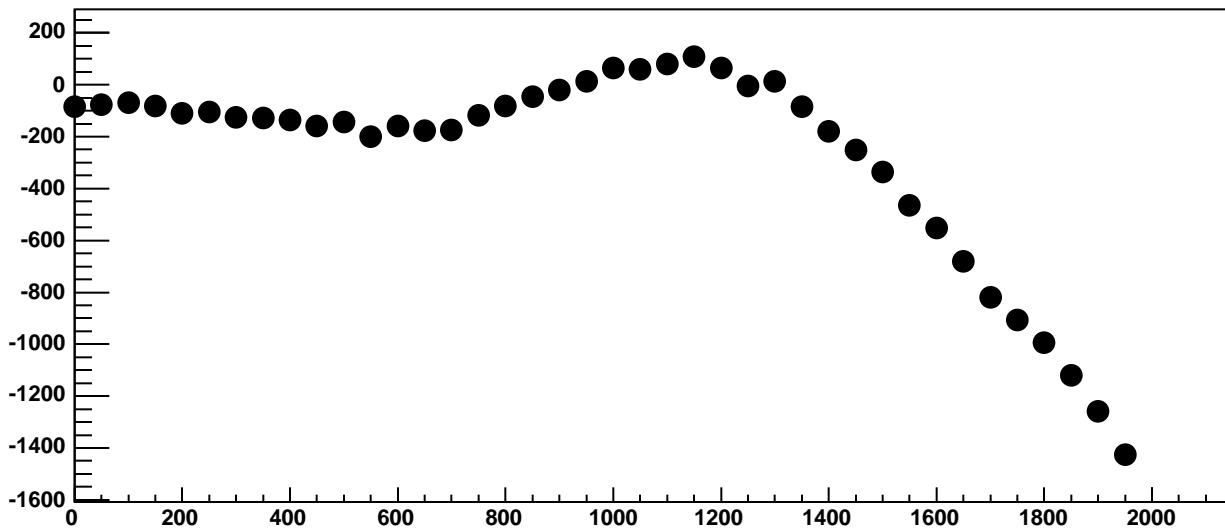
p1

$0.1988 \pm 0.02892$

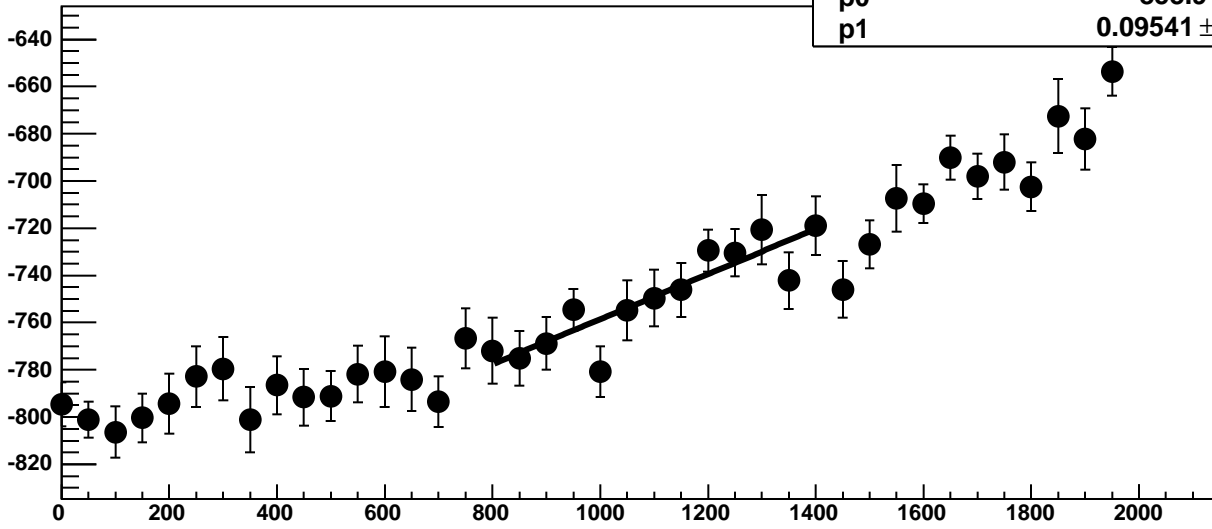
Chip 10, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC



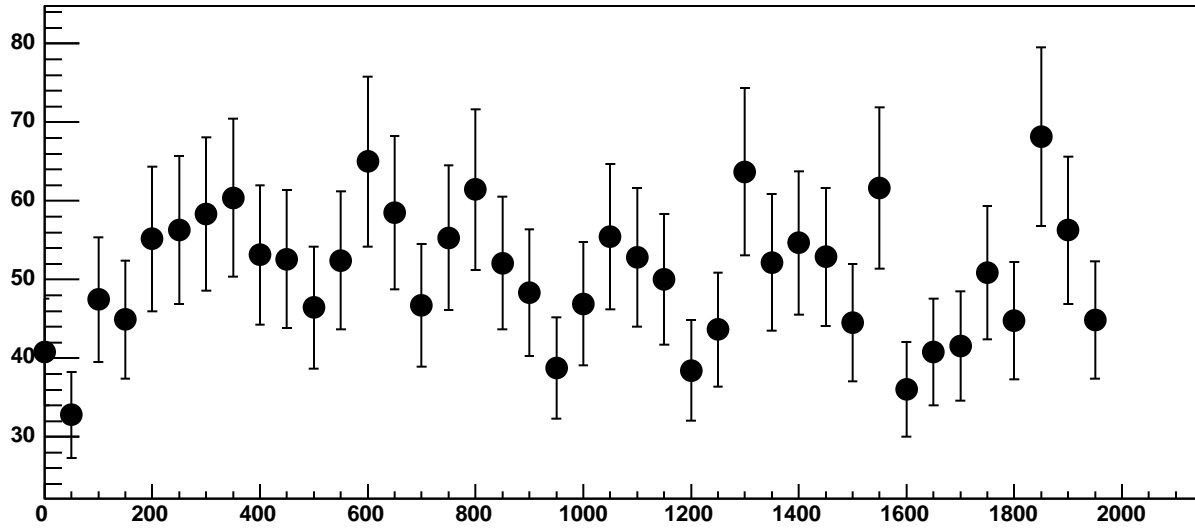
Chip 10, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC



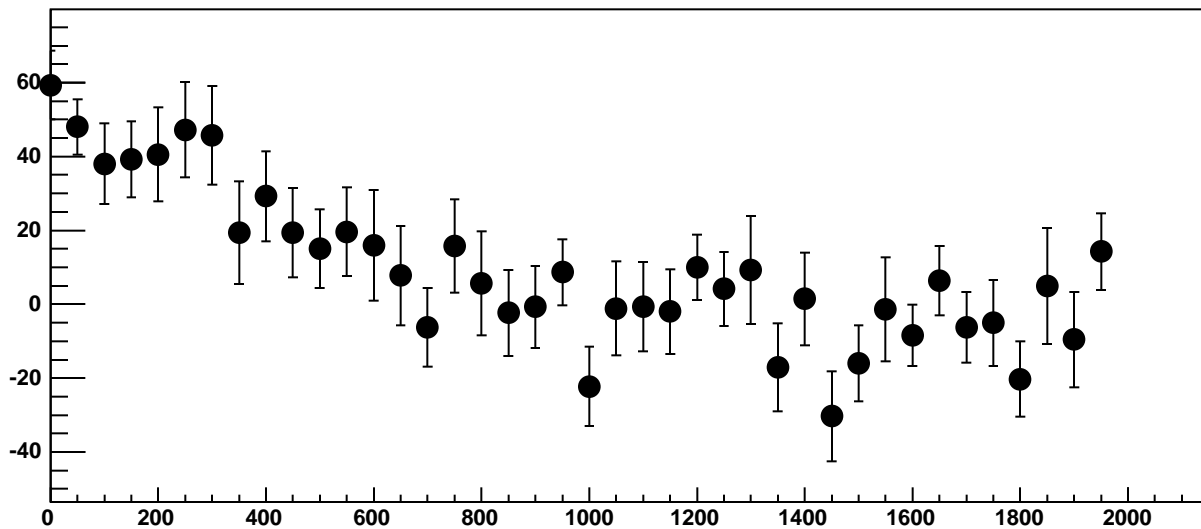
Chip 10, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC



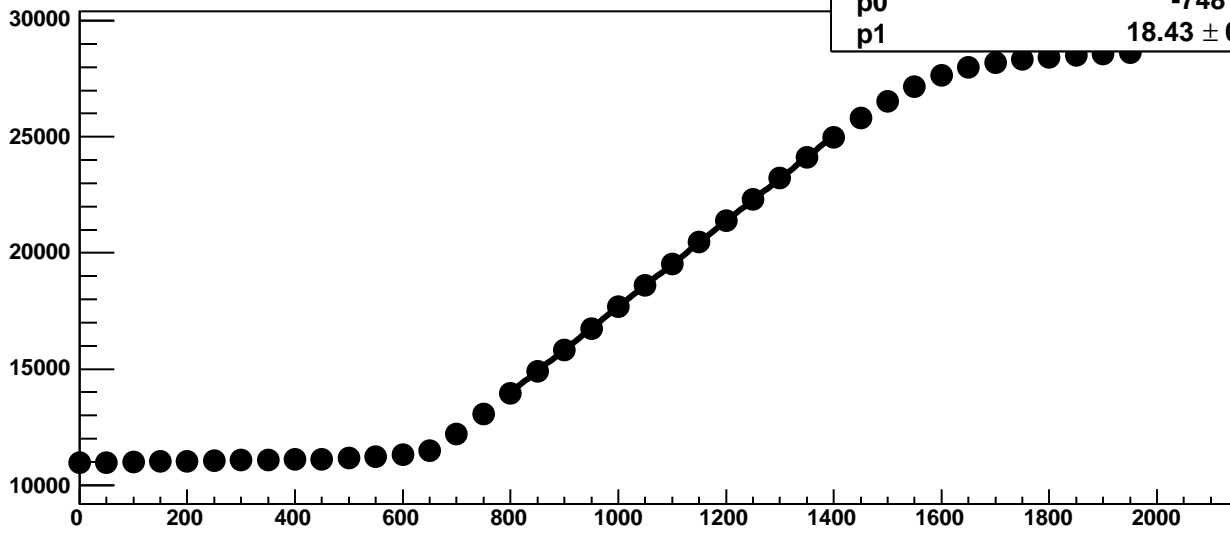
Chip 10, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC



Chip 10, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC

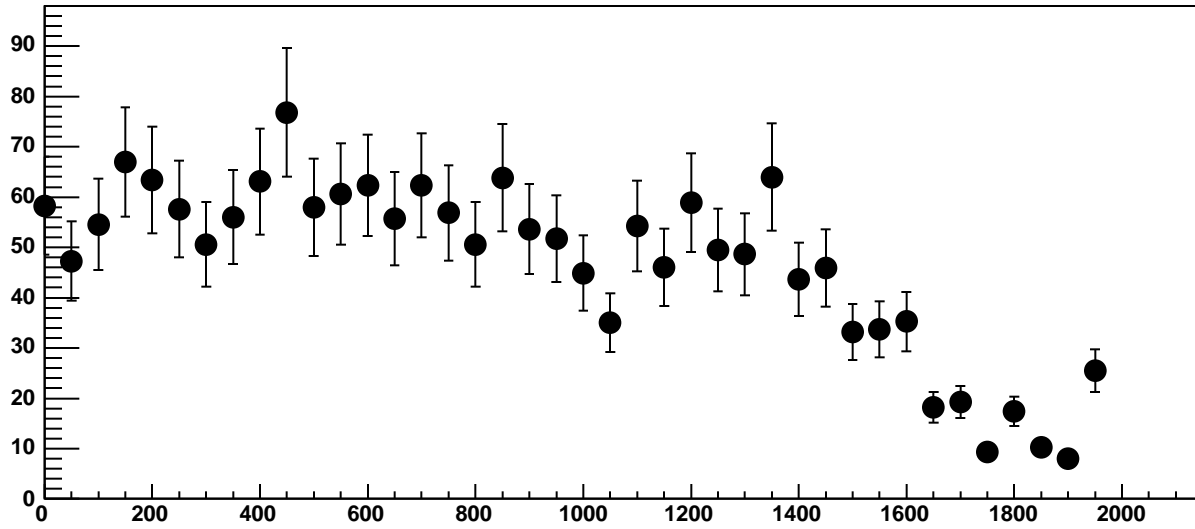


Chip 10, Channel 15, Enable 2!, Hold=30, ADC Mean vs DAC

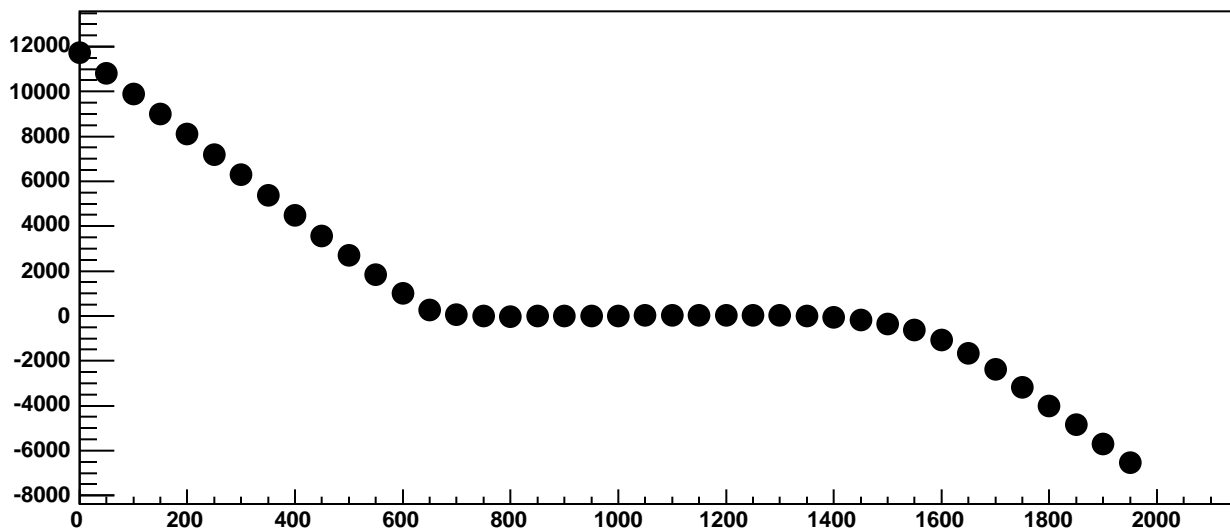


$\chi^2 / \text{ndf}$  73.96 / 11  
p0  $-748 \pm 19.54$   
p1  $18.43 \pm 0.01747$

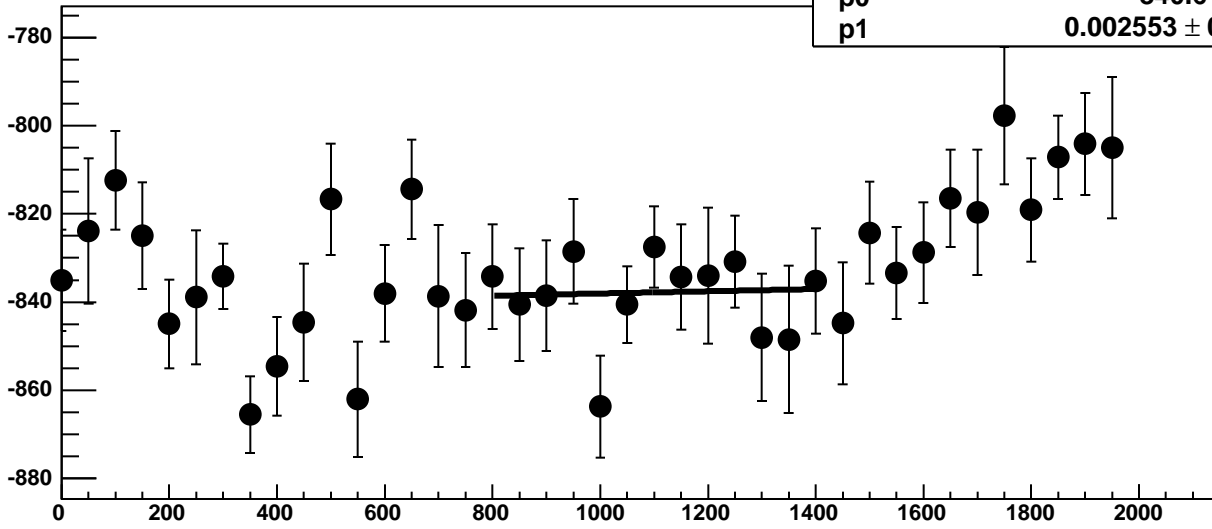
Chip 10, Channel 15, Enable 2!, Hold=30, ADC Noise vs DAC



Chip 10, Channel 15, Enable 2!, Hold=30, ADC Residuals vs DAC

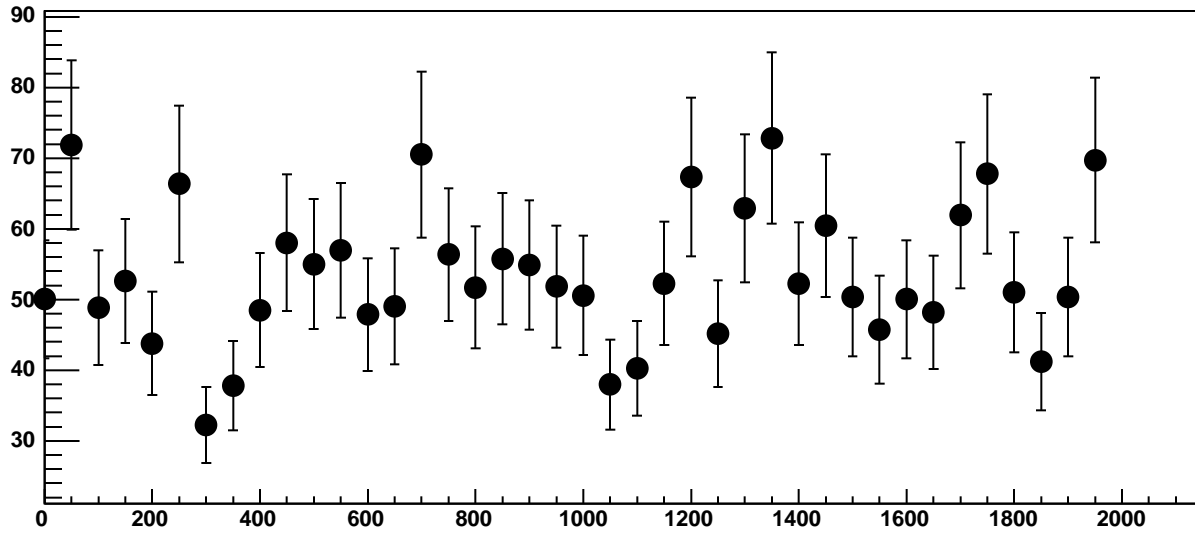


Chip 10, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC

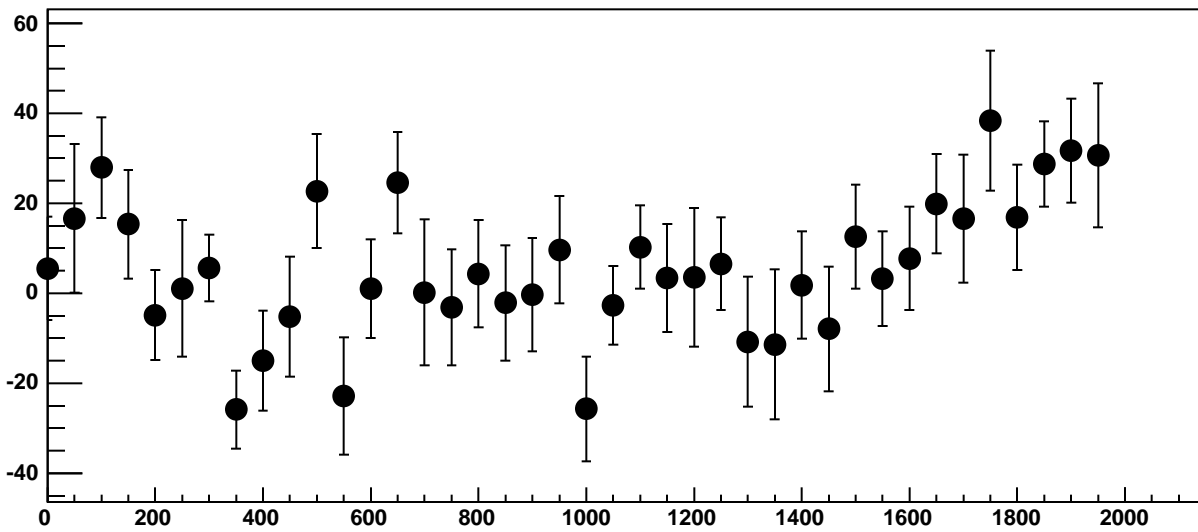


$\chi^2 / \text{ndf}$  8.624 / 11  
p0  $-840.6 \pm 20.67$   
p1  $0.002553 \pm 0.01878$

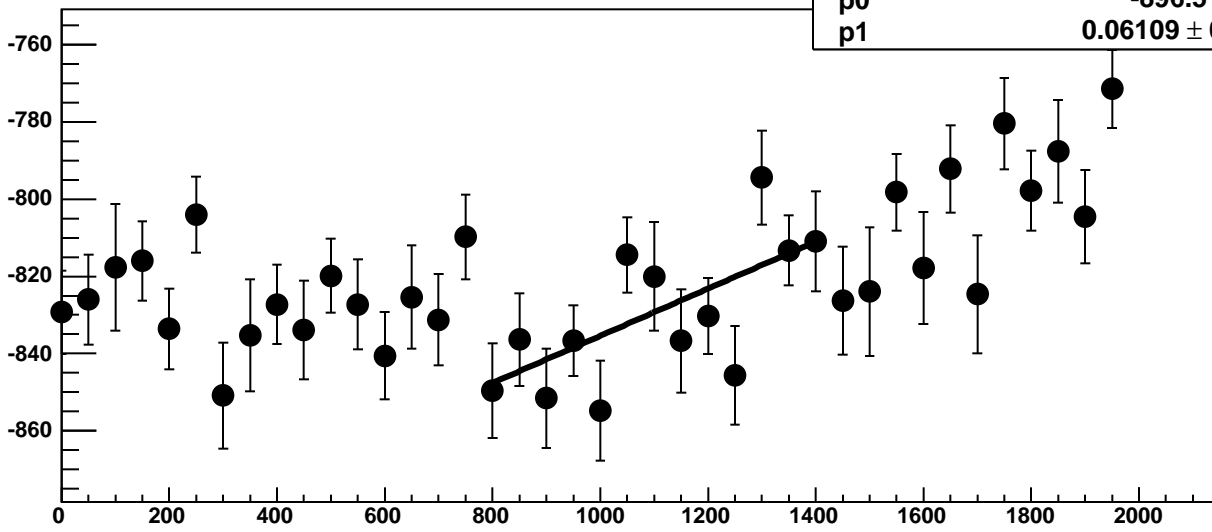
Chip 10, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC



Chip 10, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC



Chip 10, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

15.79 / 11

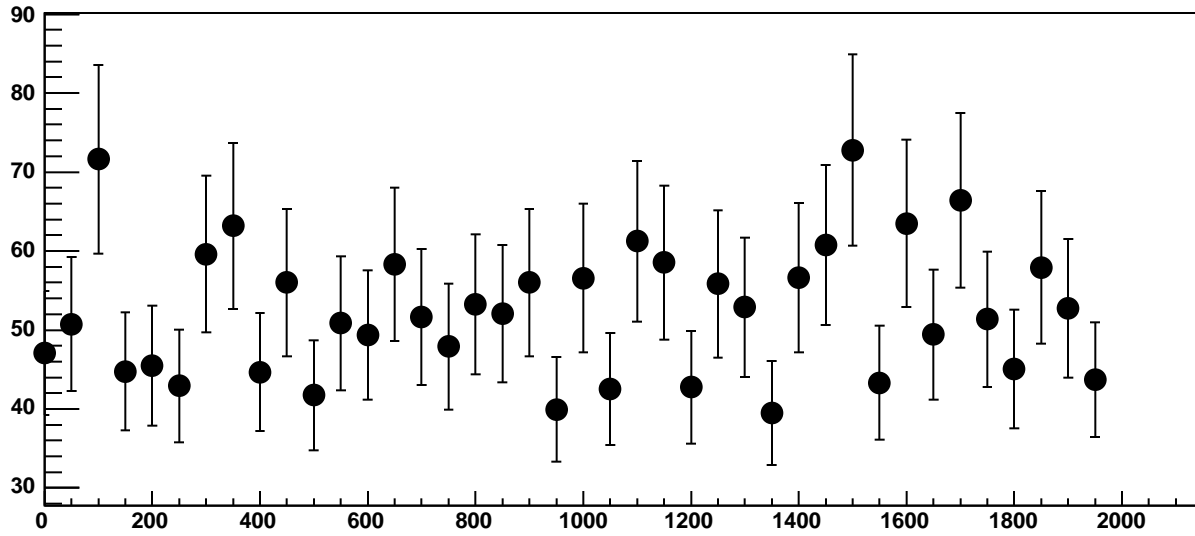
p0

$-896.5 \pm 19.07$

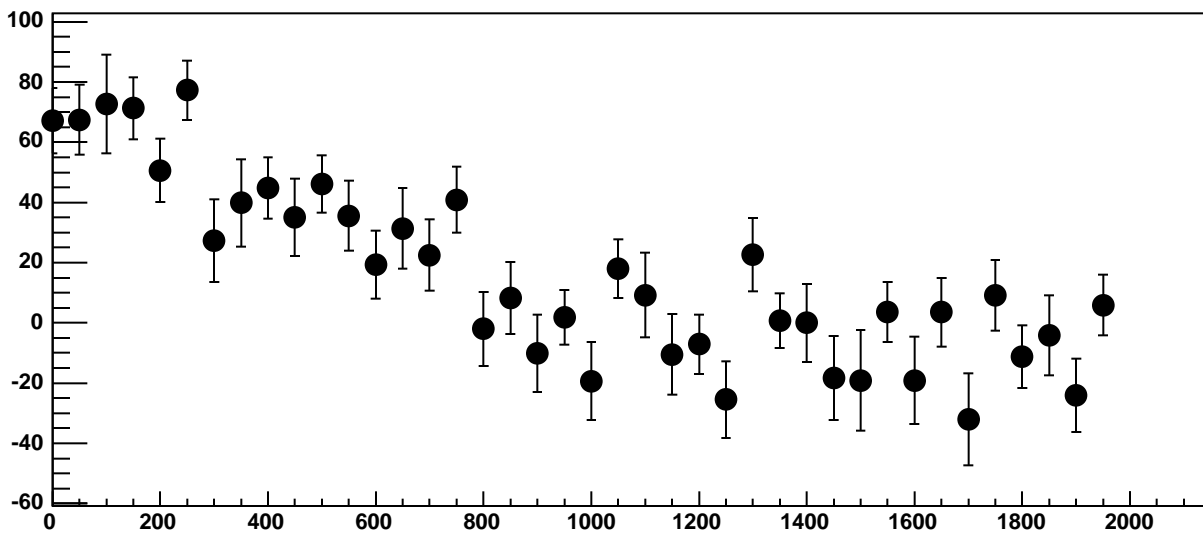
p1

$0.06109 \pm 0.01701$

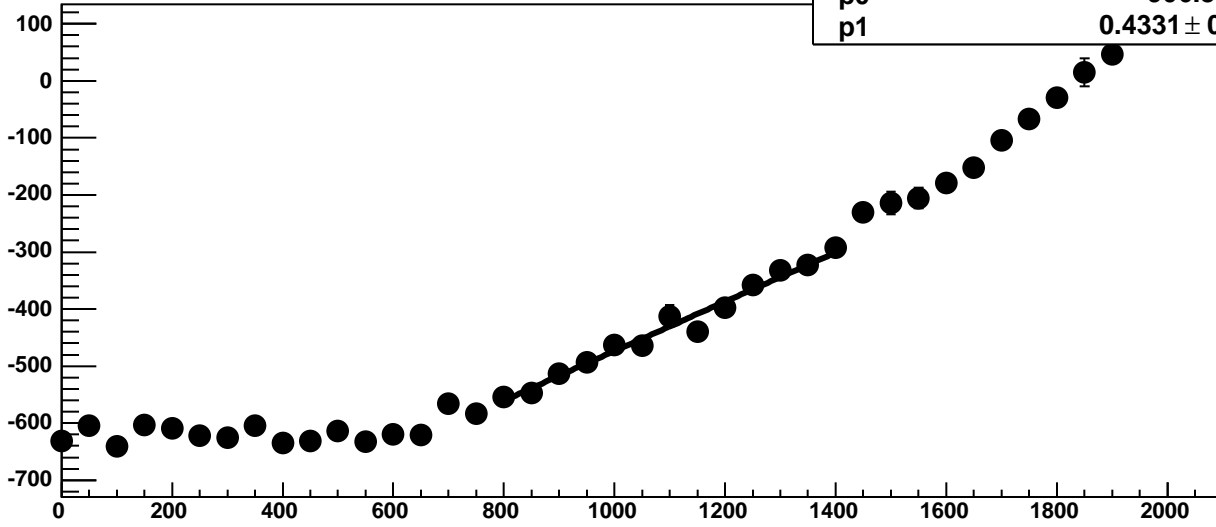
Chip 10, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC



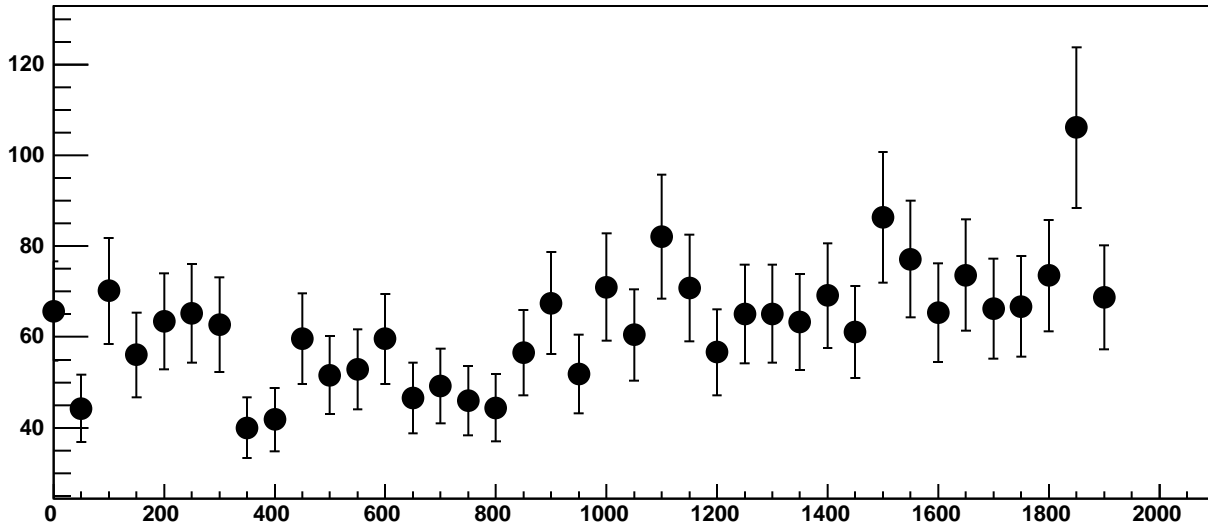
Chip 10, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC



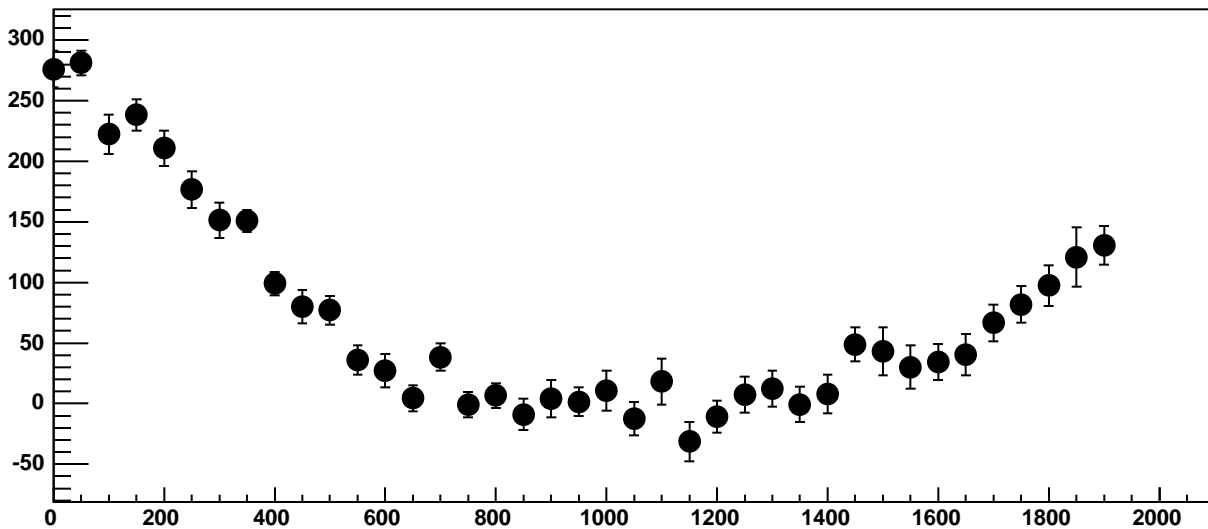
Chip 10, Channel 15, Enable 5, Hold=30, ADC Mean vs DAC



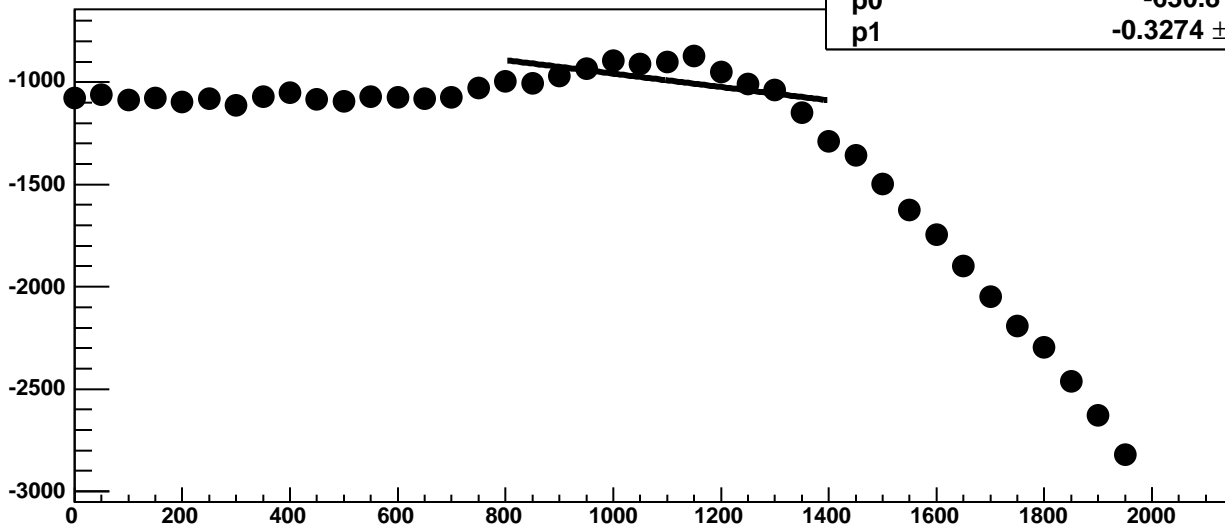
Chip 10, Channel 15, Enable 5, Hold=30, ADC Noise vs DAC



Chip 10, Channel 15, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 10, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

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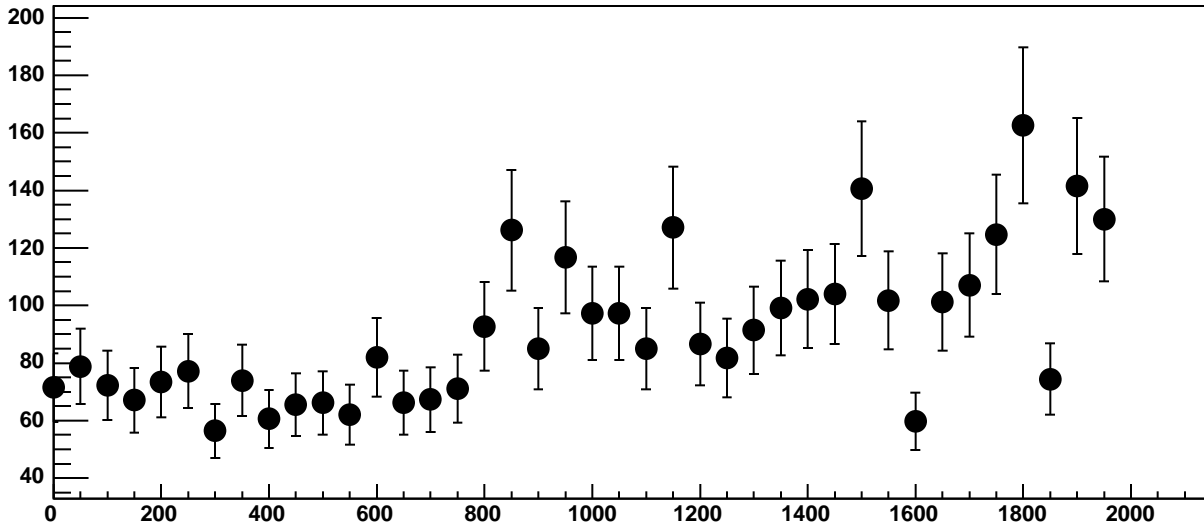
p0

$-630.8 \pm 37.42$

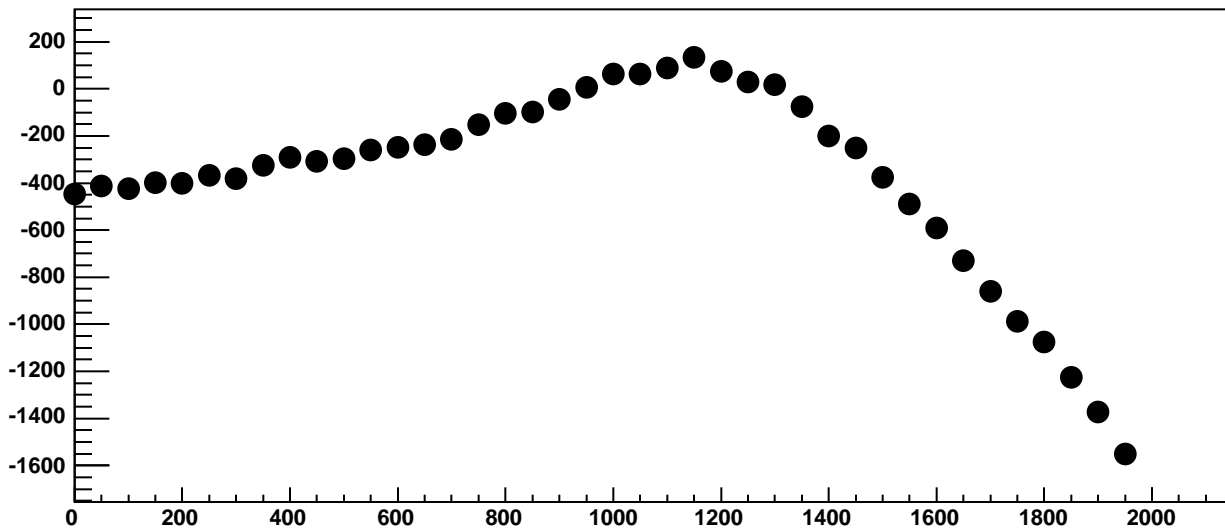
p1

$-0.3274 \pm 0.0333$

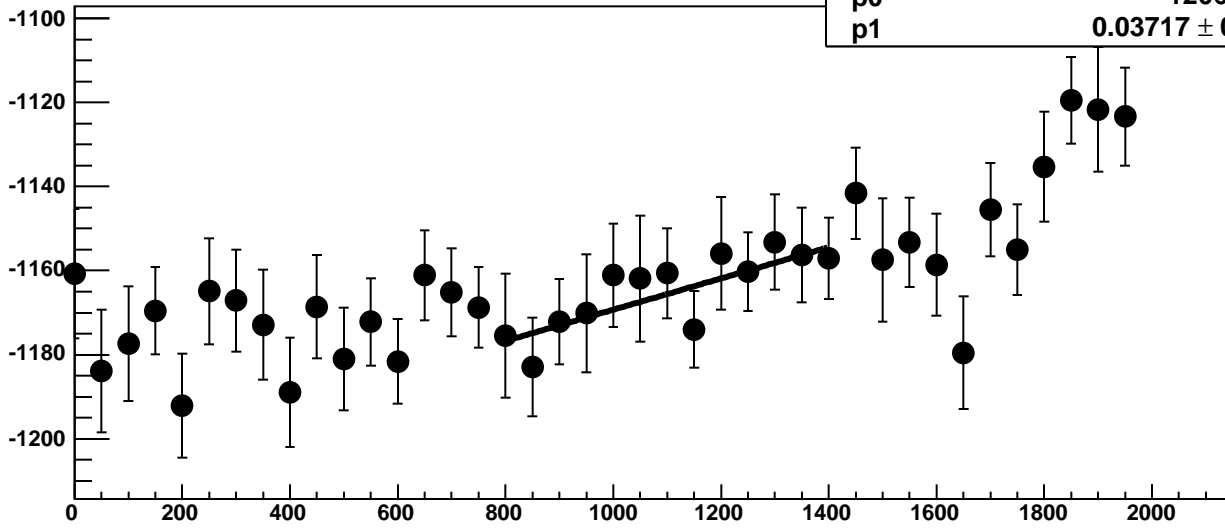
Chip 10, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC



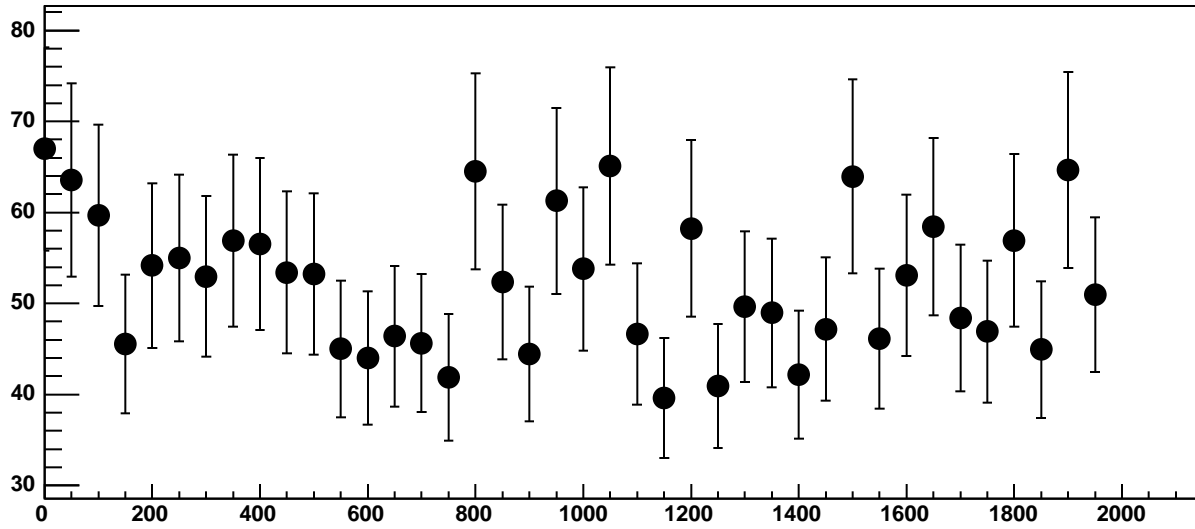
Chip 10, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC



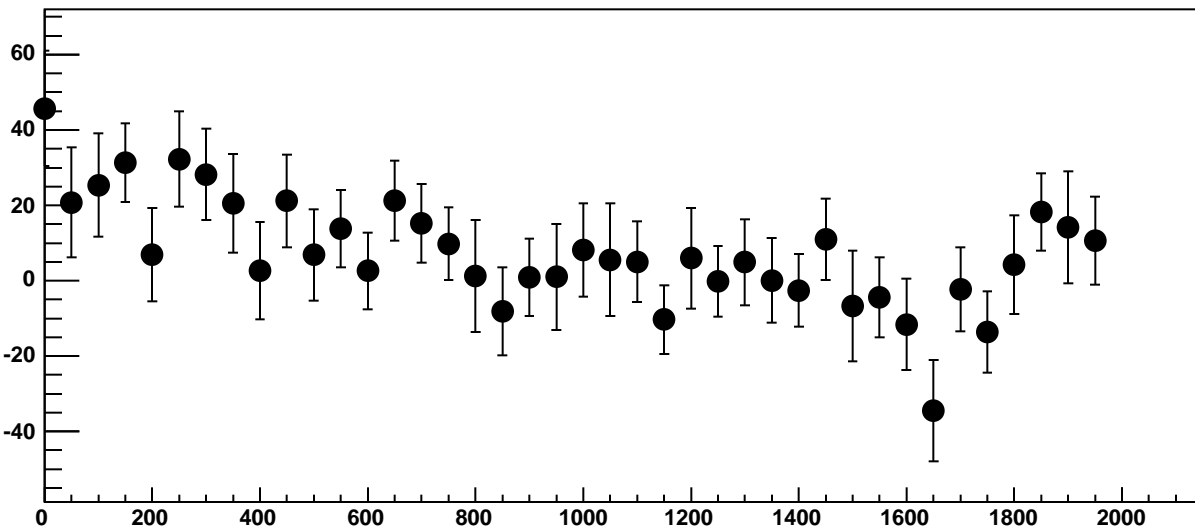
Chip 10, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC



Chip 10, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC

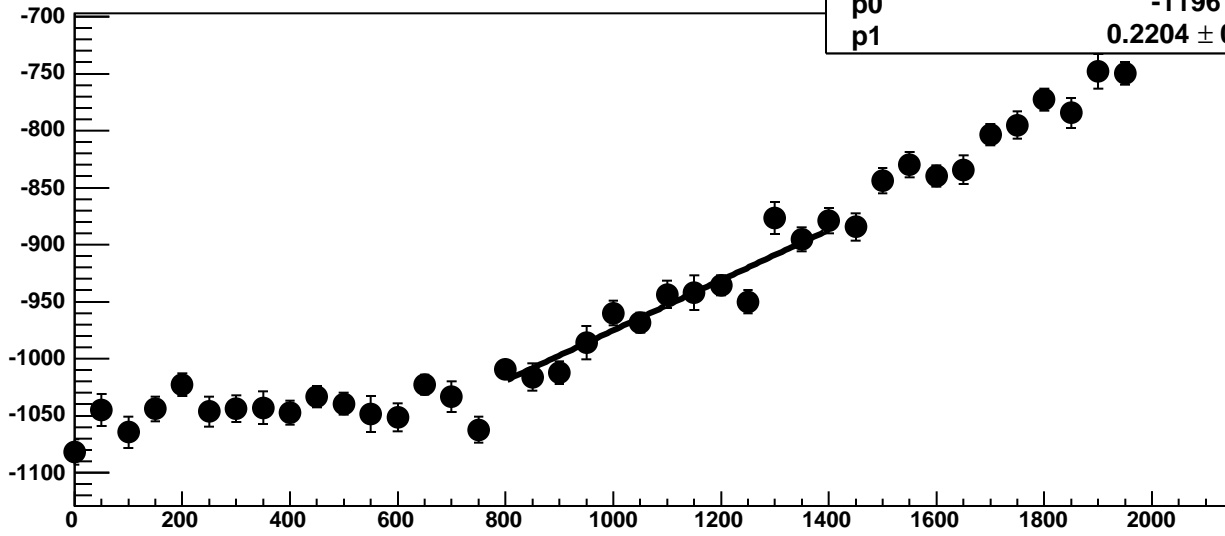


Chip 10, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC

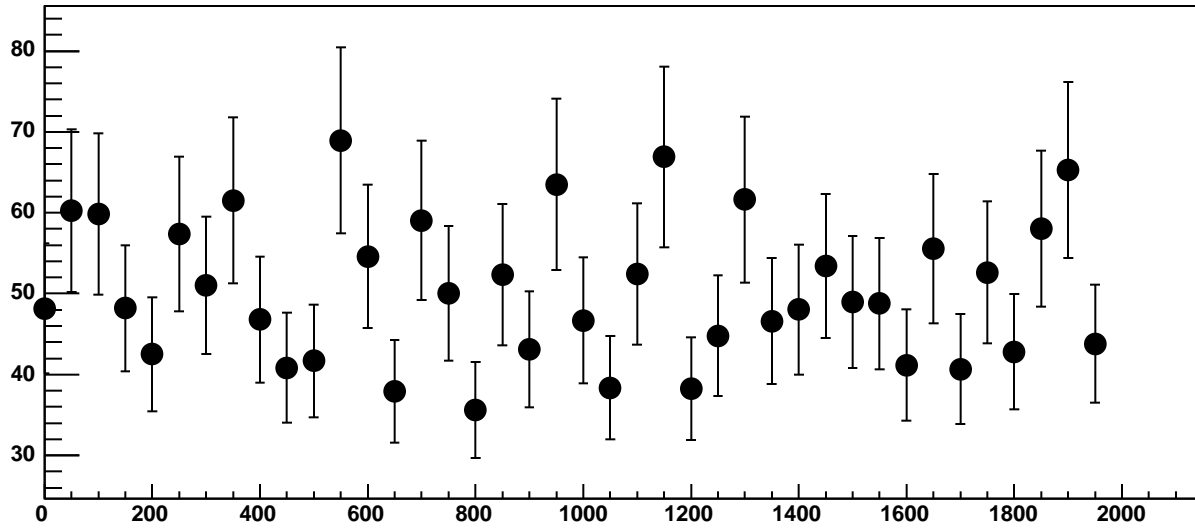




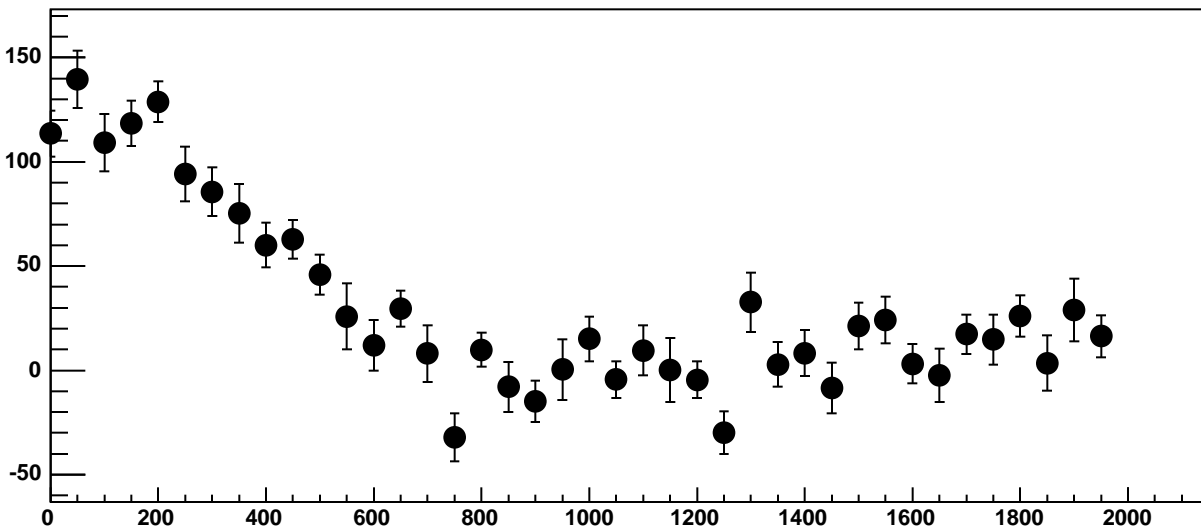
Chip 10, Channel 16, Enable 2, Hold=30, ADC Mean vs DAC



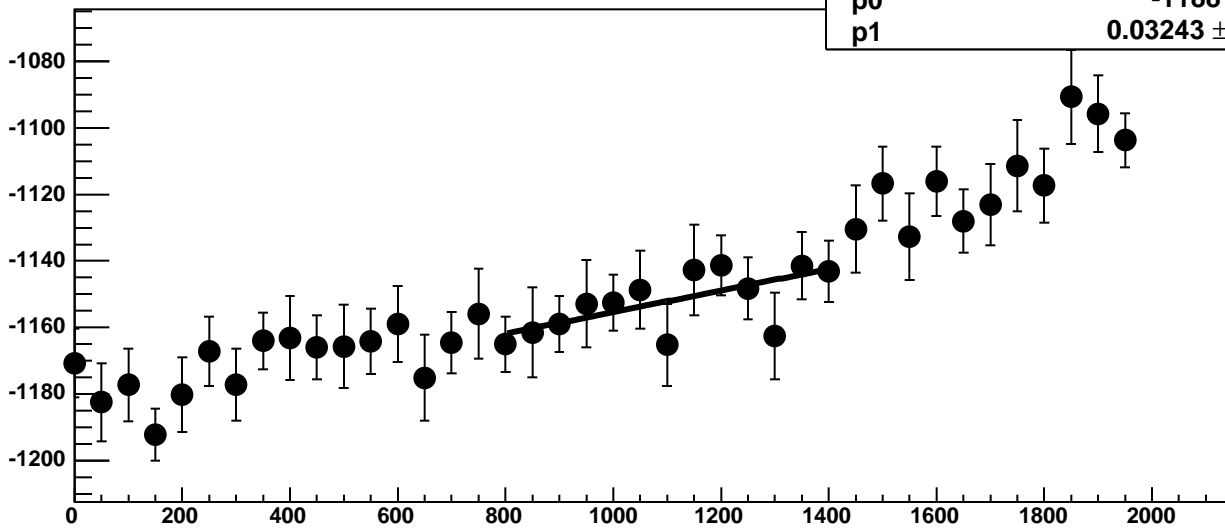
Chip 10, Channel 16, Enable 2, Hold=30, ADC Noise vs DAC



Chip 10, Channel 16, Enable 2, Hold=30, ADC Residuals vs DAC

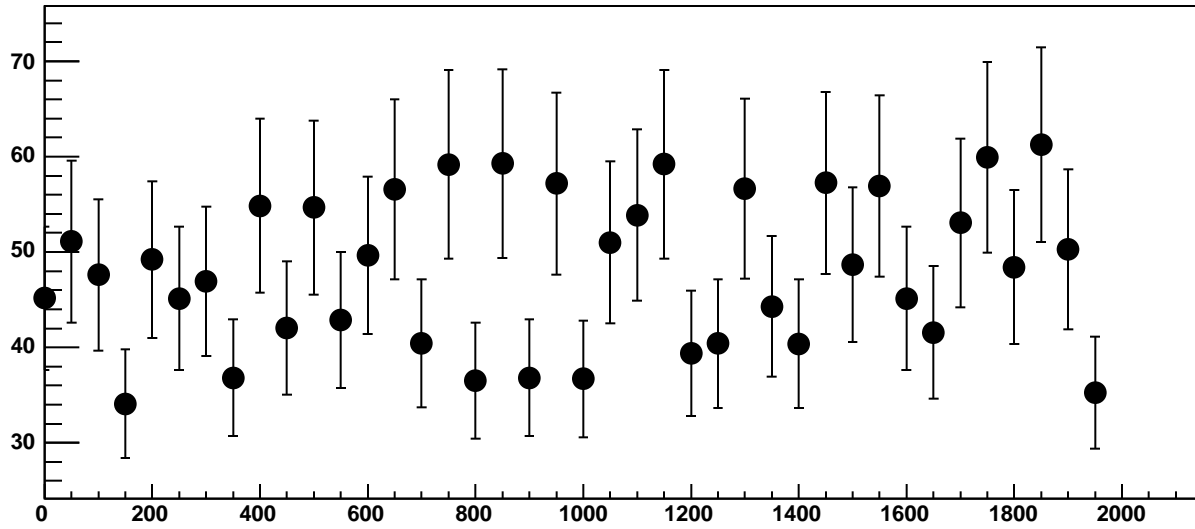


Chip 10, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC

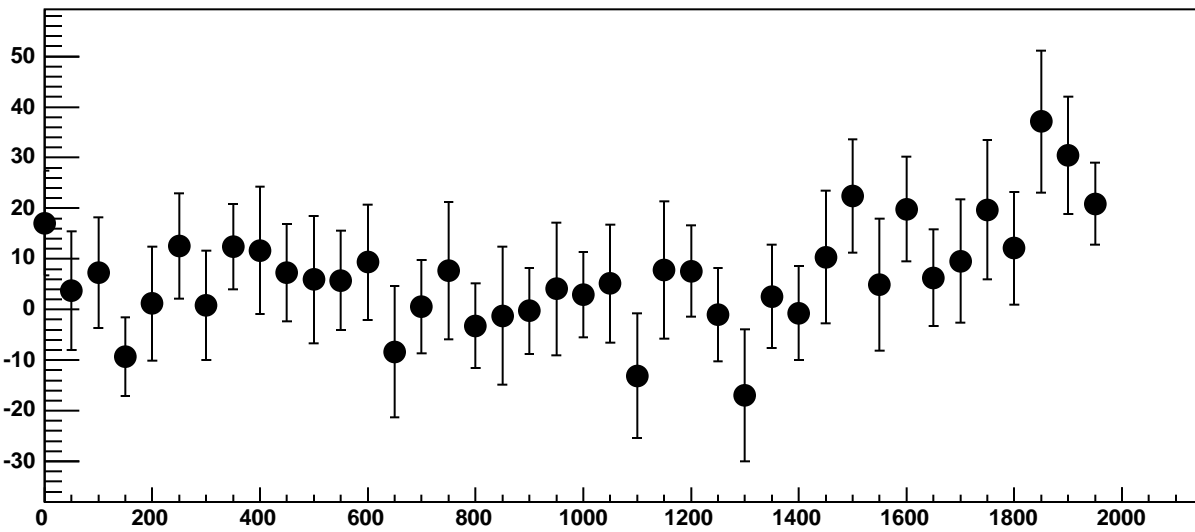


$\chi^2 / \text{ndf}$  4.494 / 11  
p0  $-1188 \pm 16.12$   
p1  $0.03243 \pm 0.0145$

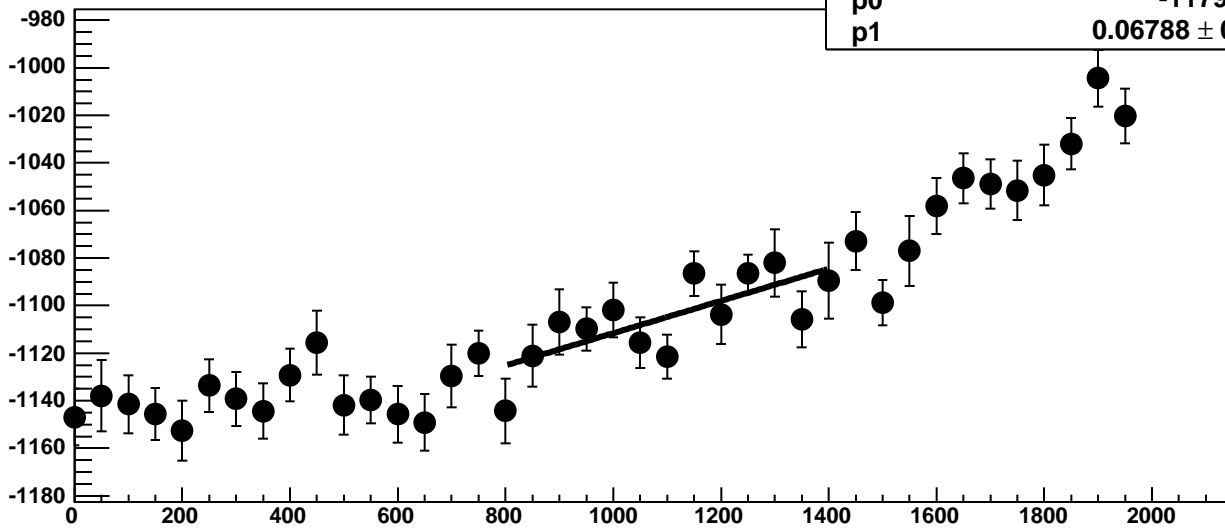
Chip 10, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC



Chip 10, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC



Chip 10, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

14.17 / 11

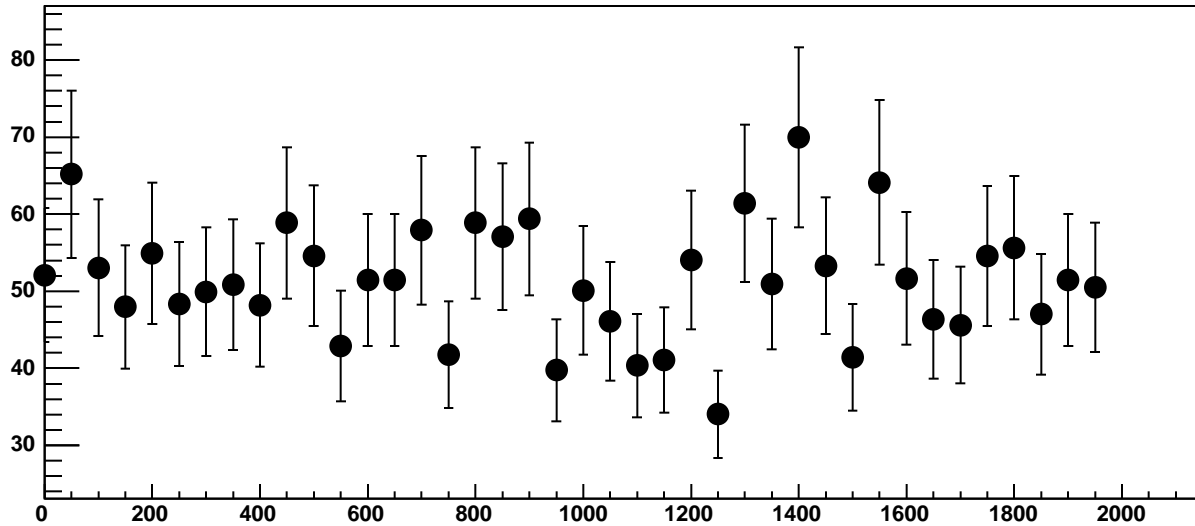
p0

$-1179 \pm 20.7$

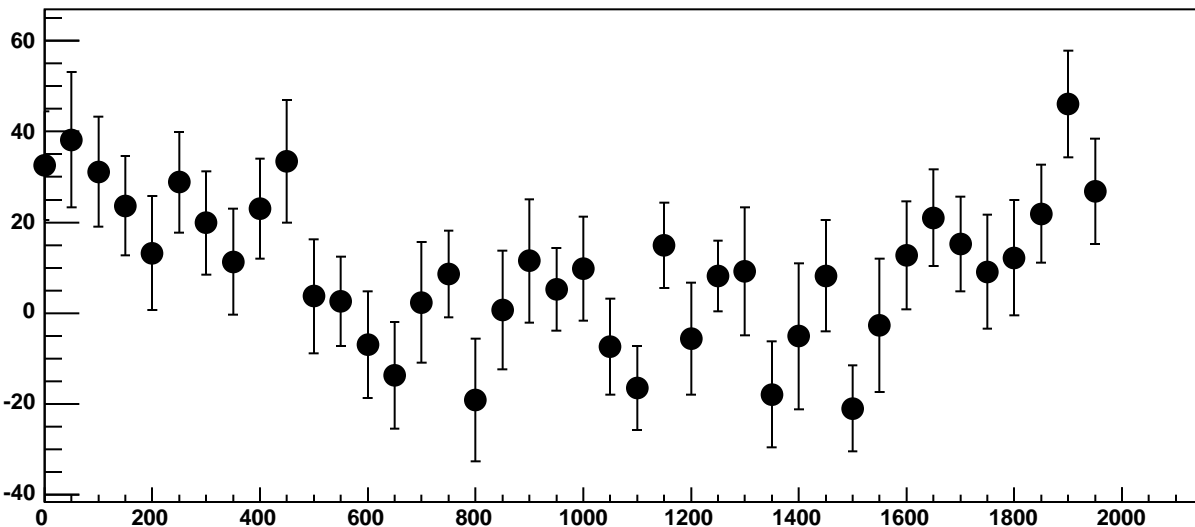
p1

$0.06788 \pm 0.01853$

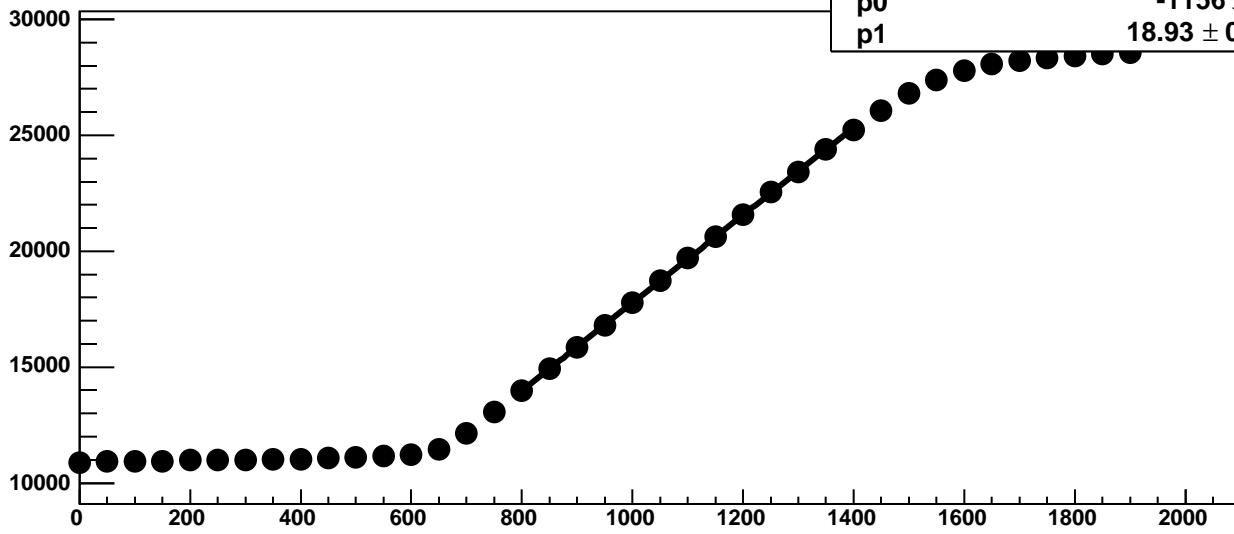
Chip 10, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC



Chip 10, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC

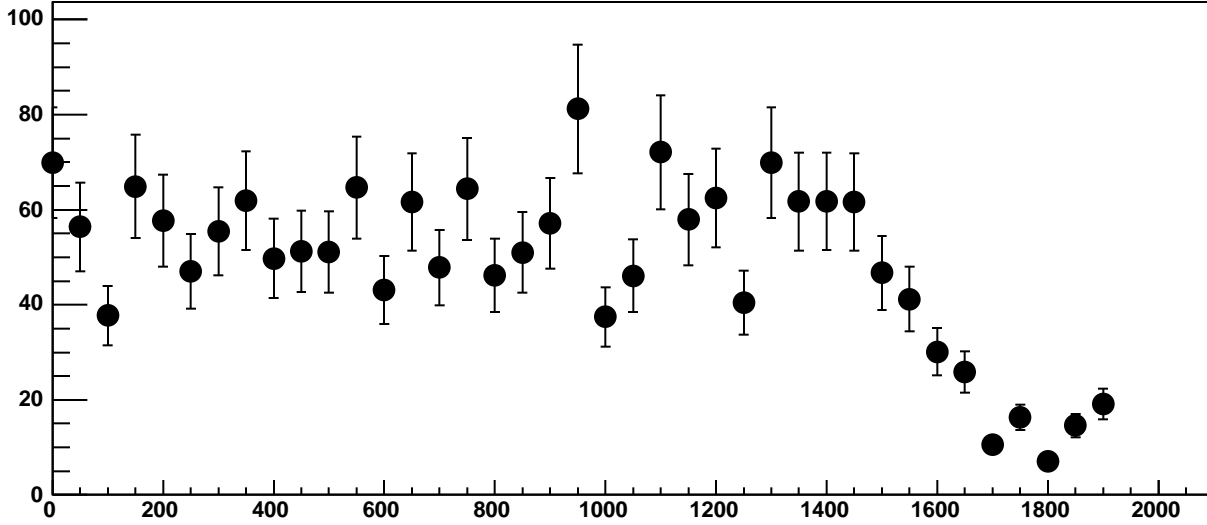


Chip 10, Channel 16, Enable 5!, Hold=30, ADC Mean vs DAC

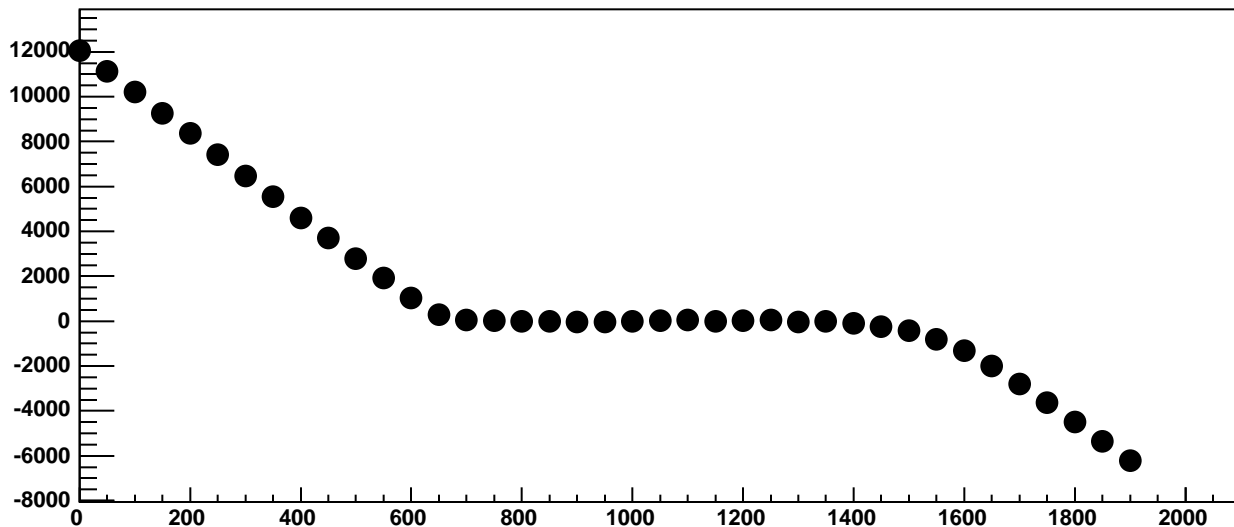


$\chi^2 / \text{ndf}$  104.6 / 11  
p0  $-1156 \pm 20.28$   
p1  $18.93 \pm 0.01854$

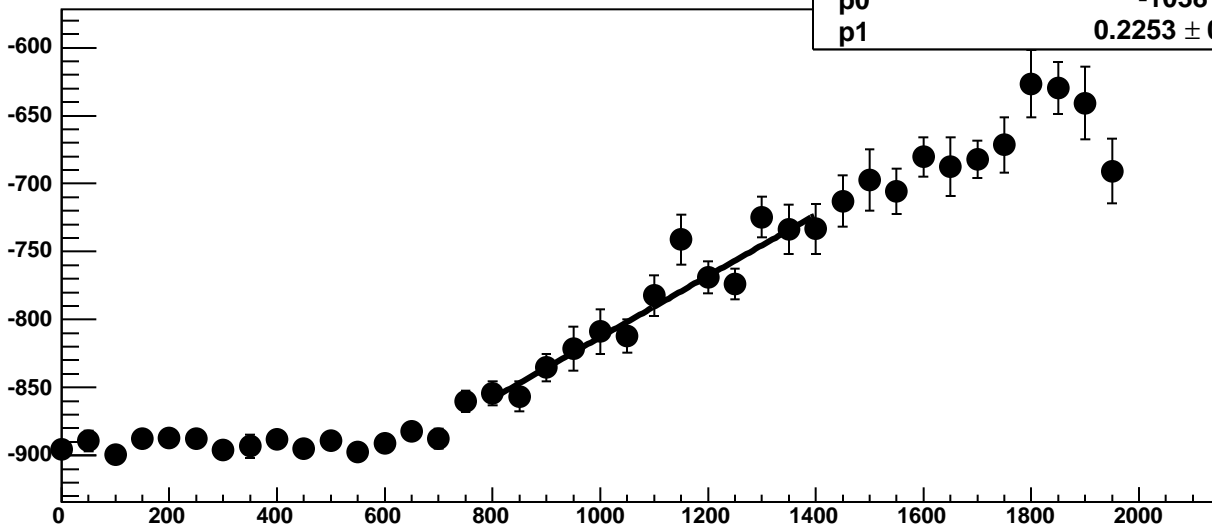
Chip 10, Channel 16, Enable 5!, Hold=30, ADC Noise vs DAC



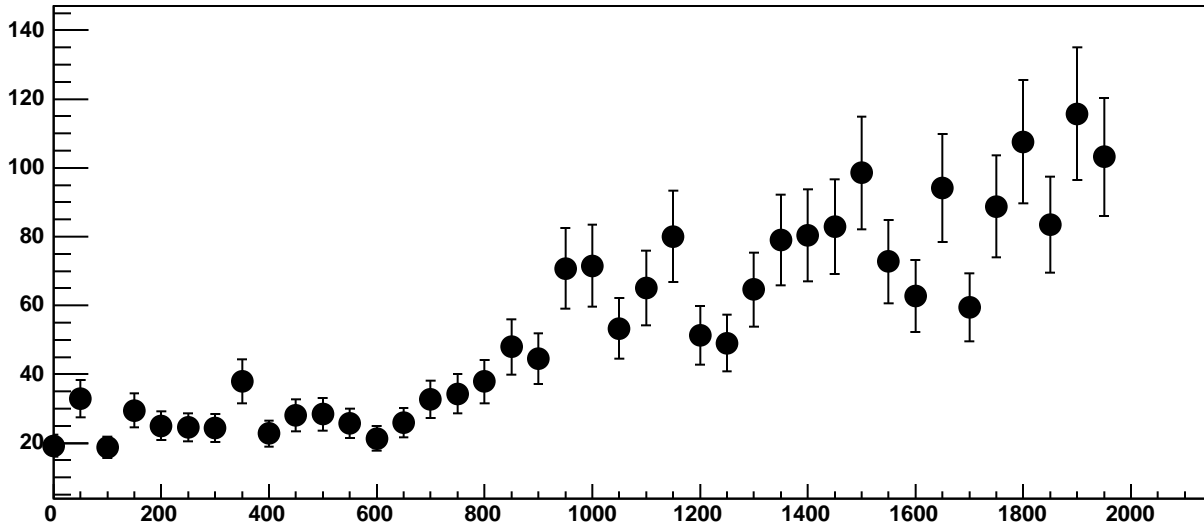
Chip 10, Channel 16, Enable 5!, Hold=30, ADC Residuals vs DAC



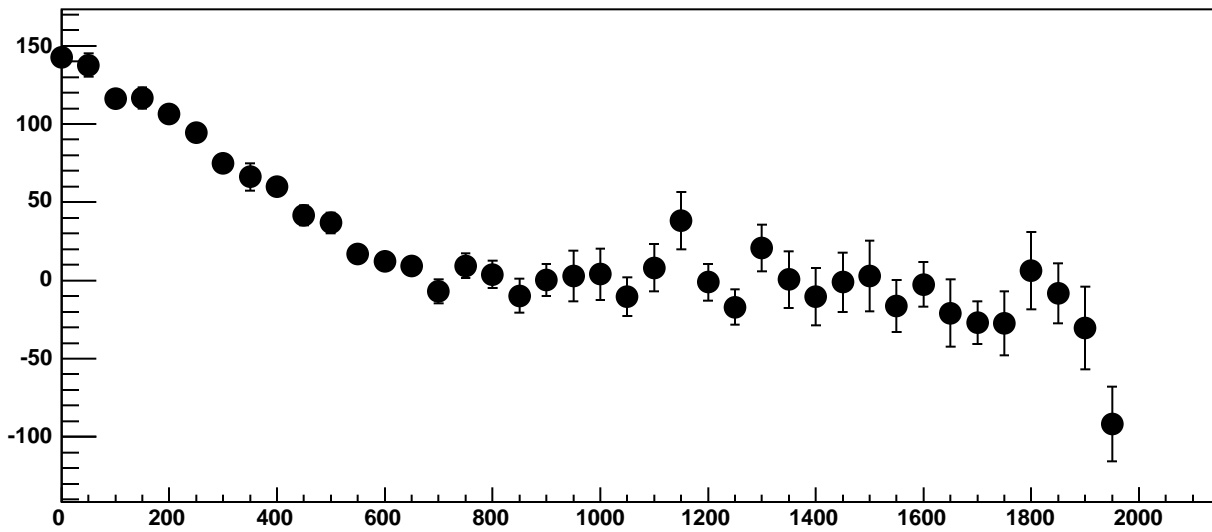
Chip 10, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC



Chip 10, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC

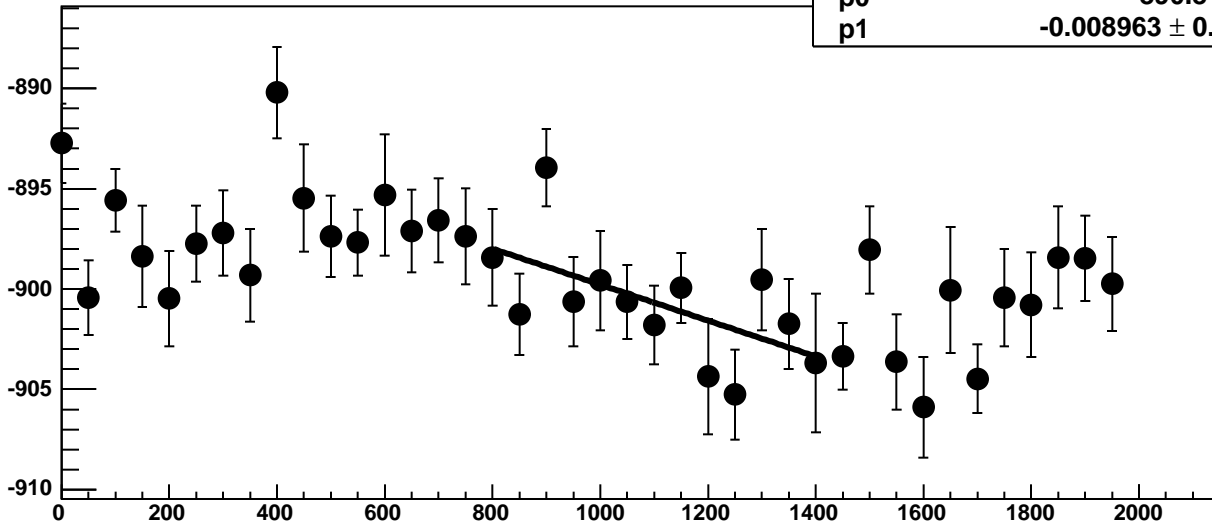


Chip 10, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC

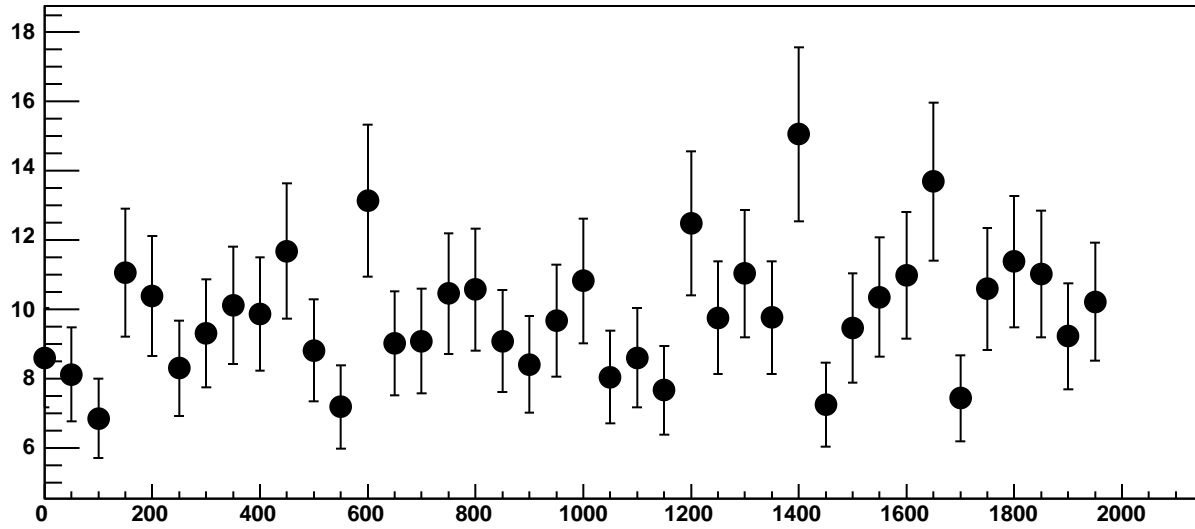


Chip 10, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC

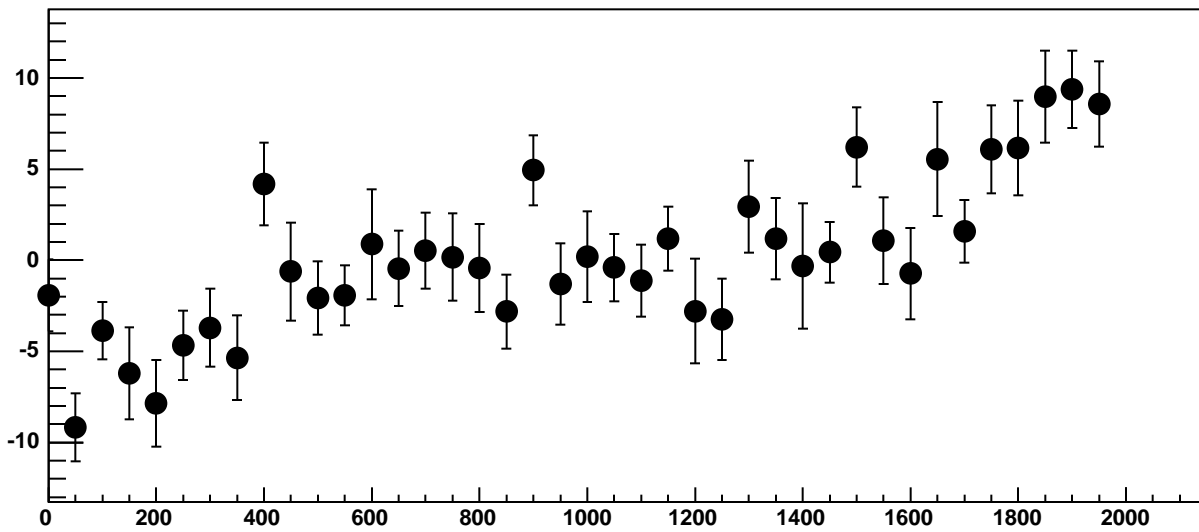
$\chi^2 / \text{ndf}$  14.35 / 11  
p0  $-890.8 \pm 3.869$   
p1  $-0.008963 \pm 0.003548$



Chip 10, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC



Chip 10, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC



Chip 10, Channel 17, Enable 2!, Hold=30, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

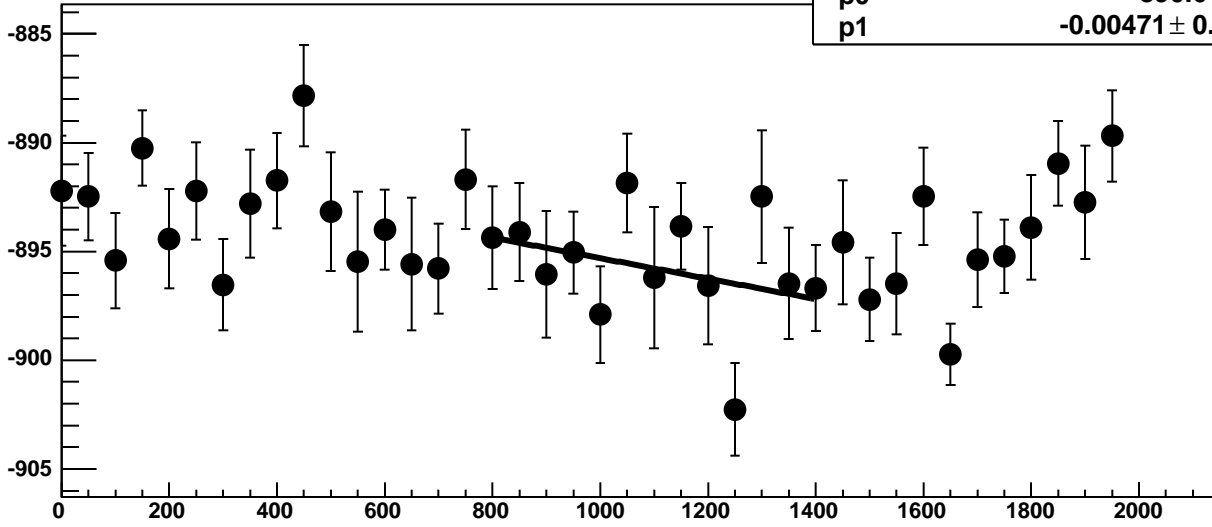
14.83 / 11

p0

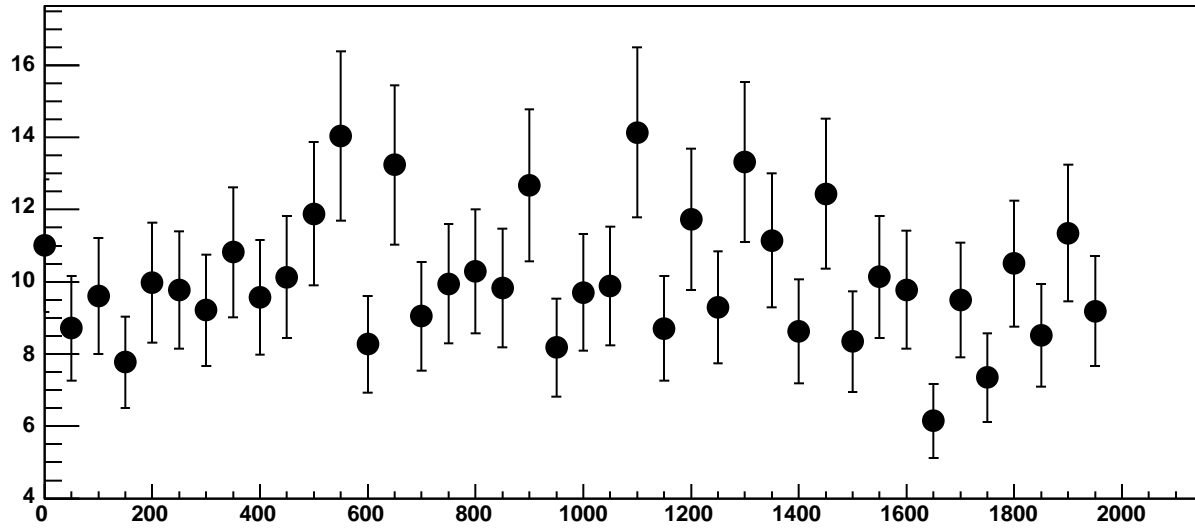
$-890.6 \pm 3.805$

p1

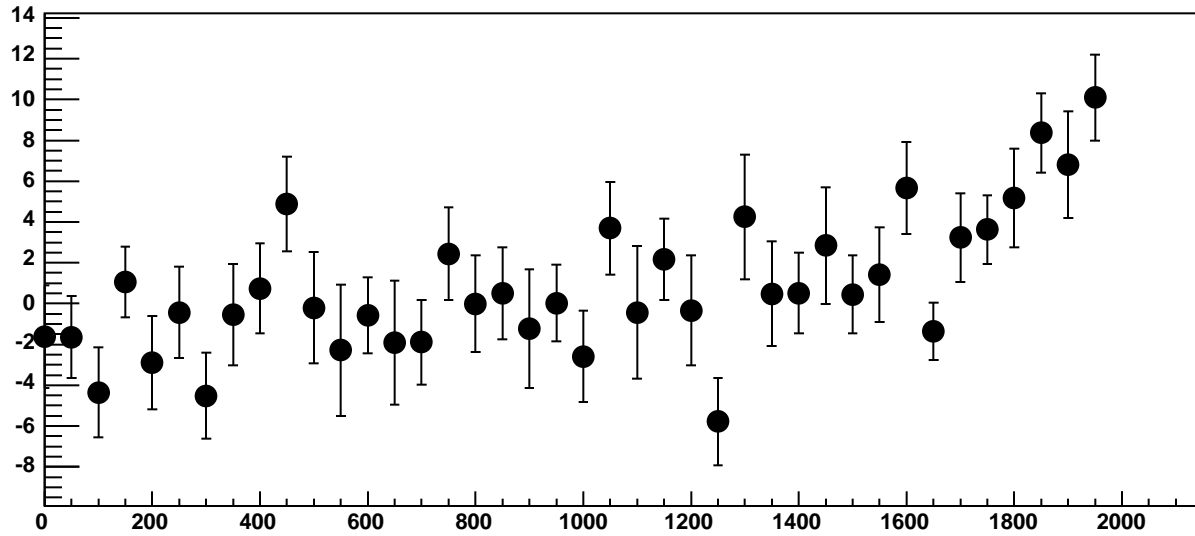
$-0.00471 \pm 0.003413$



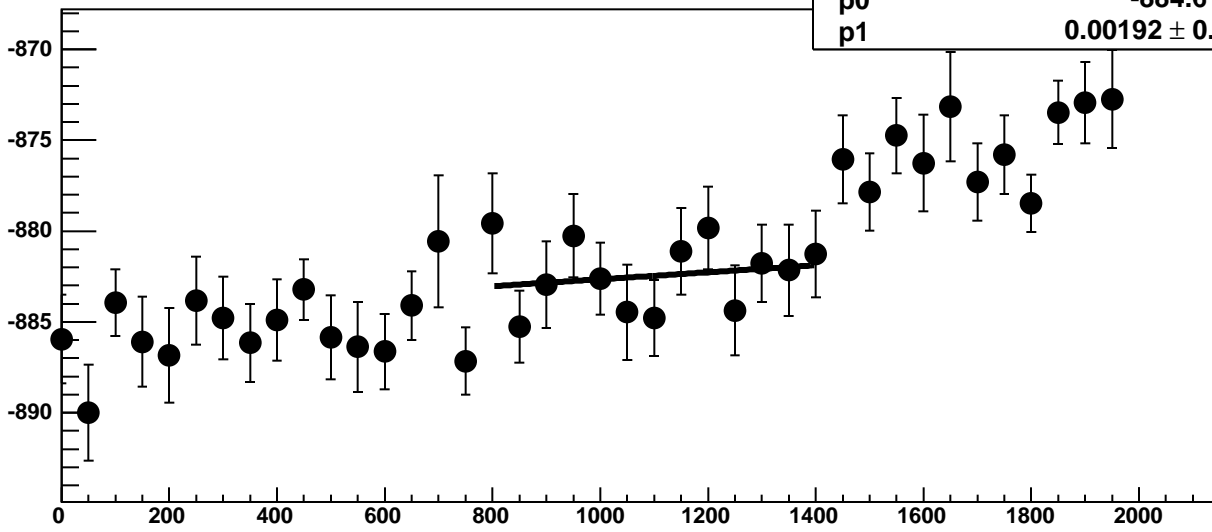
Chip 10, Channel 17, Enable 2!, Hold=30, ADC Noise vs DAC



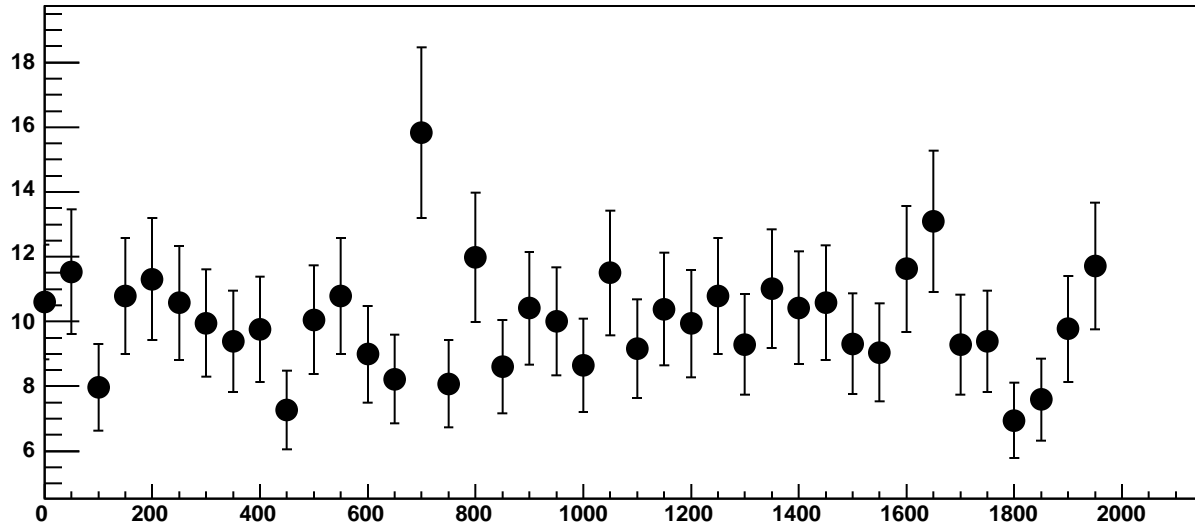
Chip 10, Channel 17, Enable 2!, Hold=30, ADC Residuals vs DAC



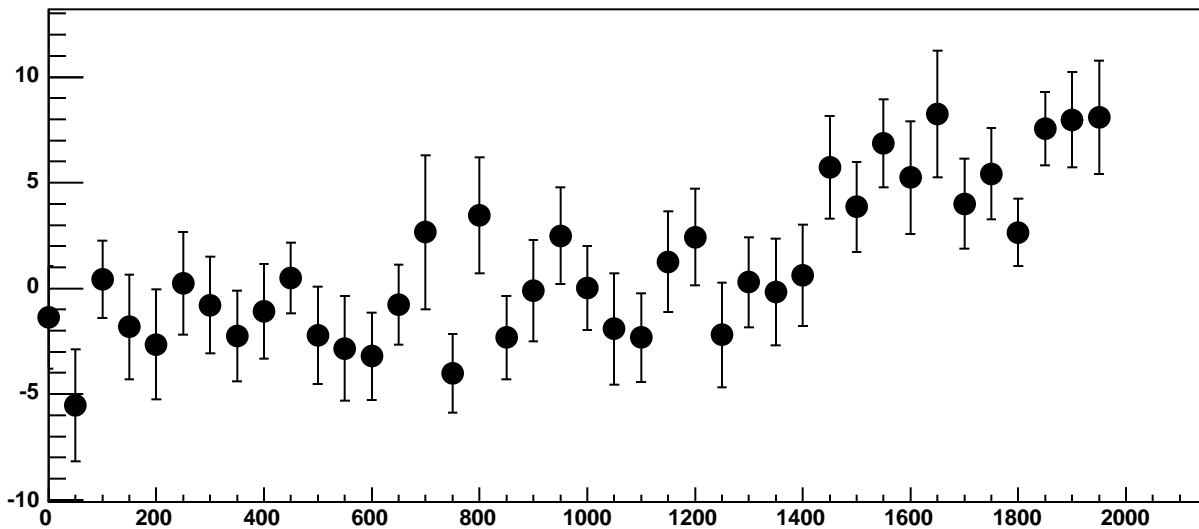
Chip 10, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC



Chip 10, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC

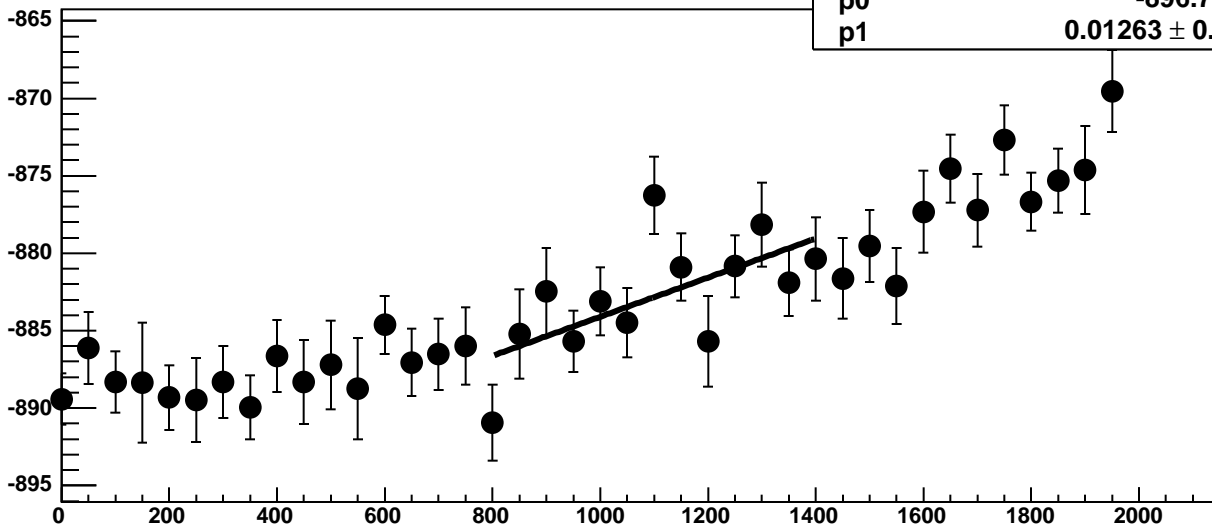


Chip 10, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC



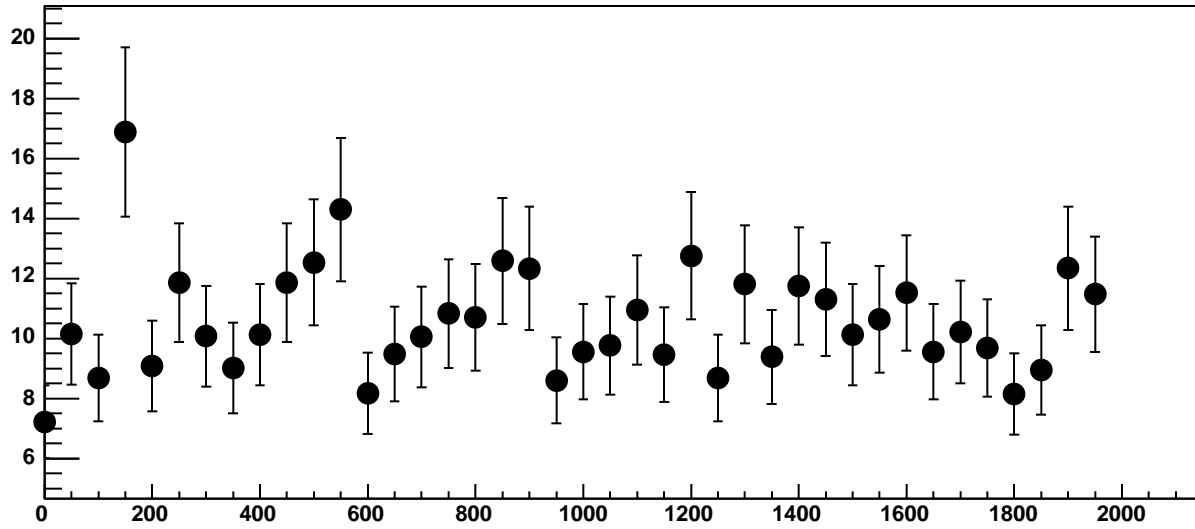


Chip 10, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC

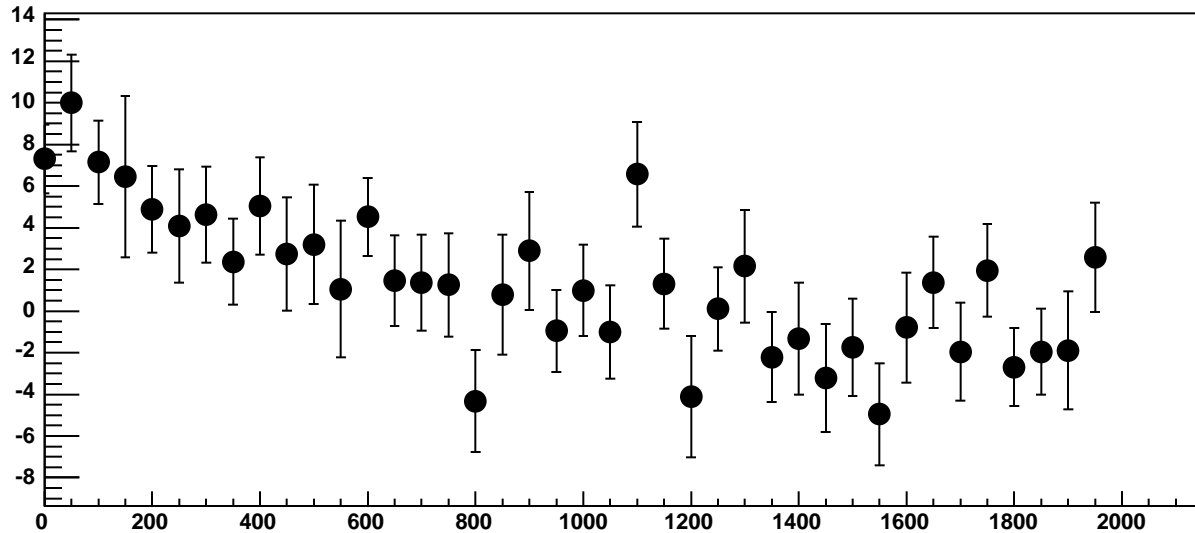


$\chi^2 / \text{ndf}$  15.96 / 11  
p0  $-896.7 \pm 4.101$   
p1  $0.01263 \pm 0.003667$

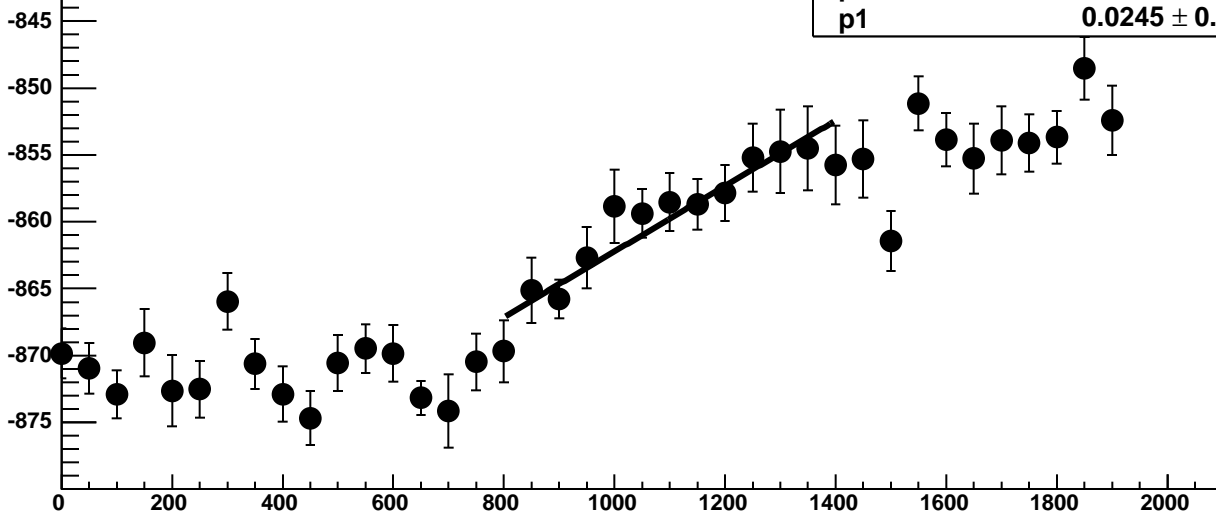
Chip 10, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC



Chip 10, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC

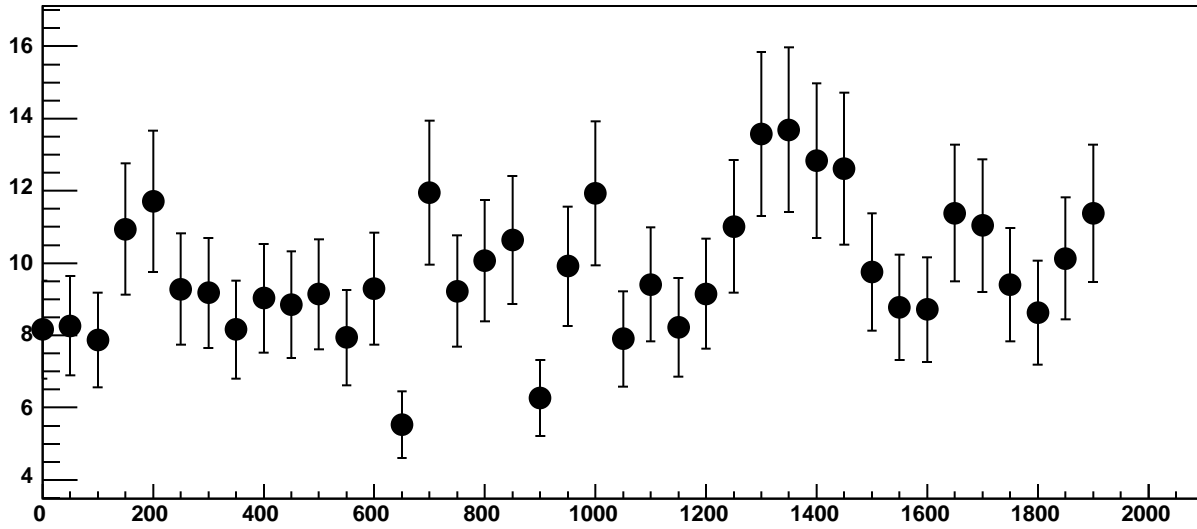


Chip 10, Channel 17, Enable 5, Hold=30, ADC Mean vs DAC

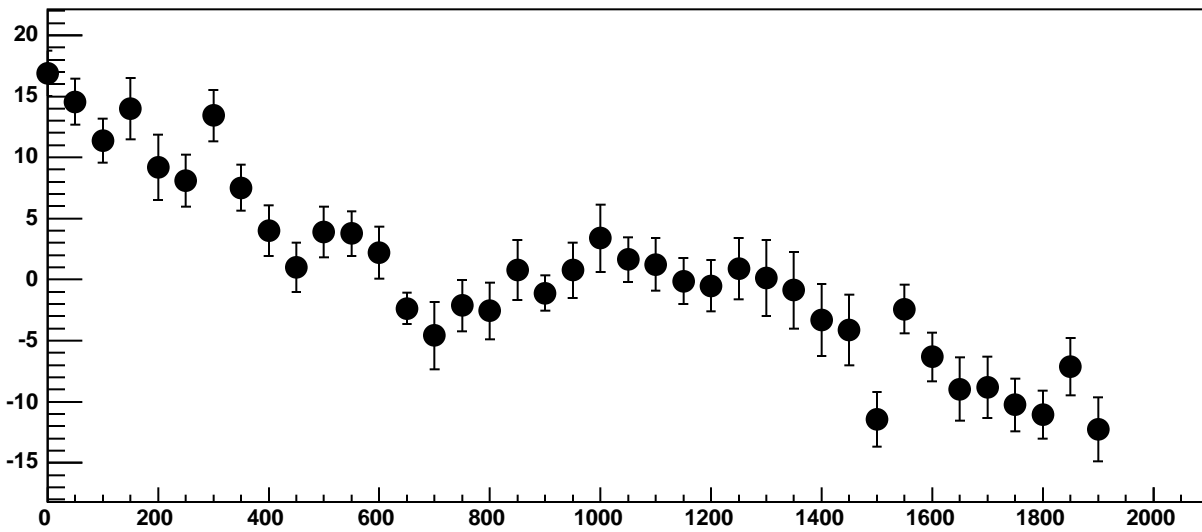


$\chi^2 / \text{ndf}$  6.254 / 11  
p0 -886.7 ± 3.891  
p1 0.0245 ± 0.003631

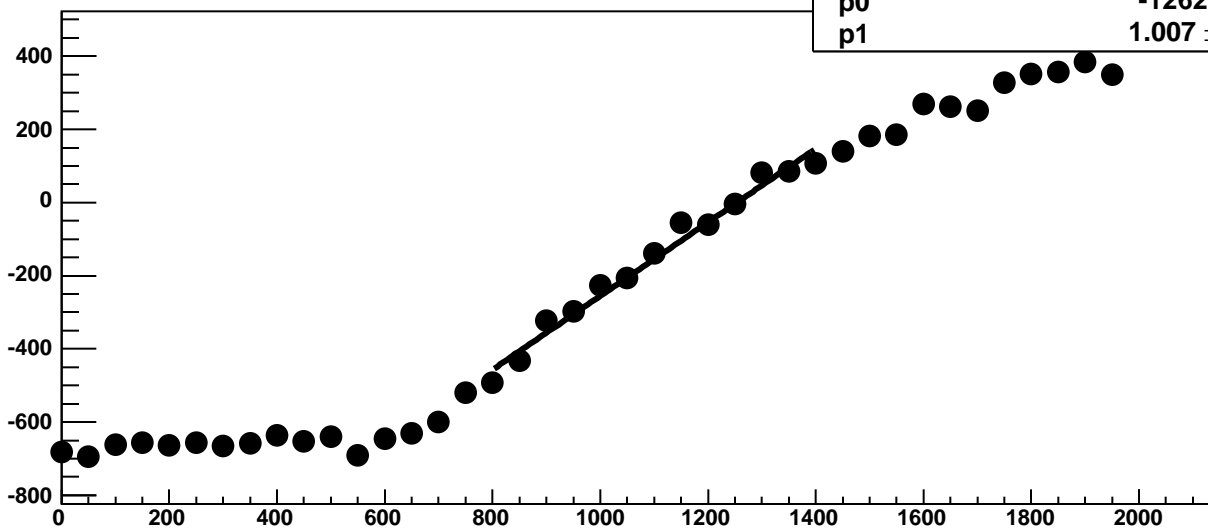
Chip 10, Channel 17, Enable 5, Hold=30, ADC Noise vs DAC



Chip 10, Channel 17, Enable 5, Hold=30, ADC Residuals vs DAC

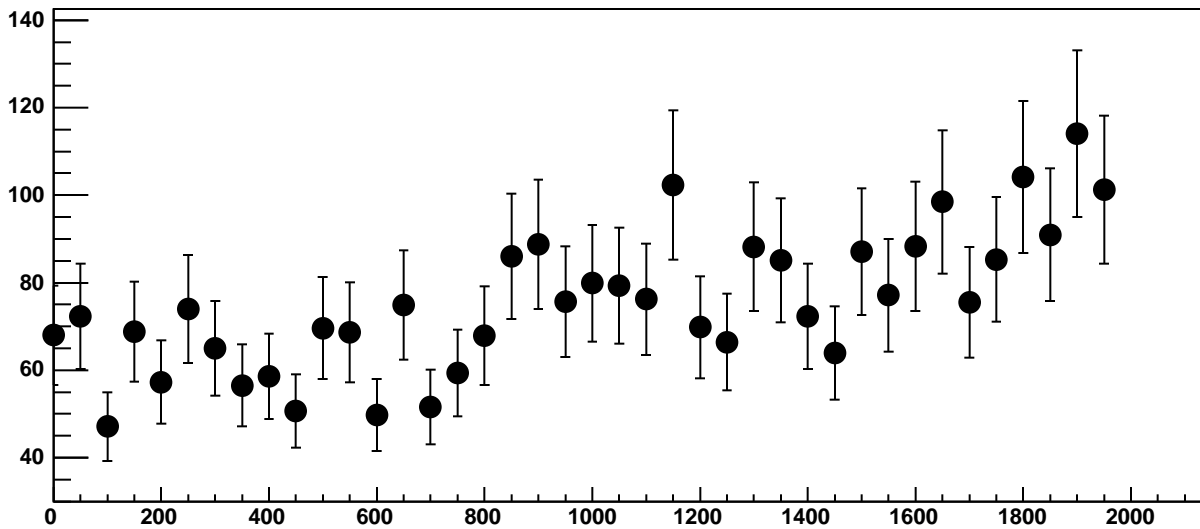


Chip 11, Channel 0, Enable 0, Hold=30, ADC Mean vs DAC

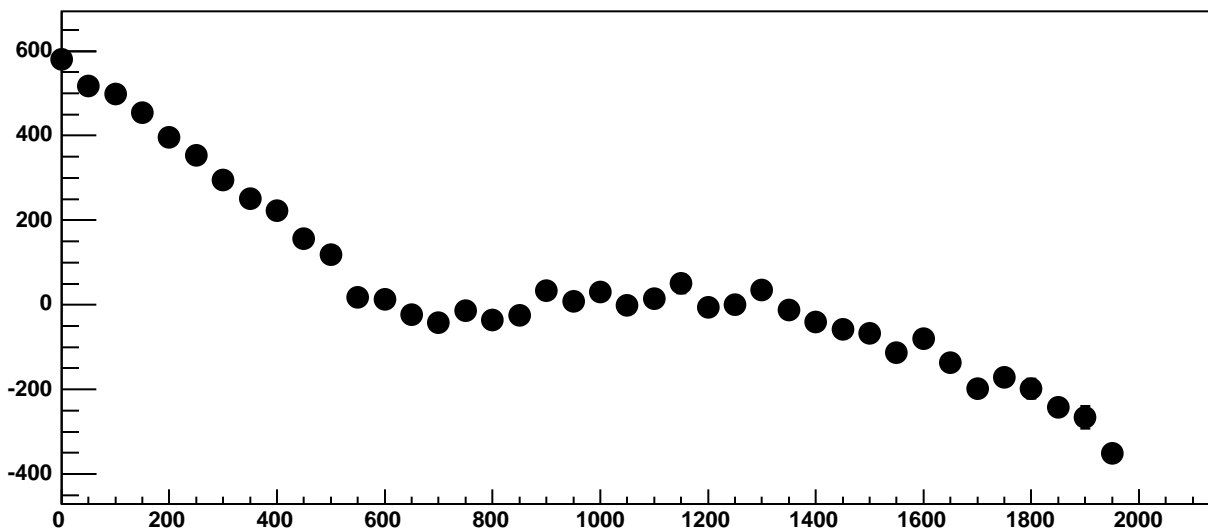


$\chi^2 / \text{ndf}$  27.39 / 11  
p0  $-1262 \pm 29.17$   
p1  $1.007 \pm 0.0261$

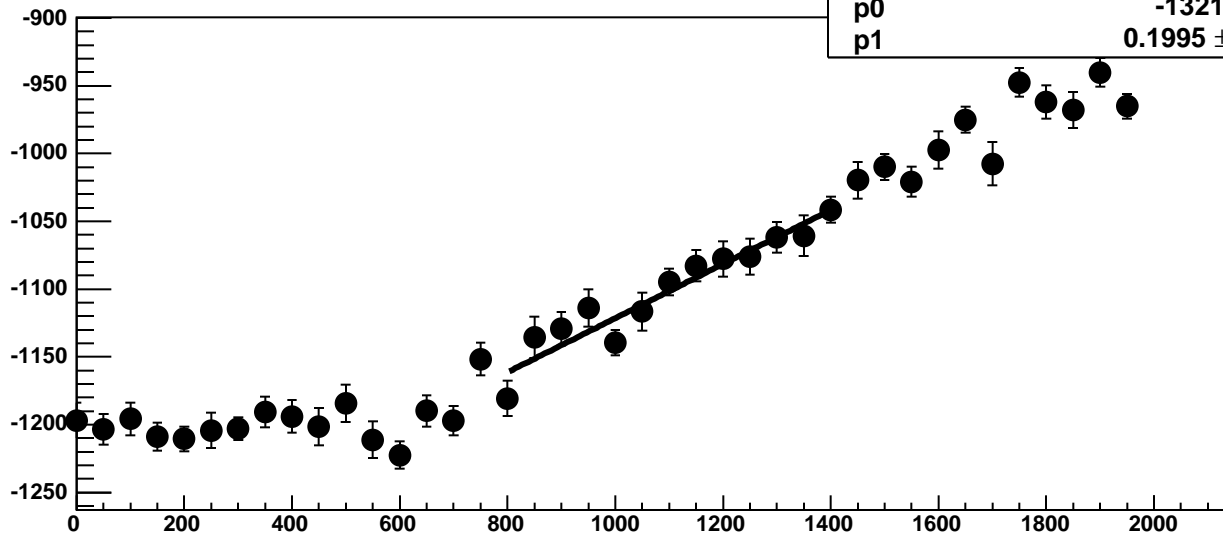
Chip 11, Channel 0, Enable 0, Hold=30, ADC Noise vs DAC



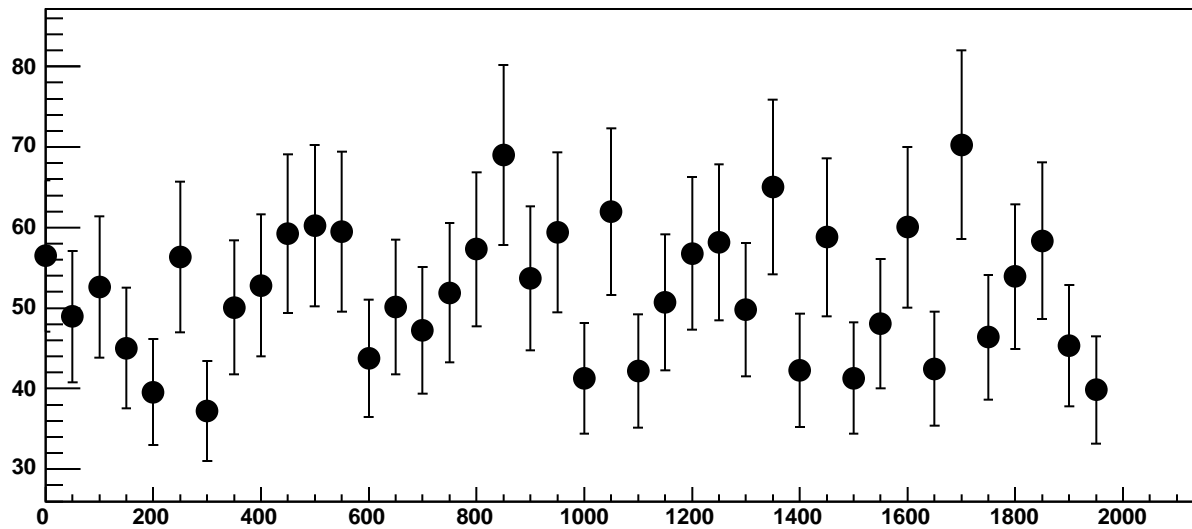
Chip 11, Channel 0, Enable 0, Hold=30, ADC Residuals vs DAC



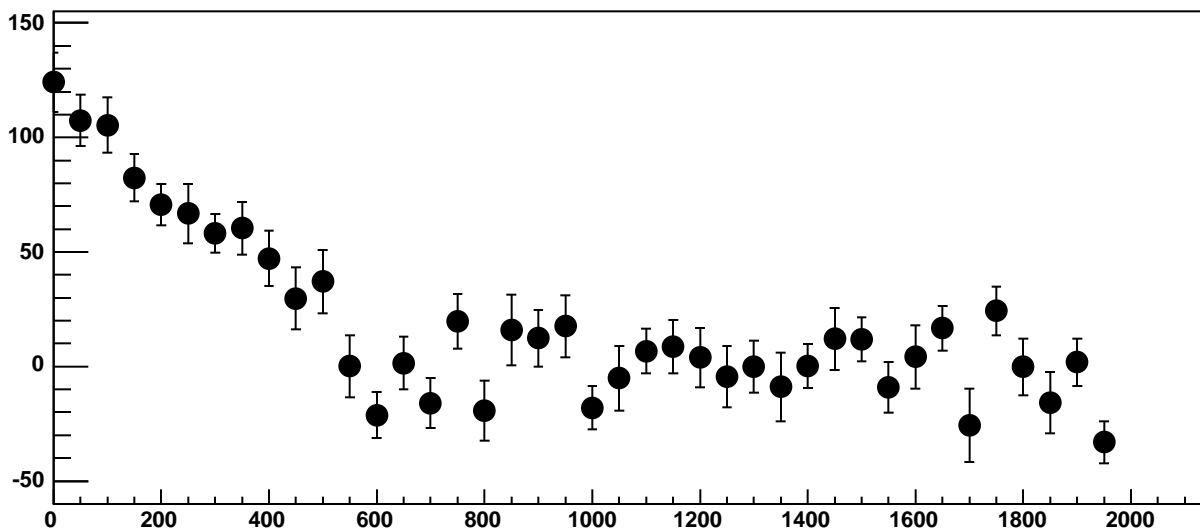
Chip 11, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC



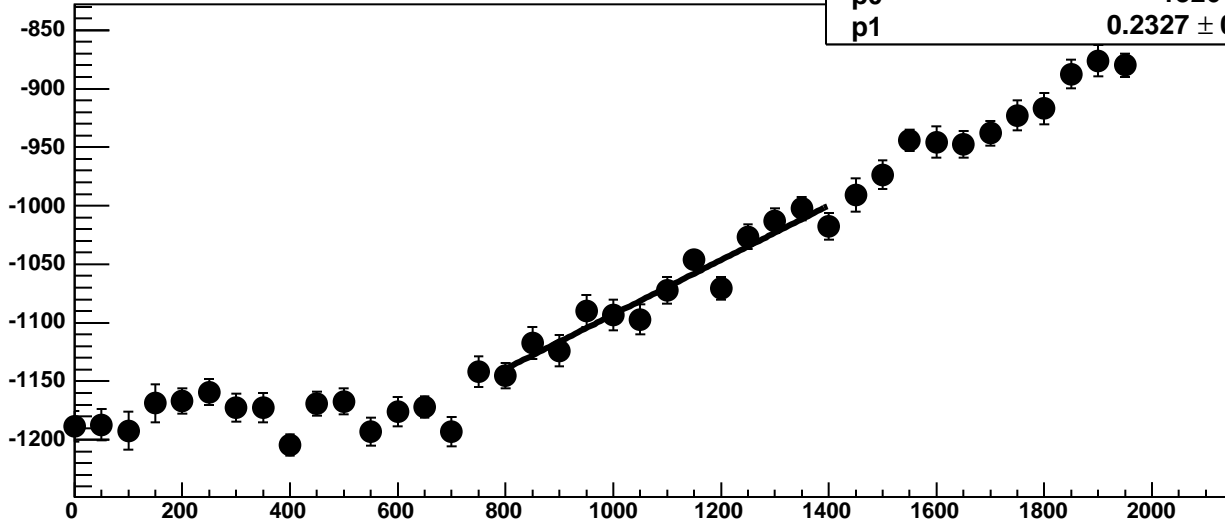
Chip 11, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC



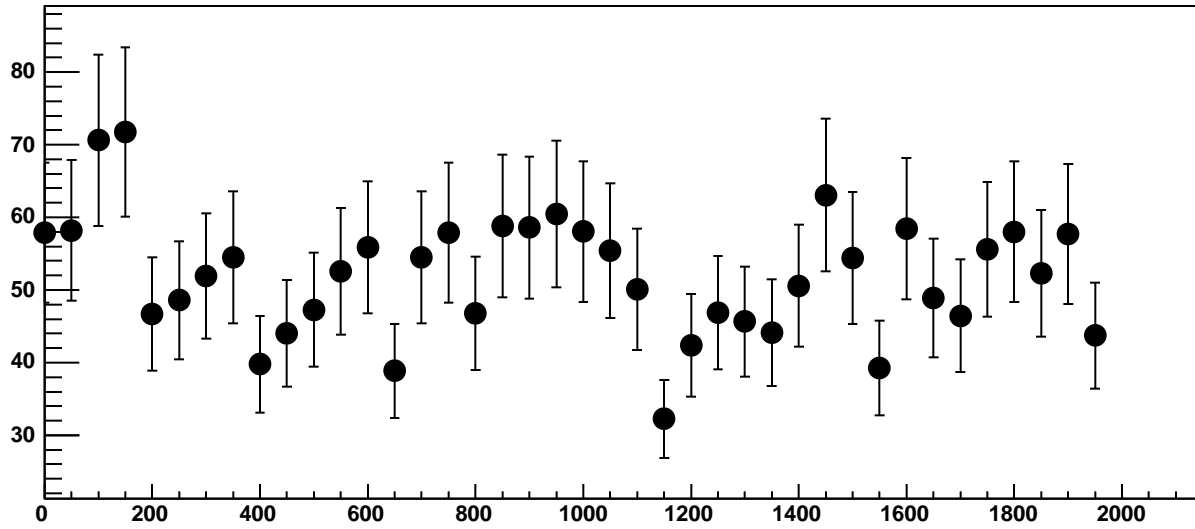
Chip 11, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC



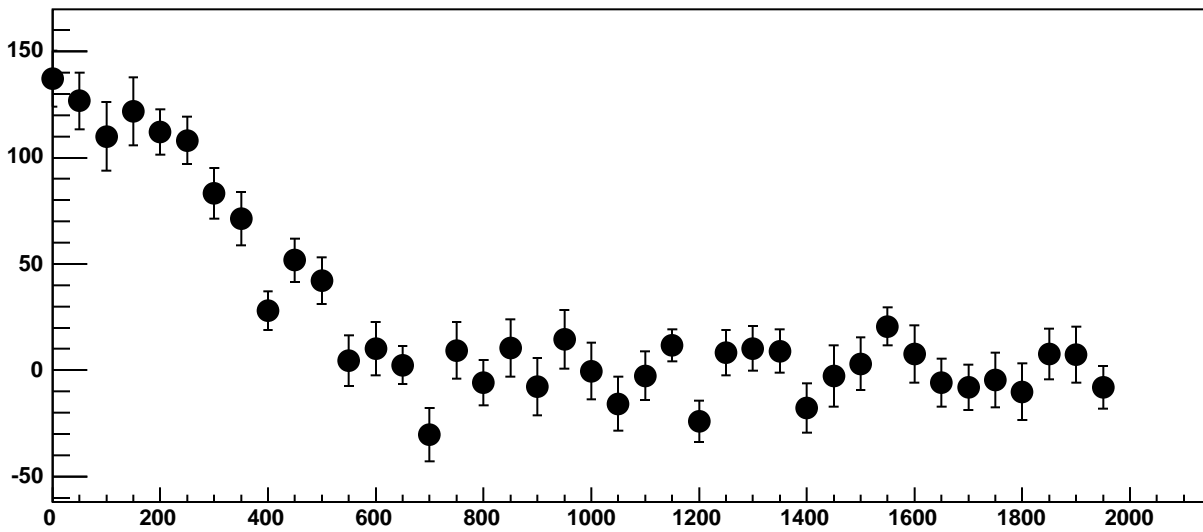
Chip 11, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC



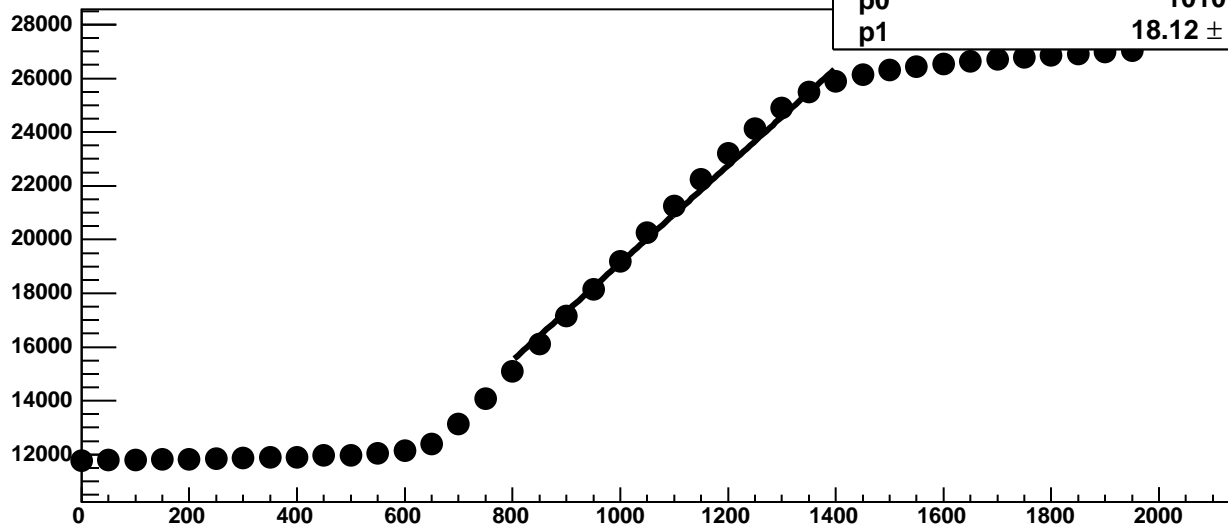
Chip 11, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC



Chip 11, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC

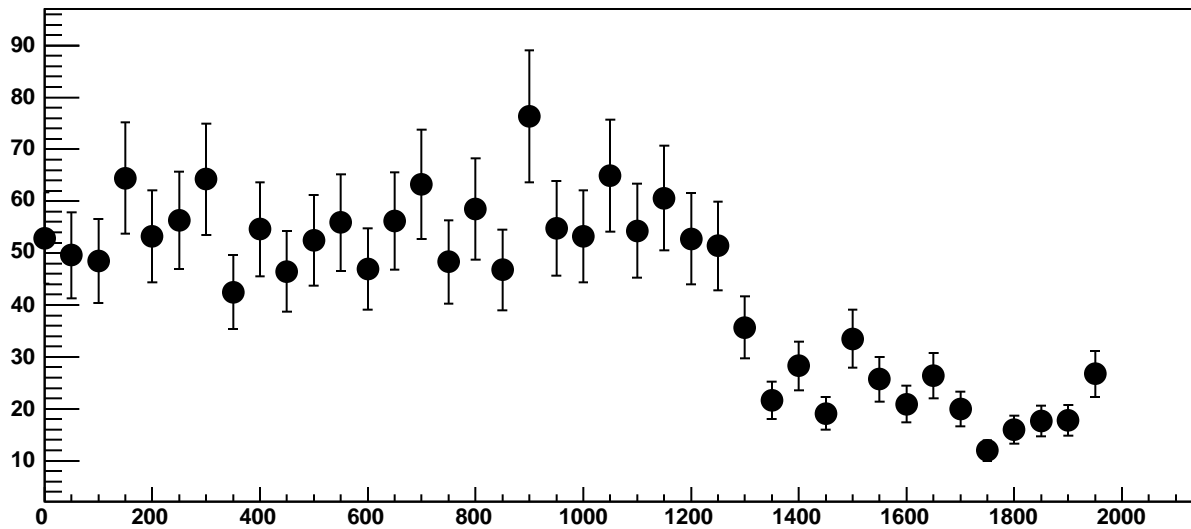


Chip 11, Channel 0, Enable 3!, Hold=30, ADC Mean vs DAC

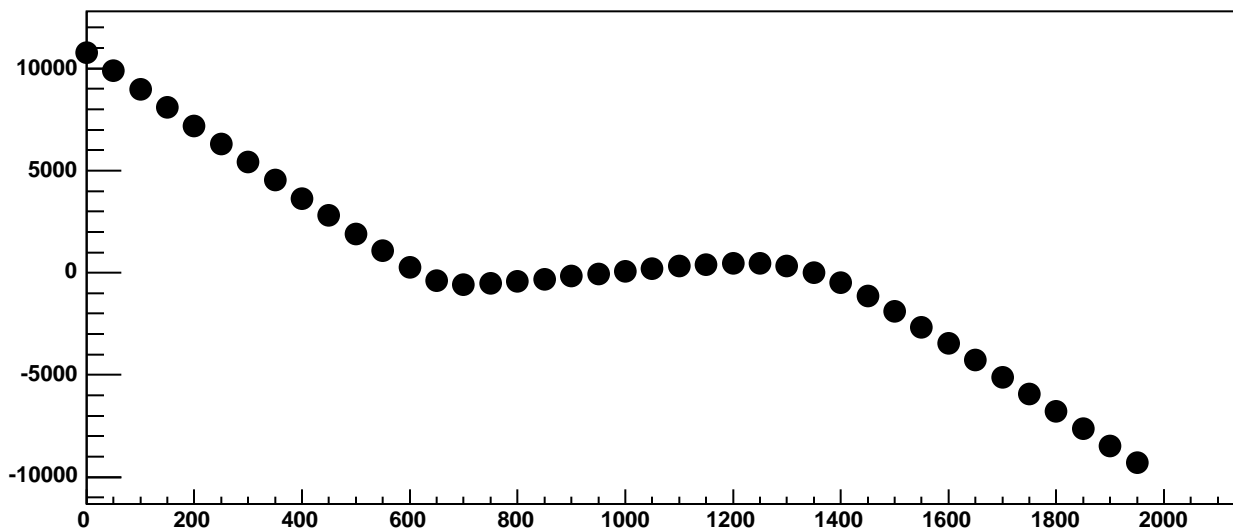


$\chi^2 / \text{ndf}$  1.411e+04 / 11  
p0 1010 ± 17.22  
p1 18.12 ± 0.01401

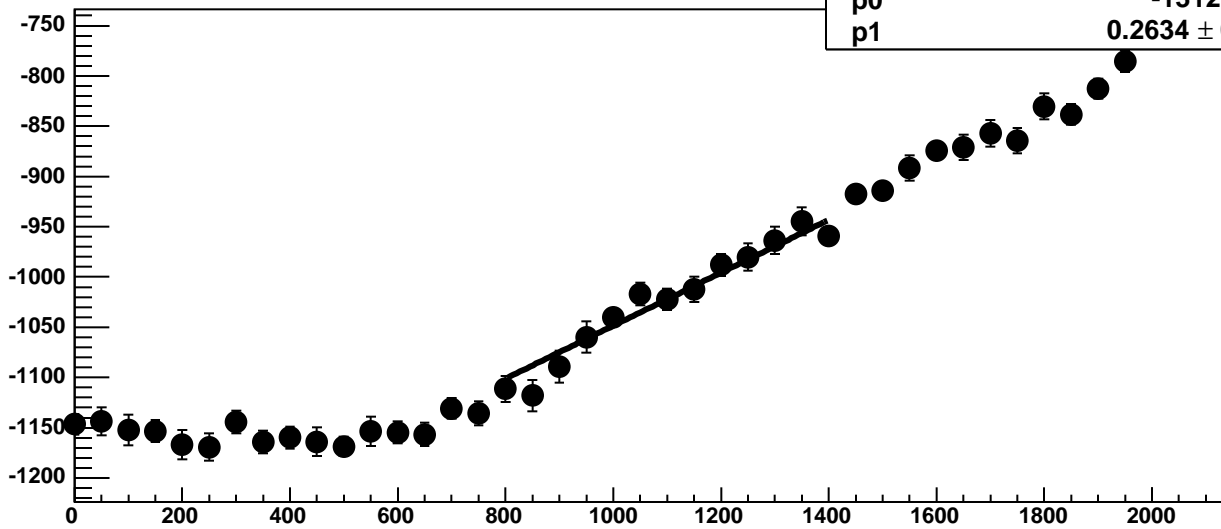
Chip 11, Channel 0, Enable 3!, Hold=30, ADC Noise vs DAC



Chip 11, Channel 0, Enable 3!, Hold=30, ADC Residuals vs DAC

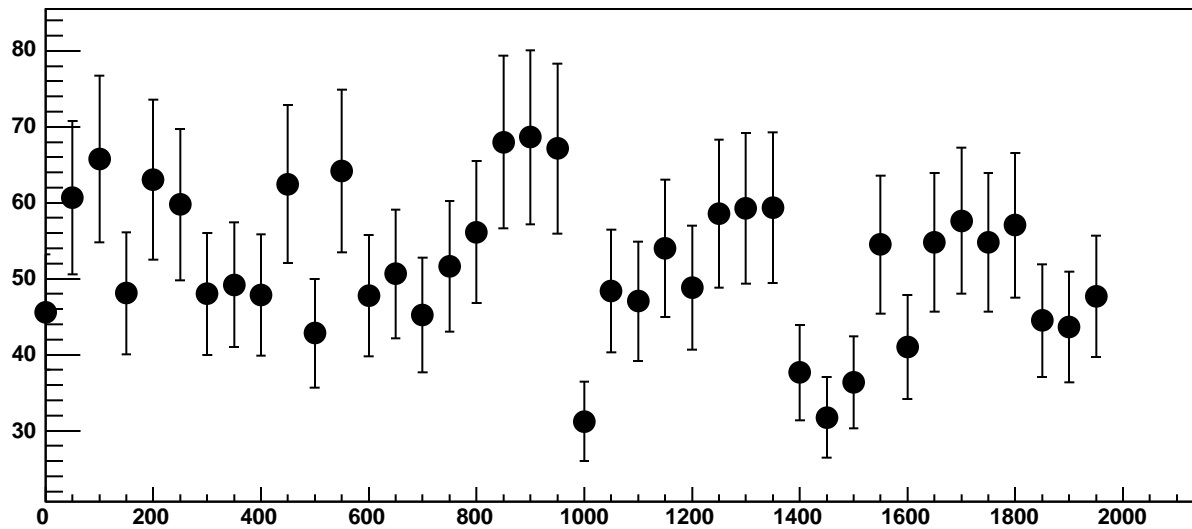


Chip 11, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC

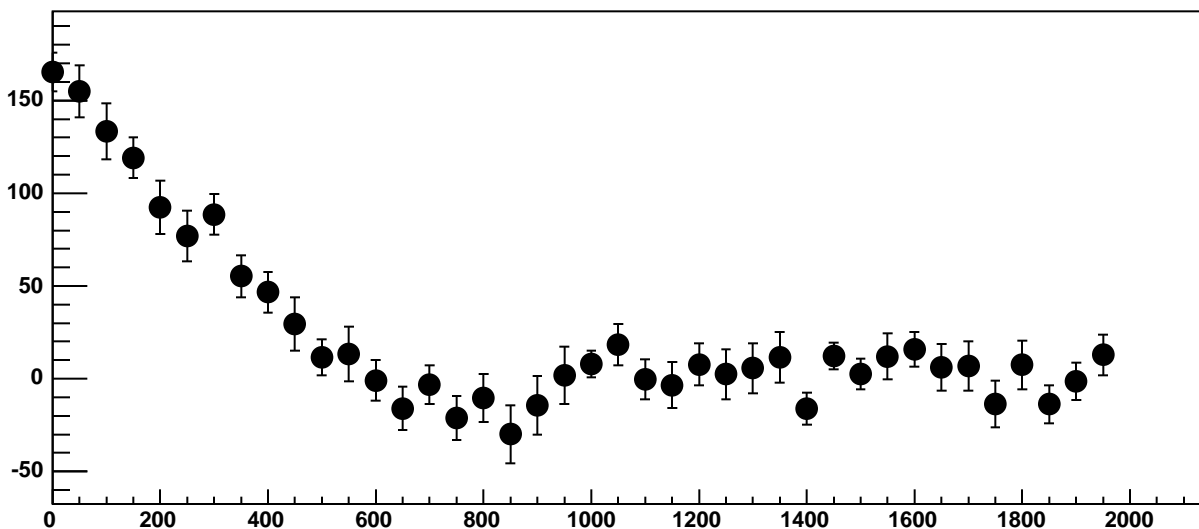


$\chi^2 / \text{ndf}$  14.21 / 11  
p0  $-1312 \pm 19.96$   
p1  $0.2634 \pm 0.01763$

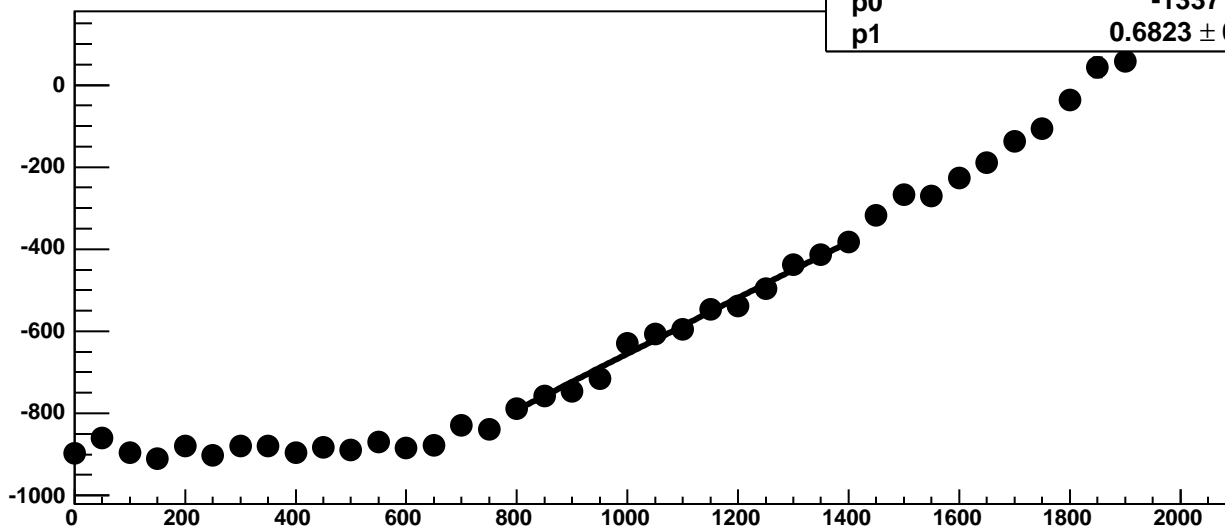
Chip 11, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC



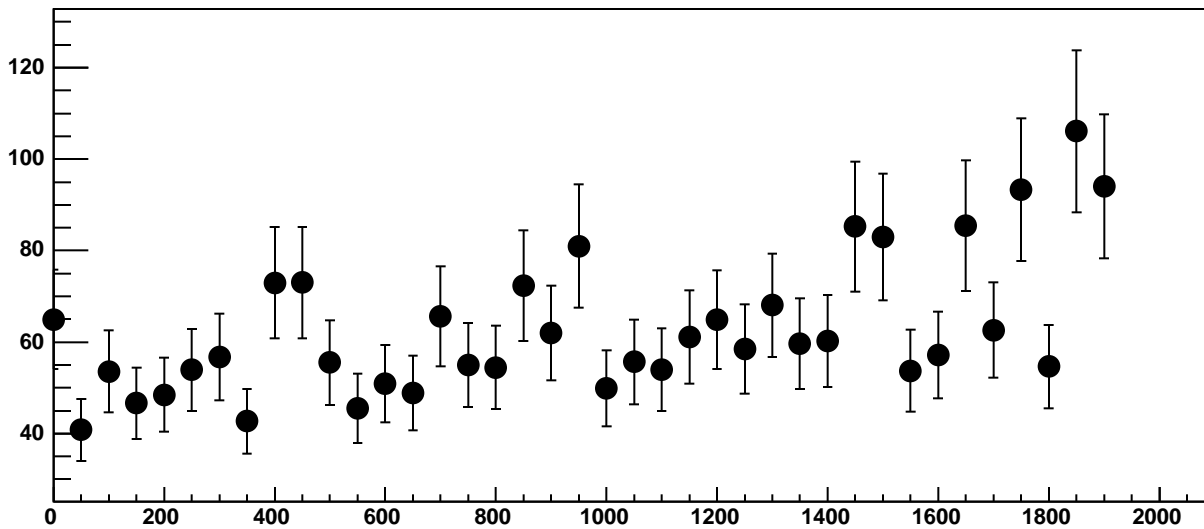
Chip 11, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC



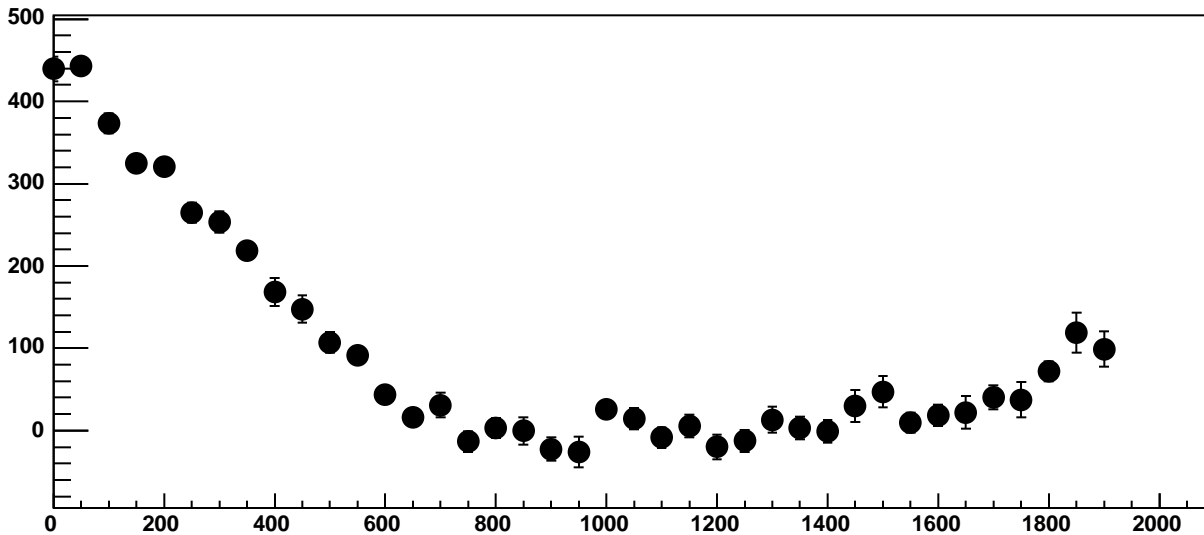
Chip 11, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC



Chip 11, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC

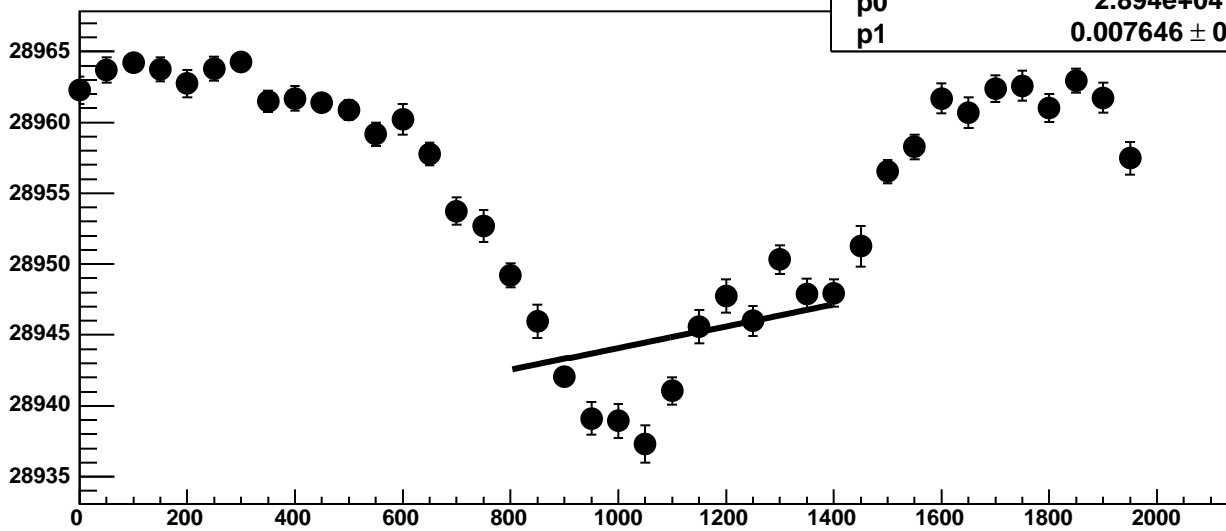


Chip 11, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC

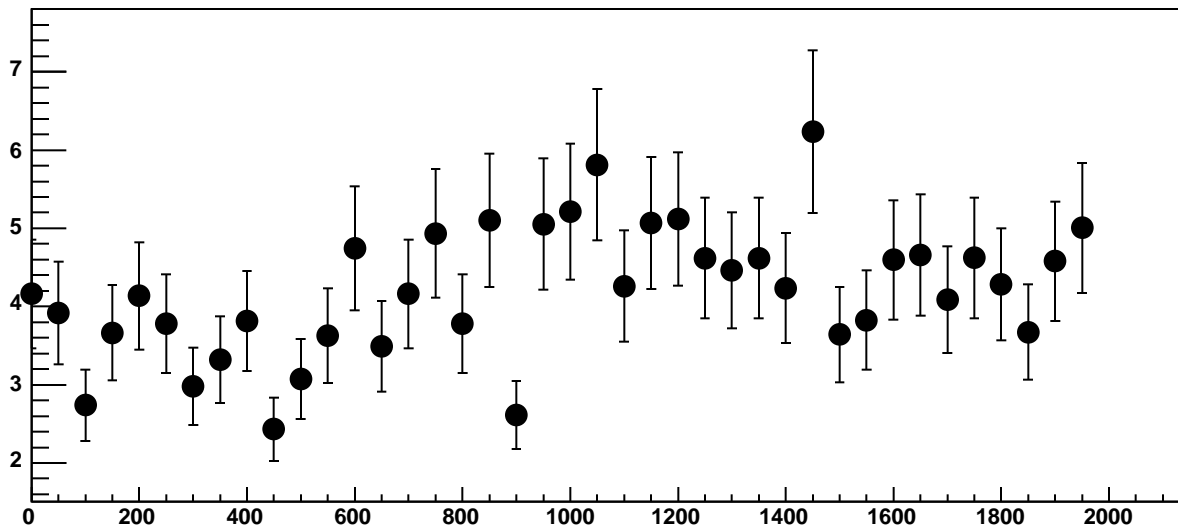




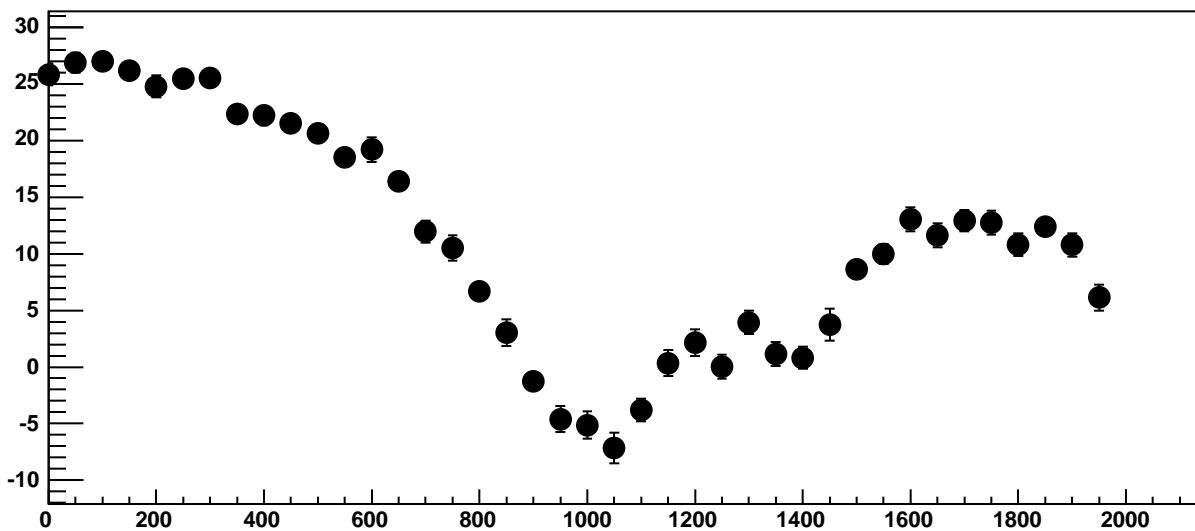
Chip 11, Channel 1, Enable 0!, Hold=30, ADC Mean vs DAC



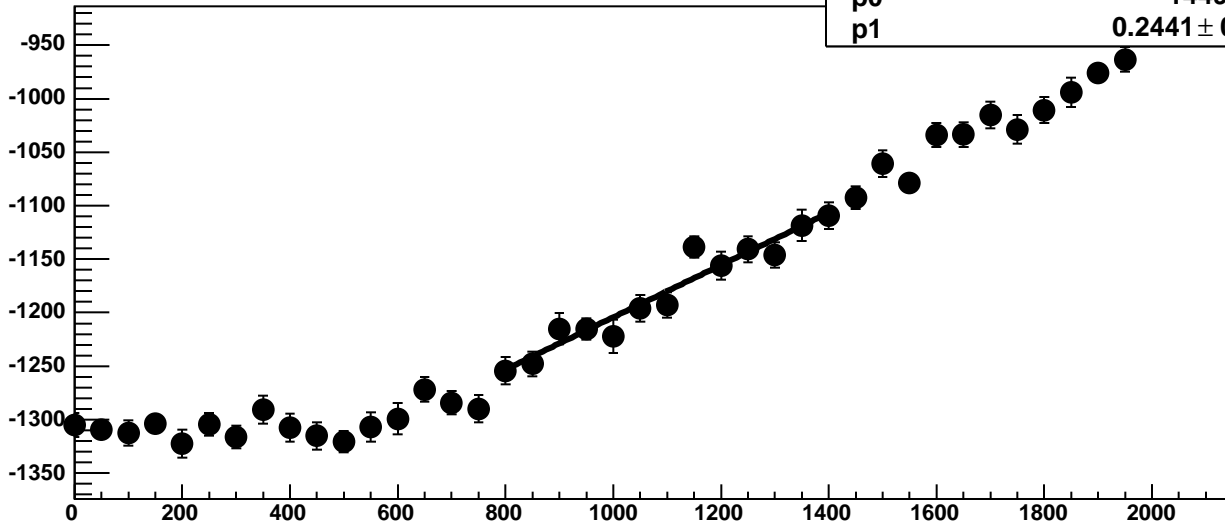
Chip 11, Channel 1, Enable 0!, Hold=30, ADC Noise vs DAC



Chip 11, Channel 1, Enable 0!, Hold=30, ADC Residuals vs DAC

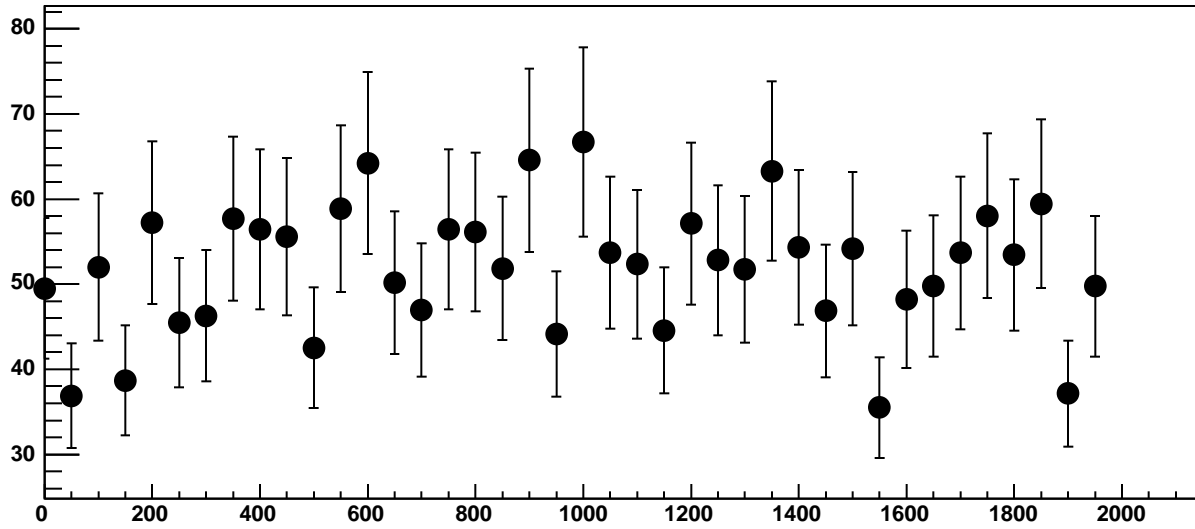


Chip 11, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC

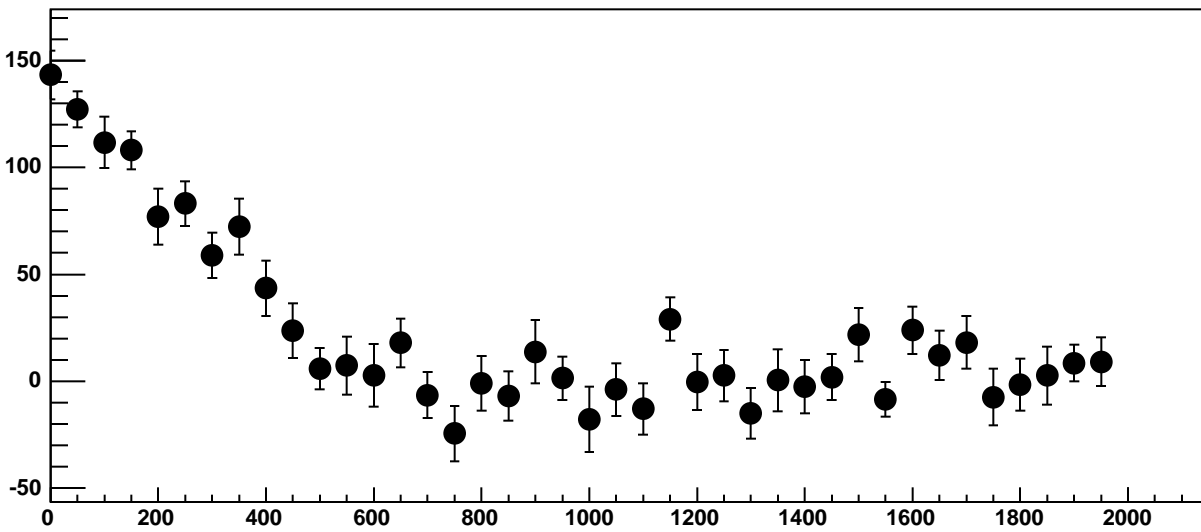


$\chi^2 / \text{ndf}$  13.61 / 11  
p0  $-1449 \pm 20.8$   
p1  $0.2441 \pm 0.01869$

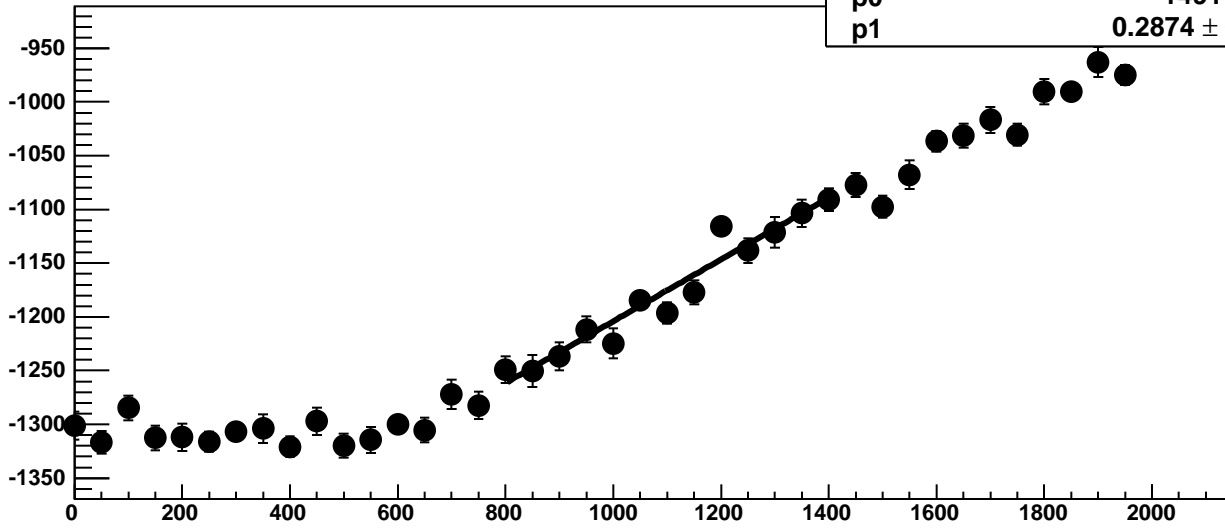
Chip 11, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC



Chip 11, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC

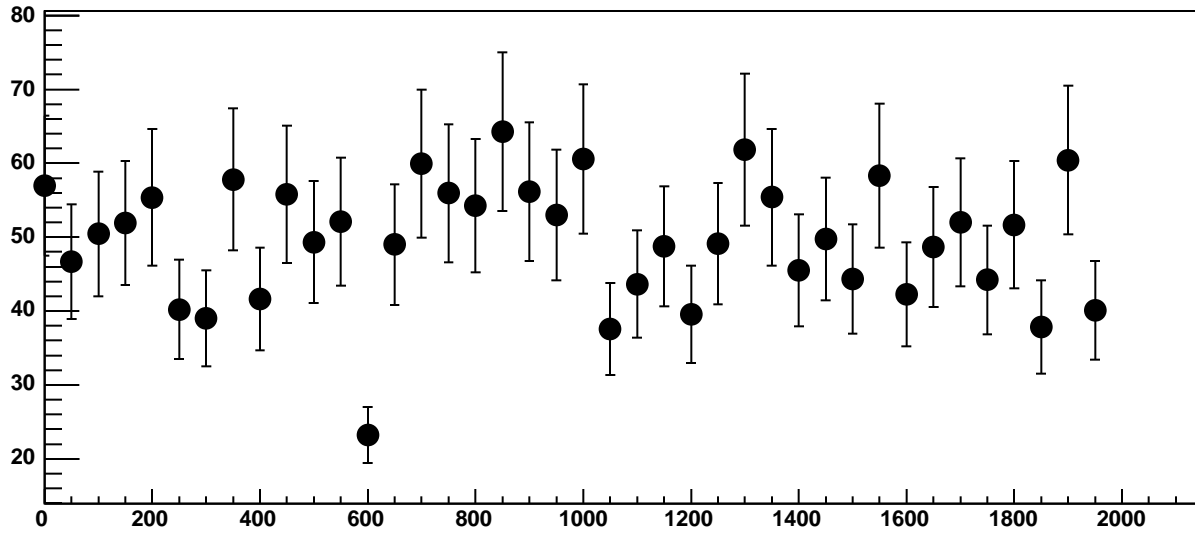


Chip 11, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC

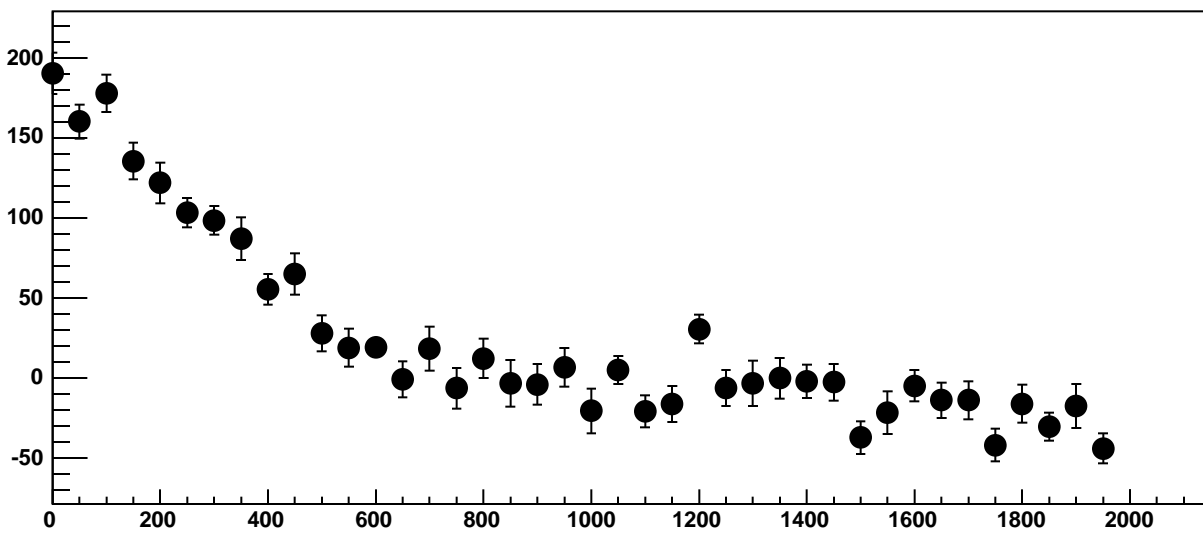


$\chi^2 / \text{ndf}$  22.15 / 11  
p0  $-1491 \pm 20.45$   
p1  $0.2874 \pm 0.01811$

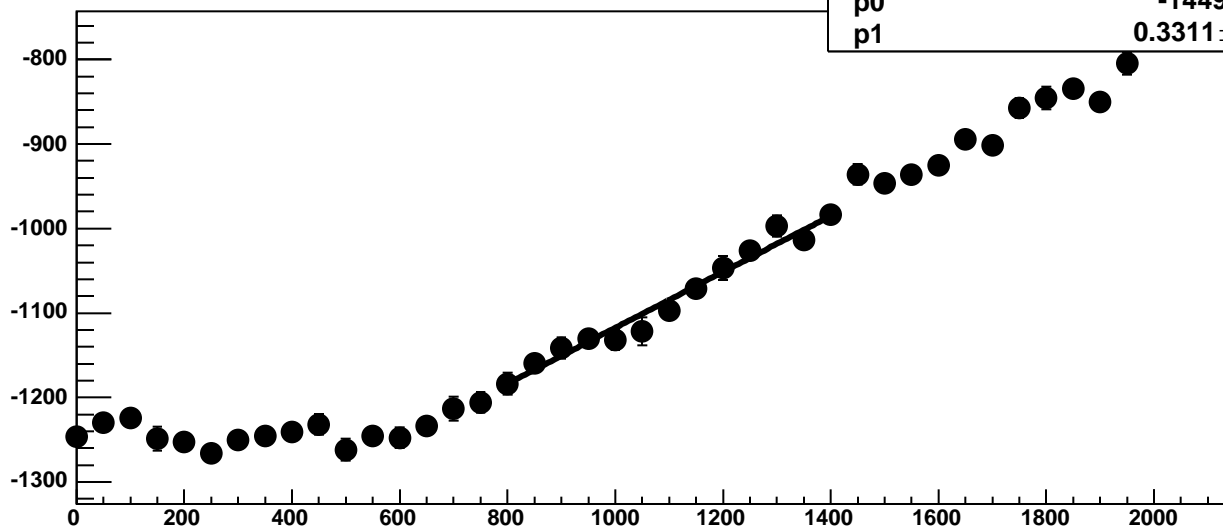
Chip 11, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC



Chip 11, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 11, Channel 1, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

11.18 / 11

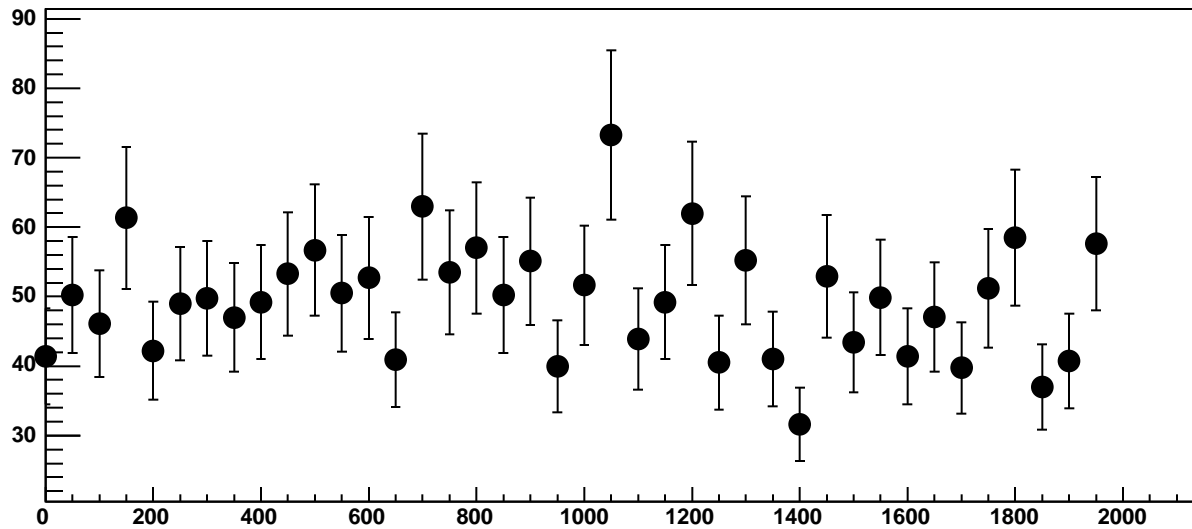
p0

$-1449 \pm 17.51$

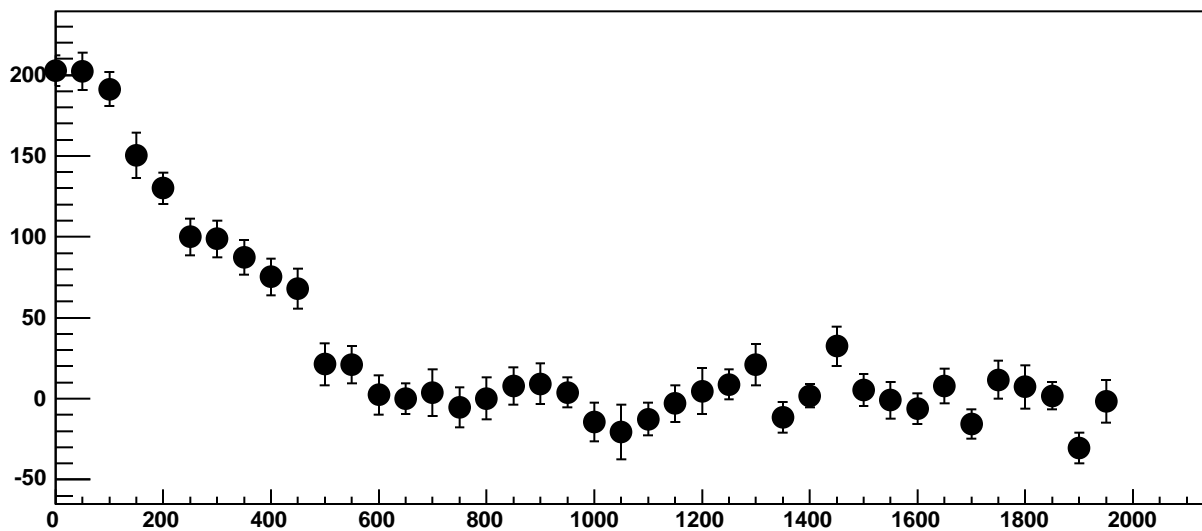
p1

$0.3311 \pm 0.0151$

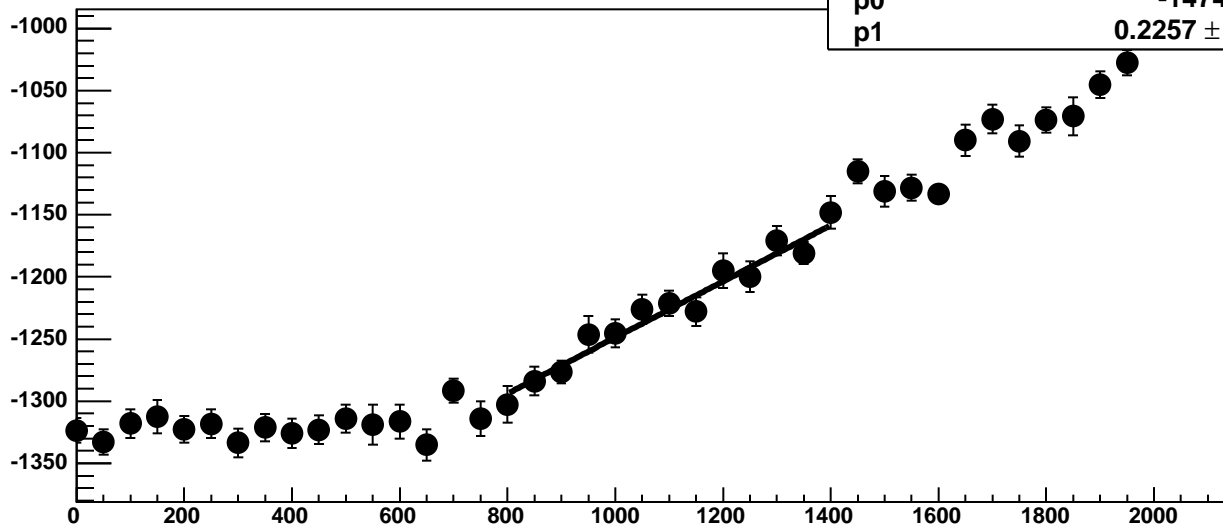
Chip 11, Channel 1, Enable 3, Hold=30, ADC Noise vs DAC



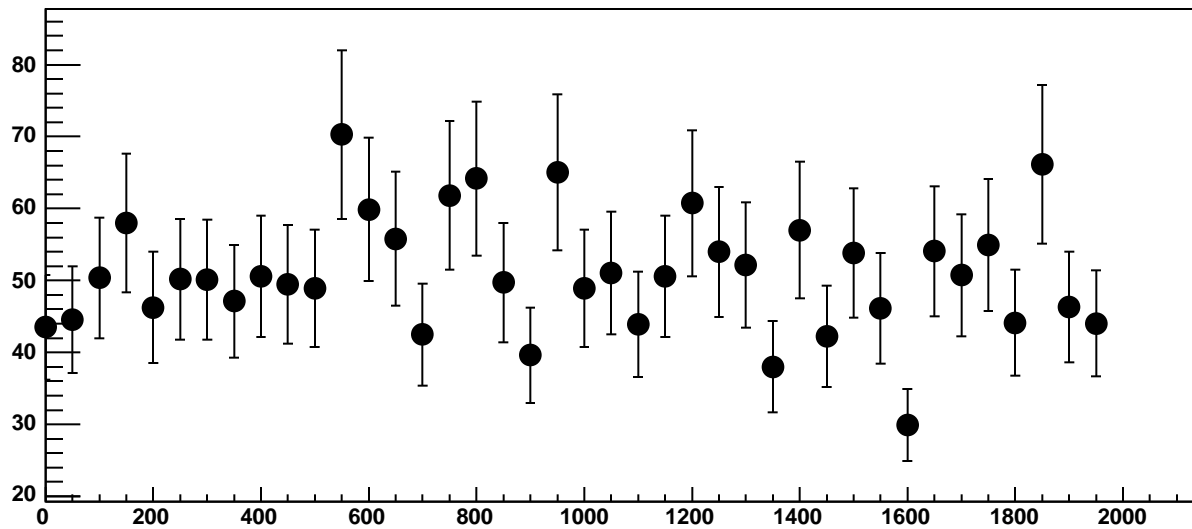
Chip 11, Channel 1, Enable 3, Hold=30, ADC Residuals vs DAC



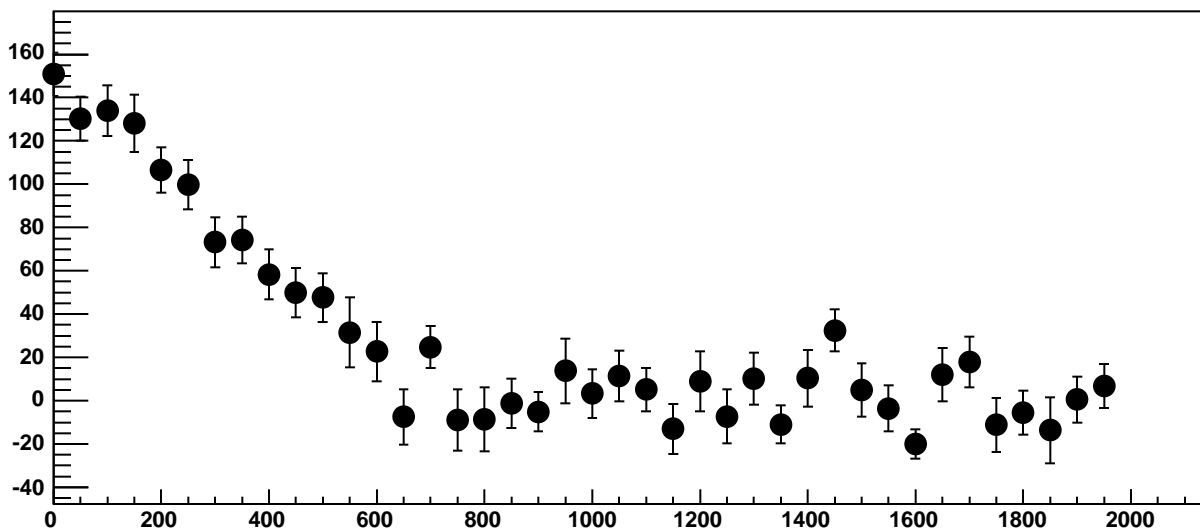
Chip 11, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC



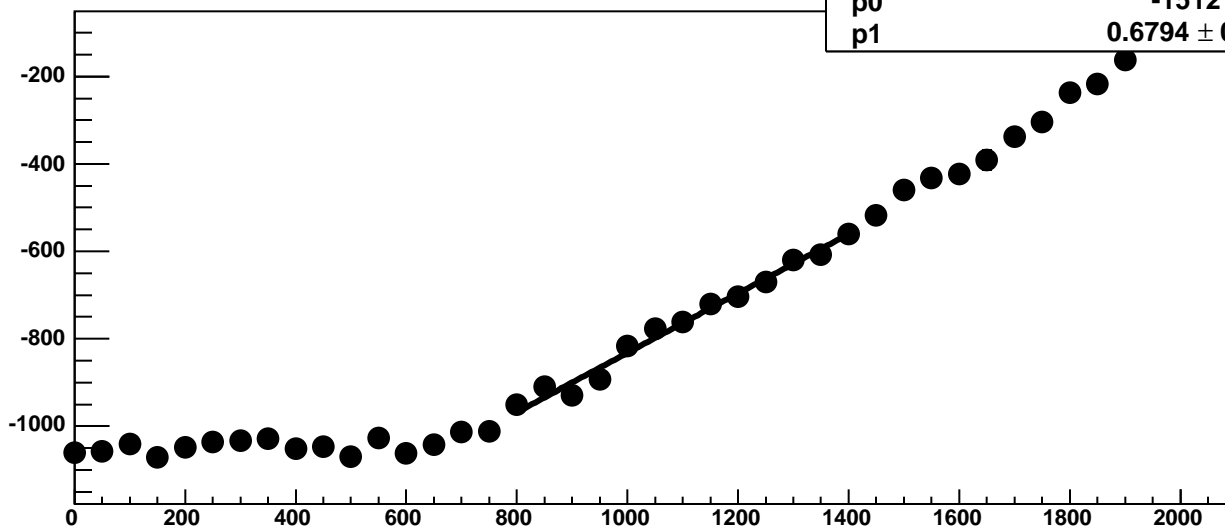
Chip 11, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC



Chip 11, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC

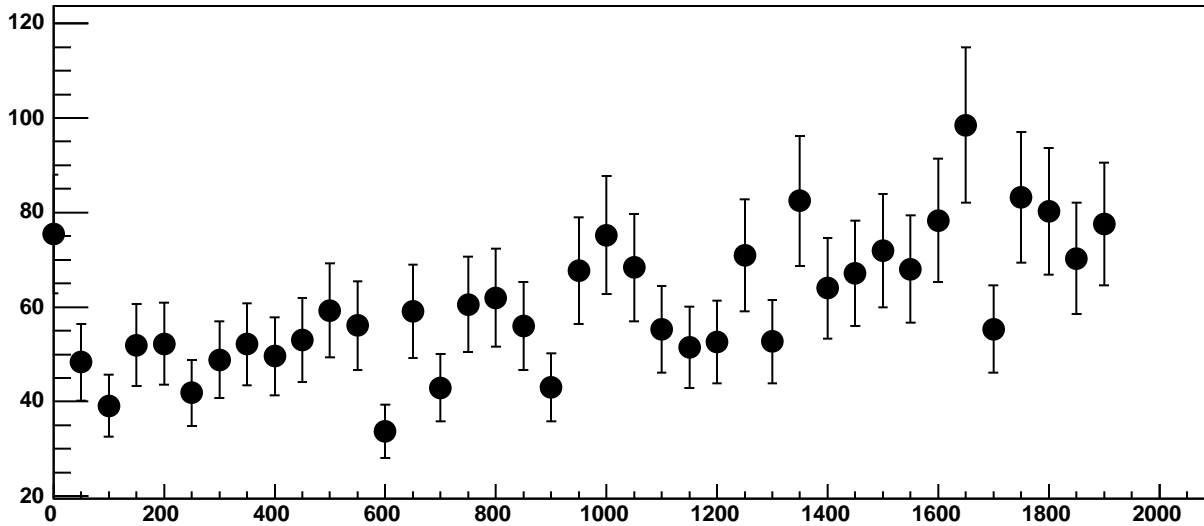


Chip 11, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC

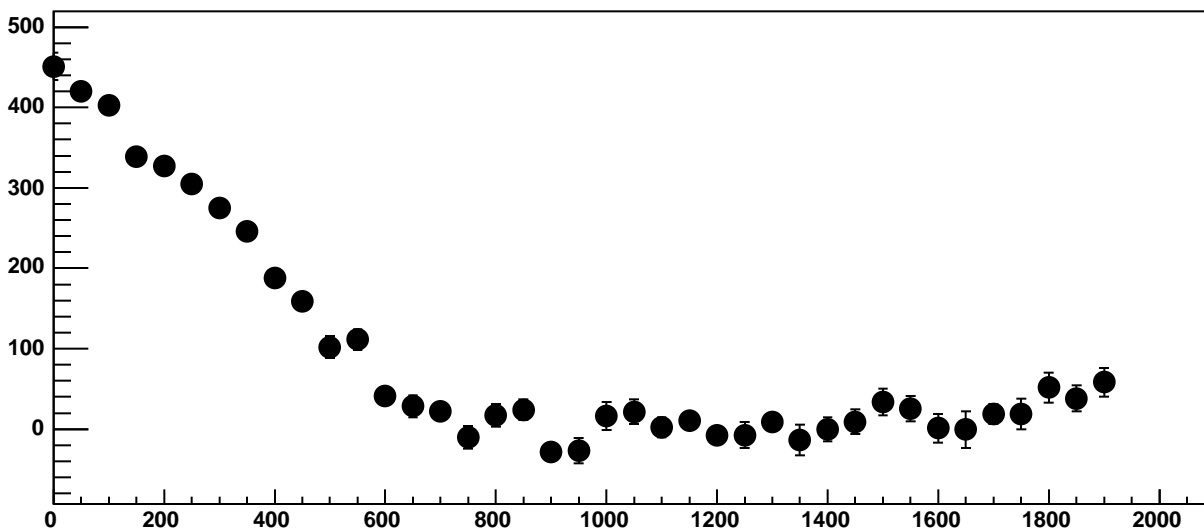


$\chi^2 / \text{ndf}$  21.57 / 11  
p0  $-1512 \pm 22.48$   
p1  $0.6794 \pm 0.02045$

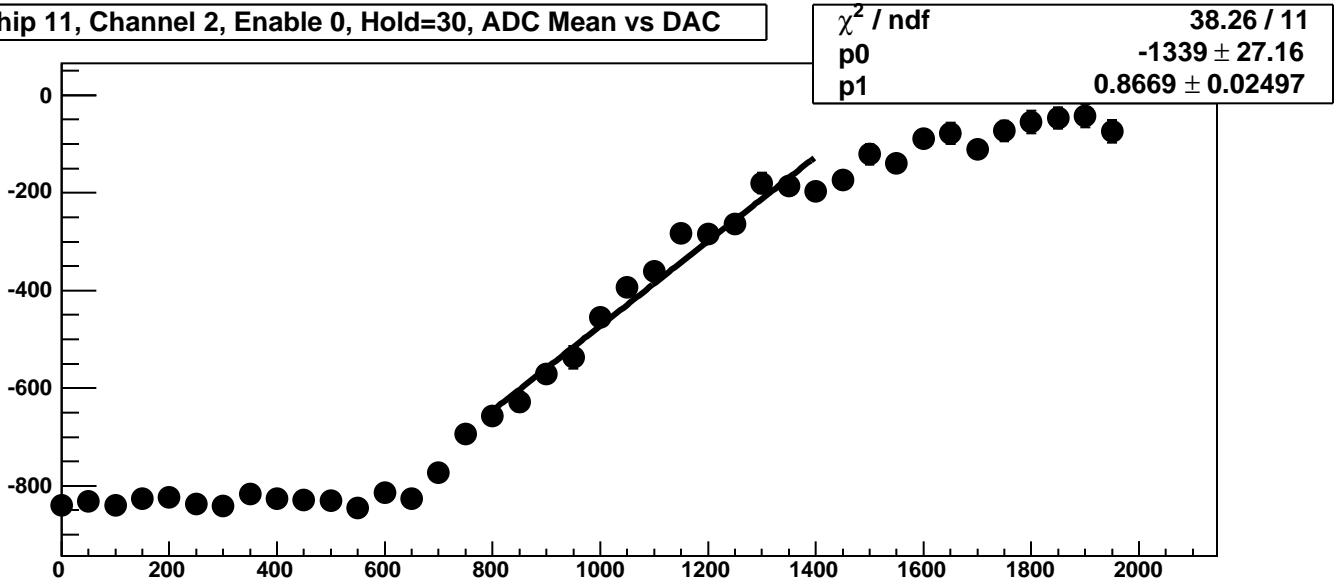
Chip 11, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC



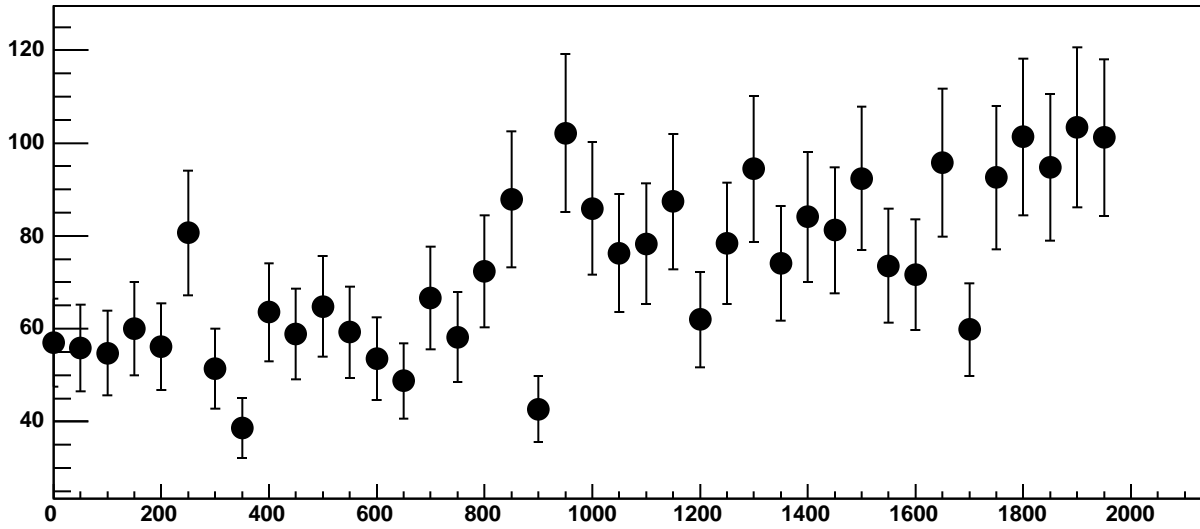
Chip 11, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC



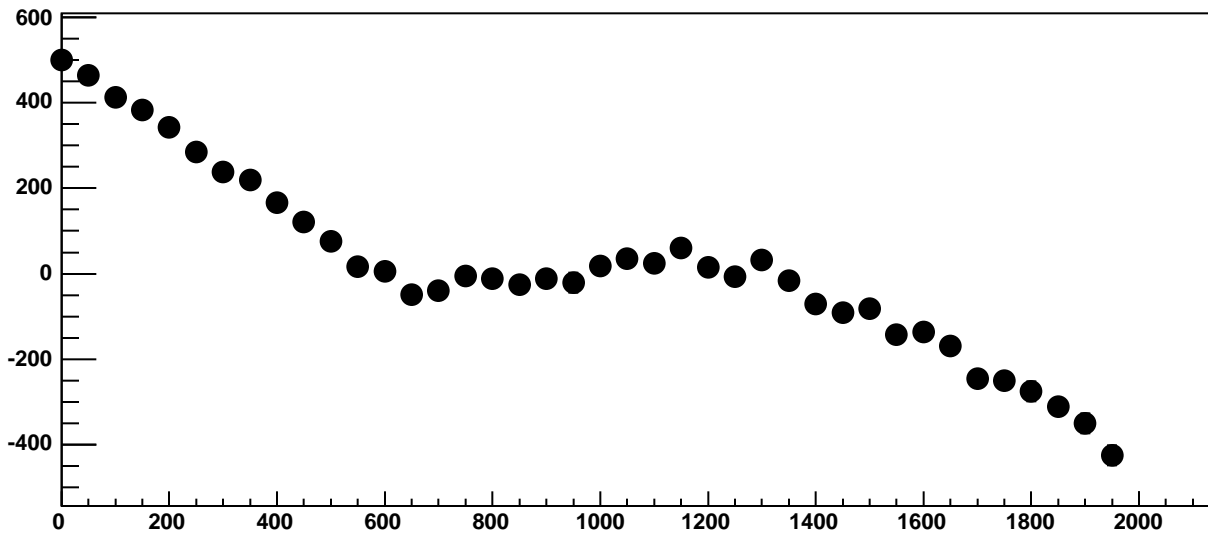
Chip 11, Channel 2, Enable 0, Hold=30, ADC Mean vs DAC



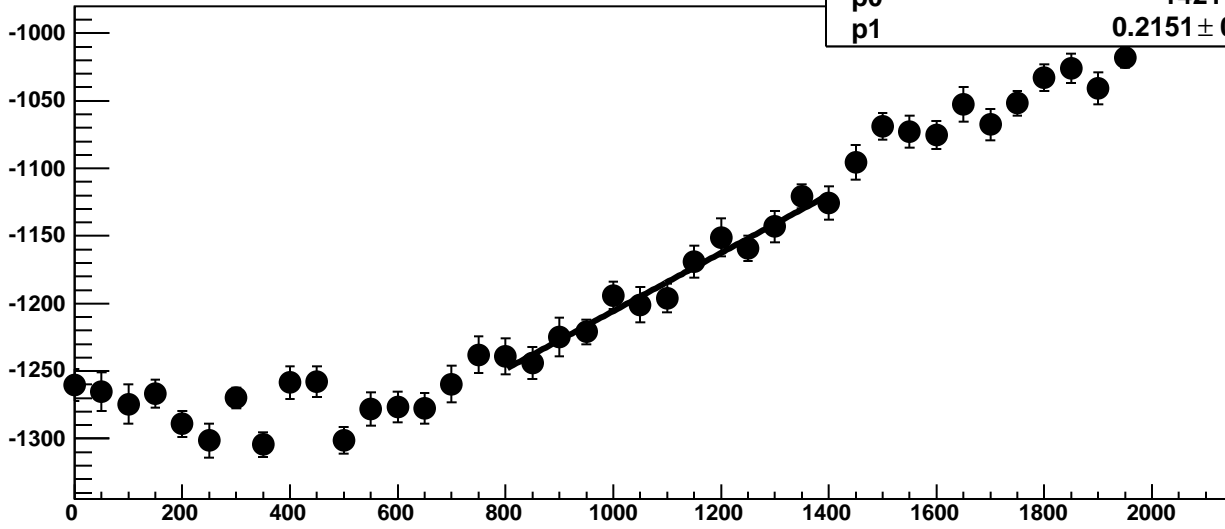
Chip 11, Channel 2, Enable 0, Hold=30, ADC Noise vs DAC



Chip 11, Channel 2, Enable 0, Hold=30, ADC Residuals vs DAC

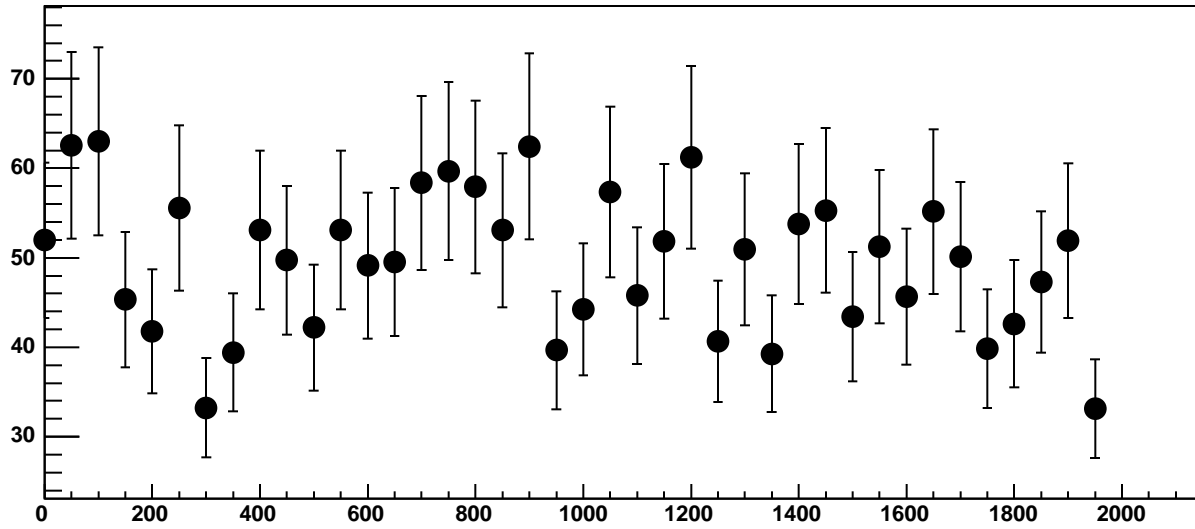


Chip 11, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC

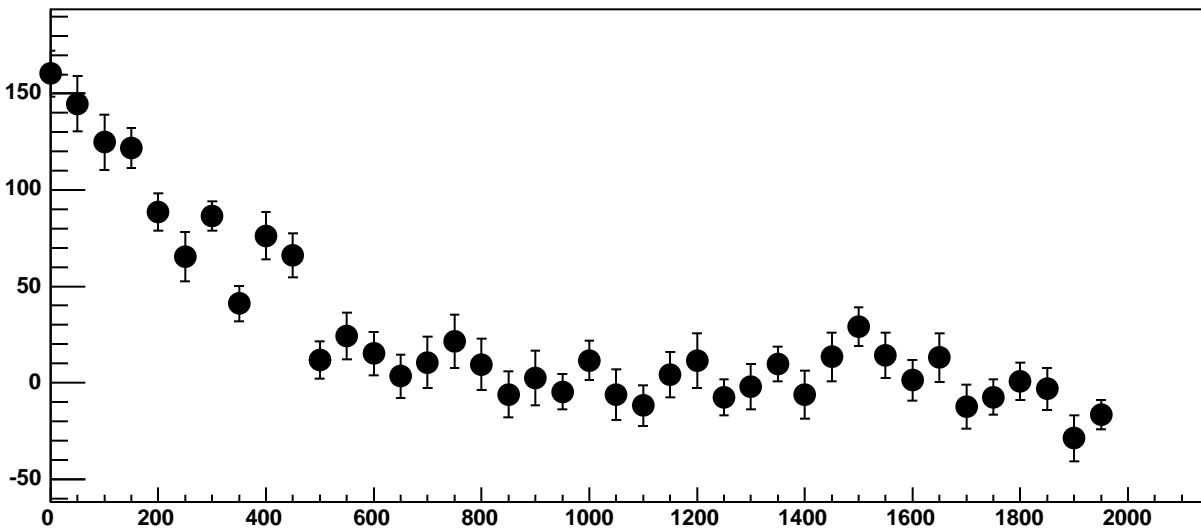


$\chi^2 / \text{ndf}$  6.678 / 11  
p0  $-1421 \pm 19.14$   
p1  $0.2151 \pm 0.01693$

Chip 11, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC

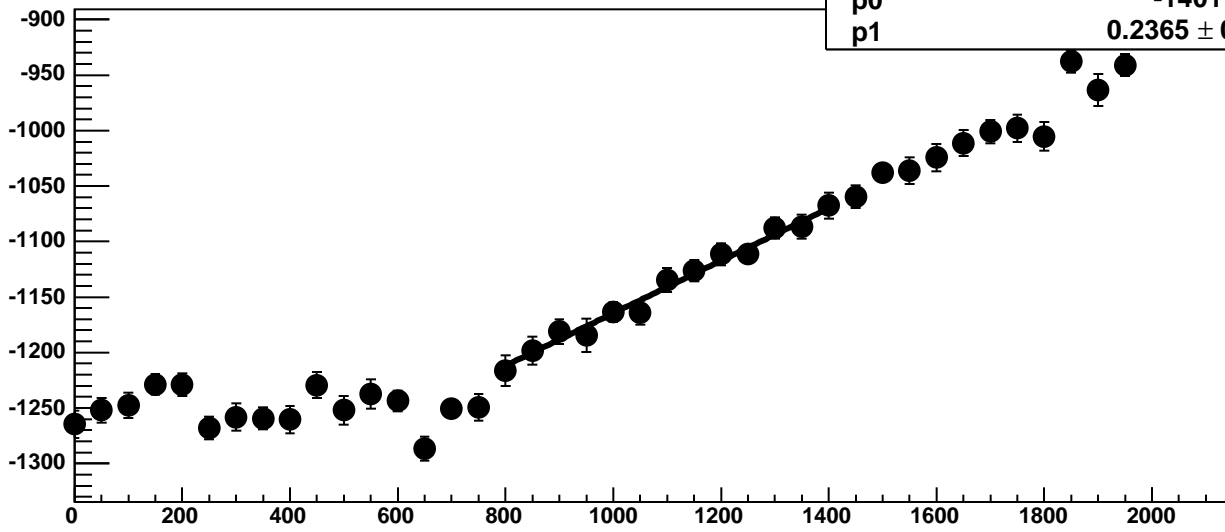


Chip 11, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC

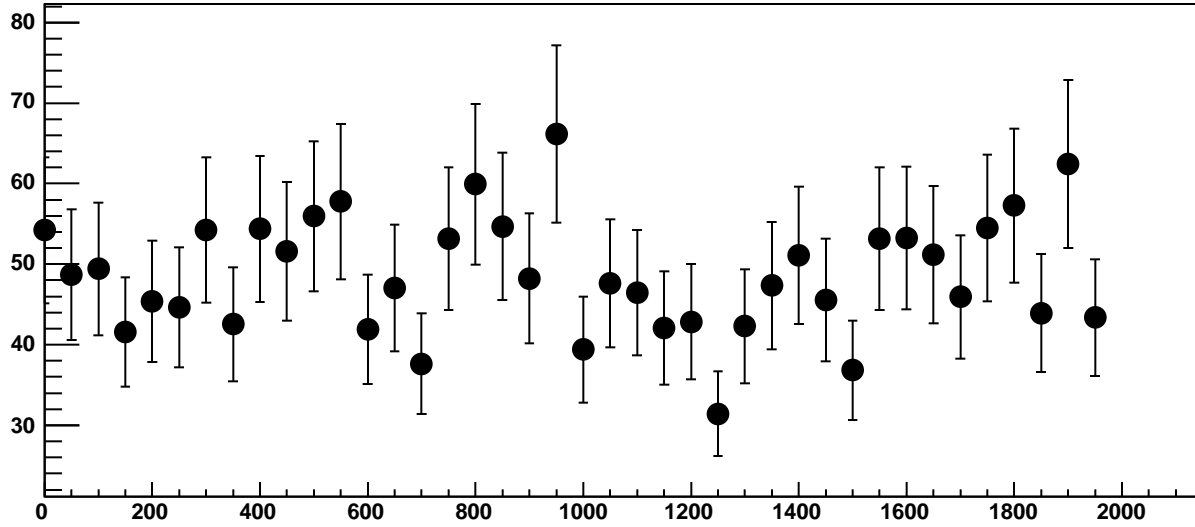




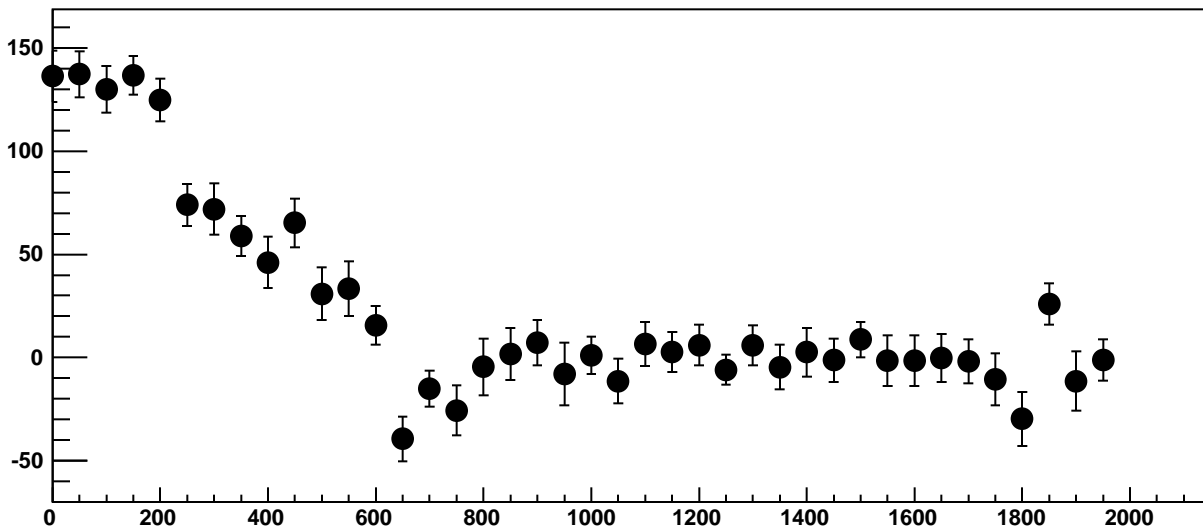
Chip 11, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC



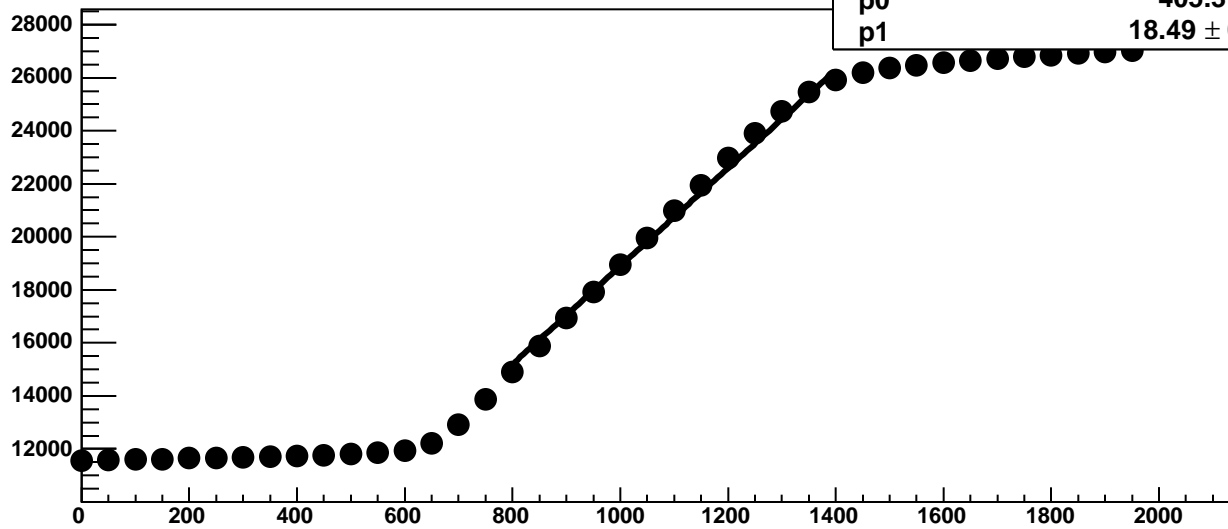
Chip 11, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC



Chip 11, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC

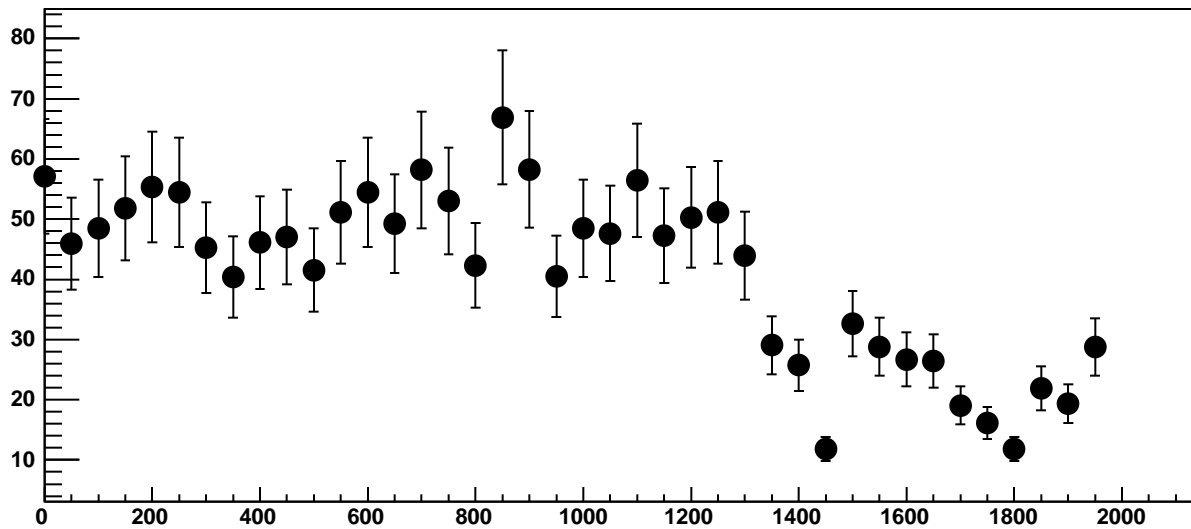


Chip 11, Channel 2, Enable 3!, Hold=30, ADC Mean vs DAC

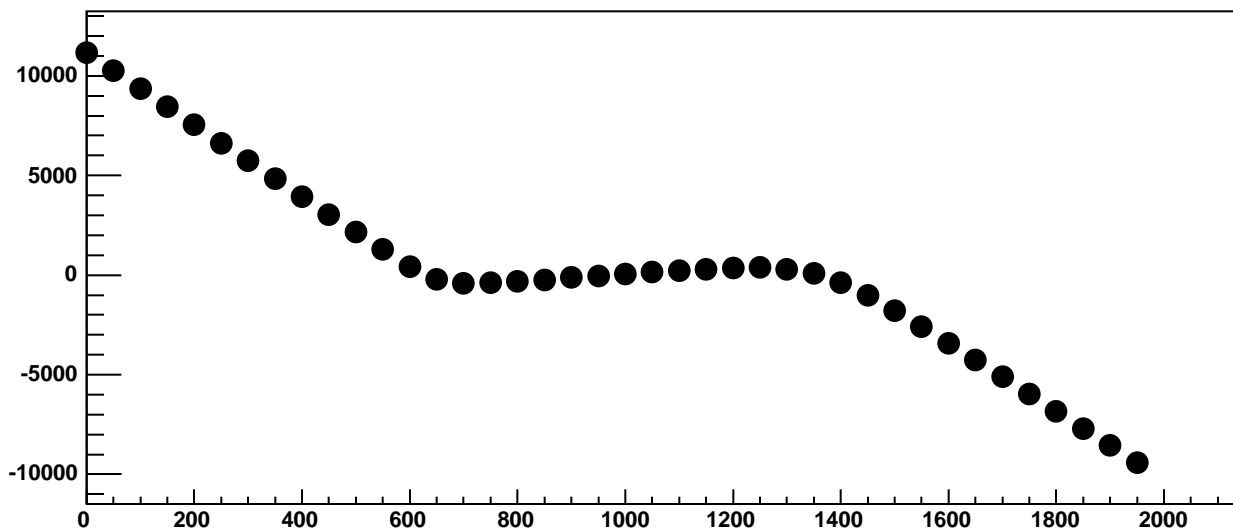


$\chi^2 / \text{ndf}$  9819 / 11  
p0  $405.3 \pm 15.75$   
p1  $18.49 \pm 0.01323$

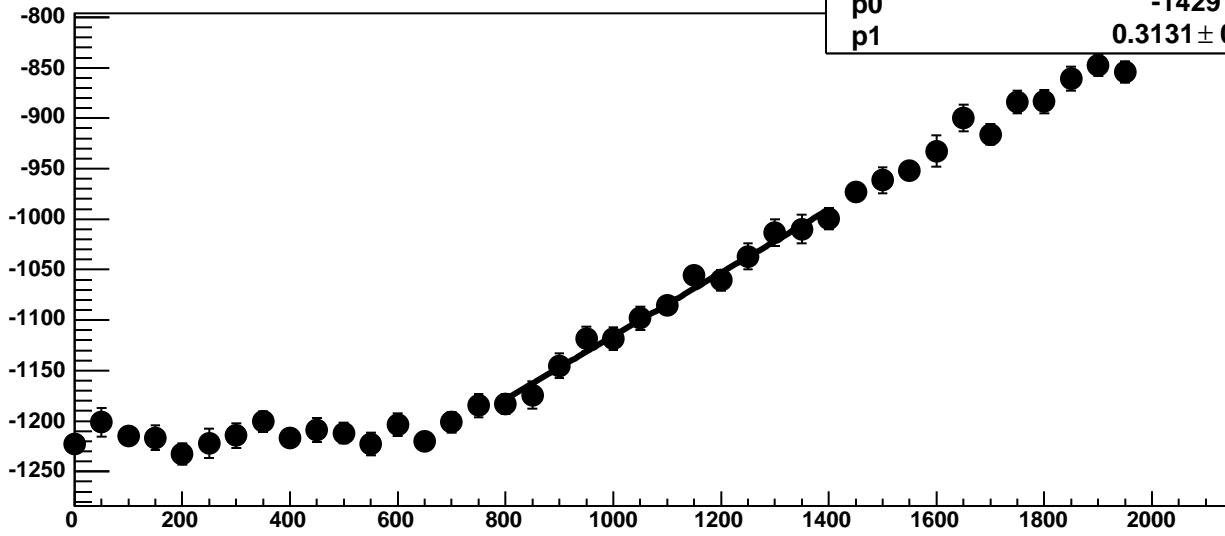
Chip 11, Channel 2, Enable 3!, Hold=30, ADC Noise vs DAC



Chip 11, Channel 2, Enable 3!, Hold=30, ADC Residuals vs DAC

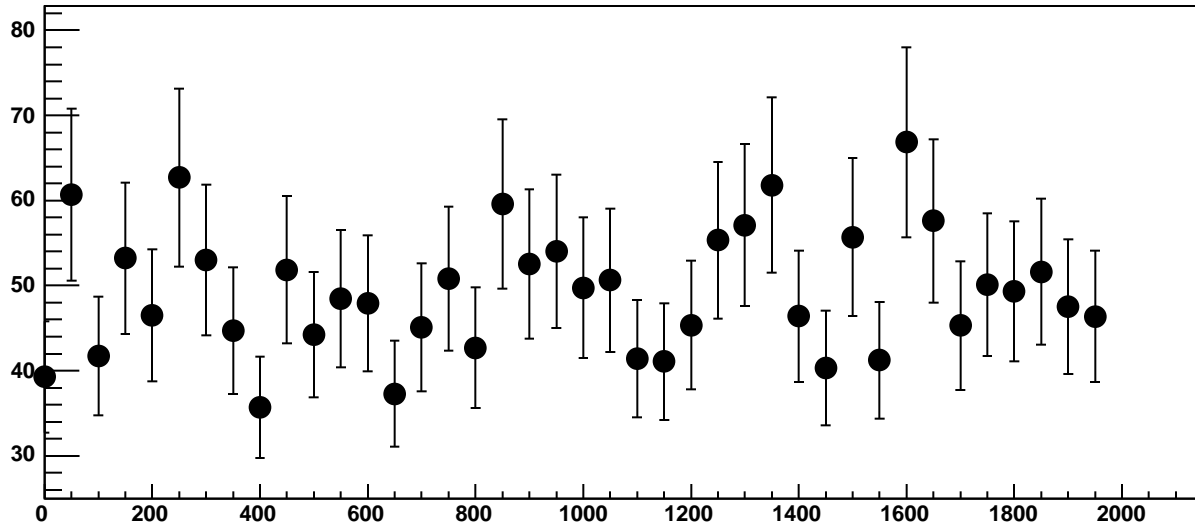


Chip 11, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC

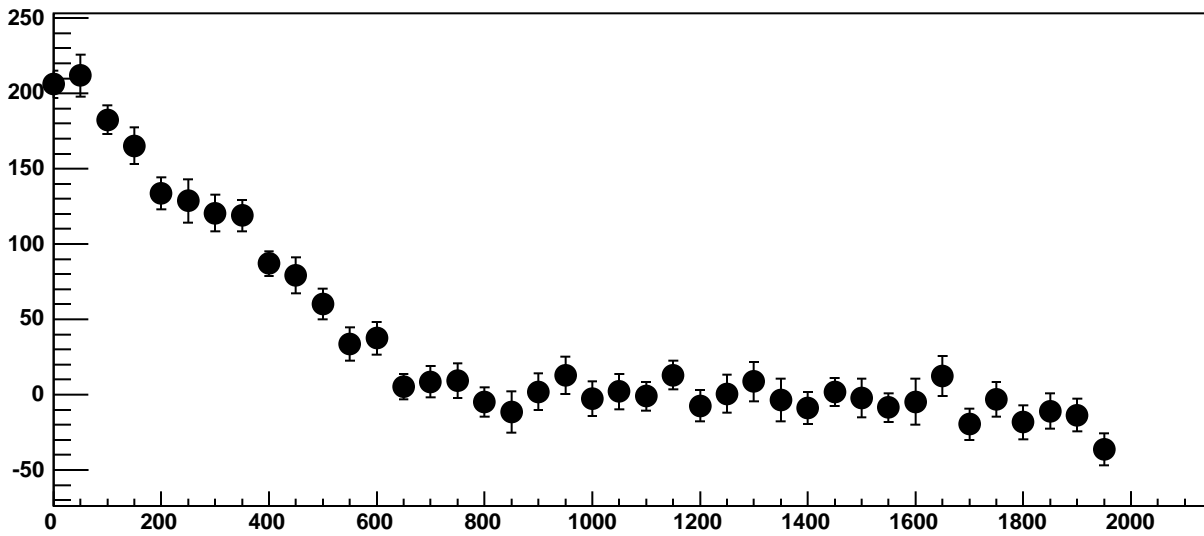


$\chi^2 / \text{ndf}$  5.726 / 11  
p0  $-1429 \pm 19.08$   
p1  $0.3131 \pm 0.01719$

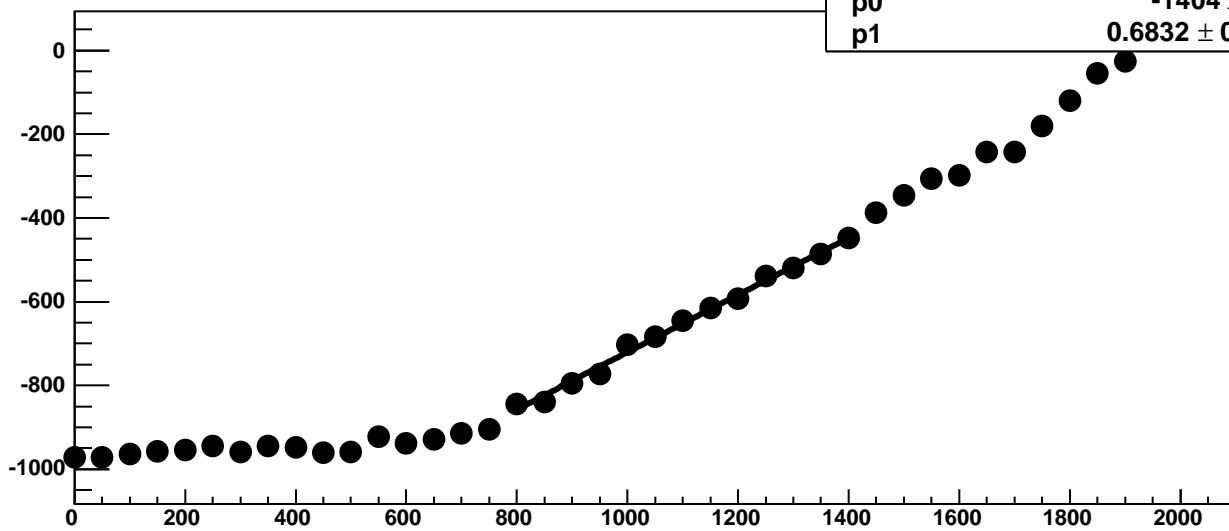
Chip 11, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC



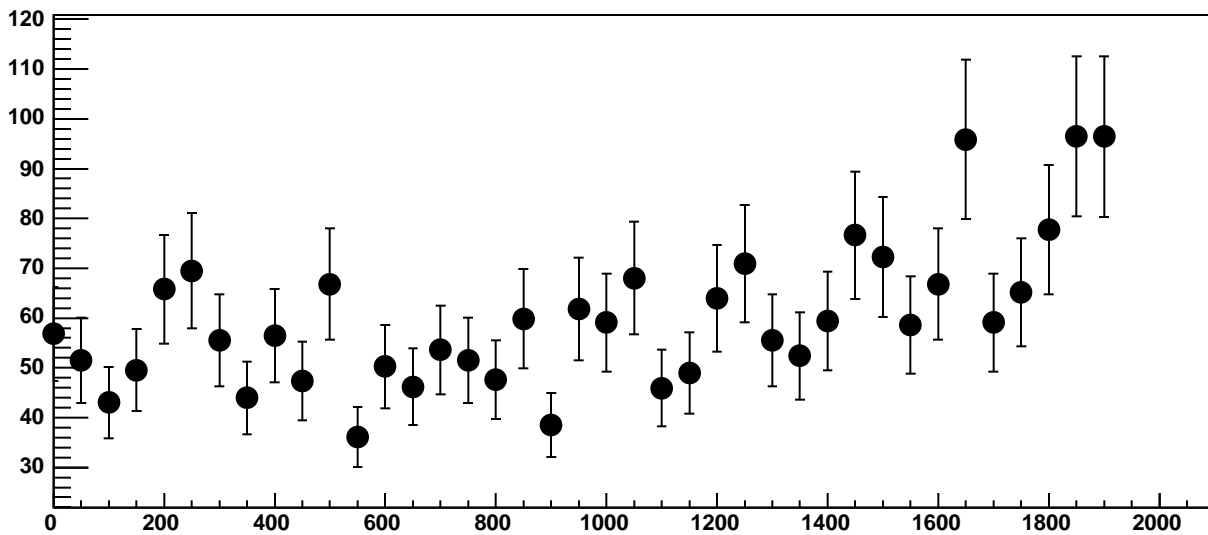
Chip 11, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC



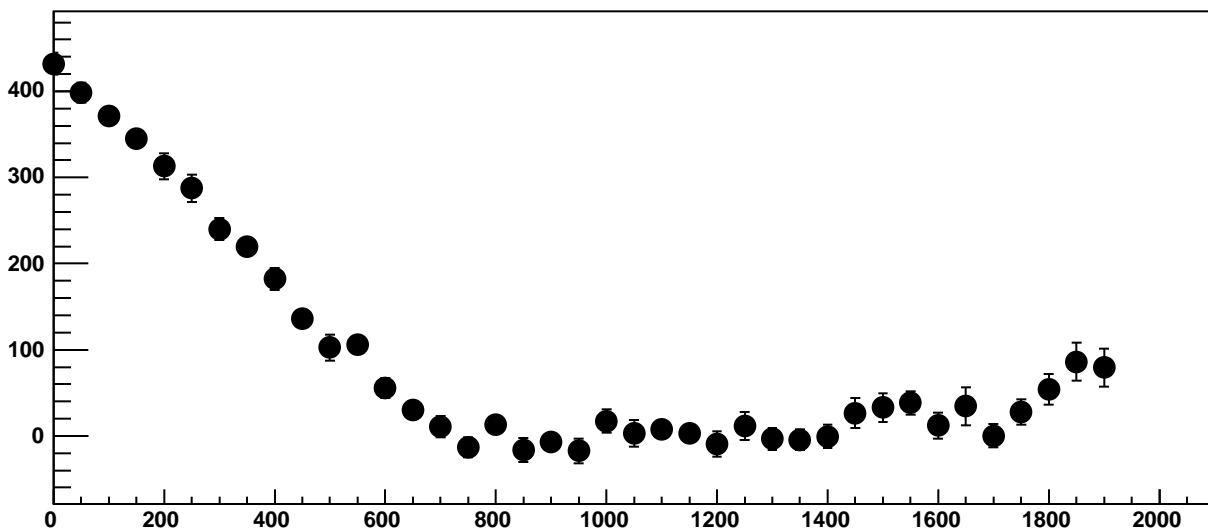
Chip 11, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC



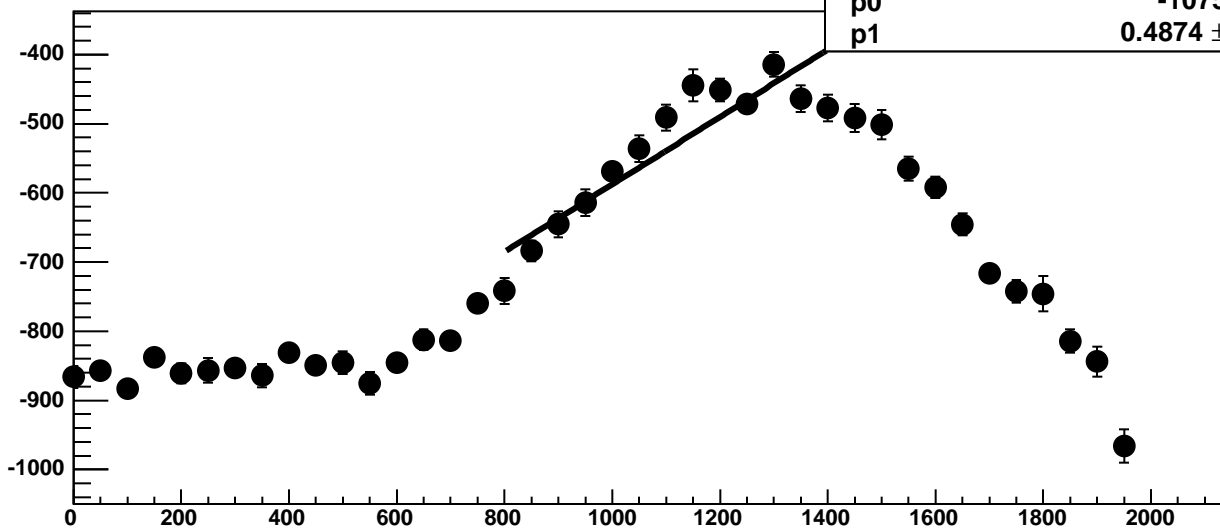
Chip 11, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC



Chip 11, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 11, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

64.11 / 11

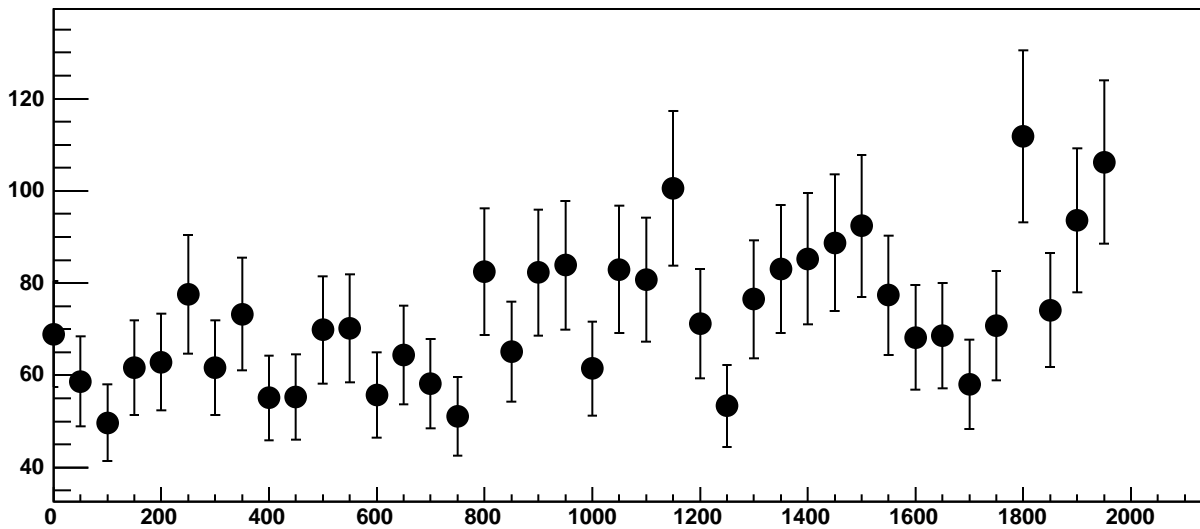
p0

$-1075 \pm 28.9$

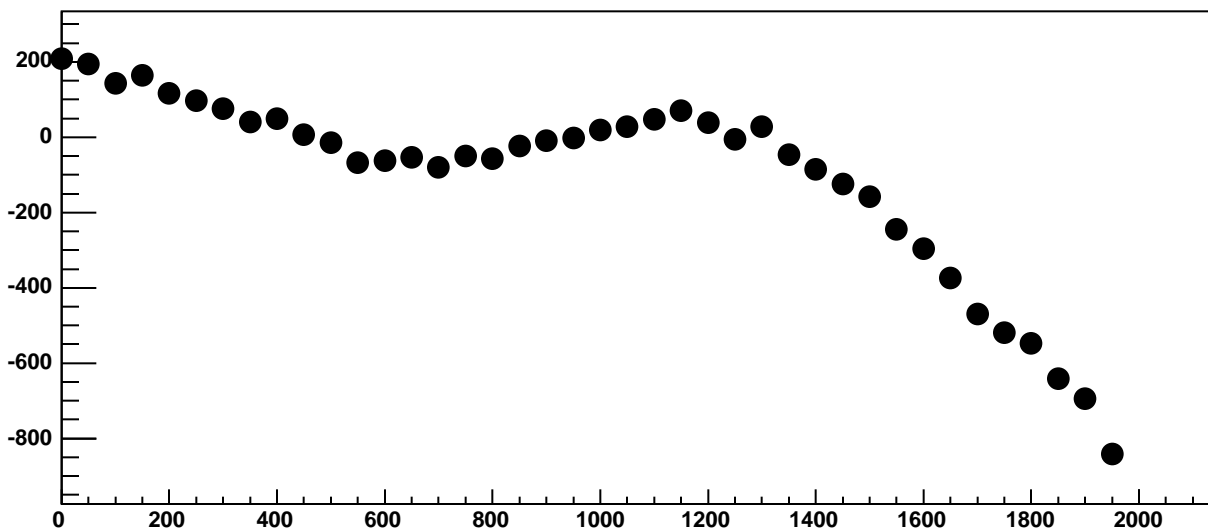
p1

$0.4874 \pm 0.0259$

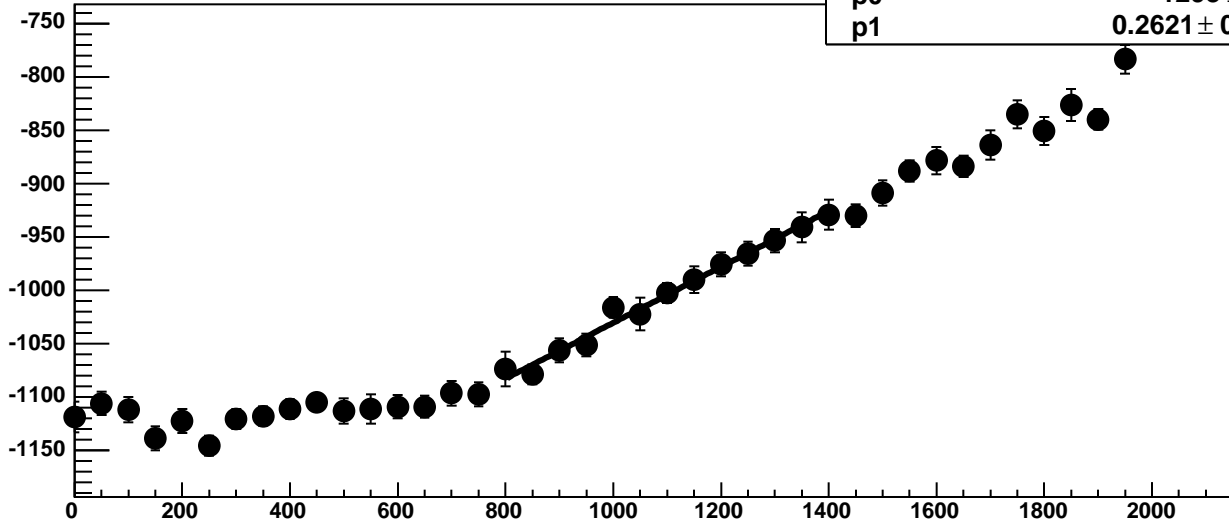
Chip 11, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC



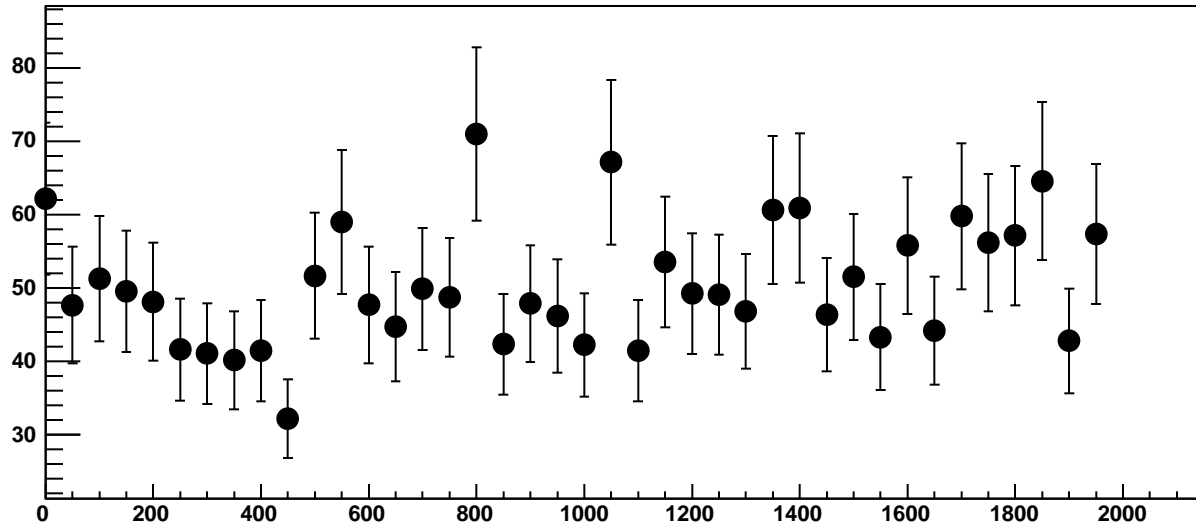
Chip 11, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC



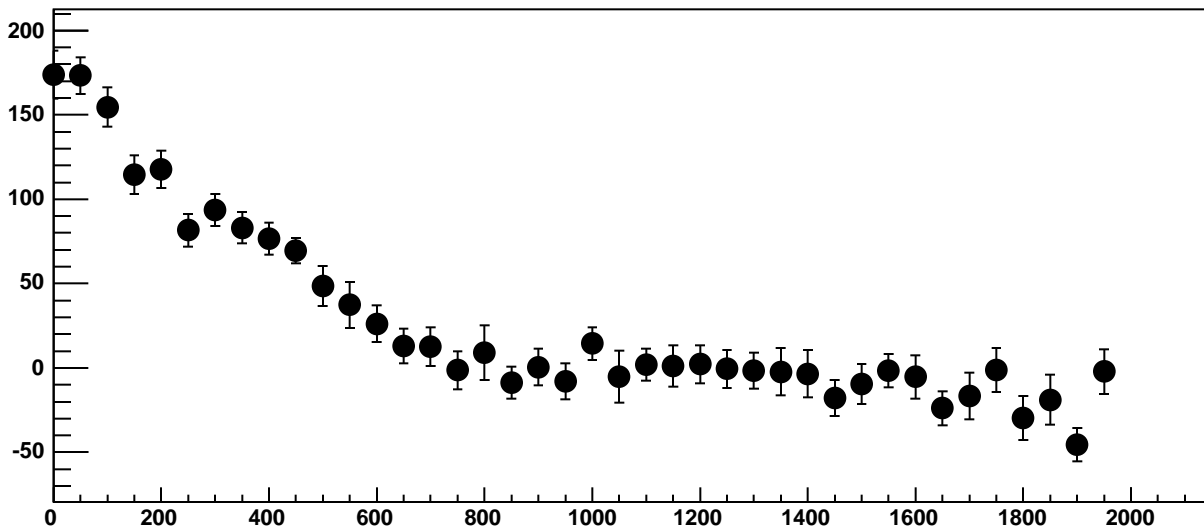
Chip 11, Channel 3, Enable 1, Hold=30, ADC Mean vs DAC



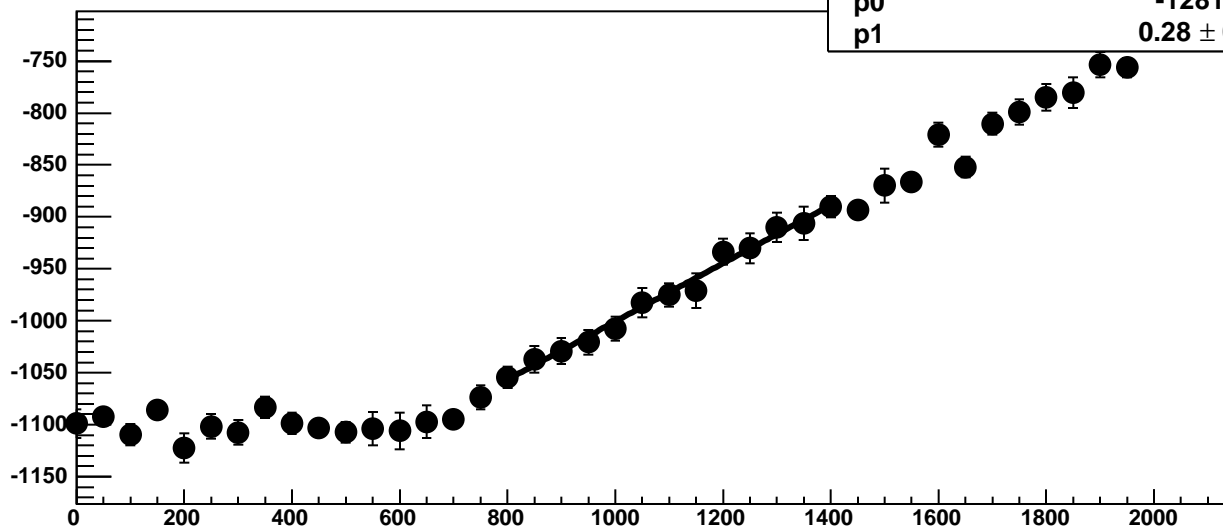
Chip 11, Channel 3, Enable 1, Hold=30, ADC Noise vs DAC



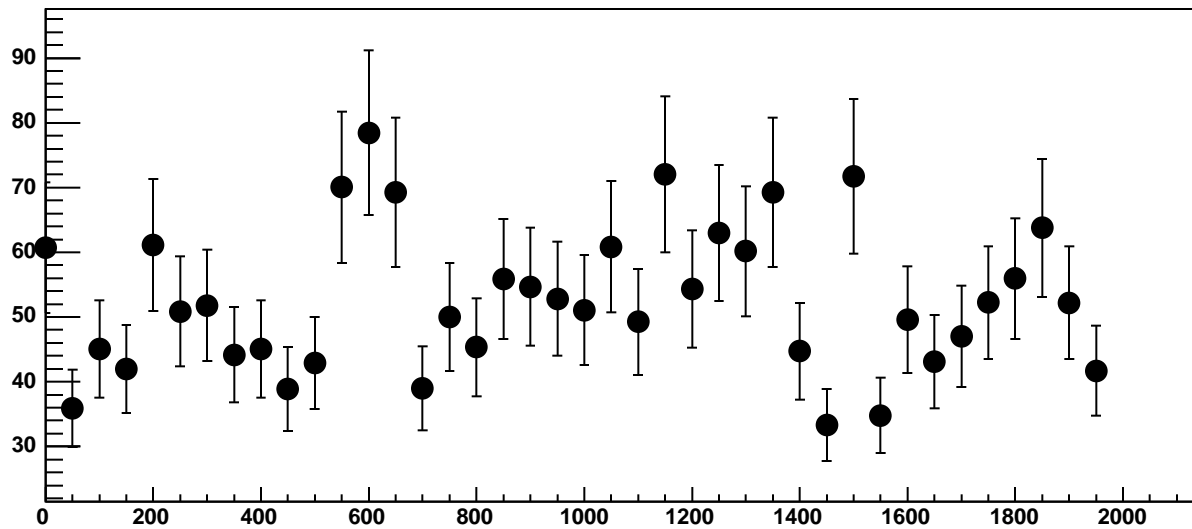
Chip 11, Channel 3, Enable 1, Hold=30, ADC Residuals vs DAC



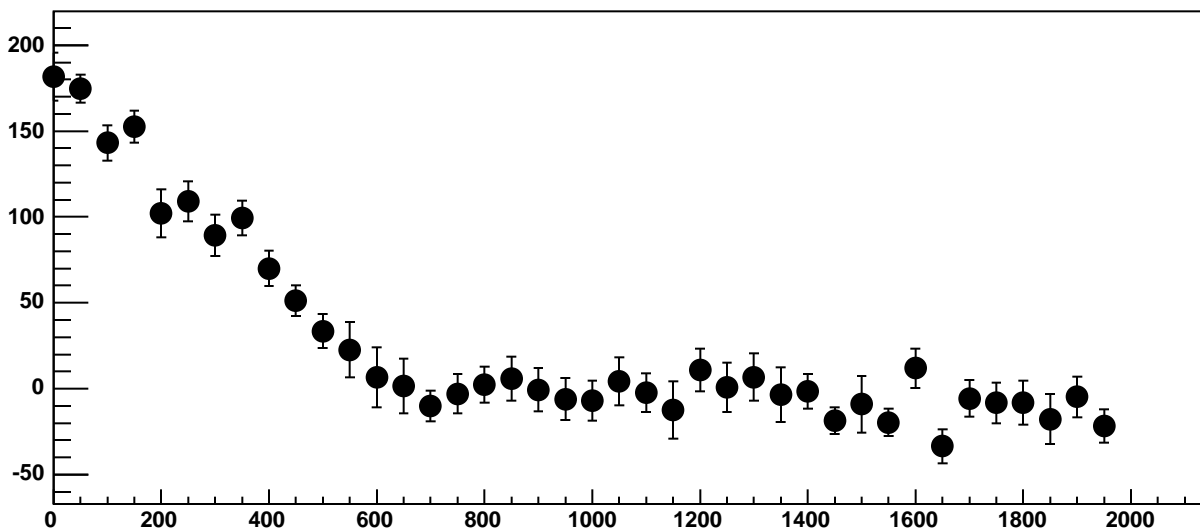
Chip 11, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC



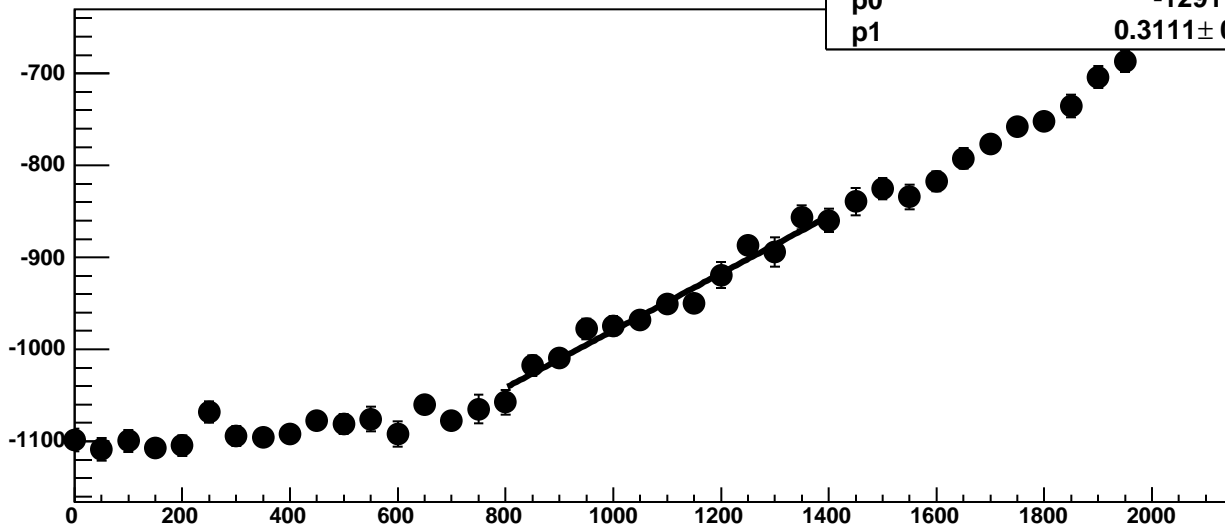
Chip 11, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC



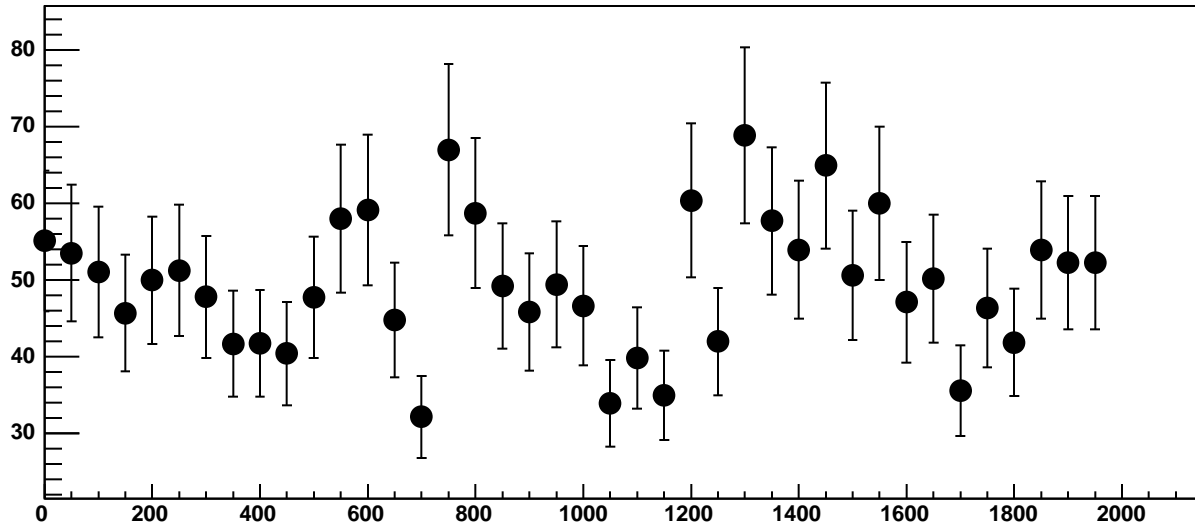
Chip 11, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC



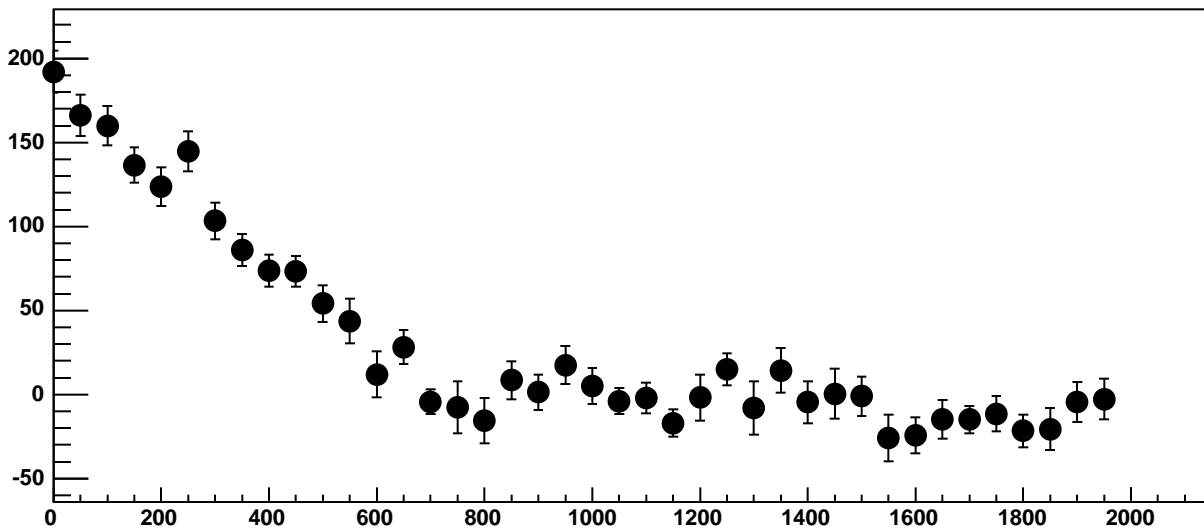
Chip 11, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC



Chip 11, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC

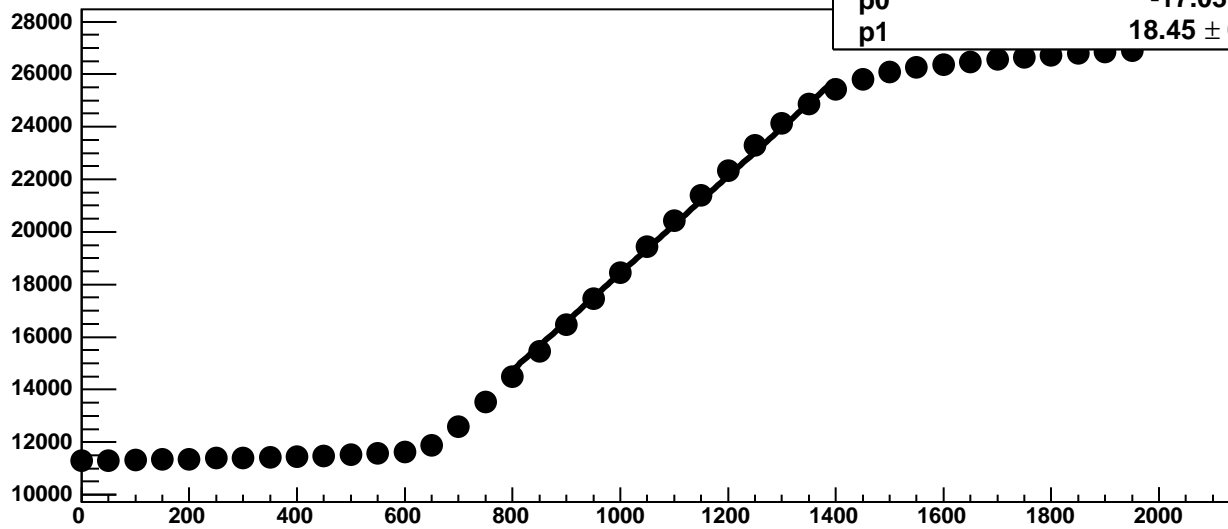


Chip 11, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC

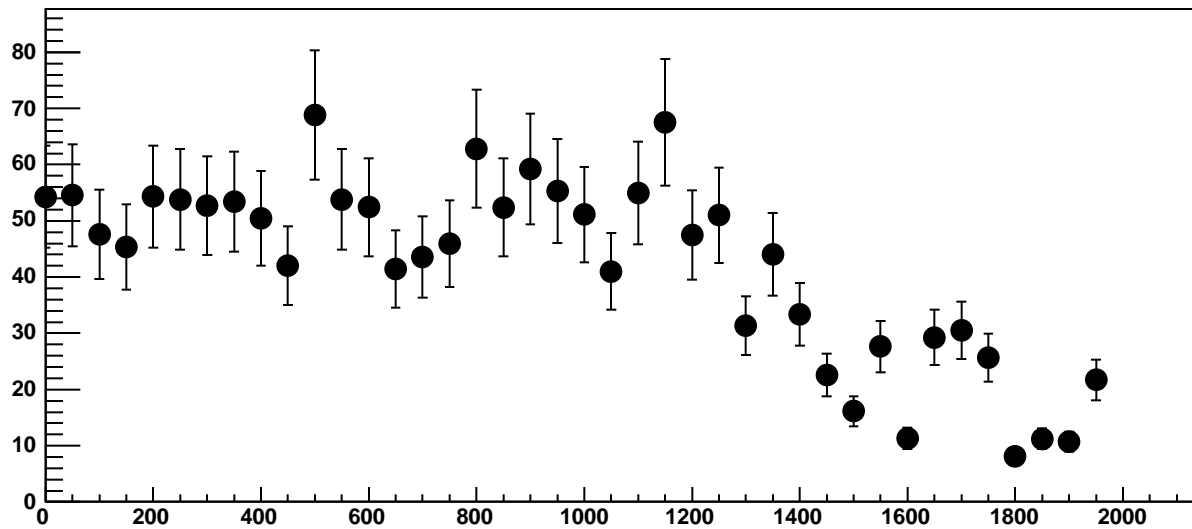




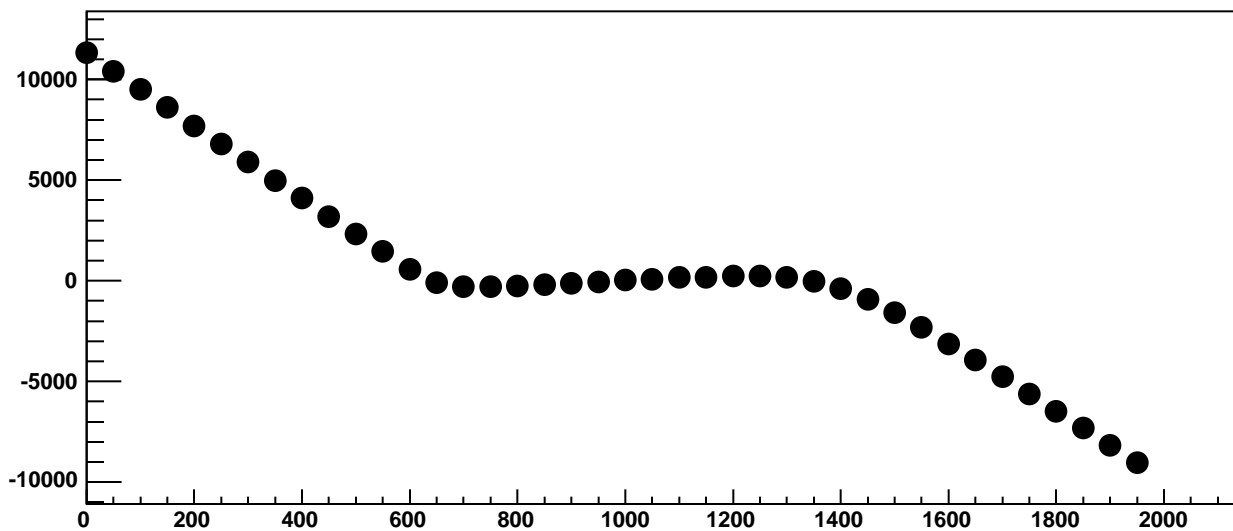
Chip 11, Channel 3, Enable 4!, Hold=30, ADC Mean vs DAC



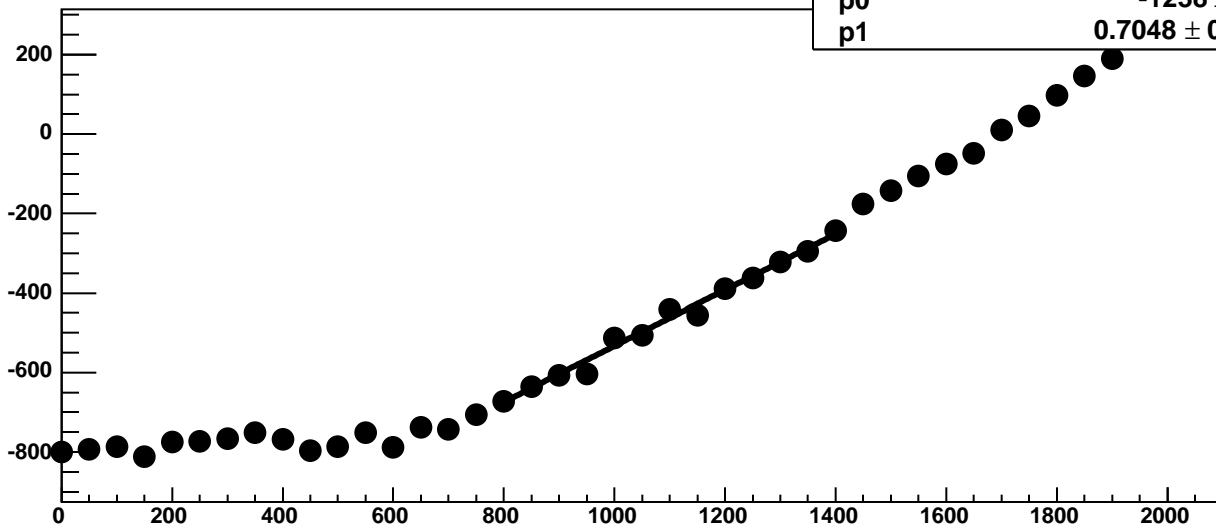
Chip 11, Channel 3, Enable 4!, Hold=30, ADC Noise vs DAC



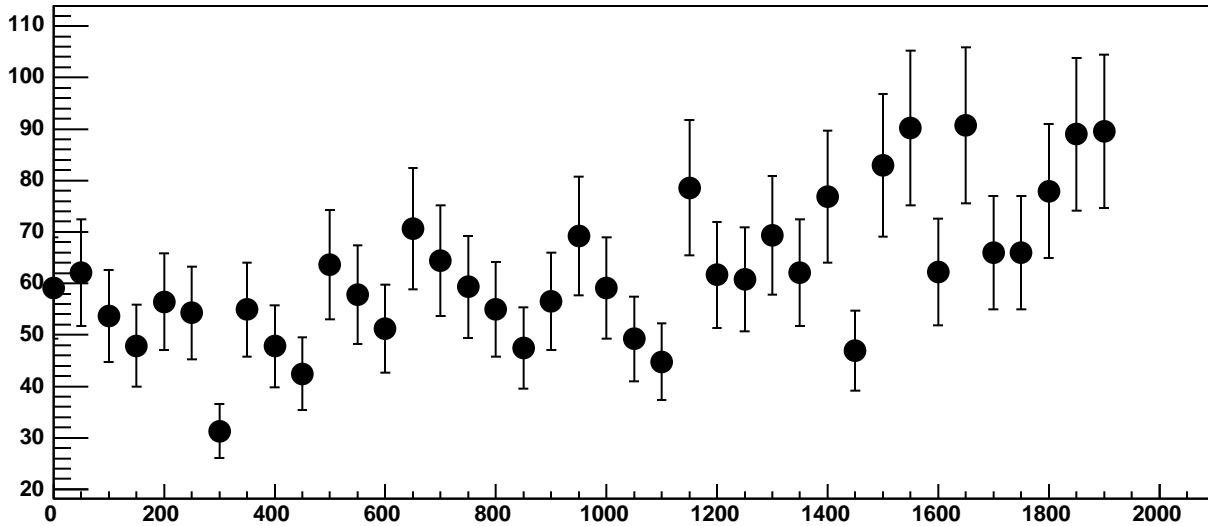
Chip 11, Channel 3, Enable 4!, Hold=30, ADC Residuals vs DAC



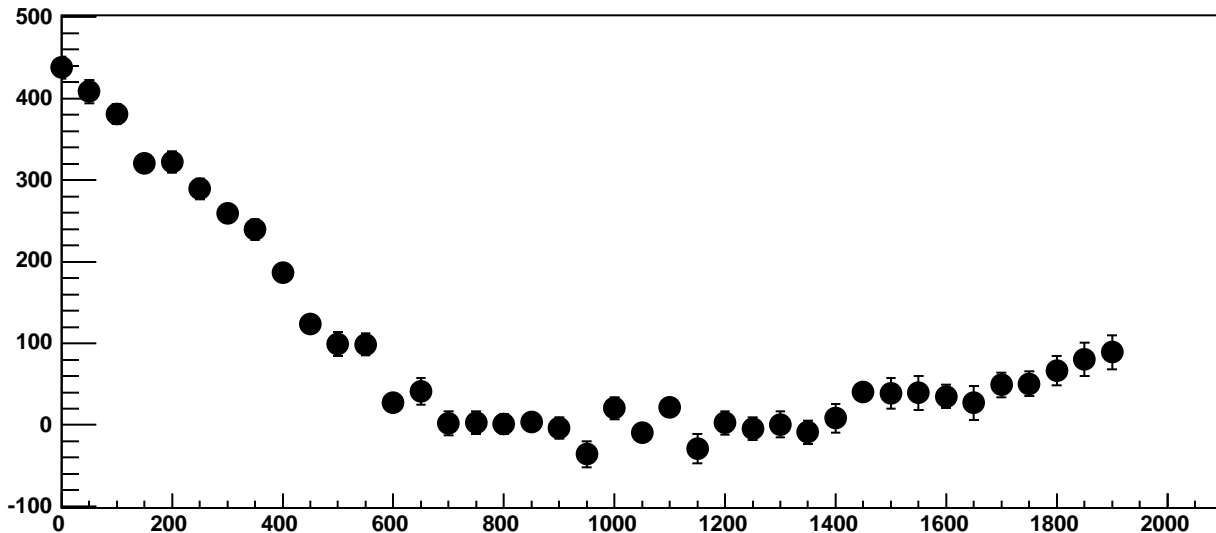
Chip 11, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC



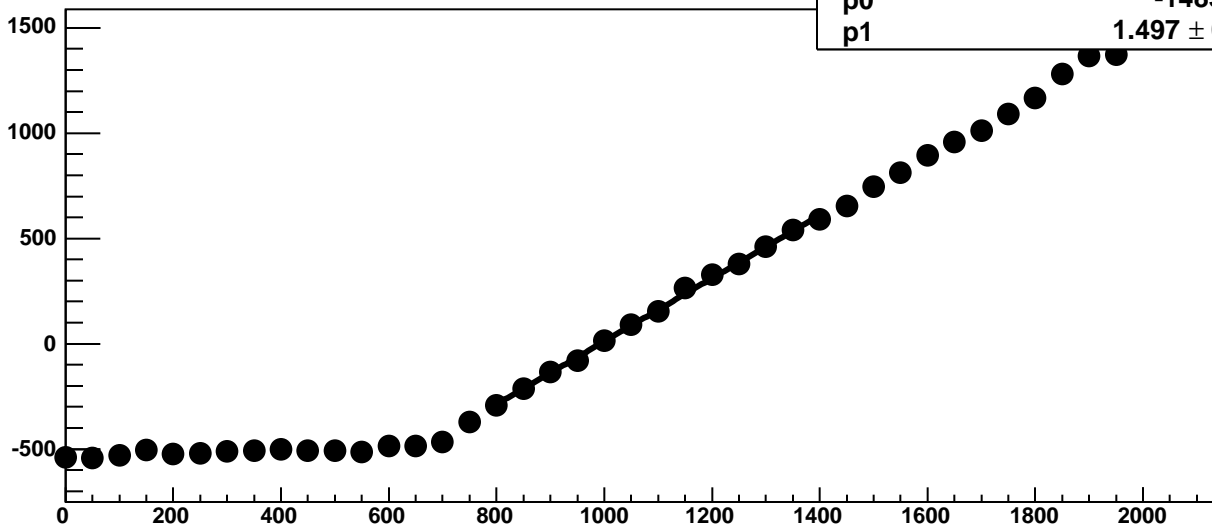
Chip 11, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC



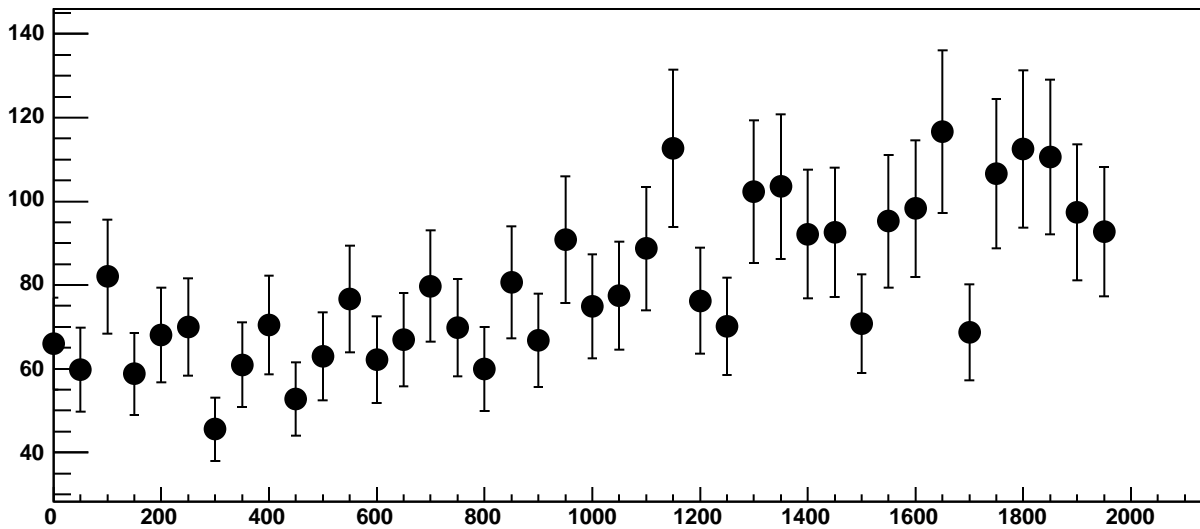
Chip 11, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC



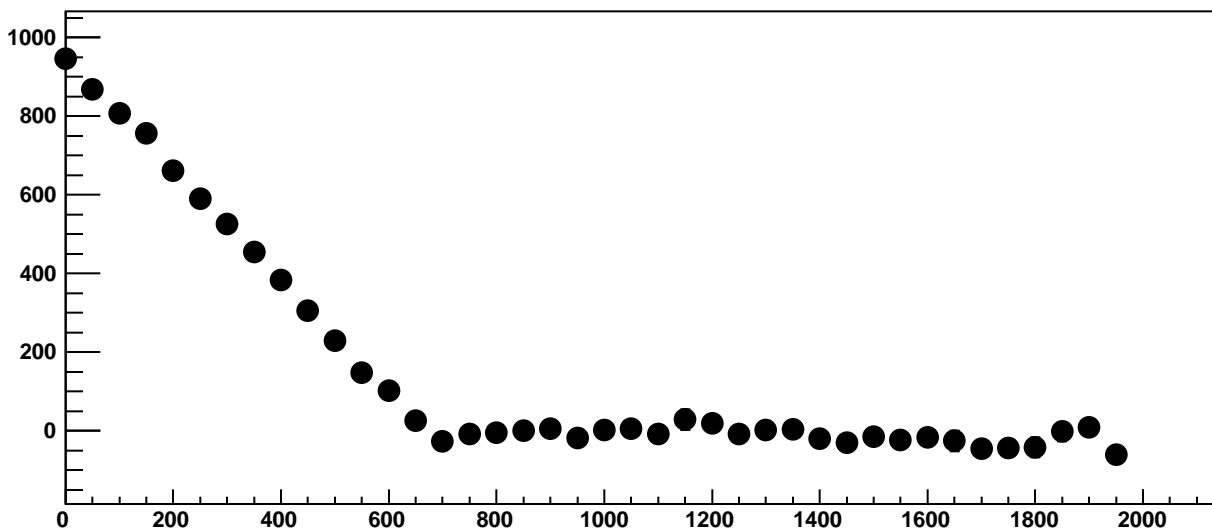
Chip 11, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC



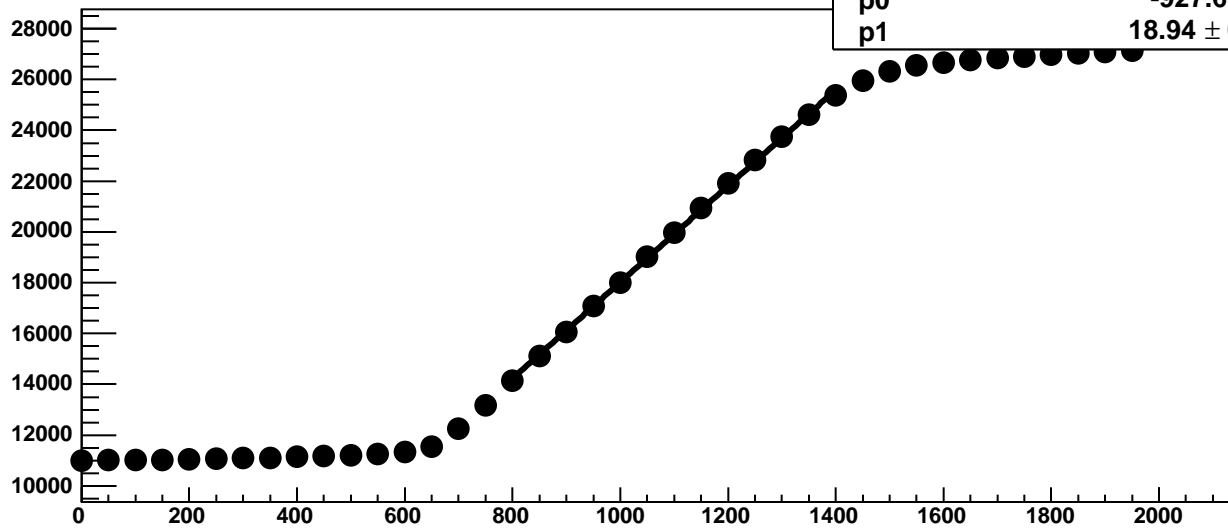
Chip 11, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC



Chip 11, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC

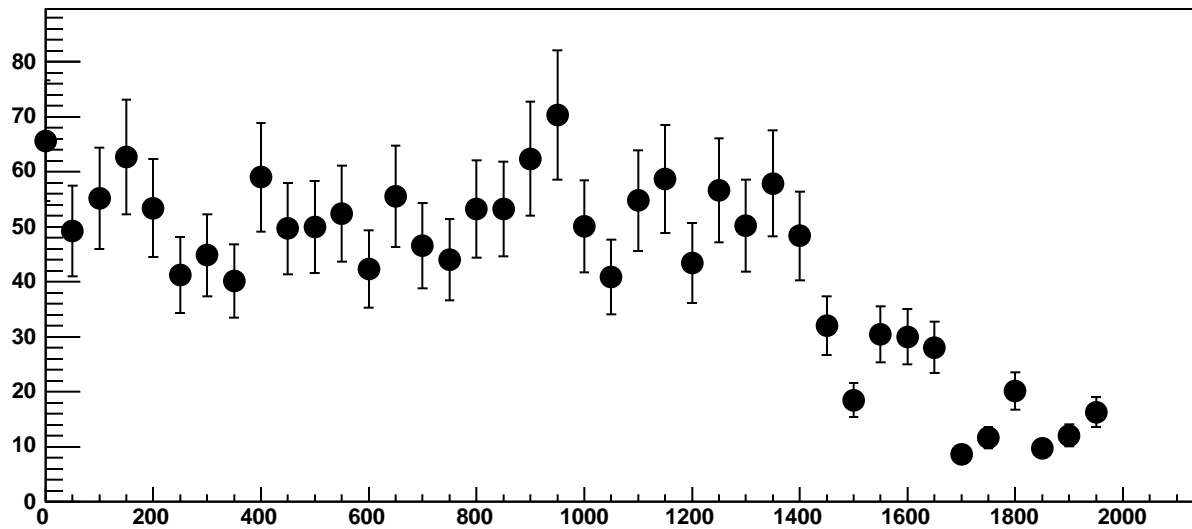


Chip 11, Channel 4, Enable 1!, Hold=30, ADC Mean vs DAC

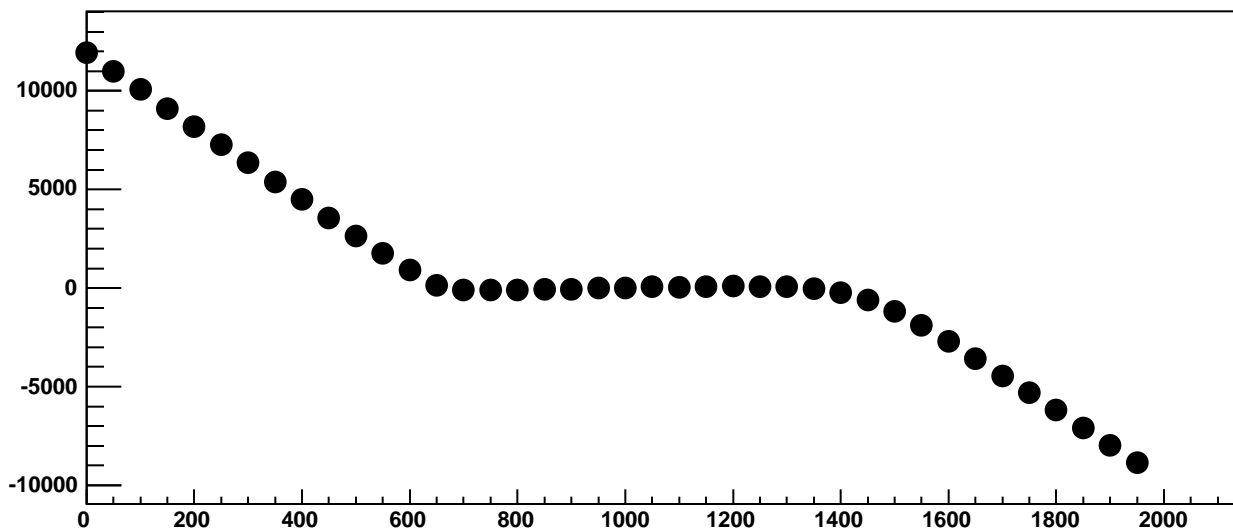


$\chi^2 / \text{ndf}$  780.8 / 11  
p0  $-927.6 \pm 20.34$   
p1  $18.94 \pm 0.01809$

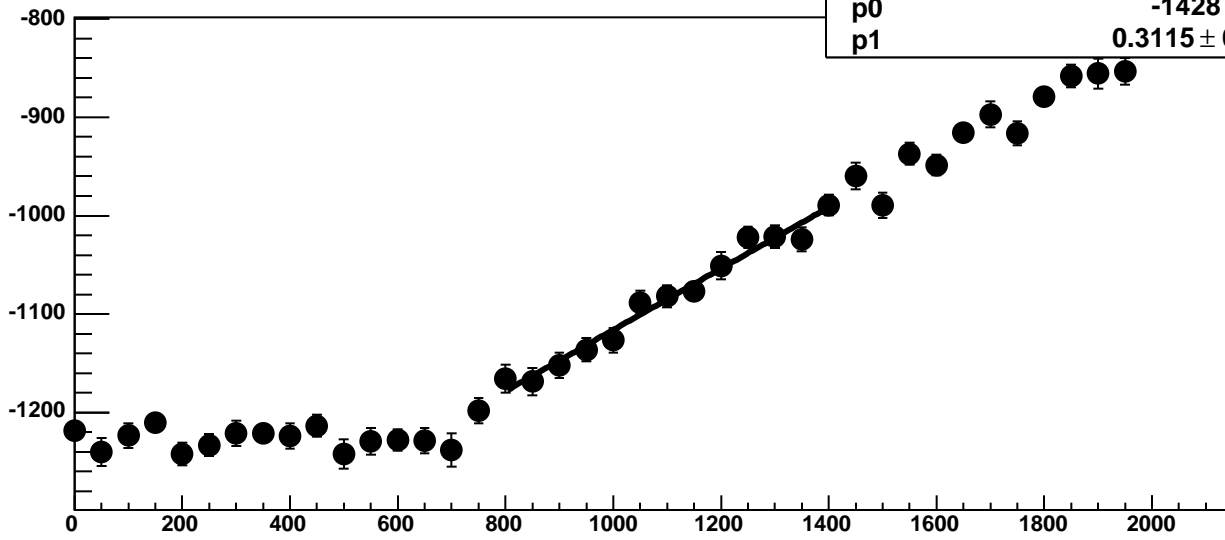
Chip 11, Channel 4, Enable 1!, Hold=30, ADC Noise vs DAC



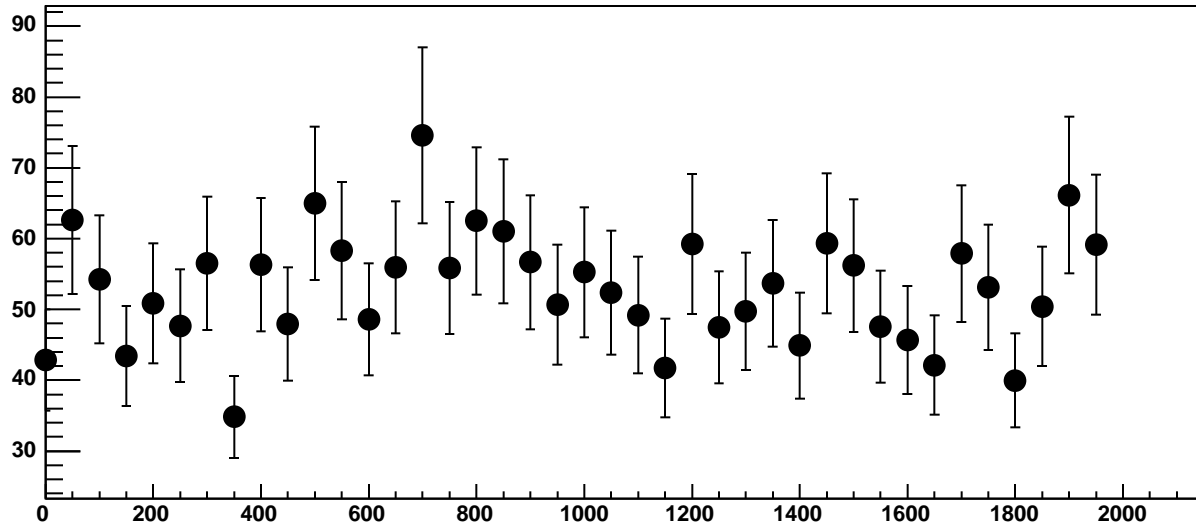
Chip 11, Channel 4, Enable 1!, Hold=30, ADC Residuals vs DAC



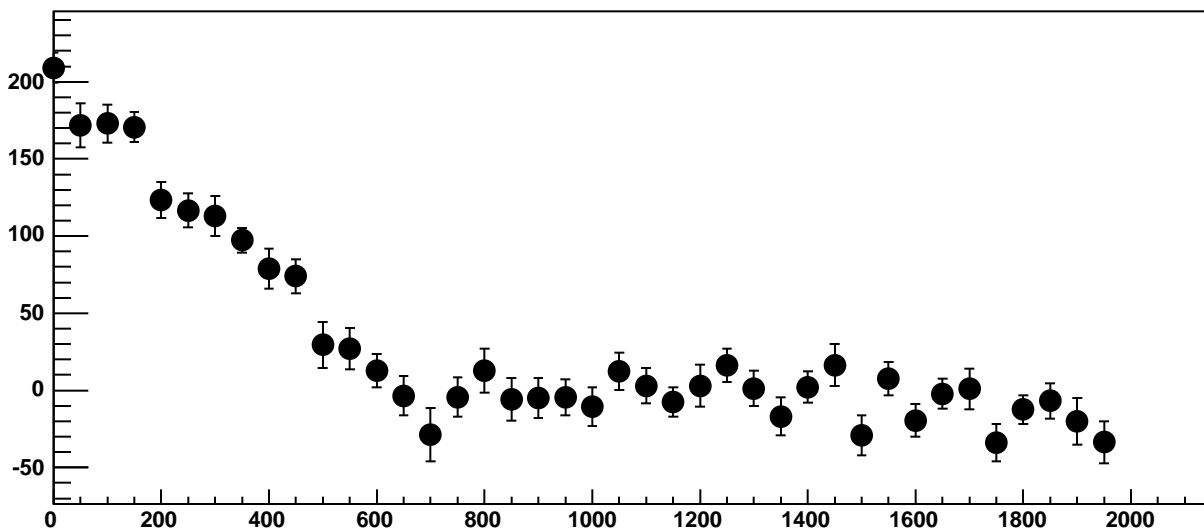
Chip 11, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC



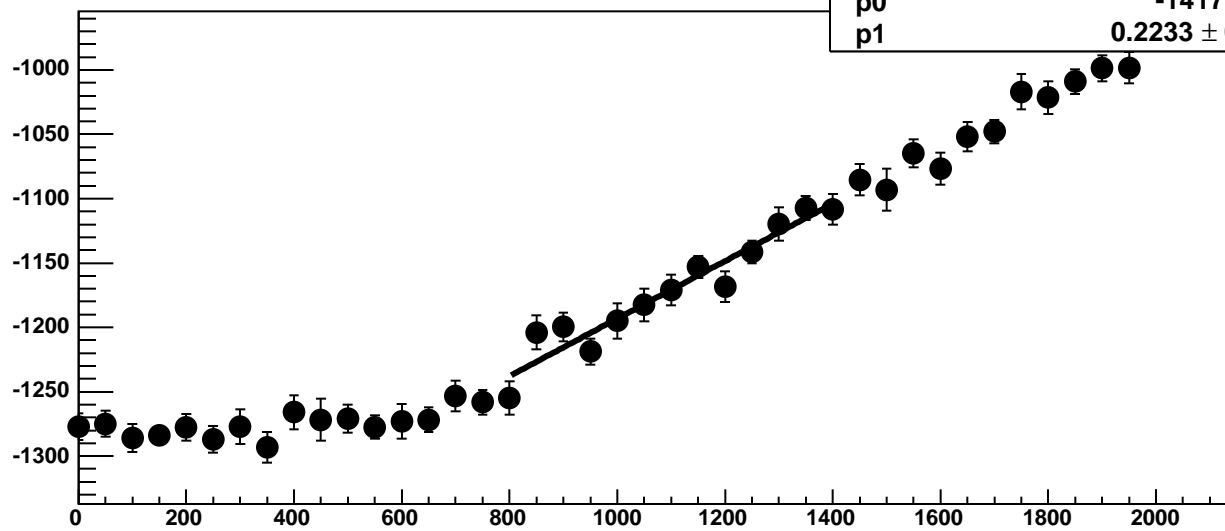
Chip 11, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC



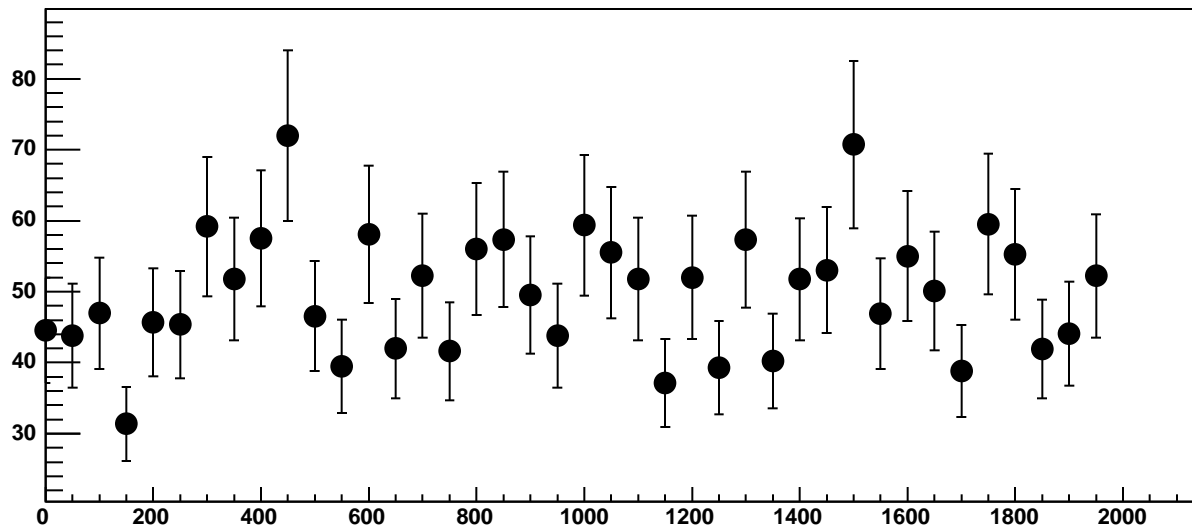
Chip 11, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC



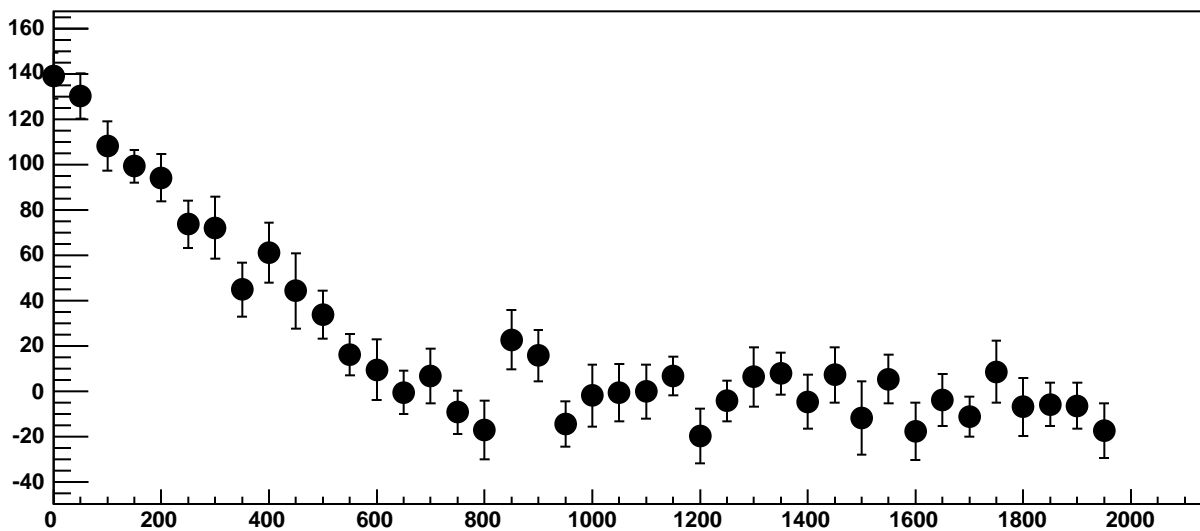
Chip 11, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC



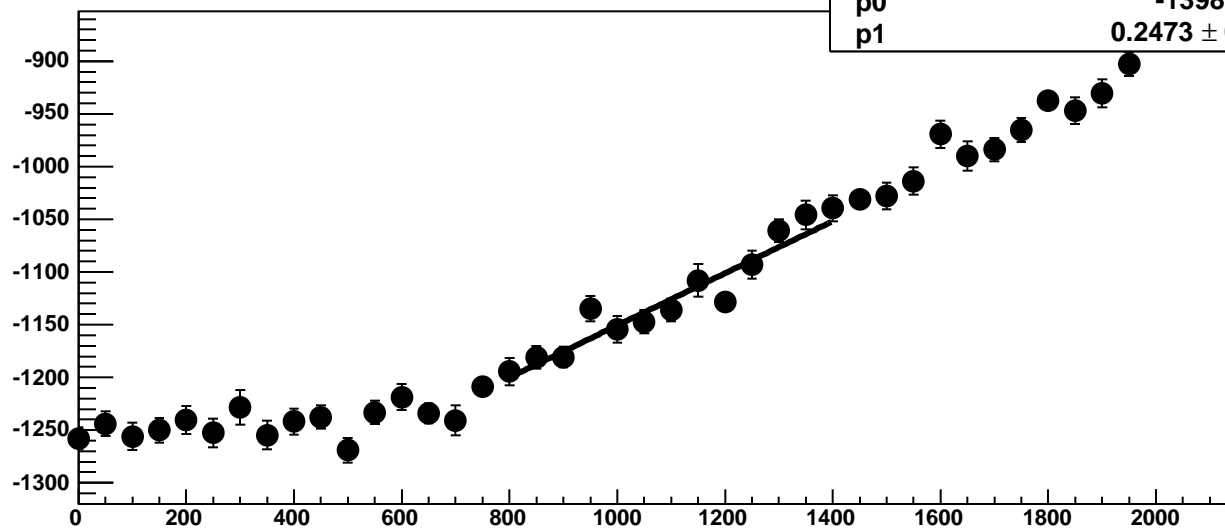
Chip 11, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC



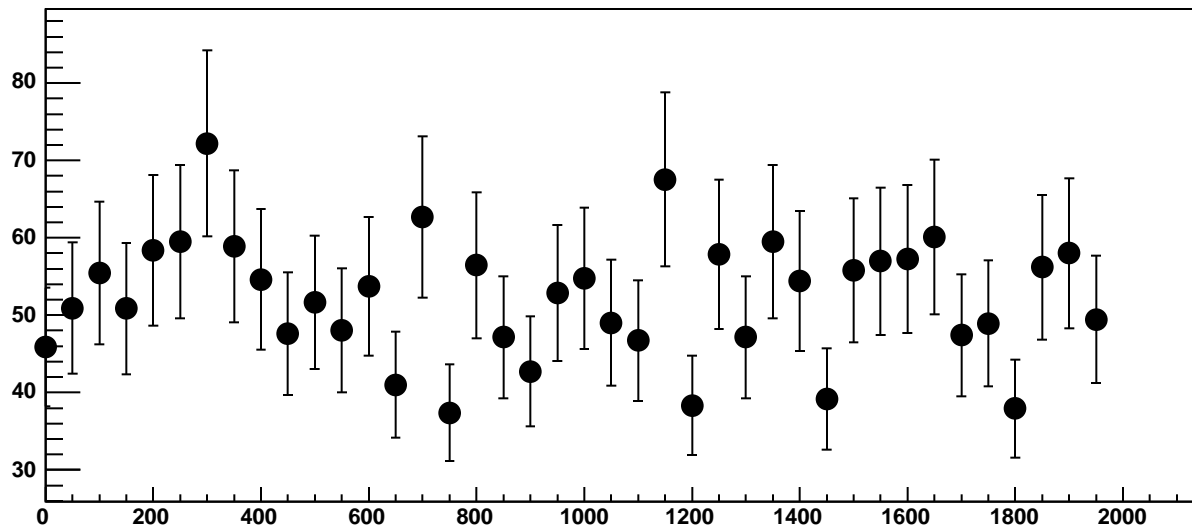
Chip 11, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC



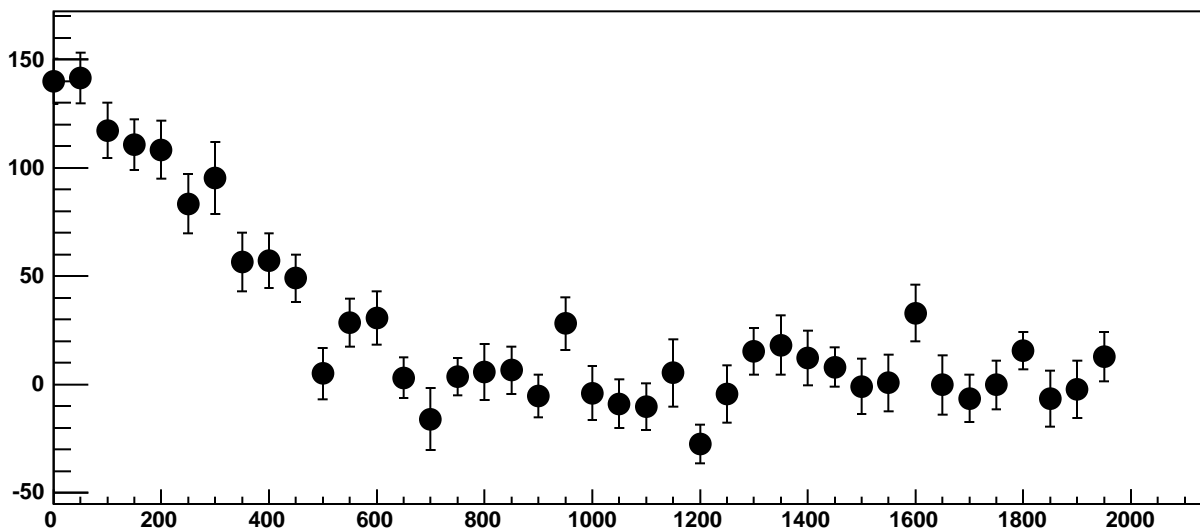
Chip 11, Channel 4, Enable 4, Hold=30, ADC Mean vs DAC



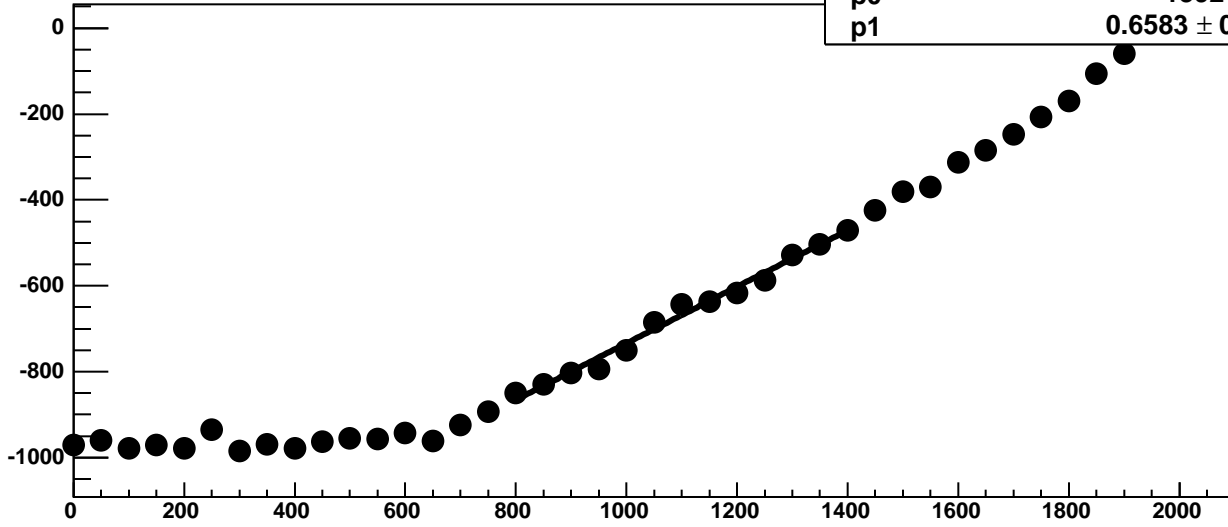
Chip 11, Channel 4, Enable 4, Hold=30, ADC Noise vs DAC



Chip 11, Channel 4, Enable 4, Hold=30, ADC Residuals vs DAC

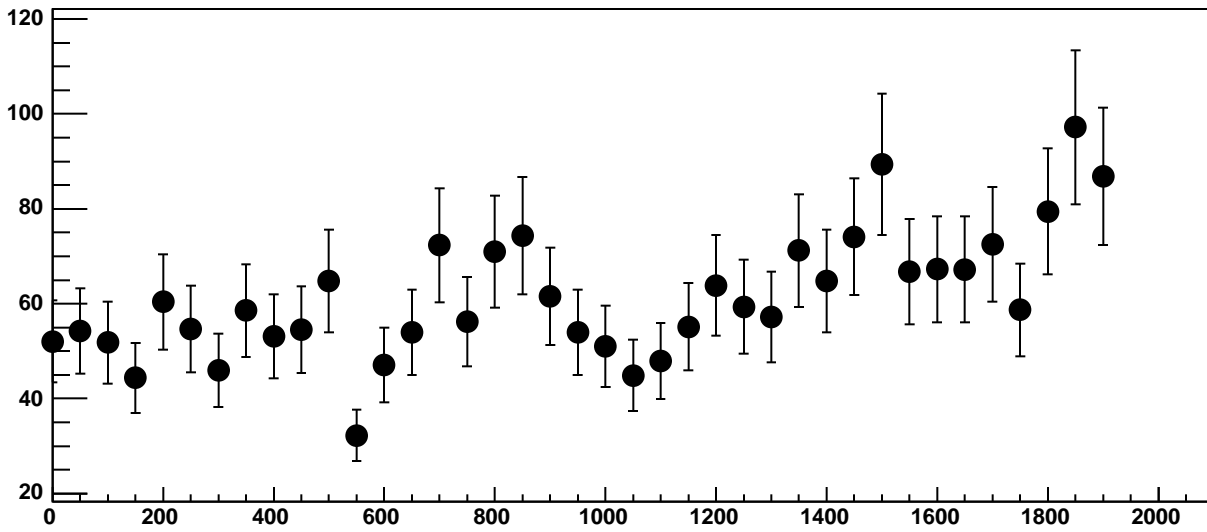


Chip 11, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC

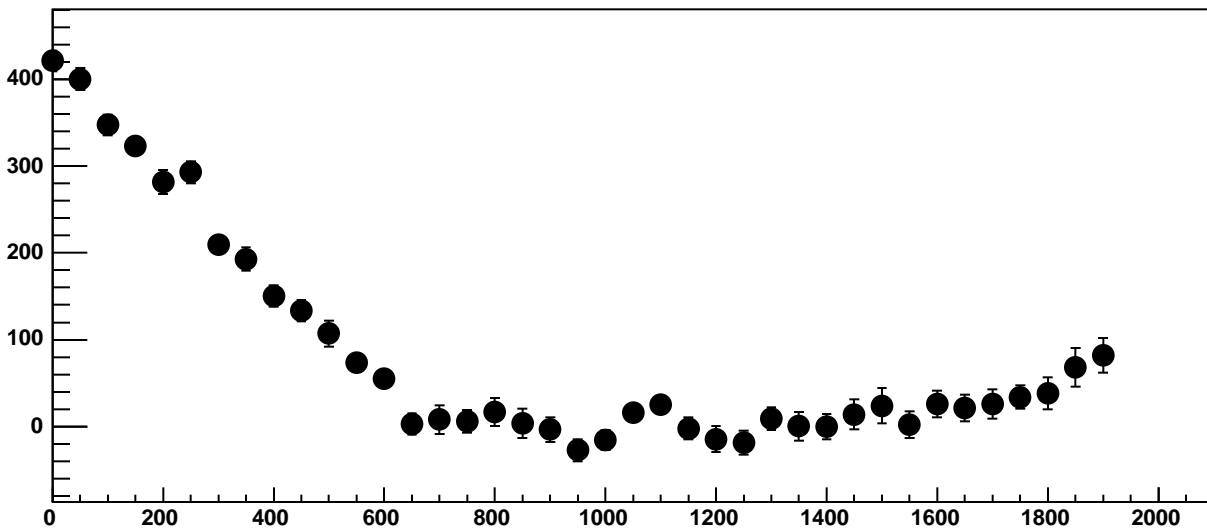


$\chi^2 / \text{ndf}$  18.85 / 11  
p0  $-1392 \pm 24.51$   
p1  $0.6583 \pm 0.02206$

Chip 11, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC

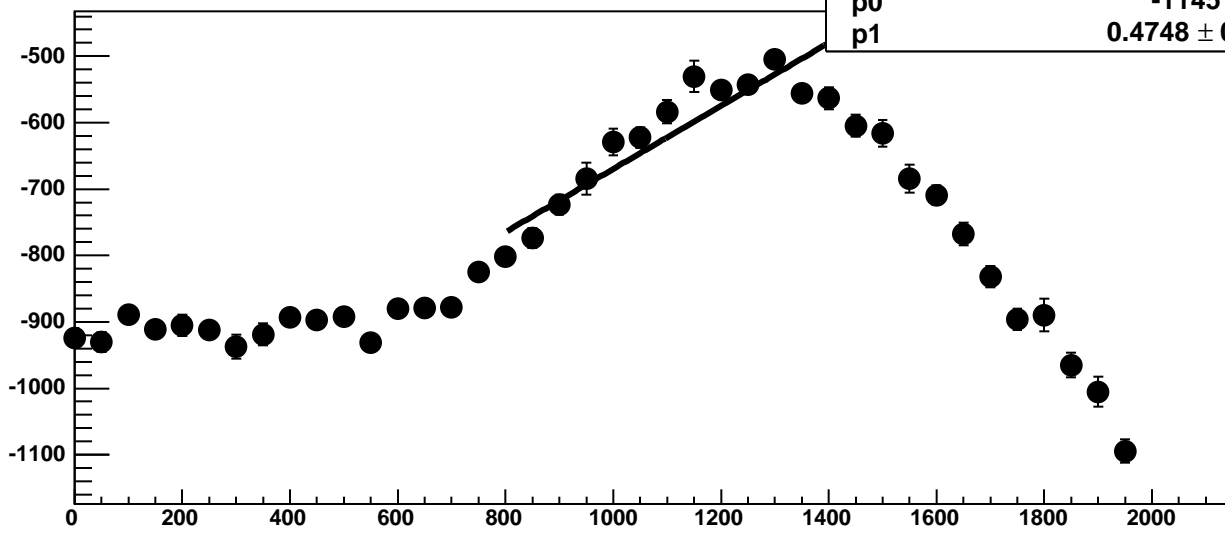


Chip 11, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC

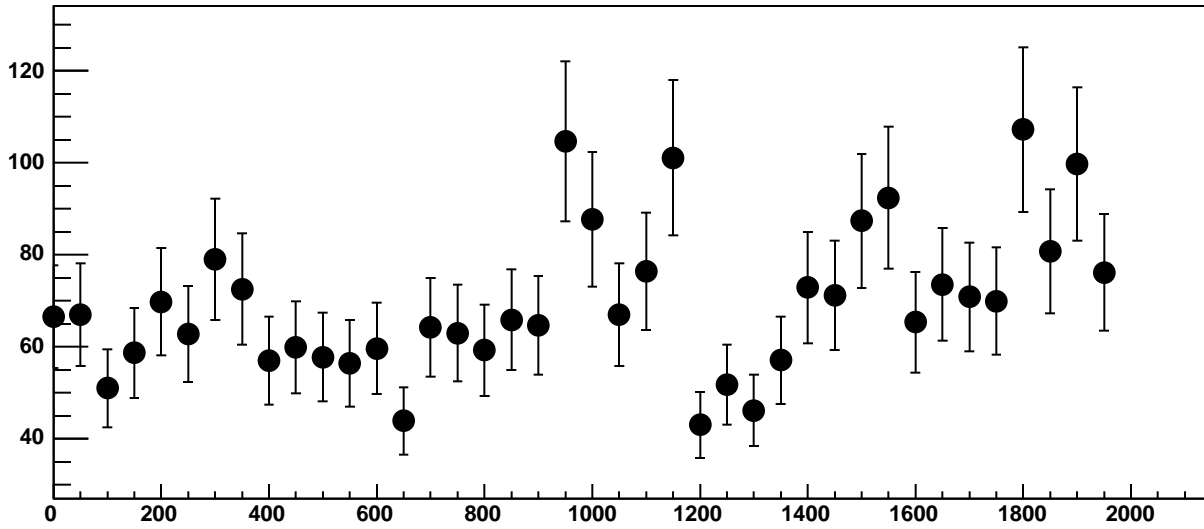




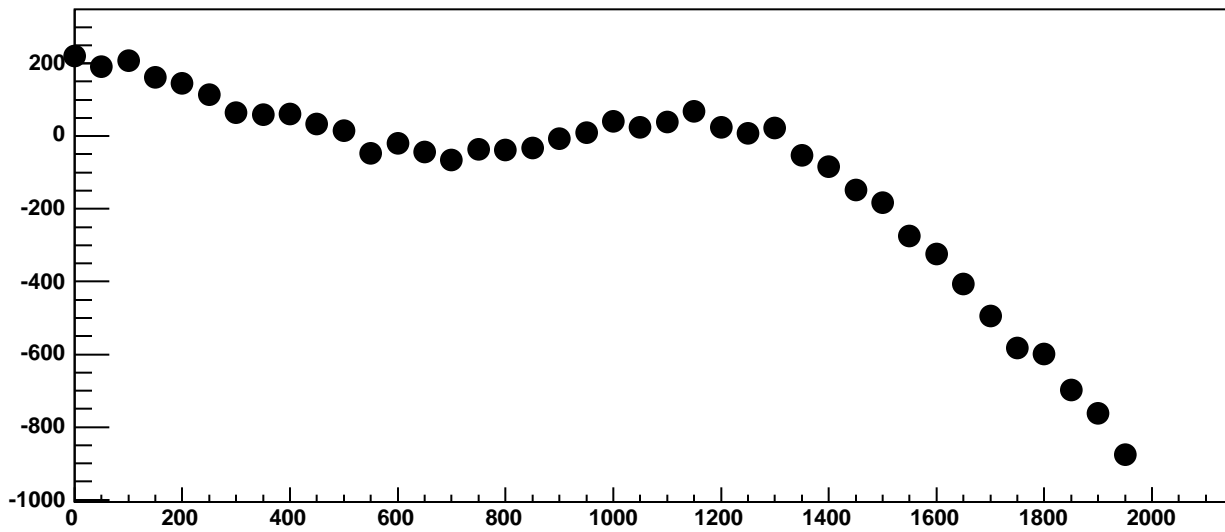
Chip 11, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC



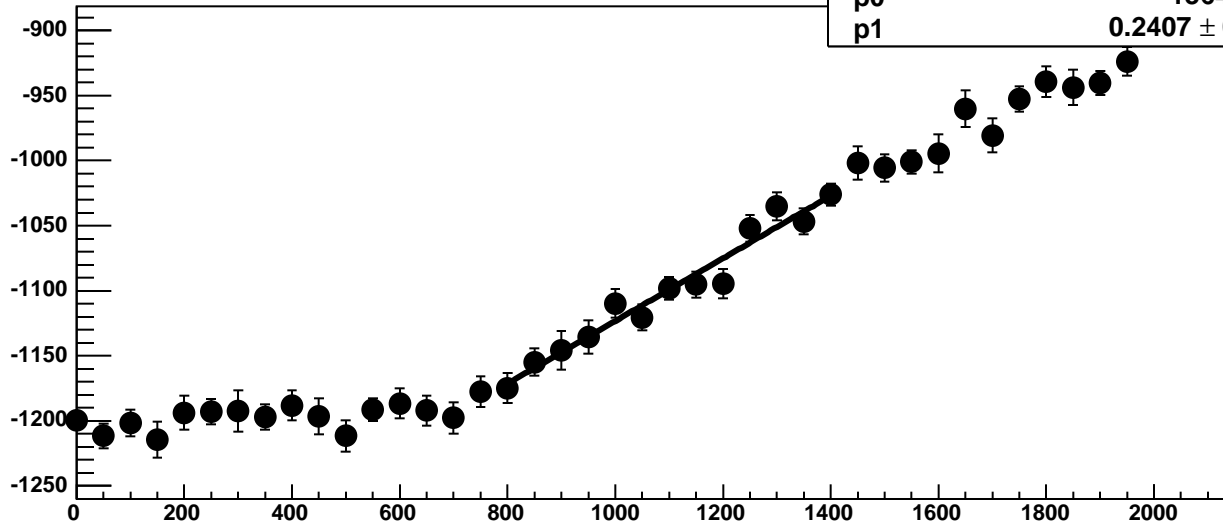
Chip 11, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC



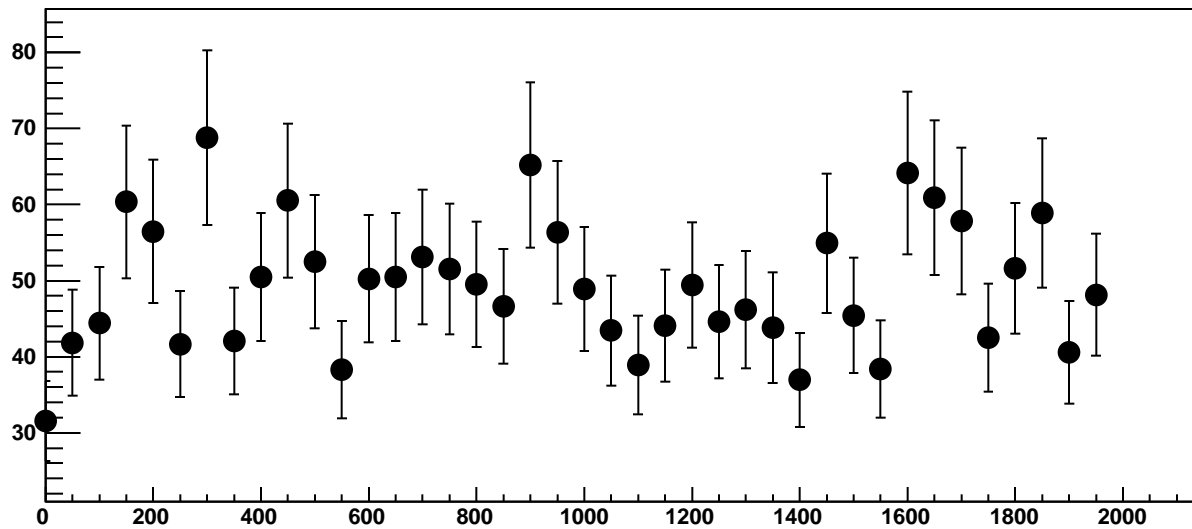
Chip 11, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC



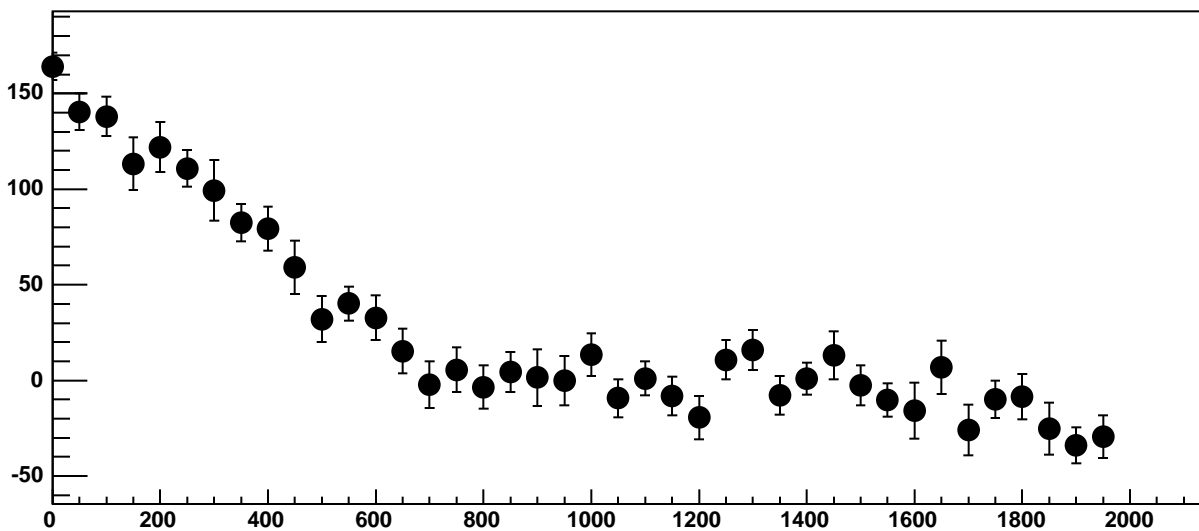
Chip 11, Channel 5, Enable 1, Hold=30, ADC Mean vs DAC



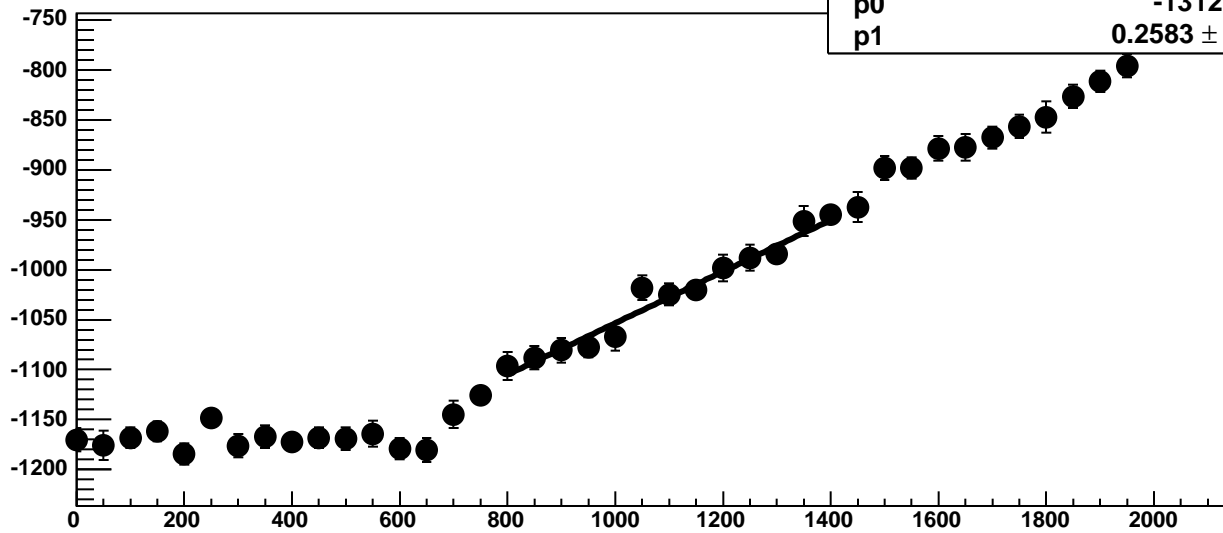
Chip 11, Channel 5, Enable 1, Hold=30, ADC Noise vs DAC



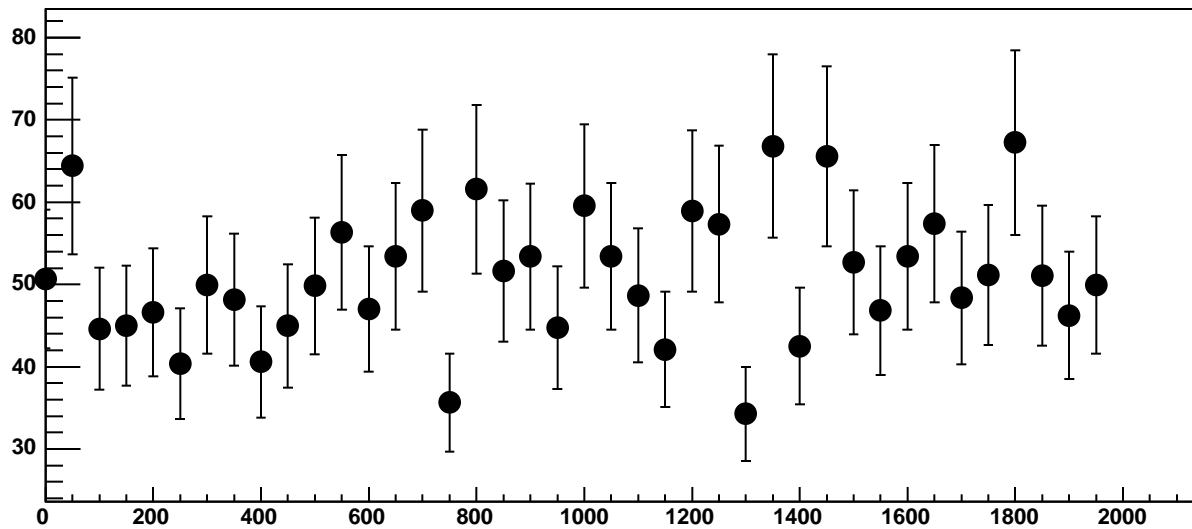
Chip 11, Channel 5, Enable 1, Hold=30, ADC Residuals vs DAC



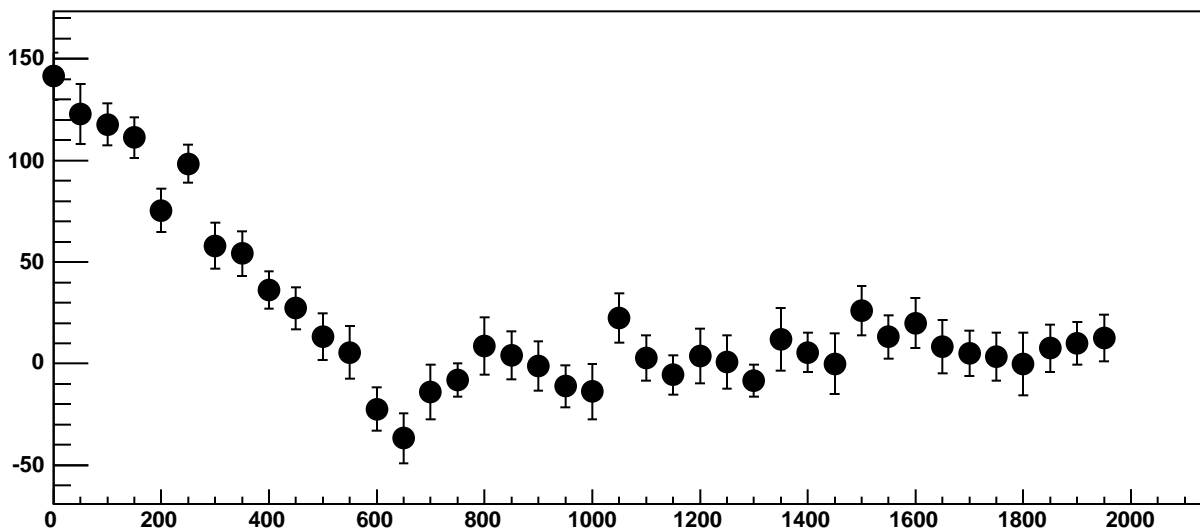
Chip 11, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC



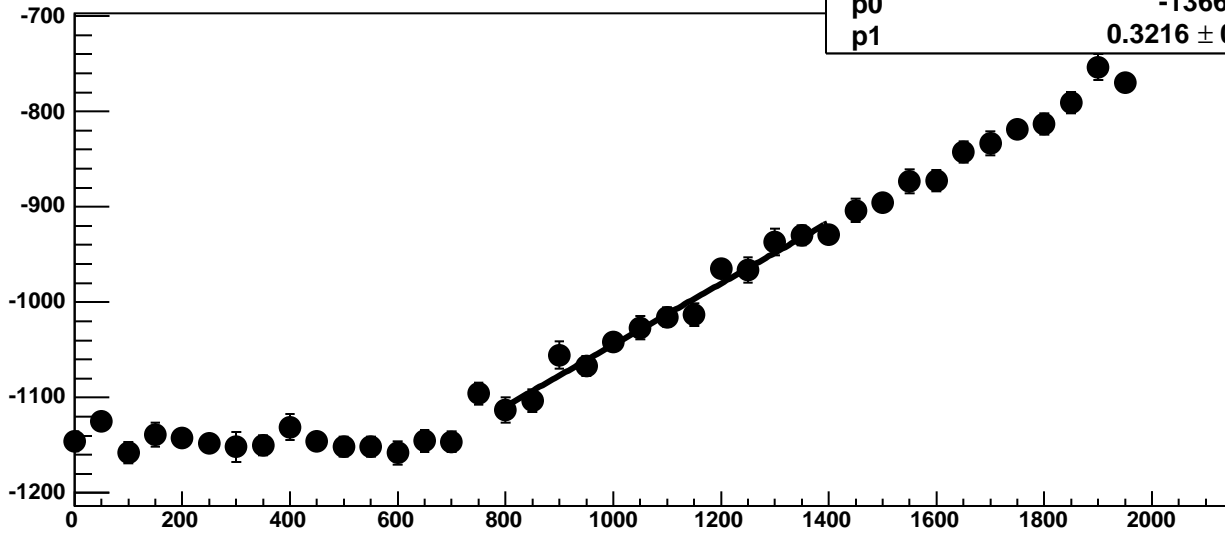
Chip 11, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC



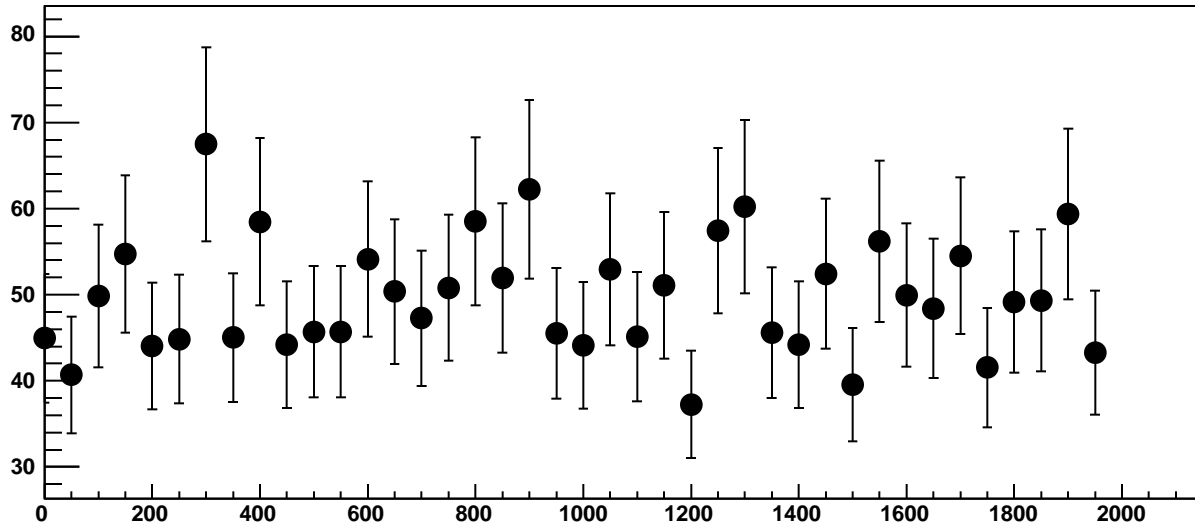
Chip 11, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC



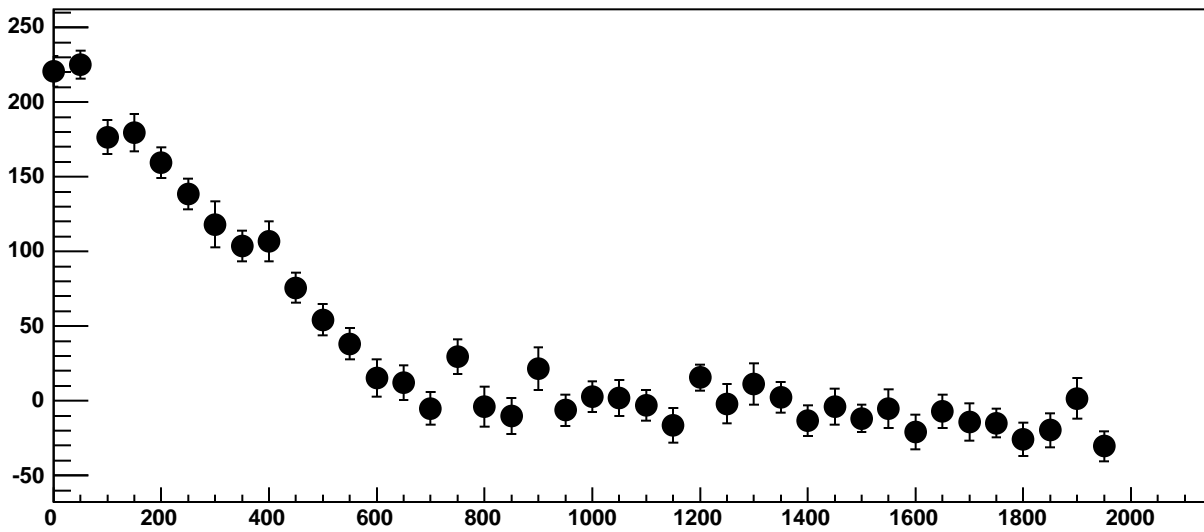
Chip 11, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC



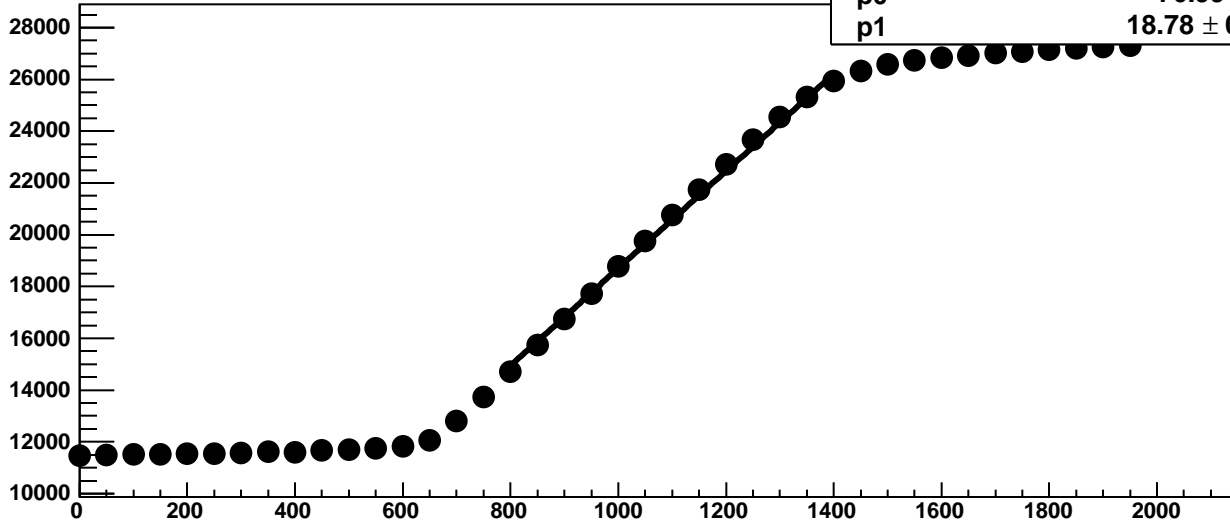
Chip 11, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC



Chip 11, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC

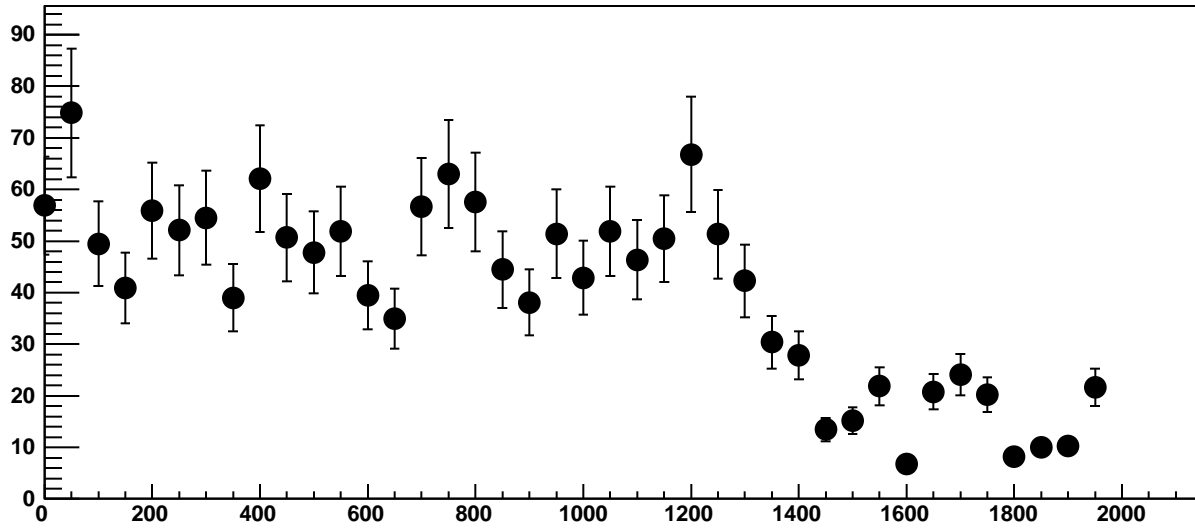


Chip 11, Channel 5, Enable 4!, Hold=30, ADC Mean vs DAC

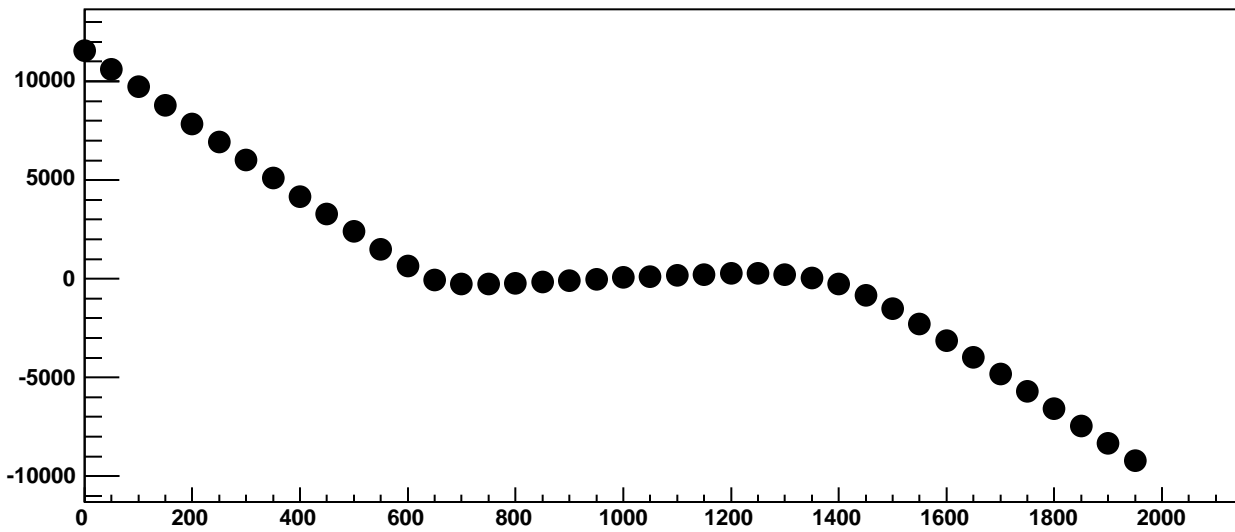


$\chi^2 / \text{ndf}$  4631 / 11  
p0  $-76.99 \pm 15.55$   
p1  $18.78 \pm 0.01327$

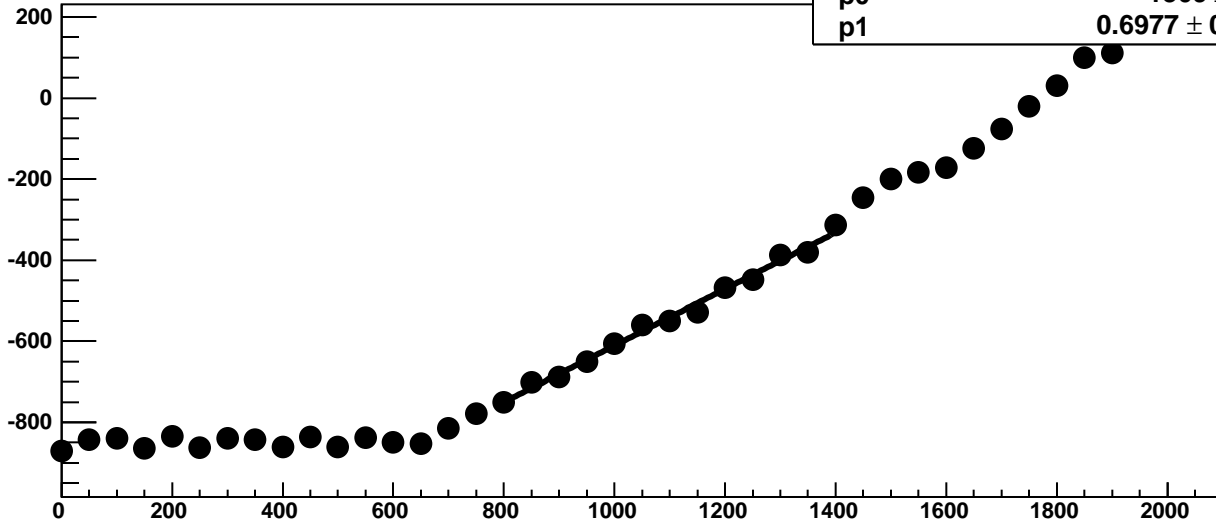
Chip 11, Channel 5, Enable 4!, Hold=30, ADC Noise vs DAC



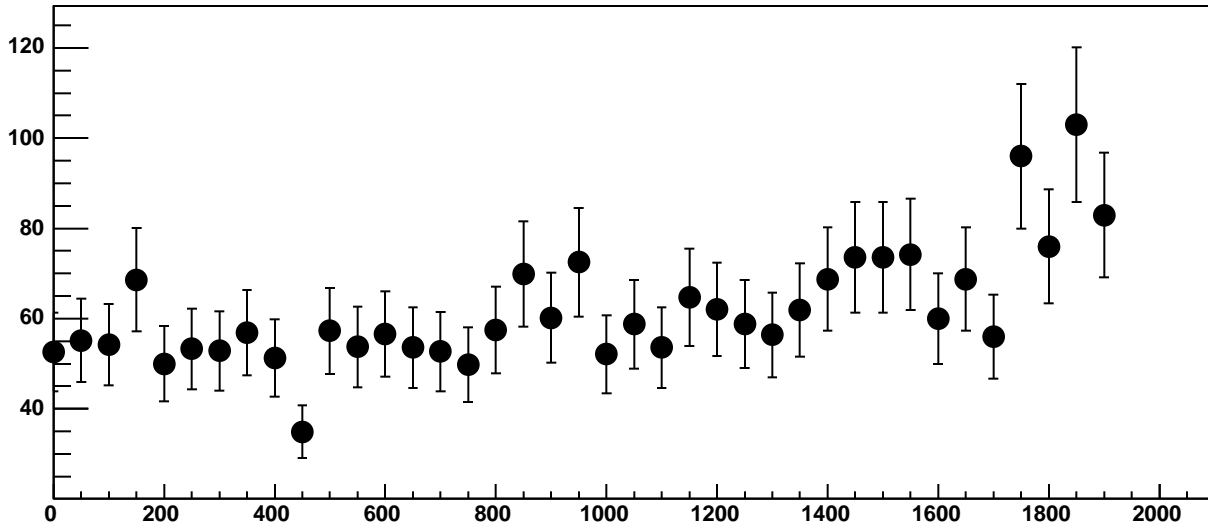
Chip 11, Channel 5, Enable 4!, Hold=30, ADC Residuals vs DAC



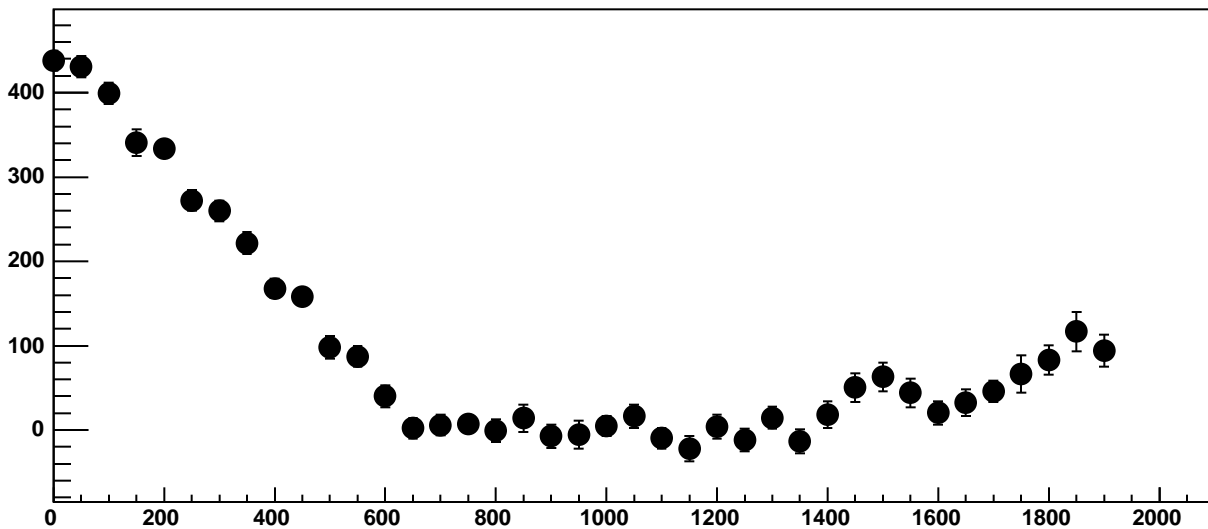
Chip 11, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC



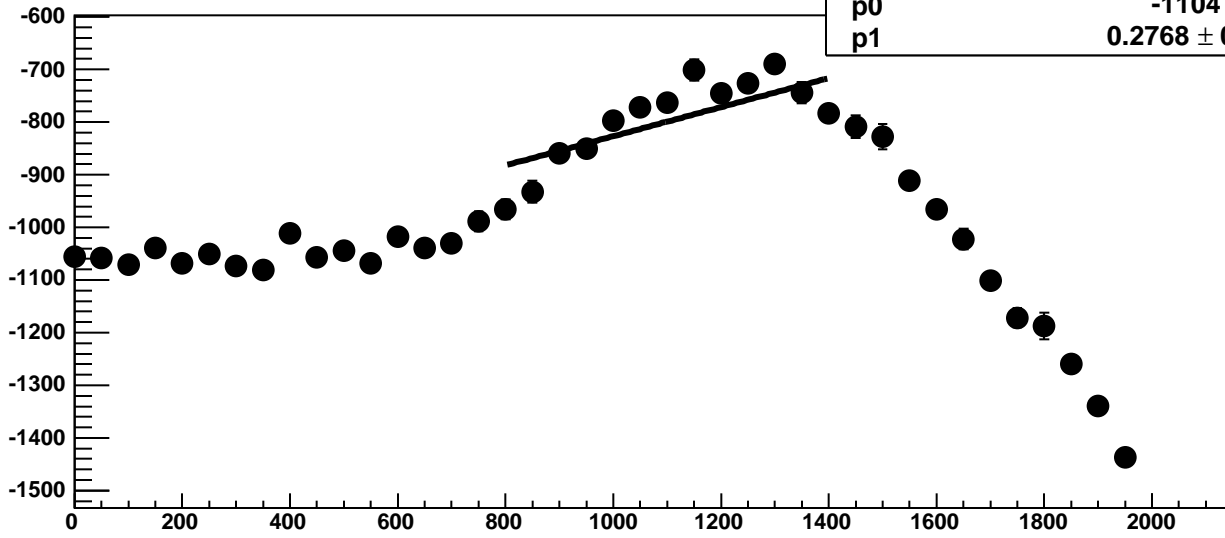
Chip 11, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC



Chip 11, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 11, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

121.8 / 11

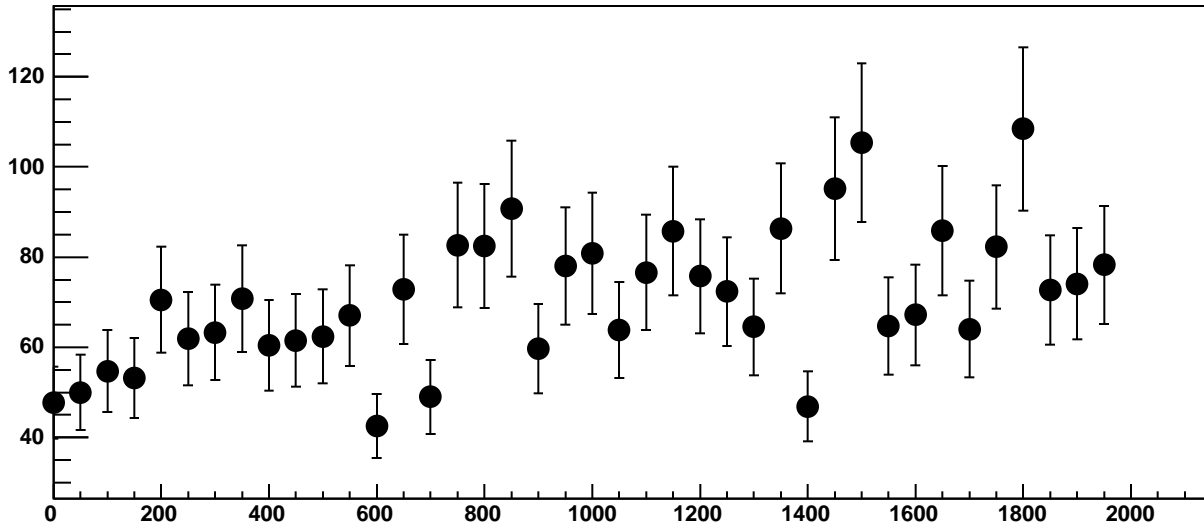
p0

$-1104 \pm 26.37$

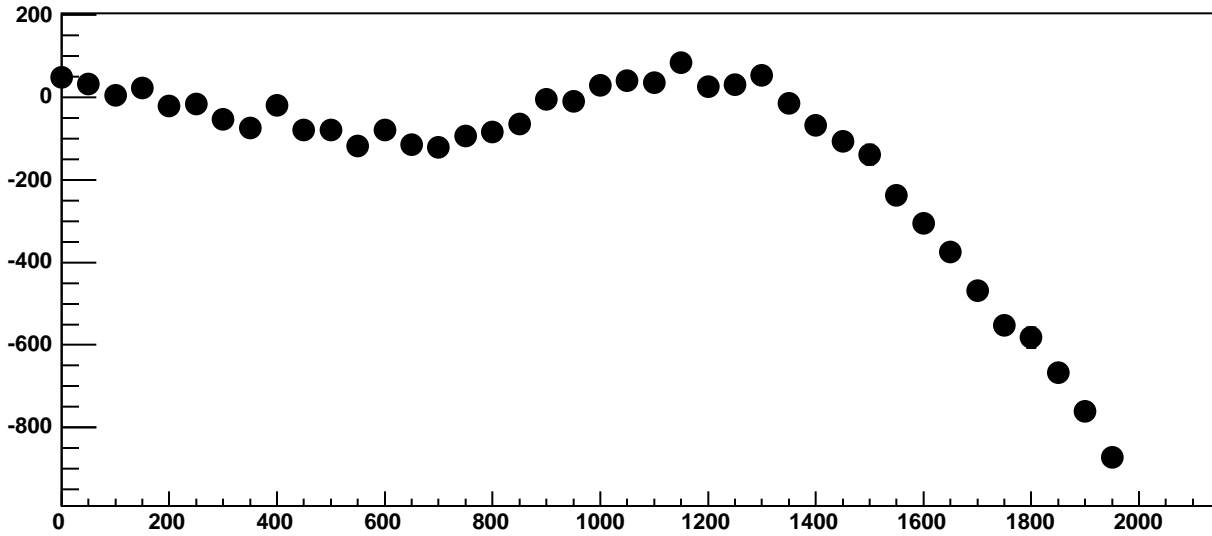
p1

$0.2768 \pm 0.02292$

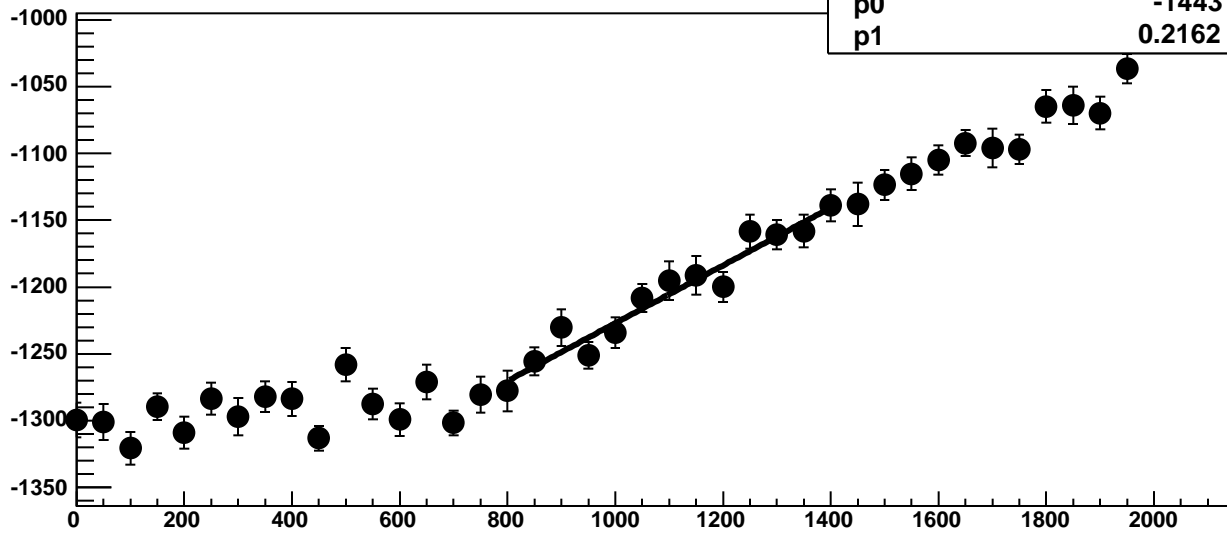
Chip 11, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC



Chip 11, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC

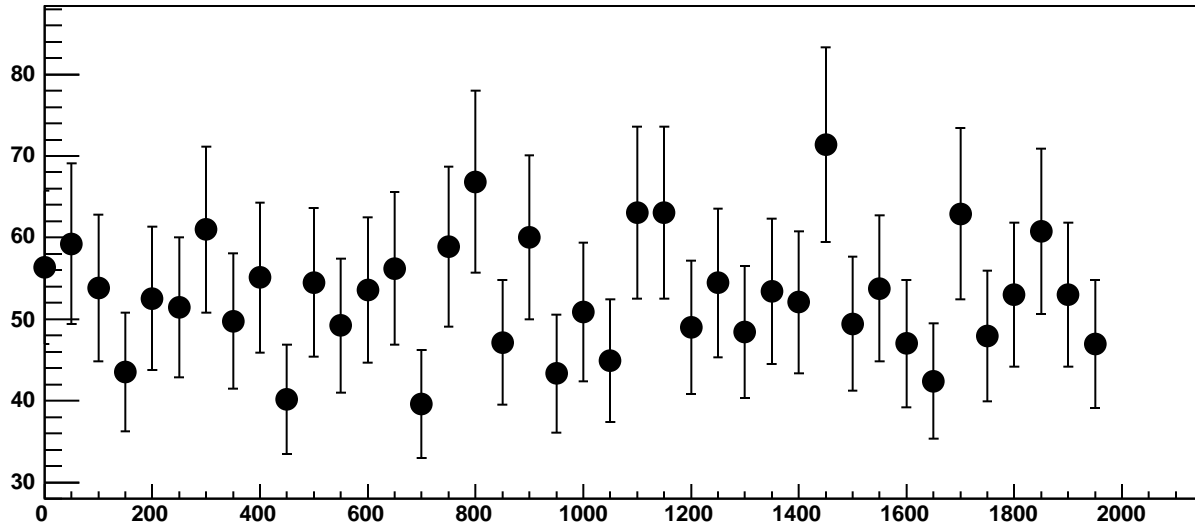


Chip 11, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC

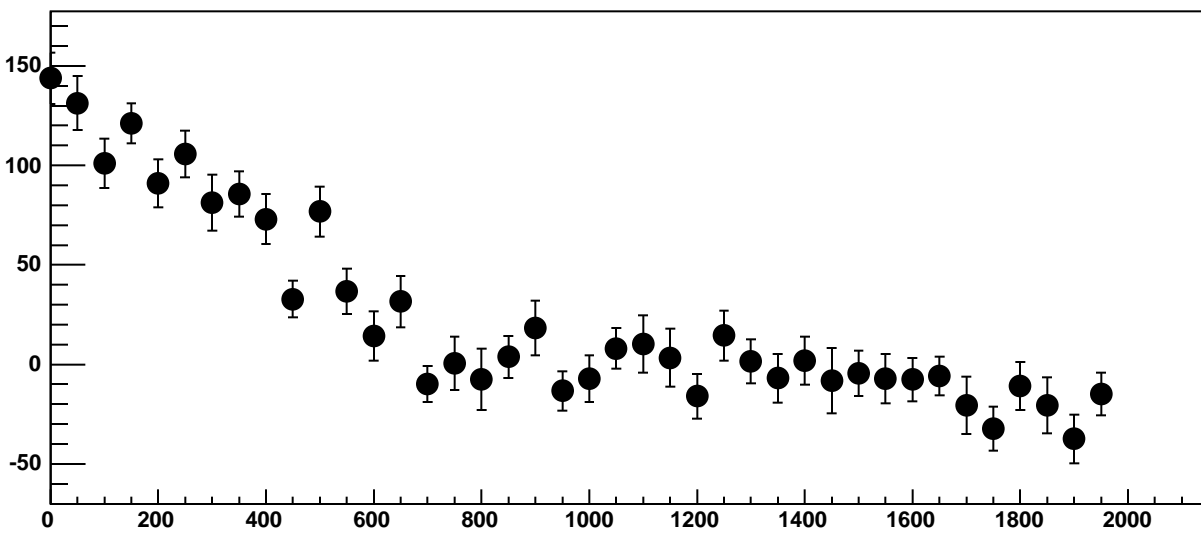


$\chi^2 / \text{ndf}$  9.175 / 11  
p0  $-1443 \pm 20.09$   
p1  $0.2162 \pm 0.018$

Chip 11, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC

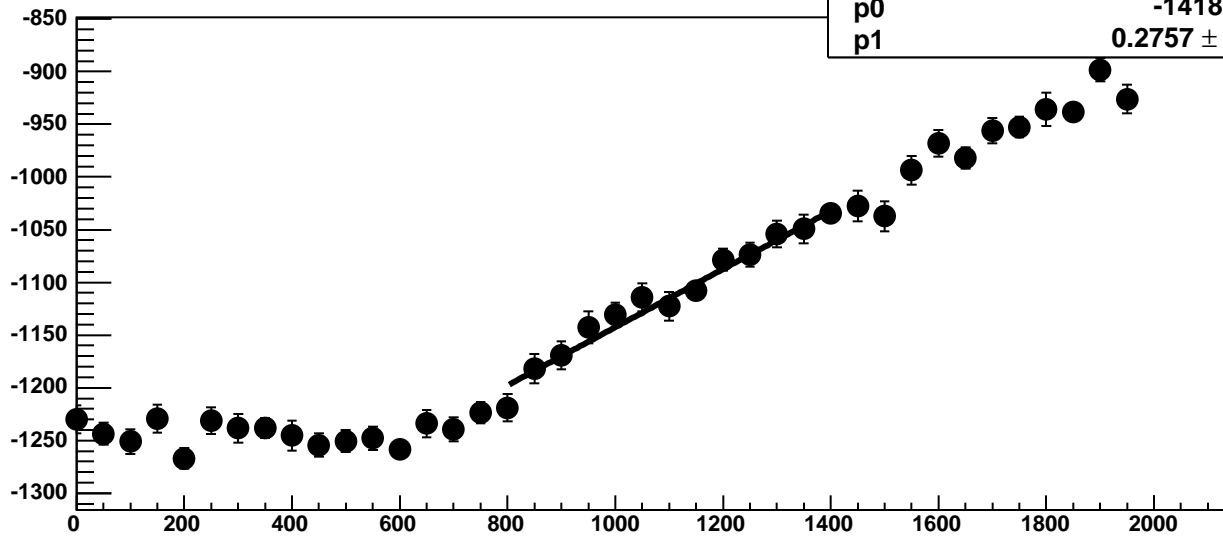


Chip 11, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC



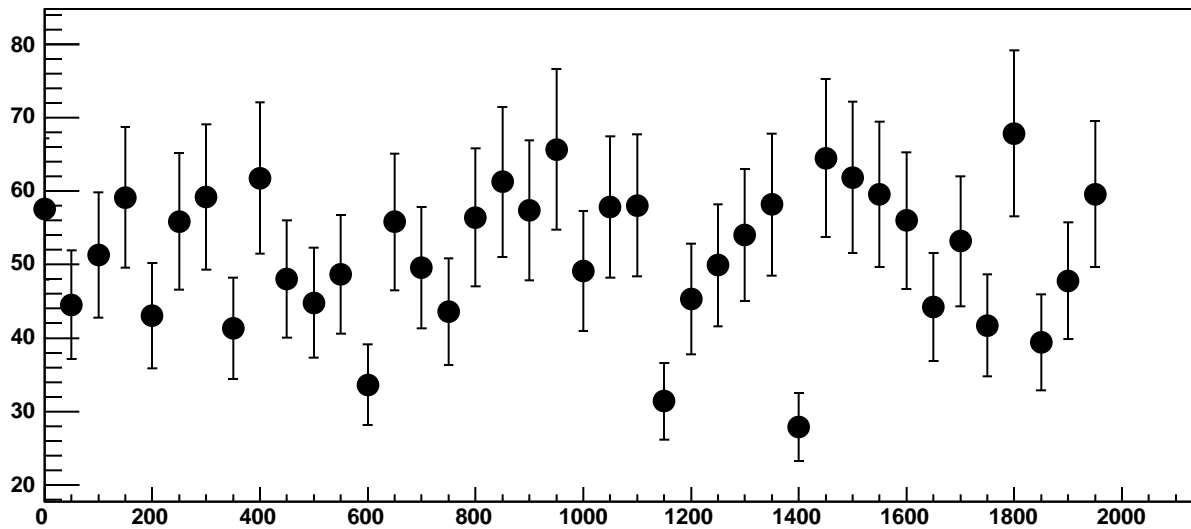


Chip 11, Channel 6, Enable 2, Hold=30, ADC Mean vs DAC

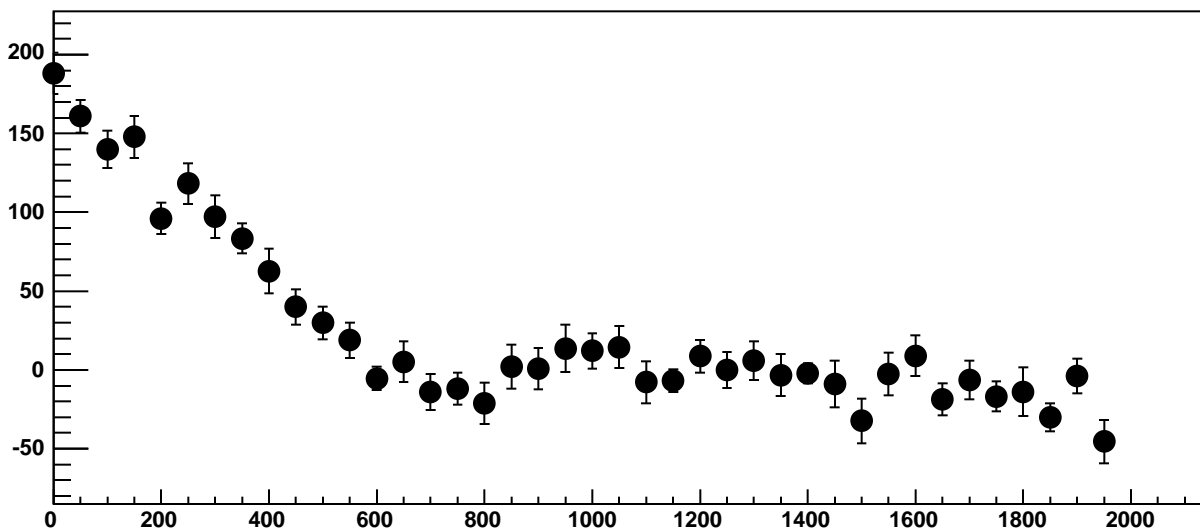


$\chi^2 / \text{ndf}$  8.183 / 11  
p0  $-1418 \pm 18.75$   
p1  $0.2757 \pm 0.01591$

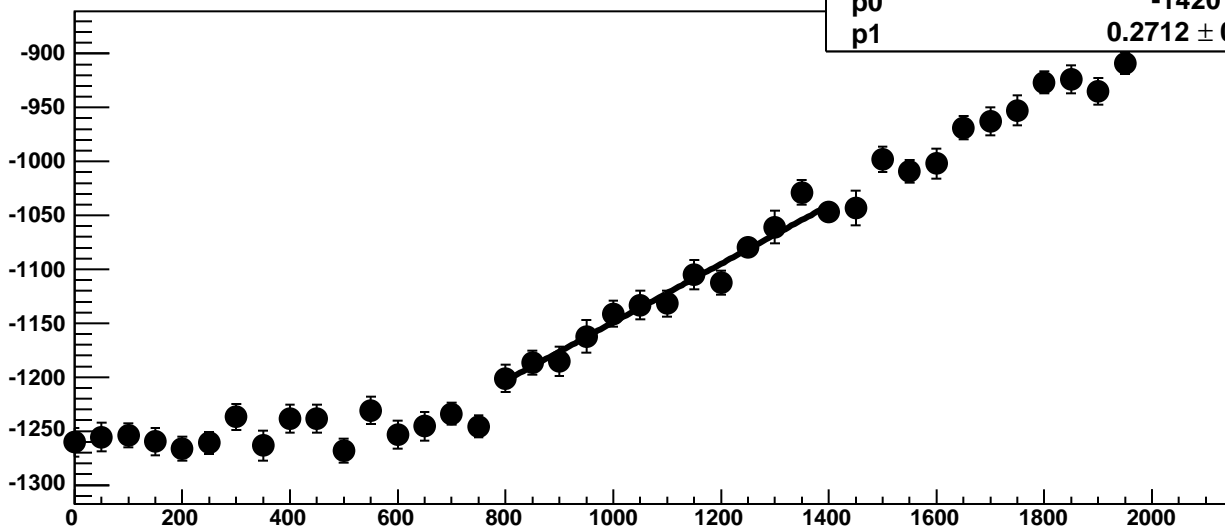
Chip 11, Channel 6, Enable 2, Hold=30, ADC Noise vs DAC



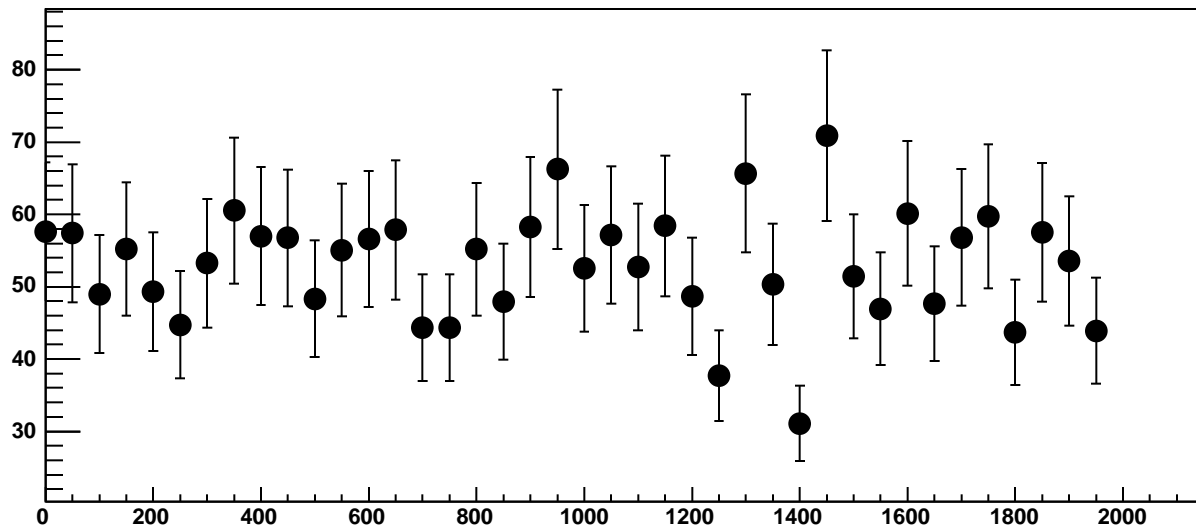
Chip 11, Channel 6, Enable 2, Hold=30, ADC Residuals vs DAC



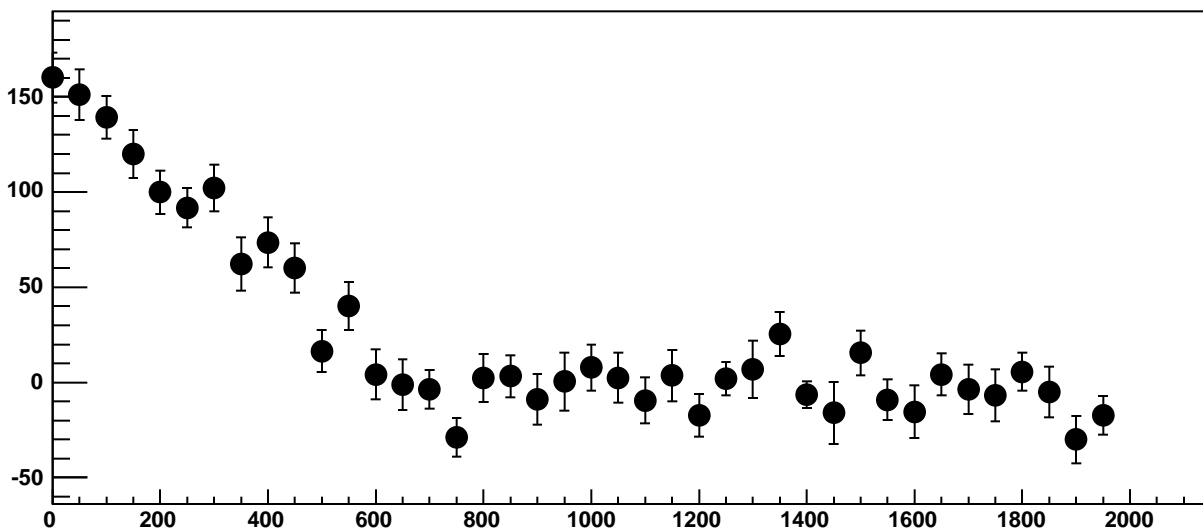
Chip 11, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC



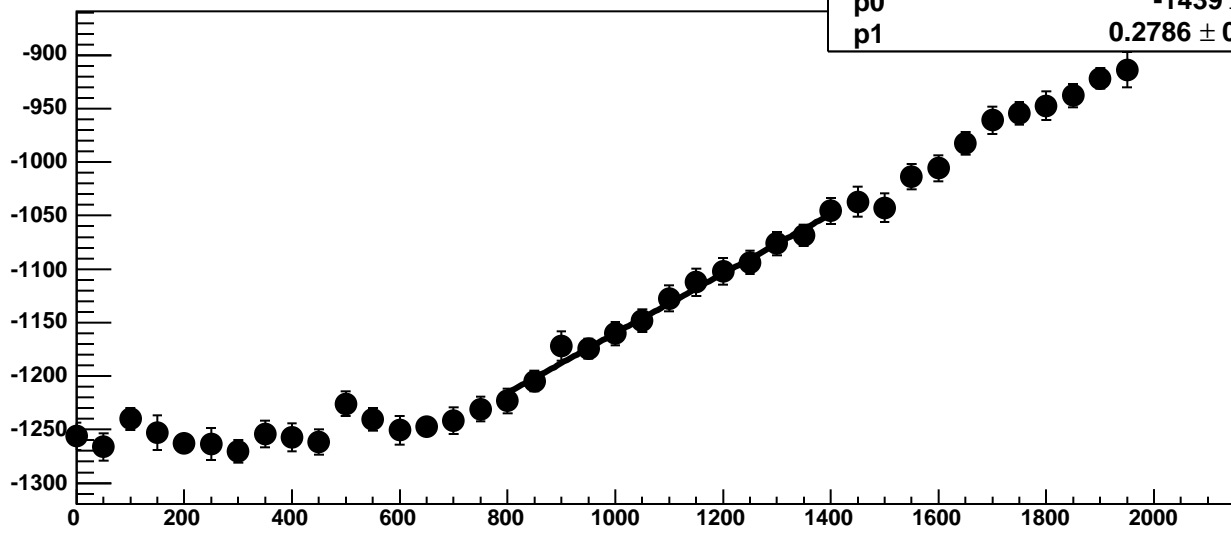
Chip 11, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC



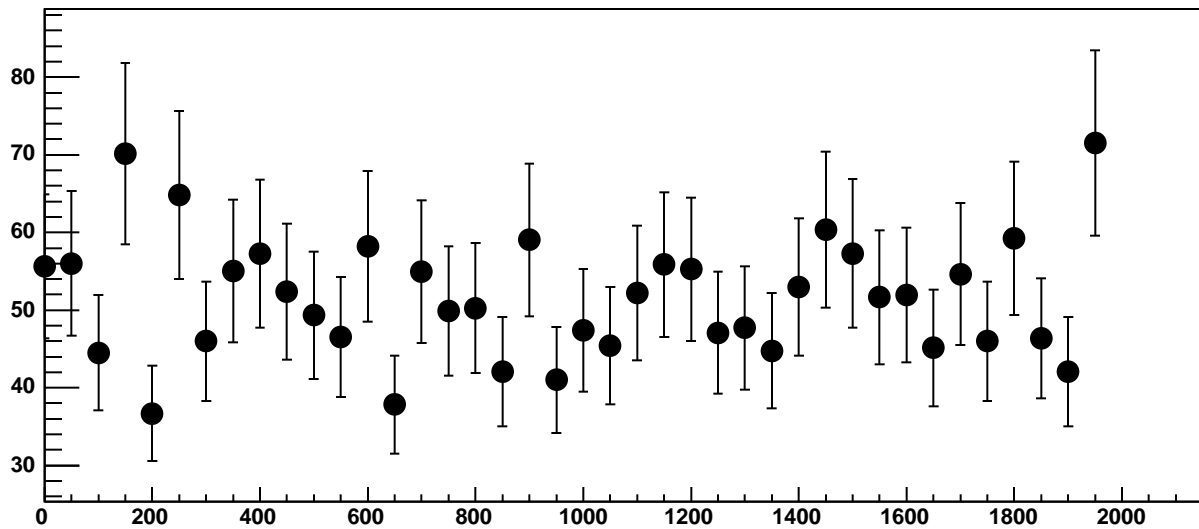
Chip 11, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC



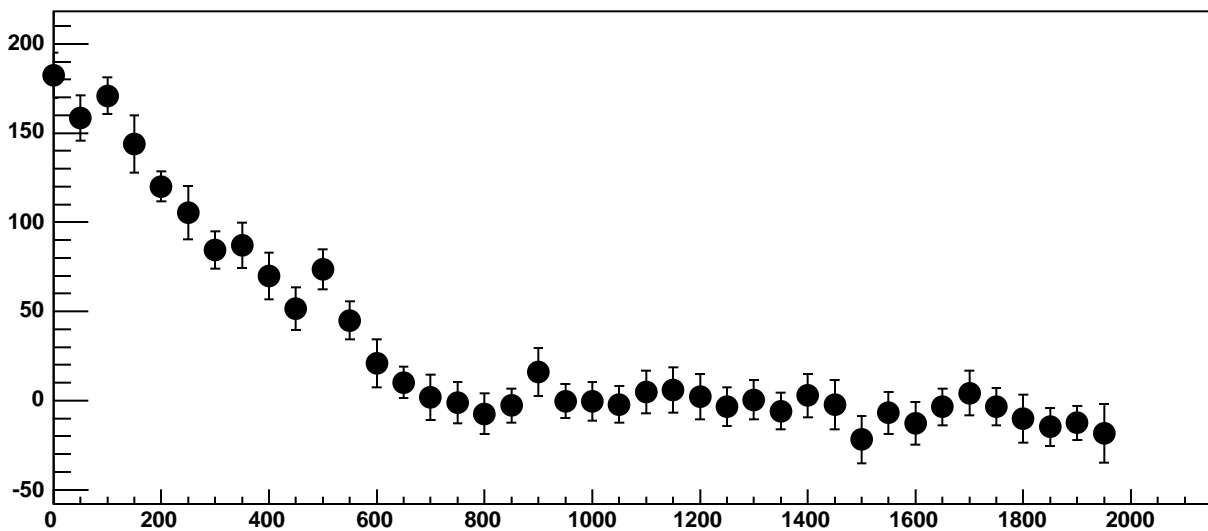
Chip 11, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC



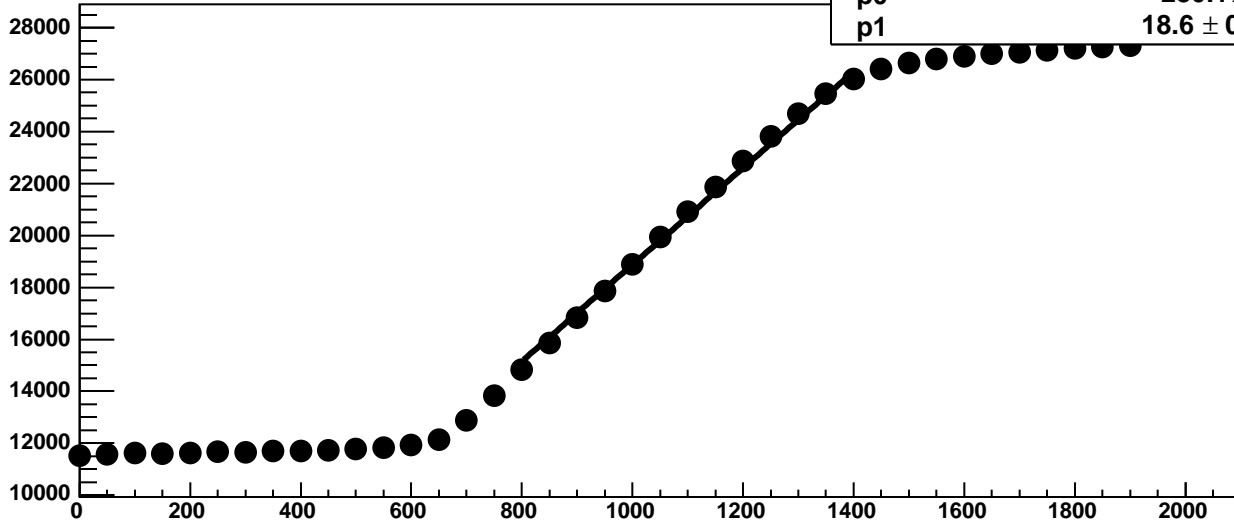
Chip 11, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC



Chip 11, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC



Chip 11, Channel 6, Enable 5!, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

4197 / 11

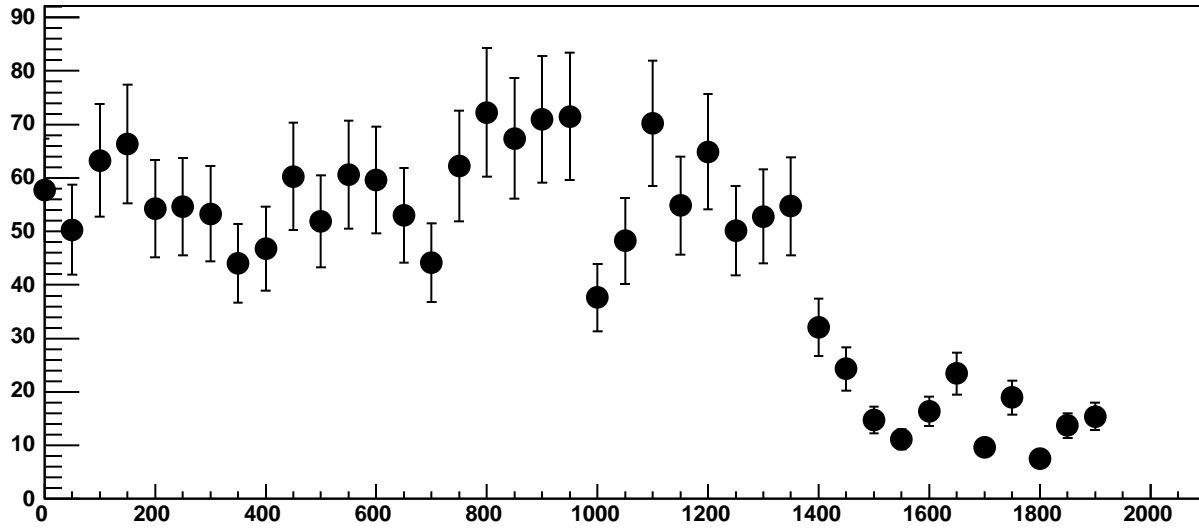
p0

$280.1 \pm 20.59$

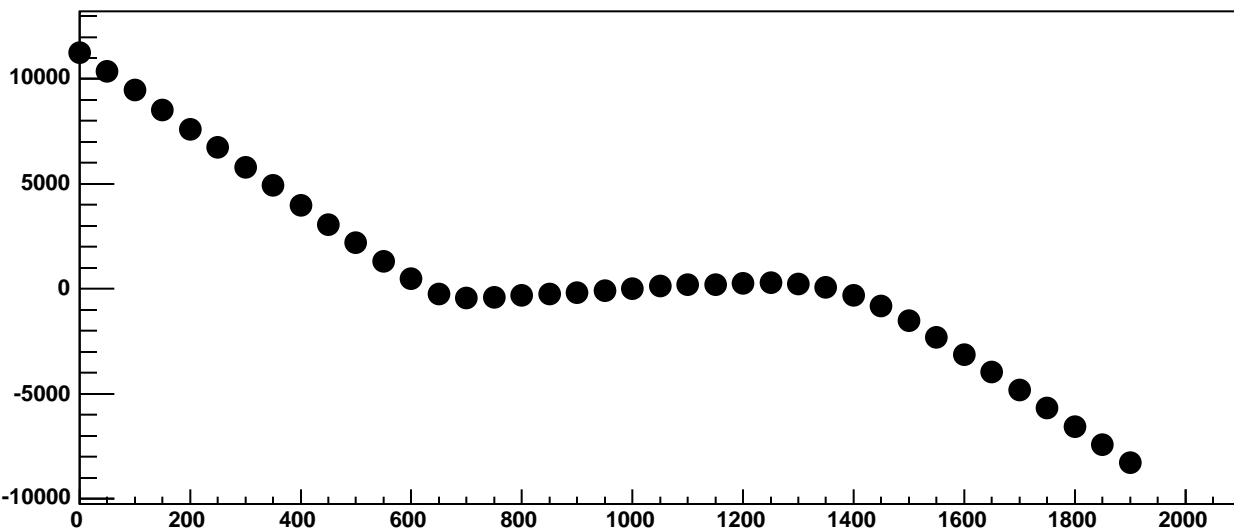
p1

$18.6 \pm 0.01756$

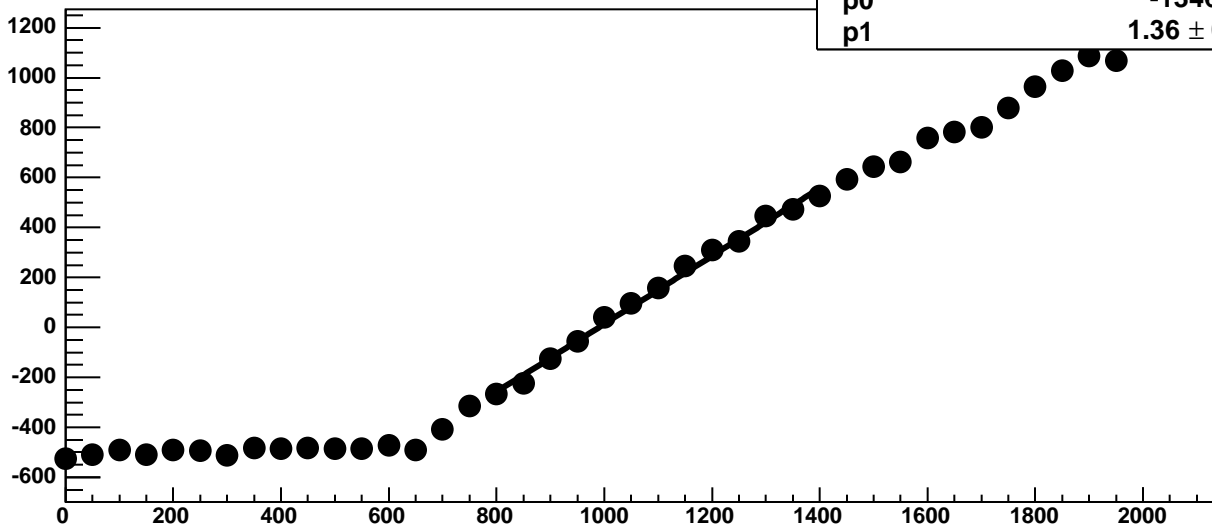
Chip 11, Channel 6, Enable 5!, Hold=30, ADC Noise vs DAC



Chip 11, Channel 6, Enable 5!, Hold=30, ADC Residuals vs DAC

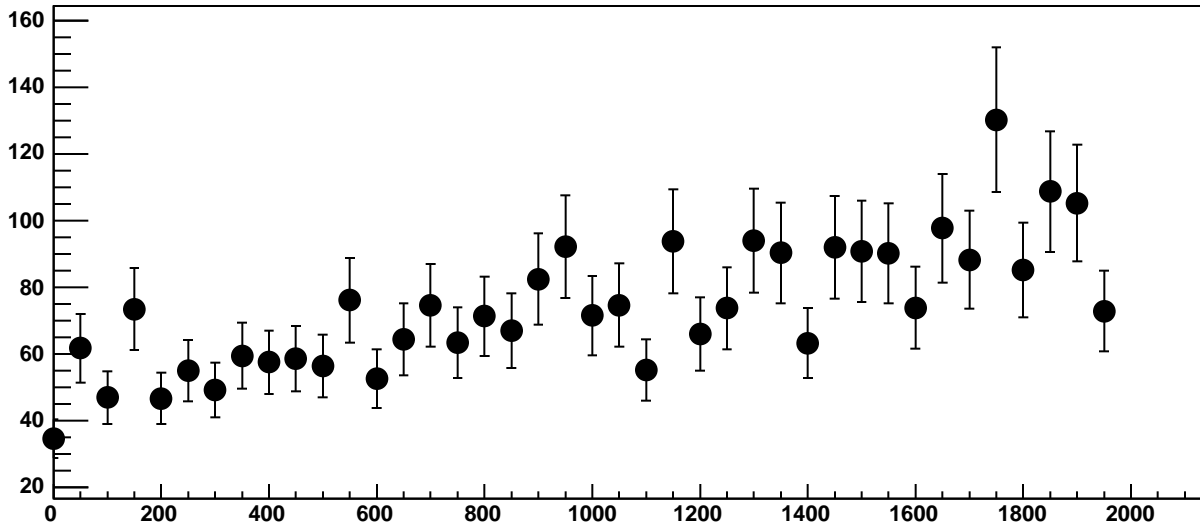


Chip 11, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC

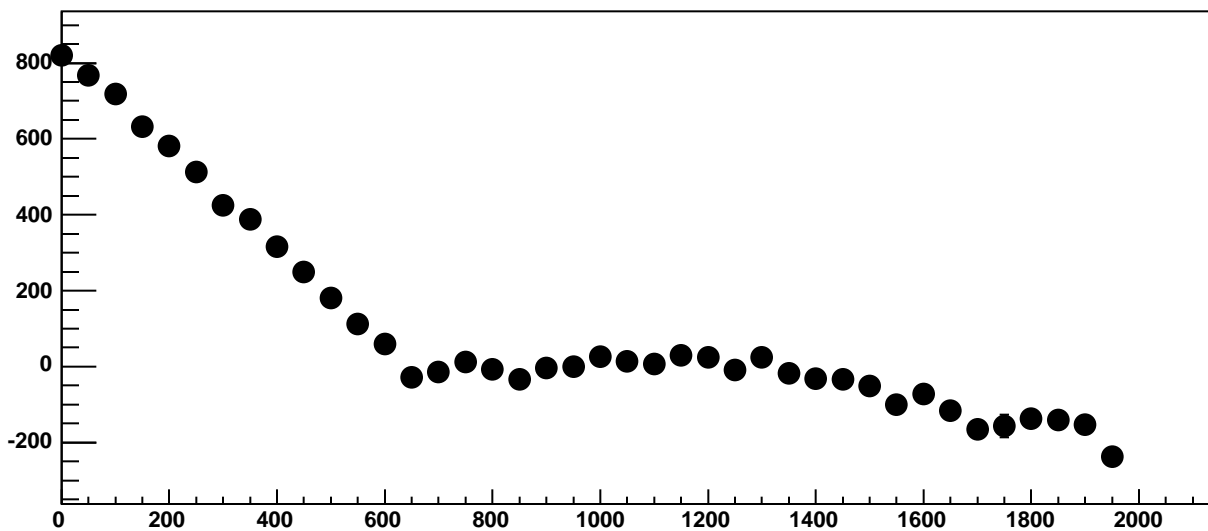


$\chi^2 / \text{ndf}$  20.15 / 11  
p0  $-1346 \pm 27.9$   
p1  $1.36 \pm 0.02506$

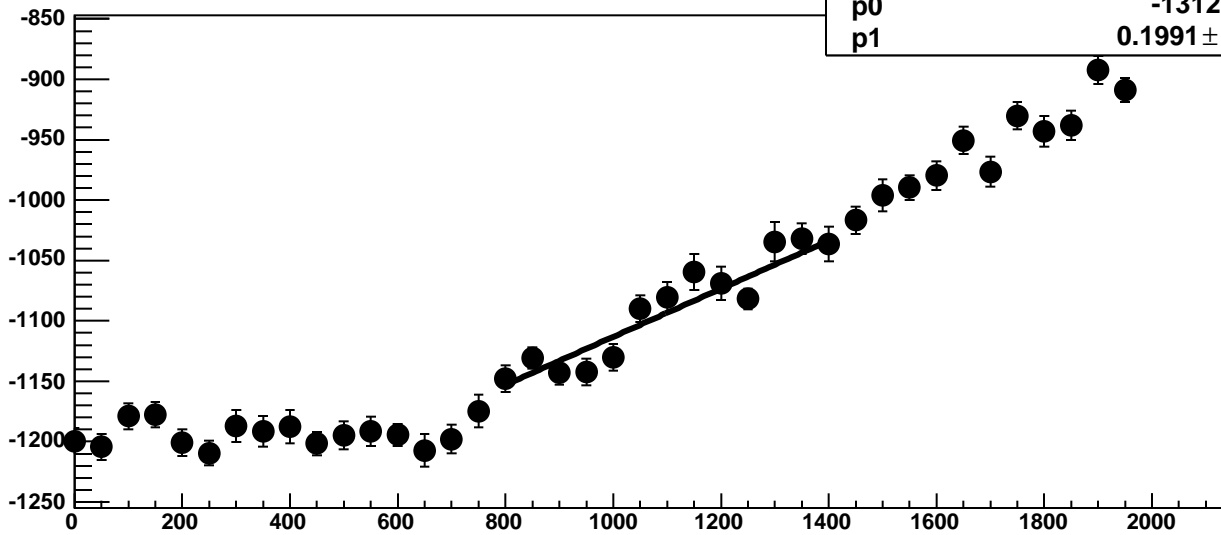
Chip 11, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC



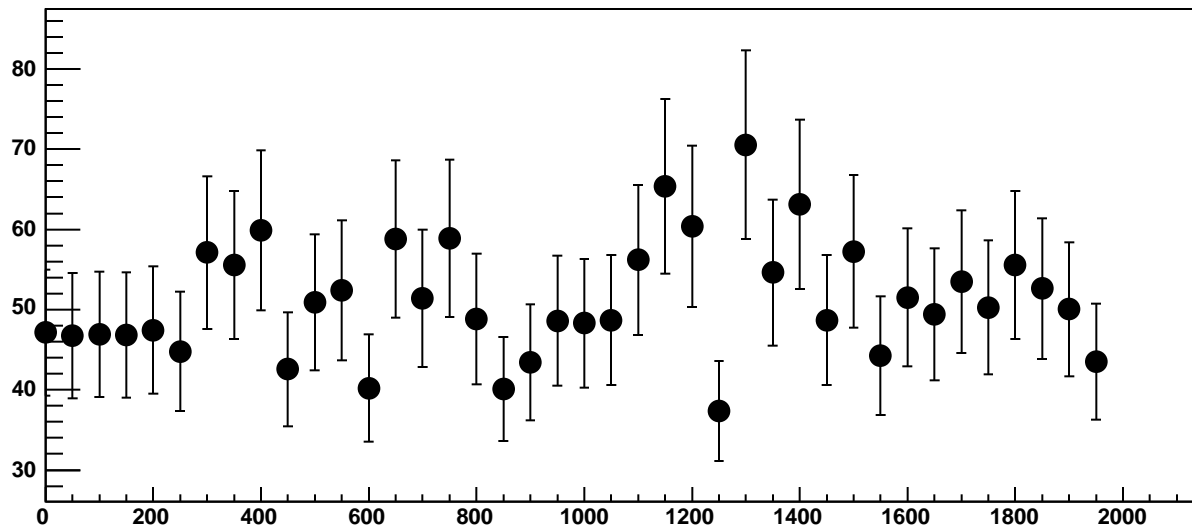
Chip 11, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC



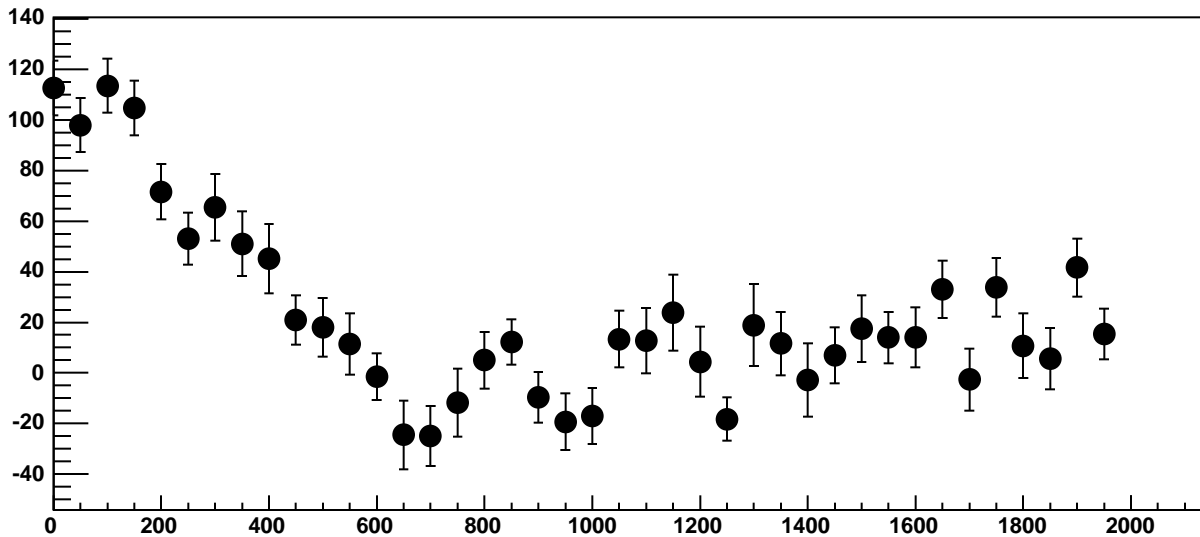
Chip 11, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC



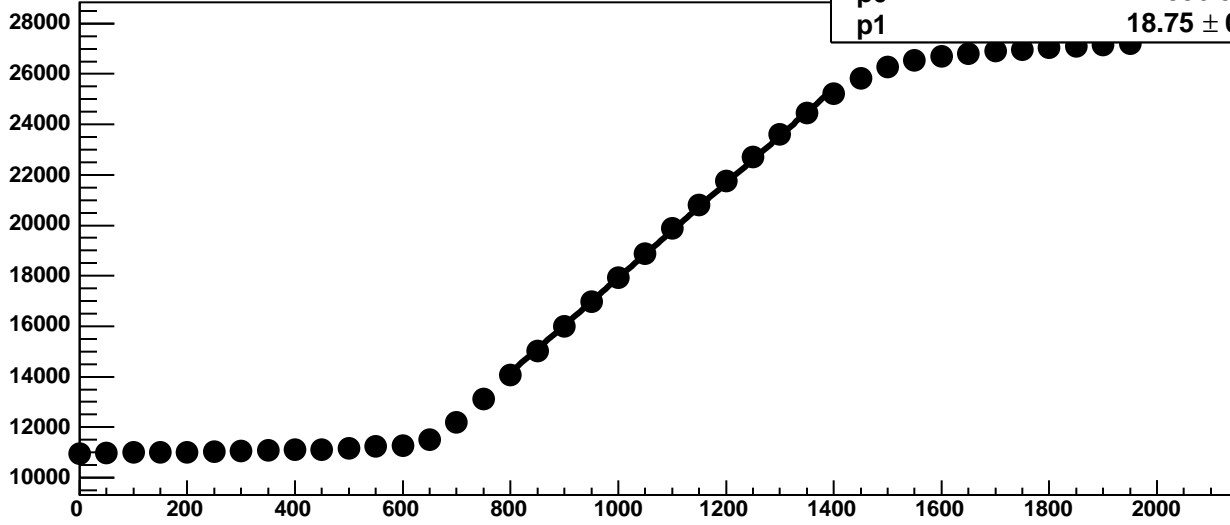
Chip 11, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC



Chip 11, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC

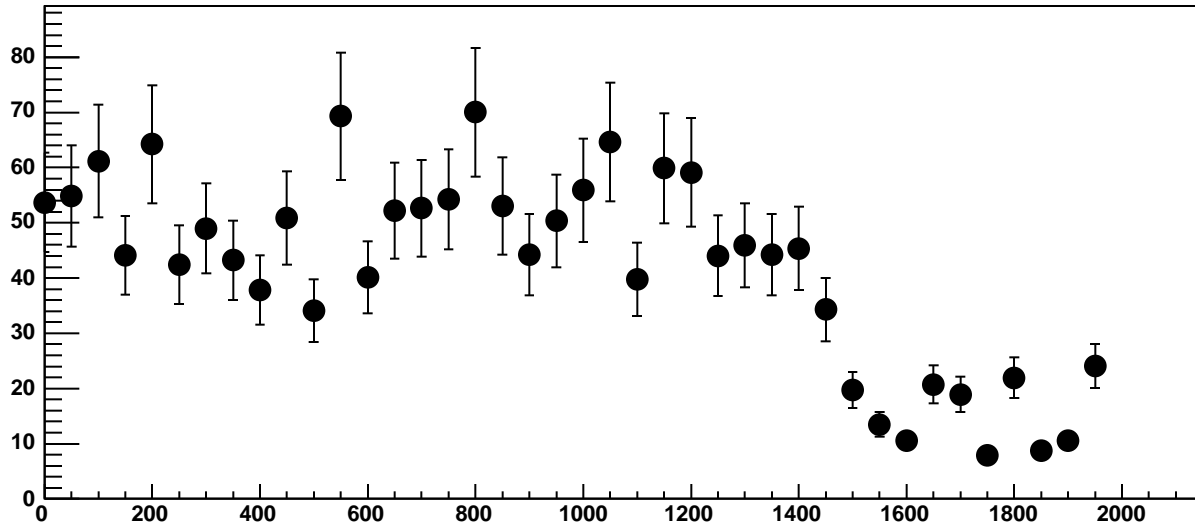


Chip 11, Channel 7, Enable 2!, Hold=30, ADC Mean vs DAC

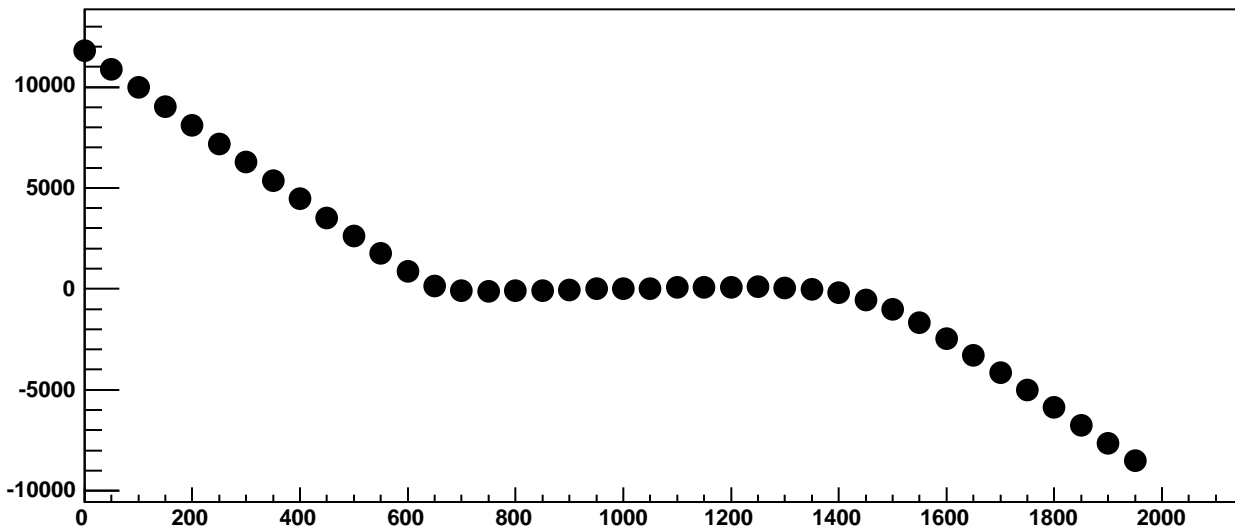


$\chi^2 / \text{ndf}$  752.7 / 11  
p0  $-836.5 \pm 19.5$   
p1  $18.75 \pm 0.01709$

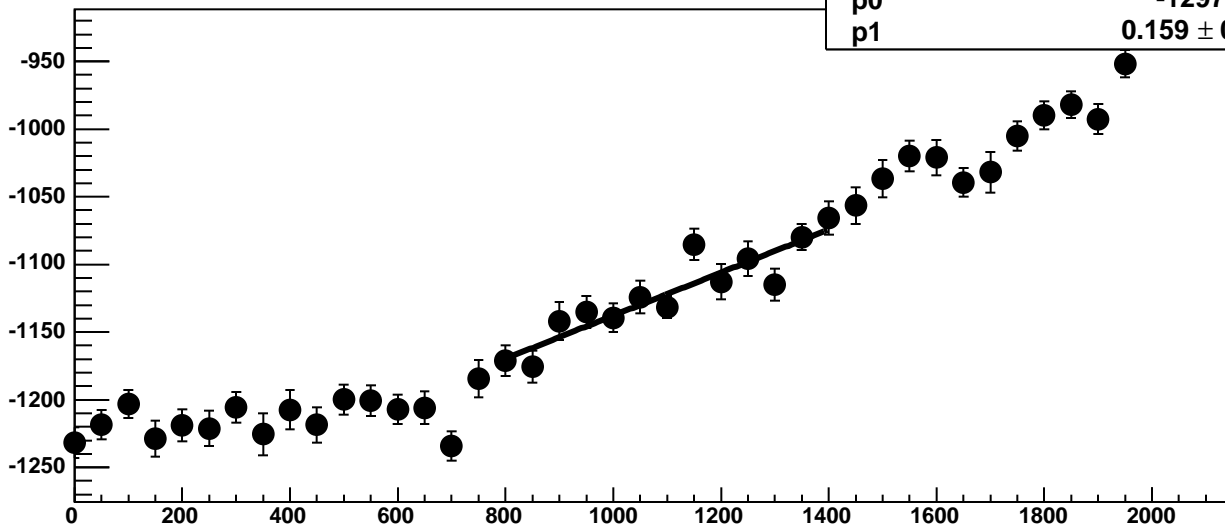
Chip 11, Channel 7, Enable 2!, Hold=30, ADC Noise vs DAC



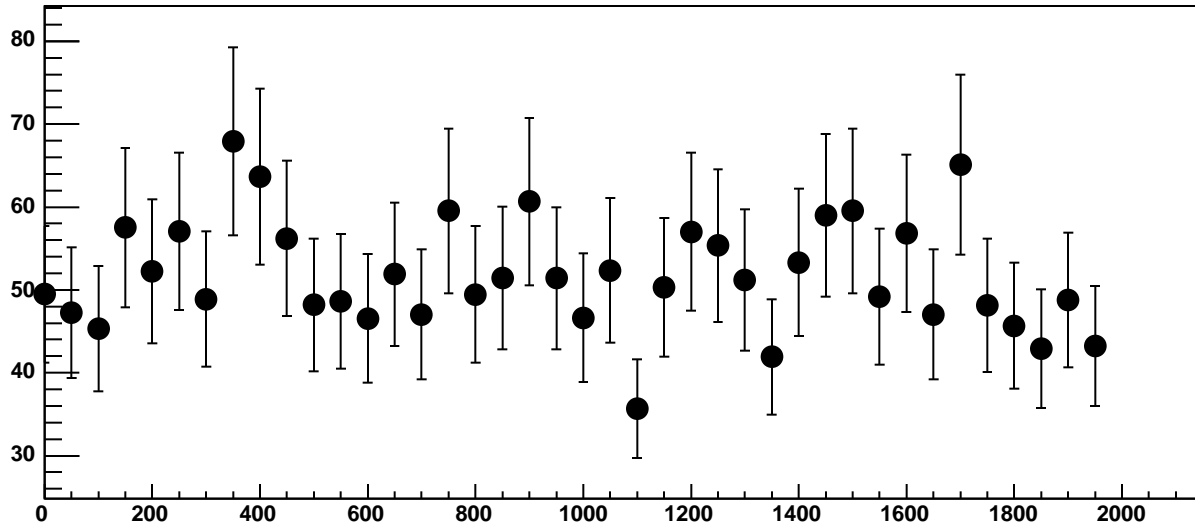
Chip 11, Channel 7, Enable 2!, Hold=30, ADC Residuals vs DAC



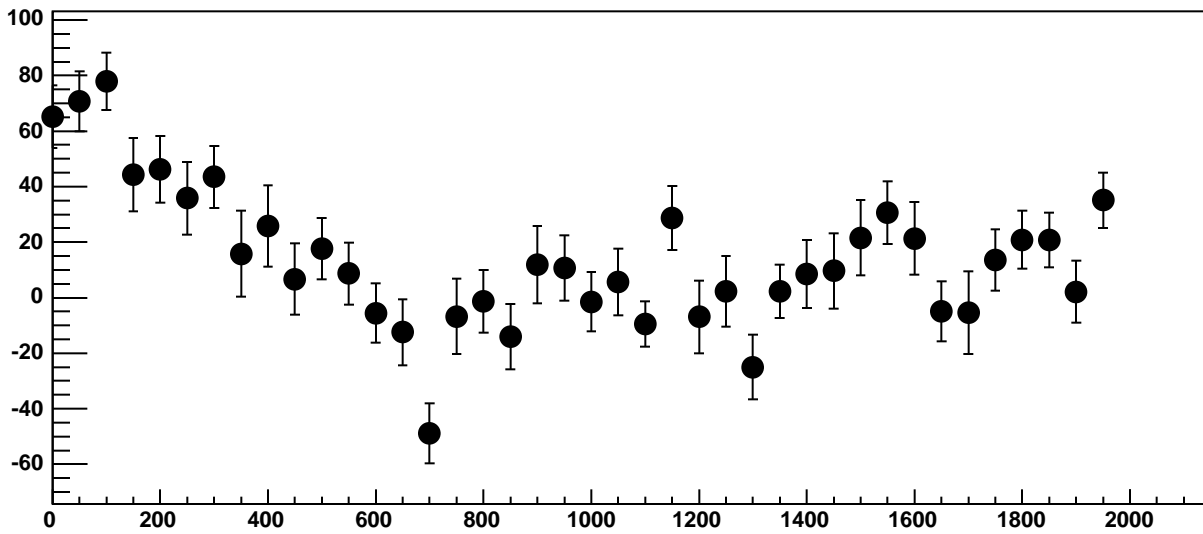
Chip 11, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC



Chip 11, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC

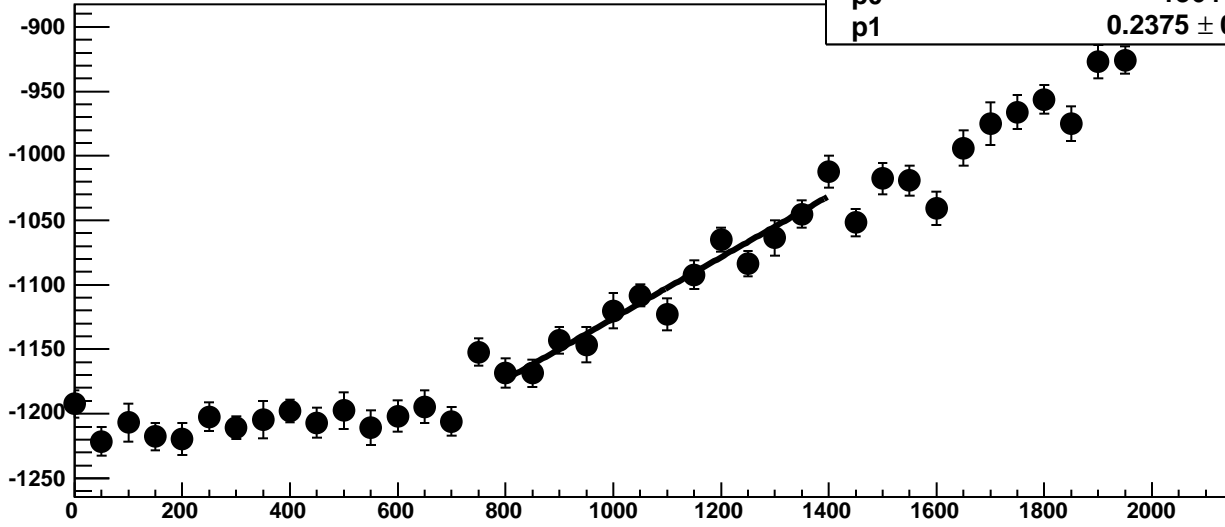


Chip 11, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC

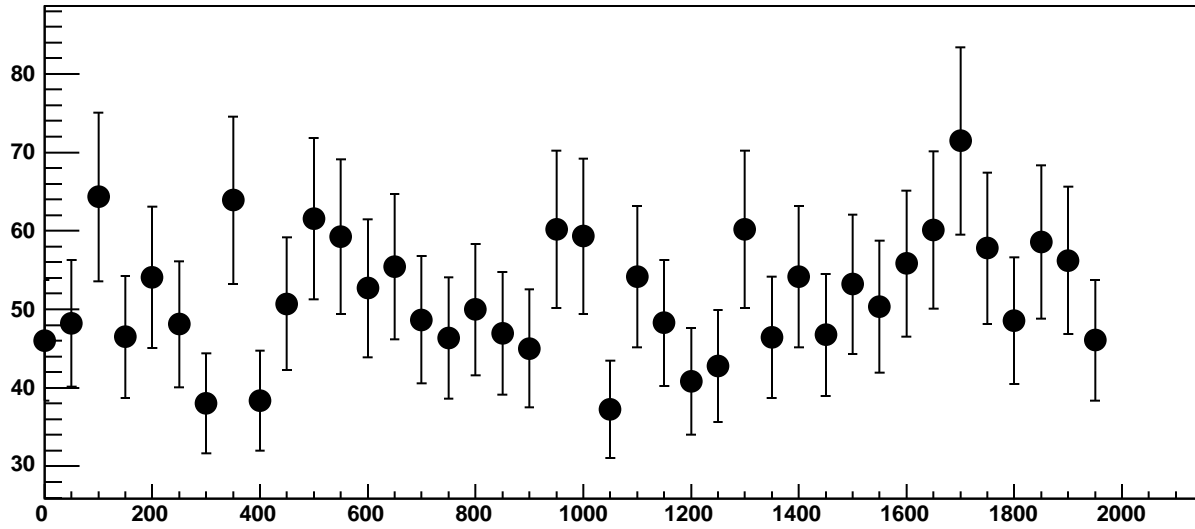




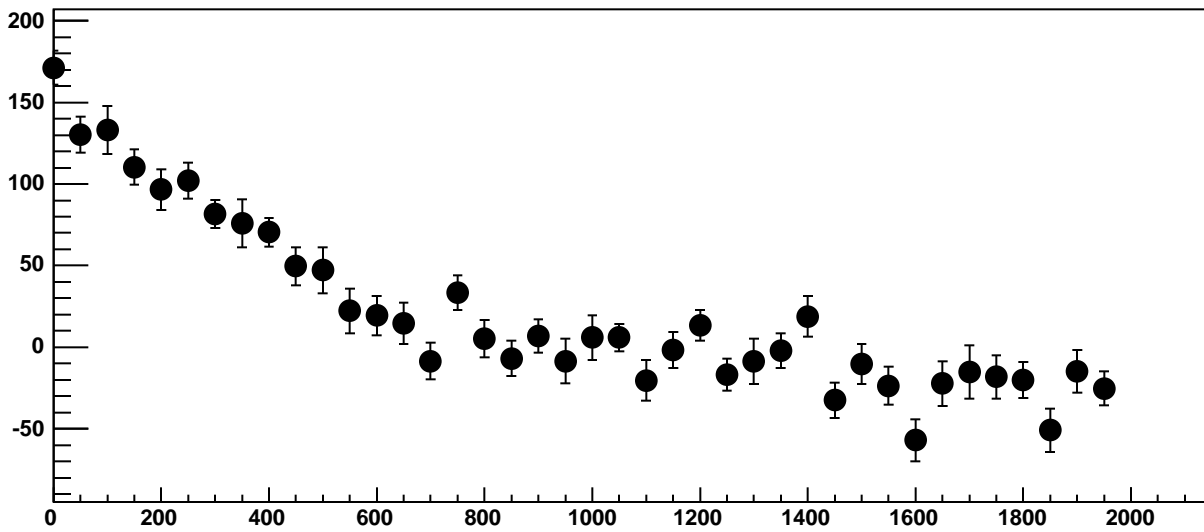
Chip 11, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC



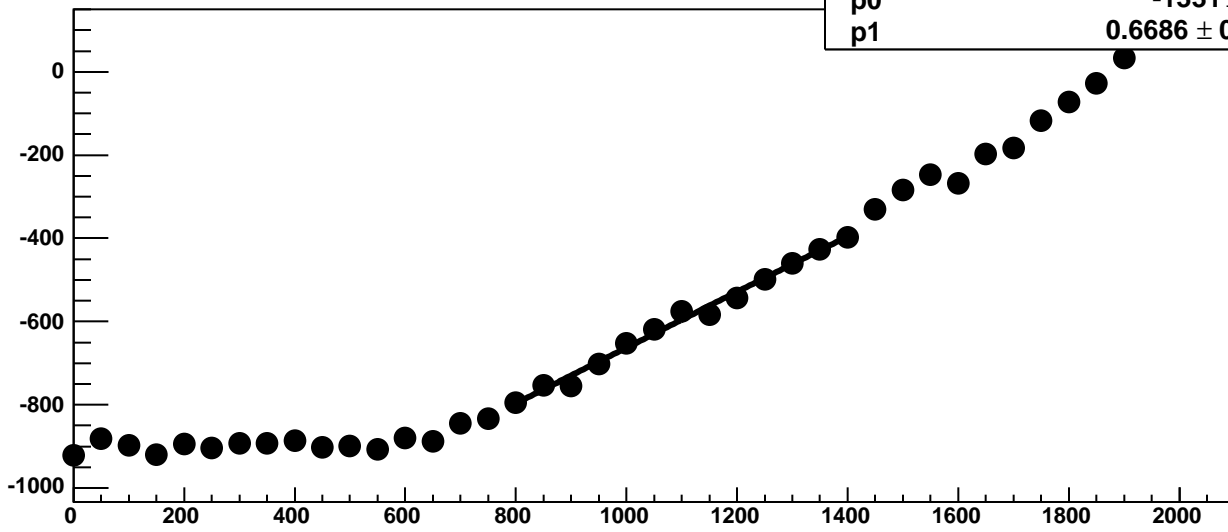
Chip 11, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC



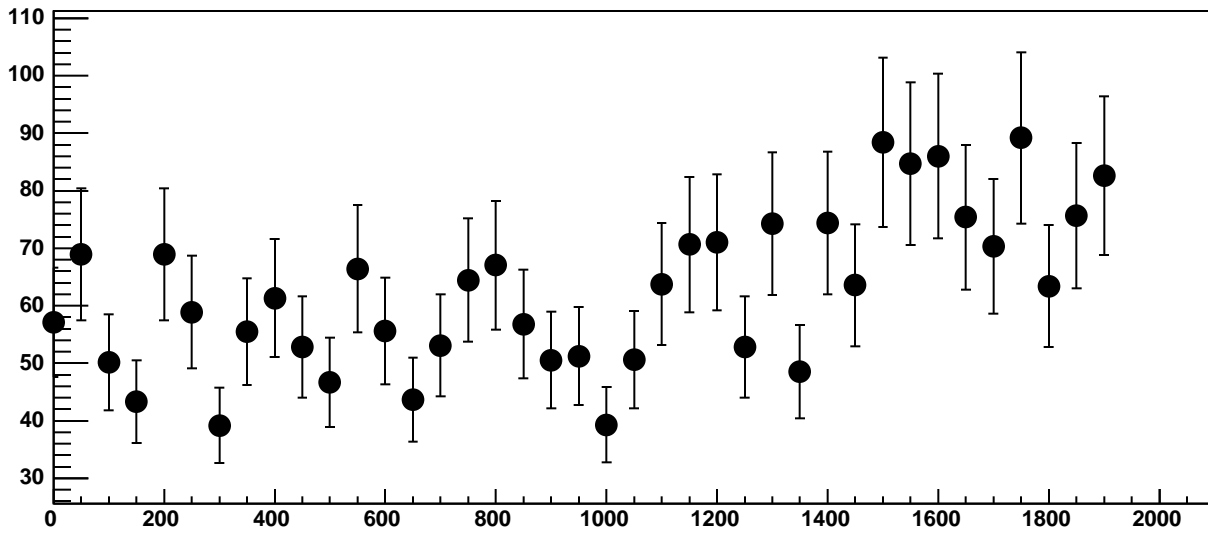
Chip 11, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC



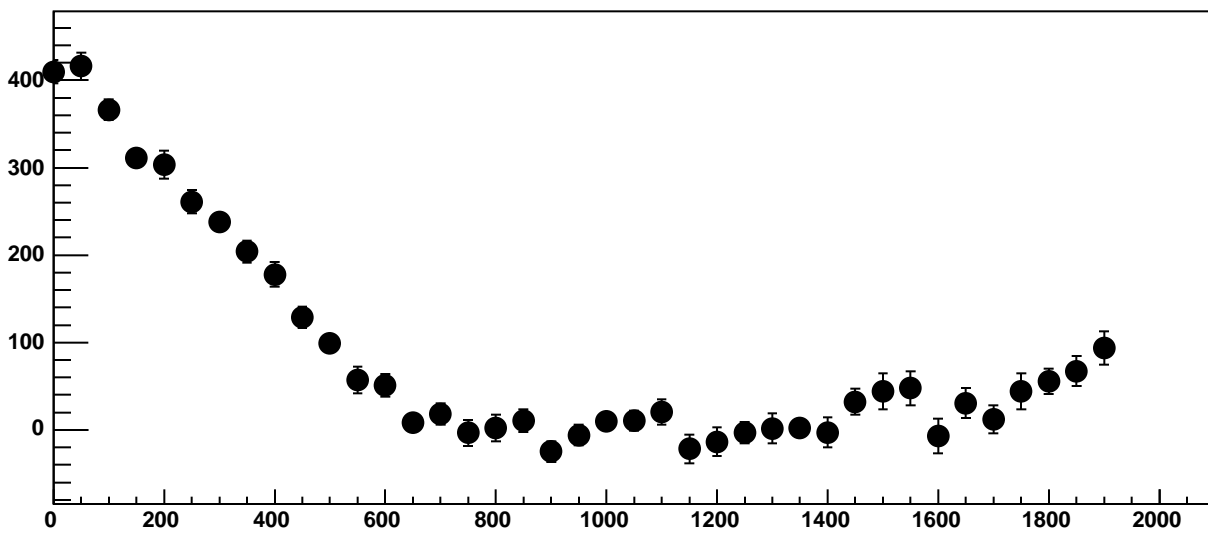
Chip 11, Channel 7, Enable 5, Hold=30, ADC Mean vs DAC



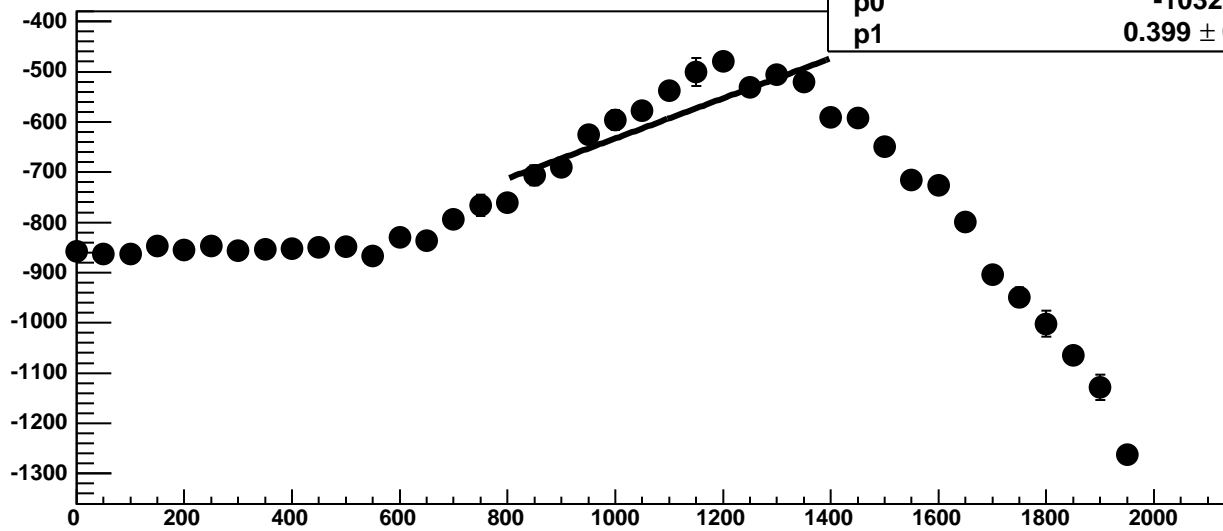
Chip 11, Channel 7, Enable 5, Hold=30, ADC Noise vs DAC



Chip 11, Channel 7, Enable 5, Hold=30, ADC Residuals vs DAC



Chip 11, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

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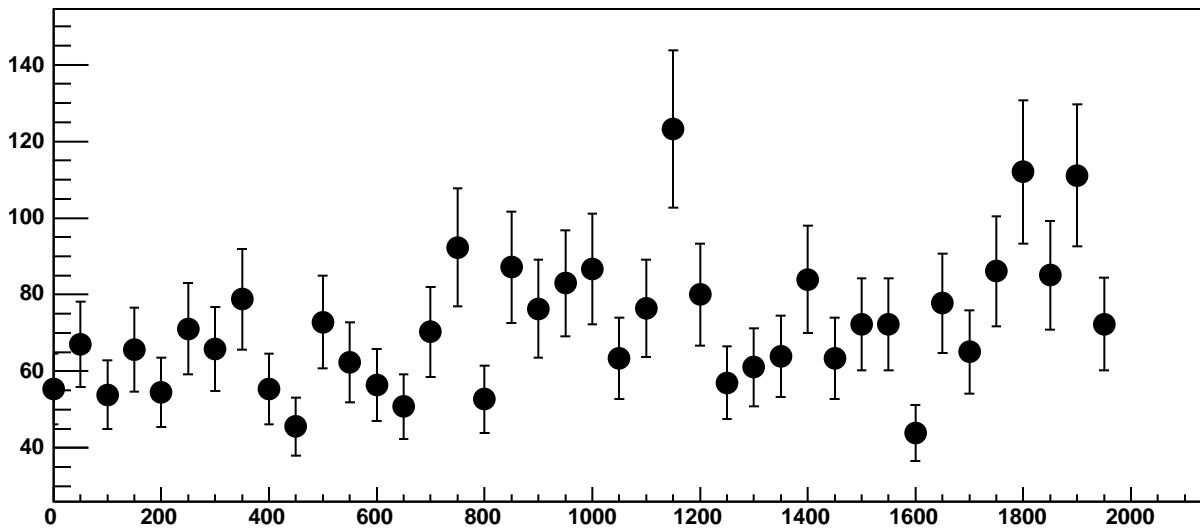
p0

$-1032 \pm 25.63$

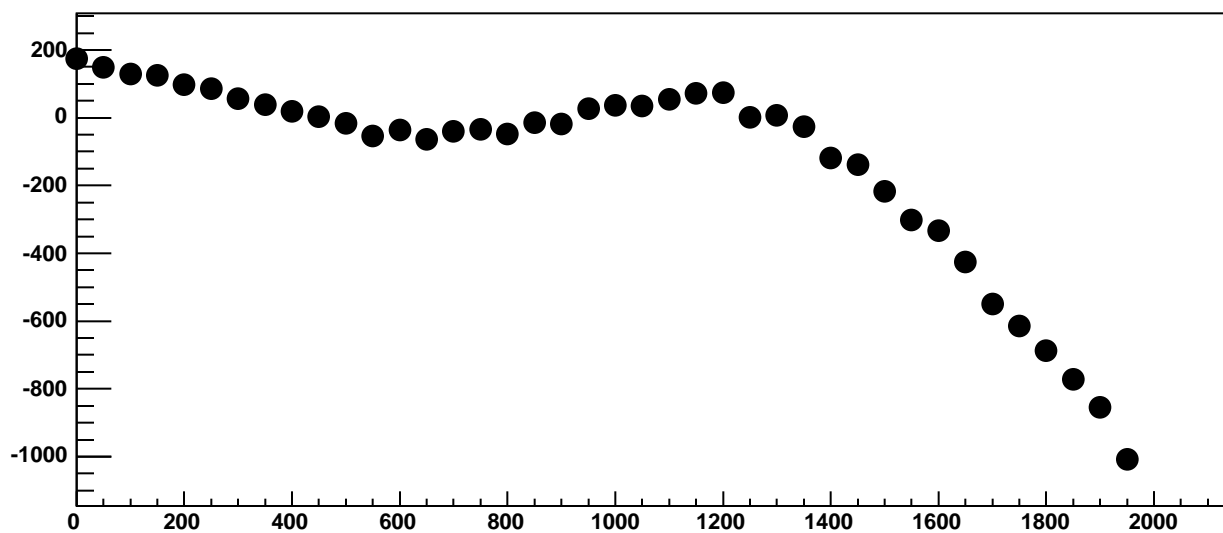
p1

$0.399 \pm 0.02293$

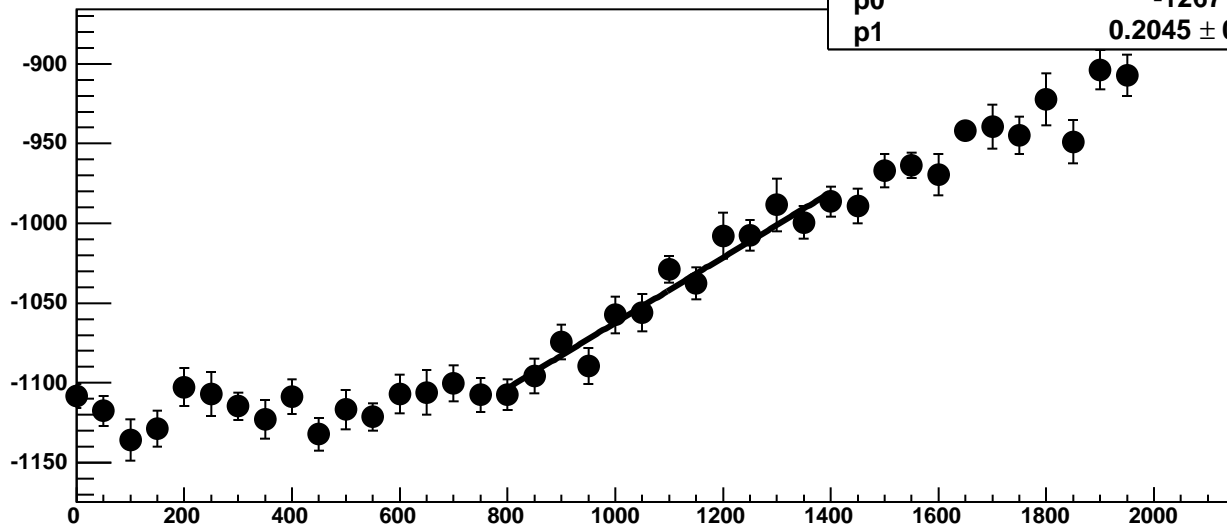
Chip 11, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC



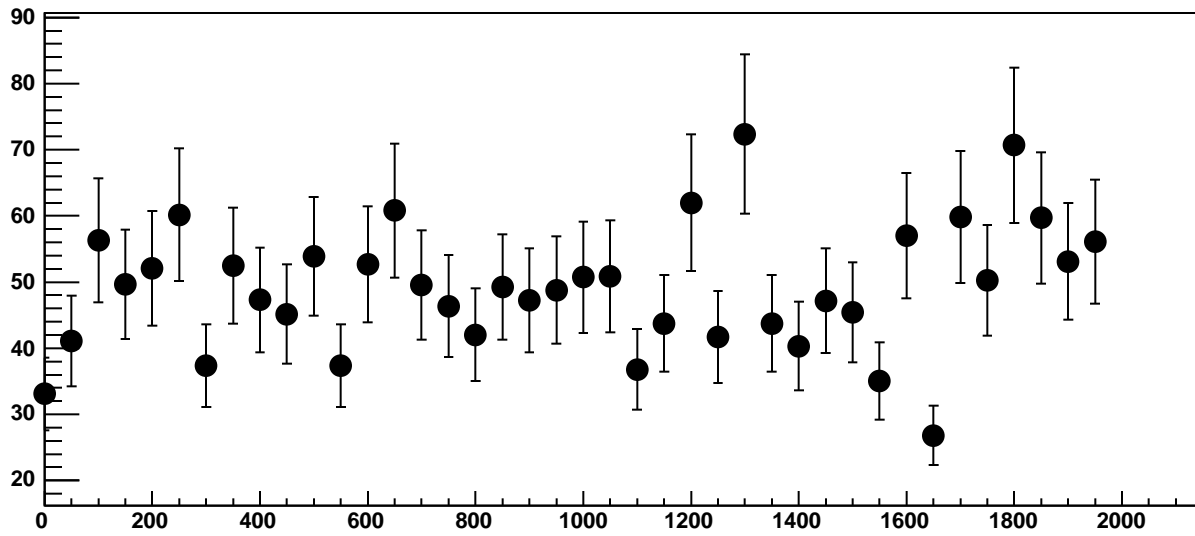
Chip 11, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC



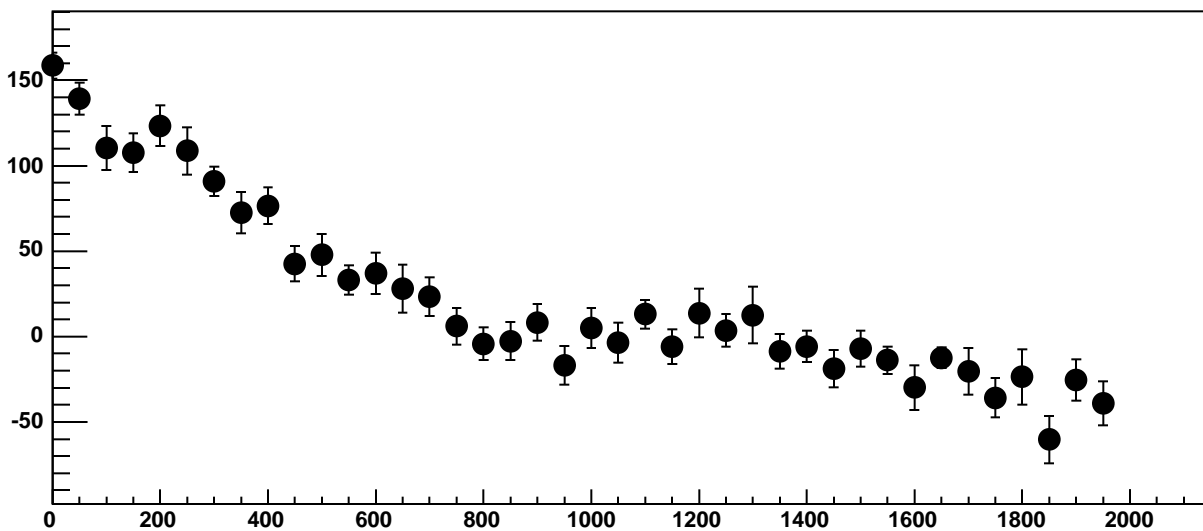
Chip 11, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC



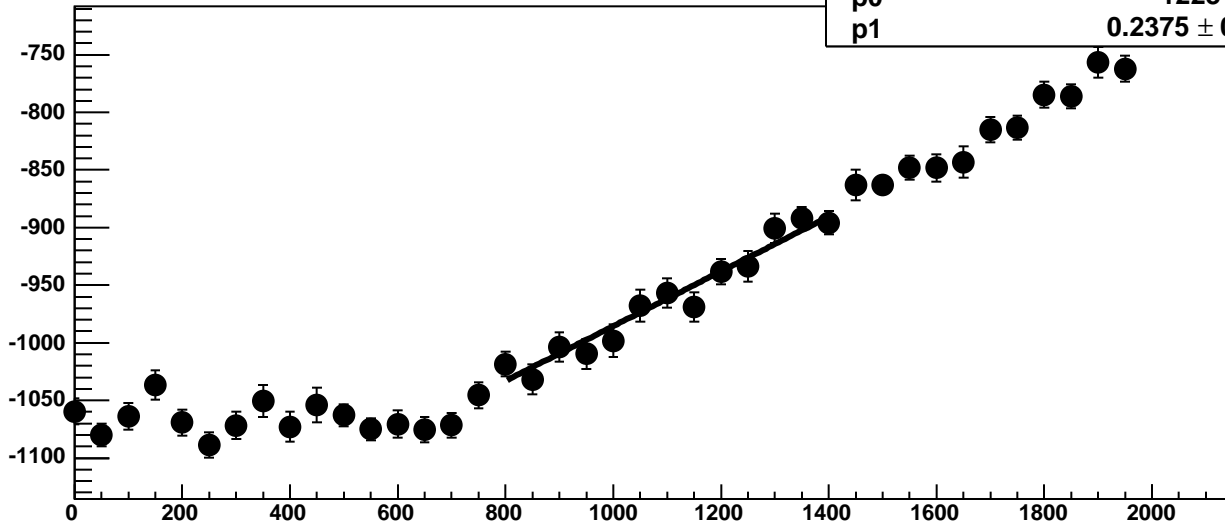
Chip 11, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC



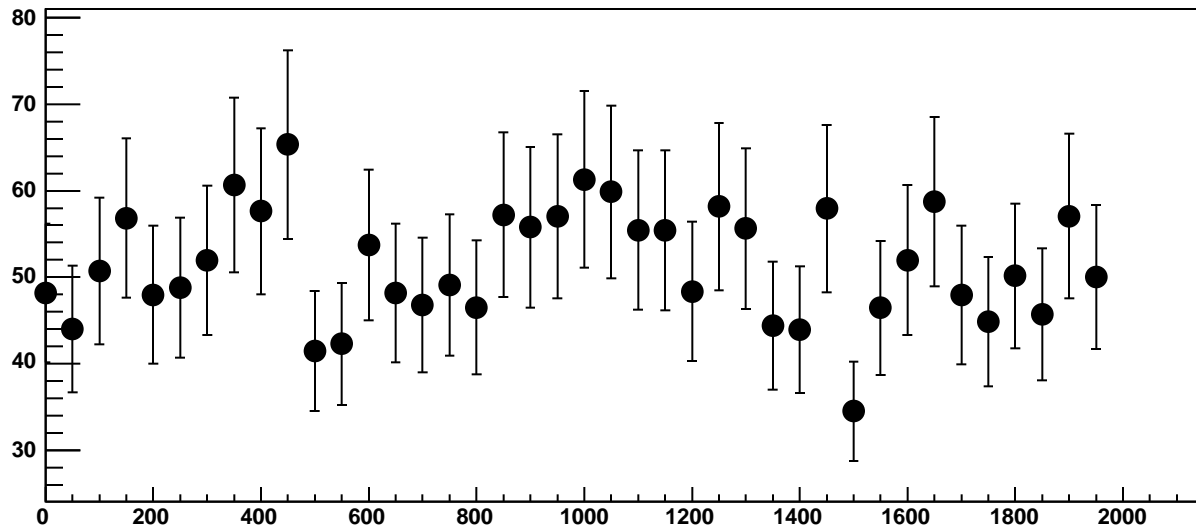
Chip 11, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC



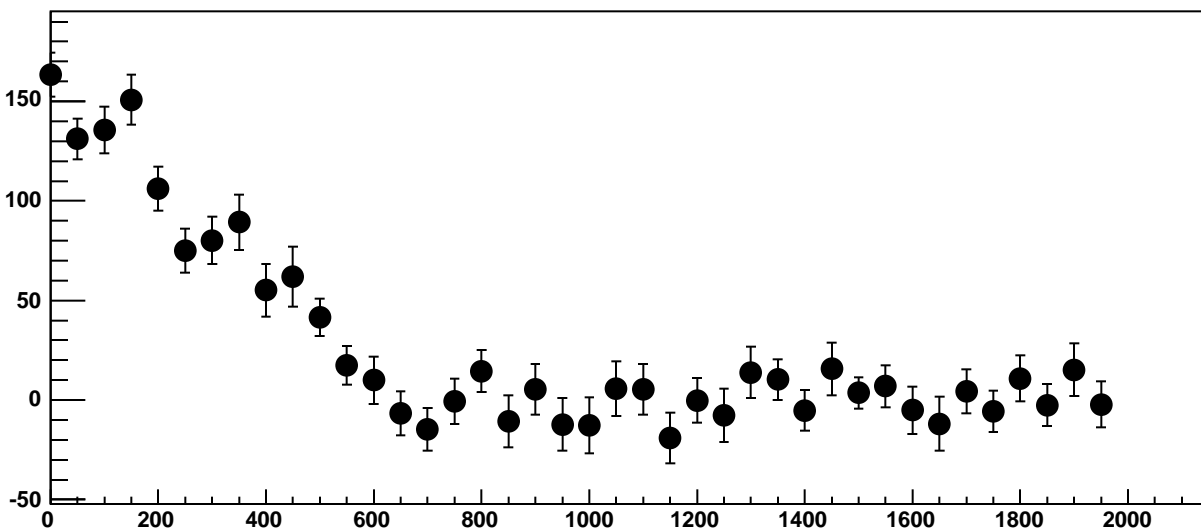
Chip 11, Channel 8, Enable 2, Hold=30, ADC Mean vs DAC



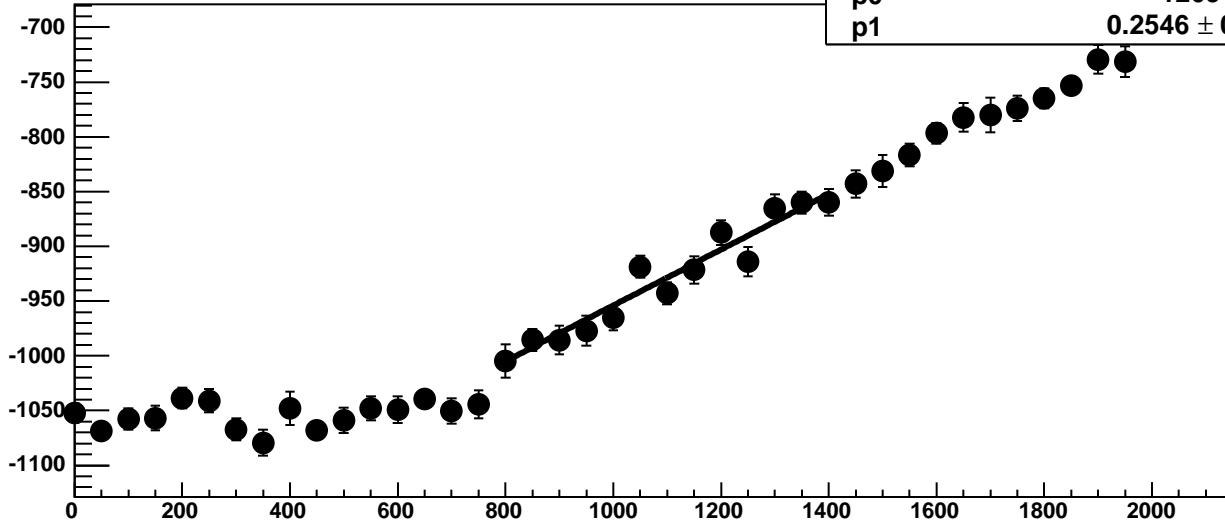
Chip 11, Channel 8, Enable 2, Hold=30, ADC Noise vs DAC



Chip 11, Channel 8, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 11, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

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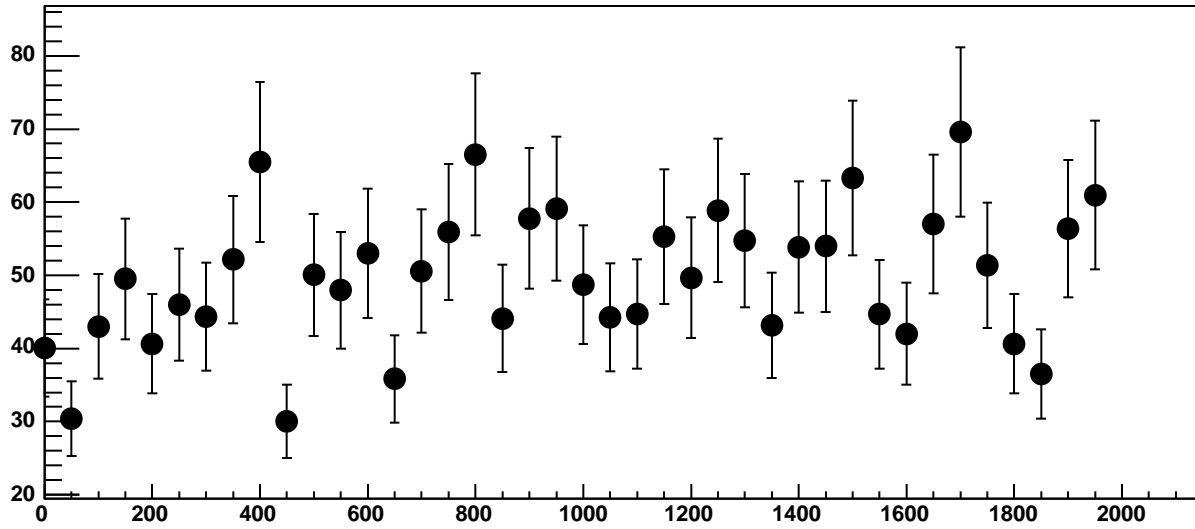
p0

$-1209 \pm 20.02$

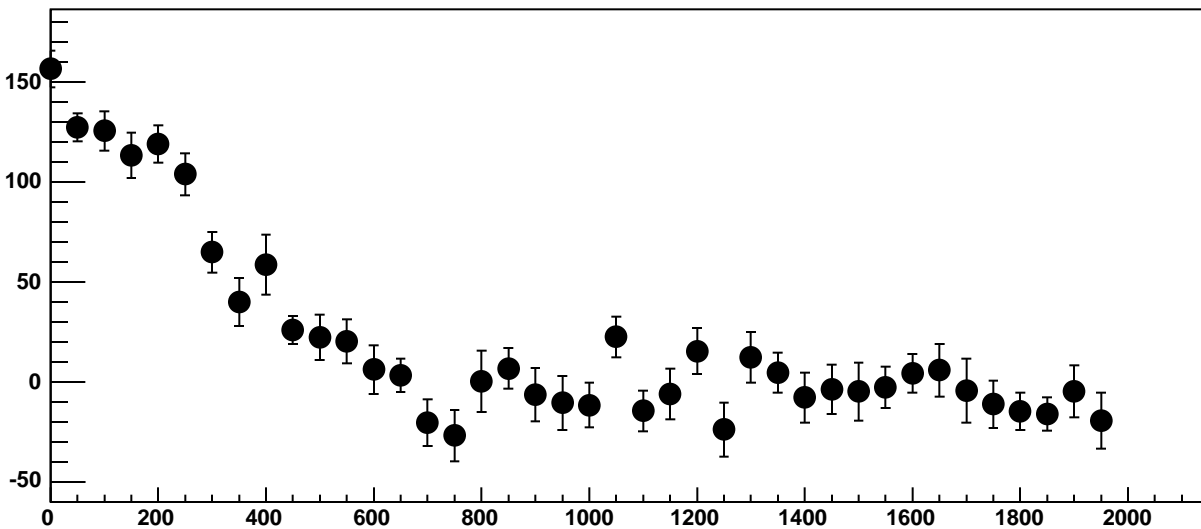
p1

$0.2546 \pm 0.01784$

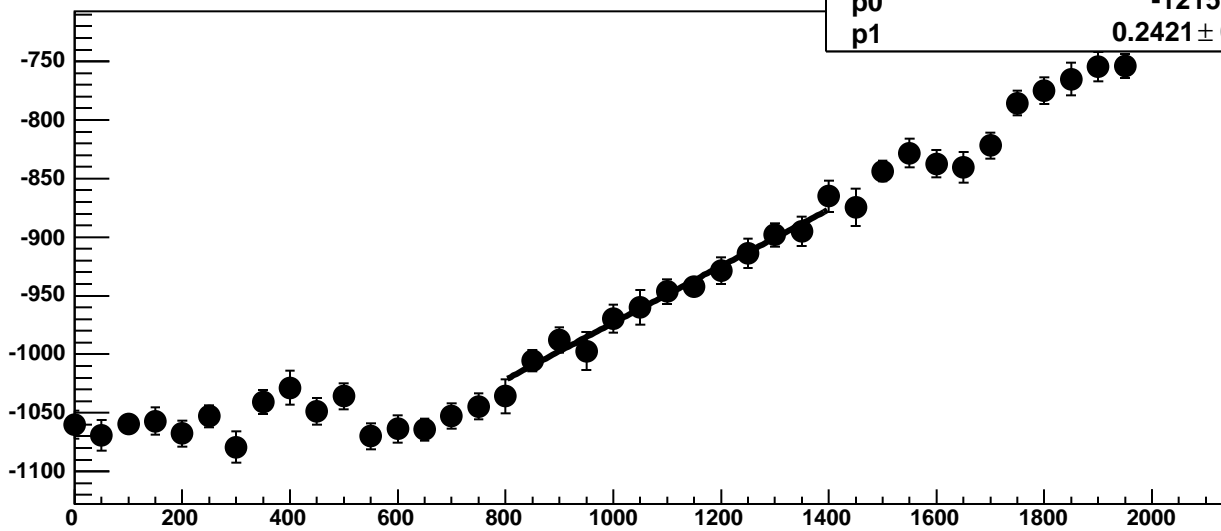
Chip 11, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC



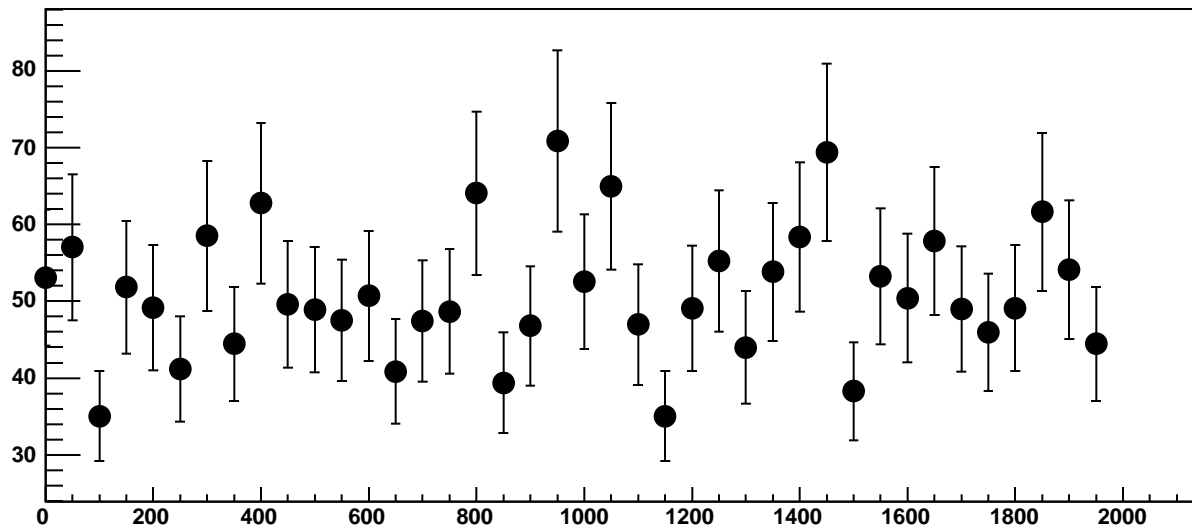
Chip 11, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC



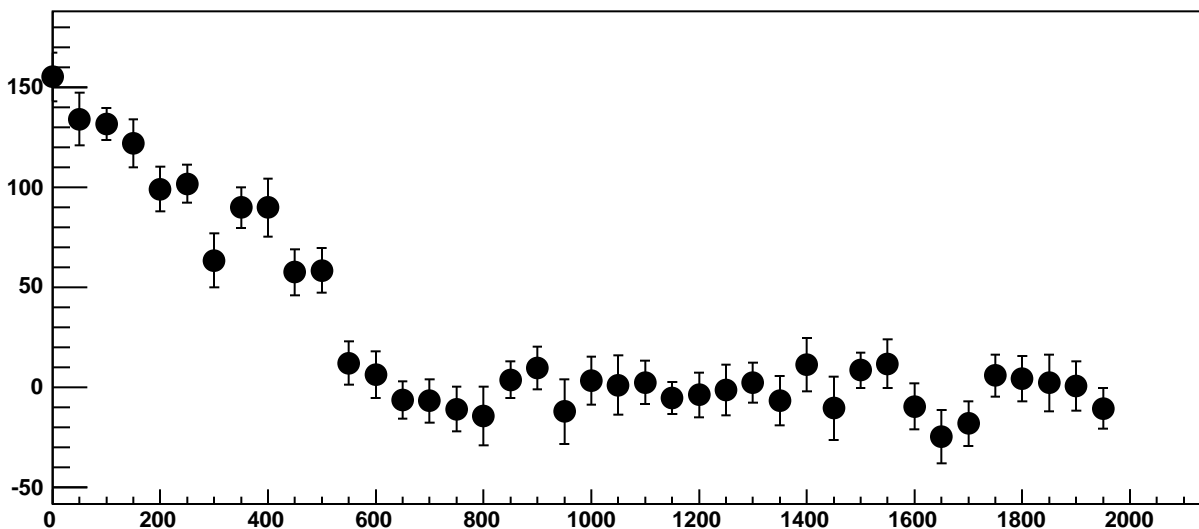
Chip 11, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC



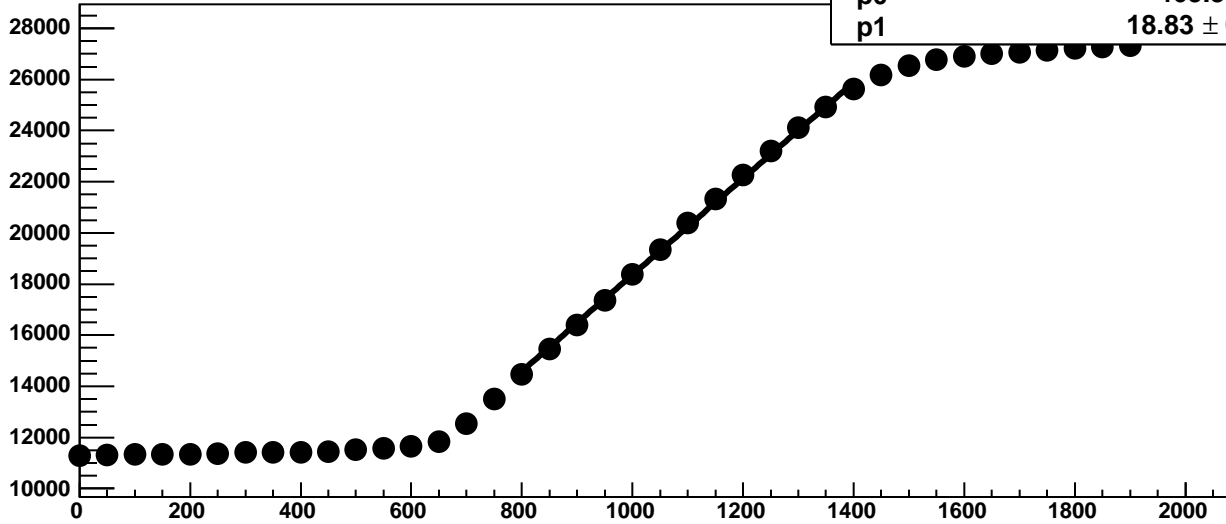
Chip 11, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC



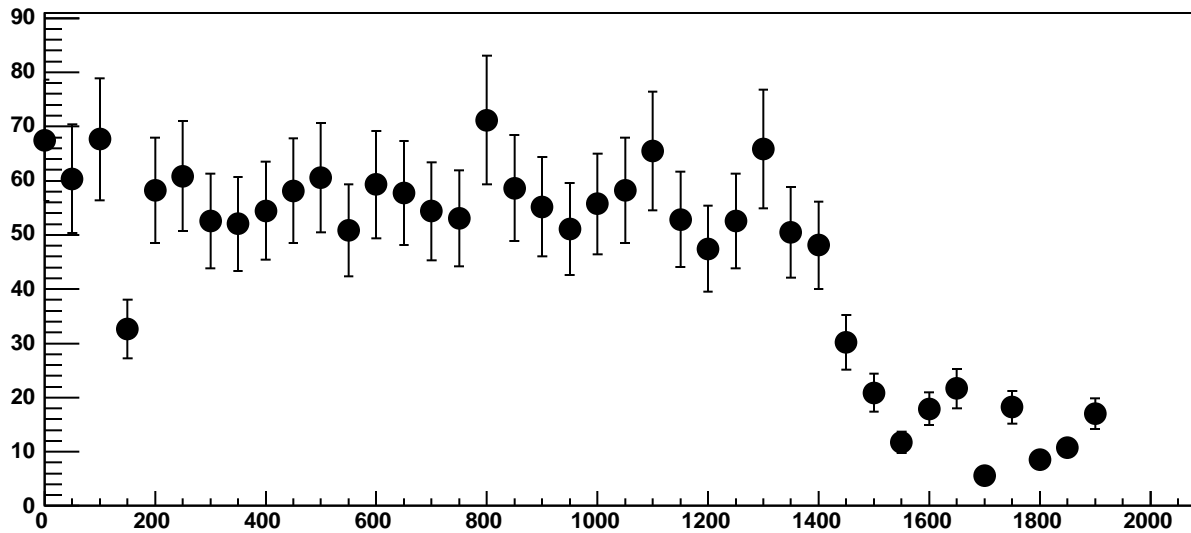
Chip 11, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC



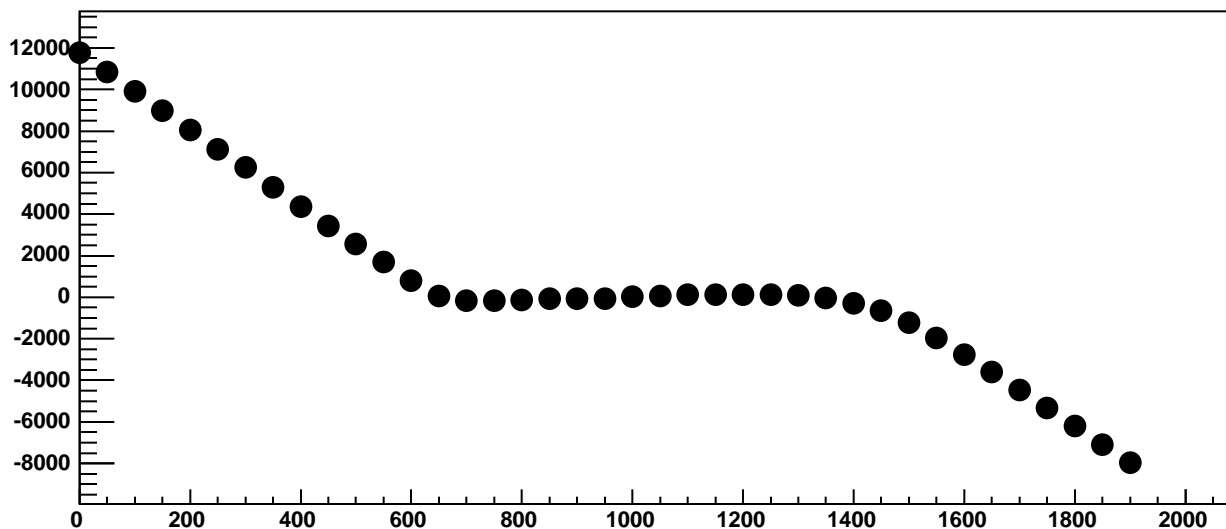
Chip 11, Channel 8, Enable 5!, Hold=30, ADC Mean vs DAC



Chip 11, Channel 8, Enable 5!, Hold=30, ADC Noise vs DAC

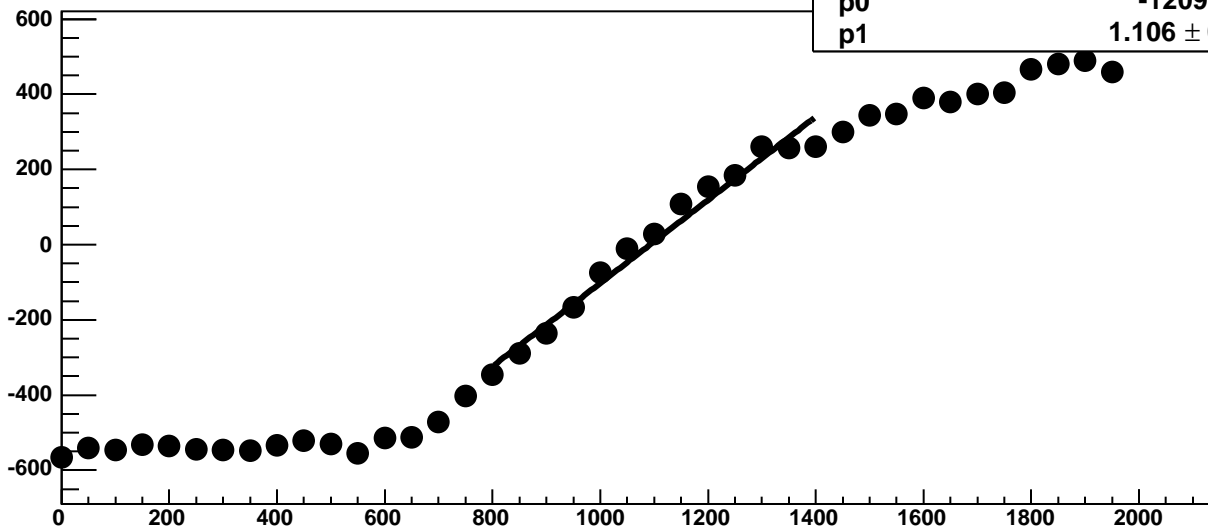


Chip 11, Channel 8, Enable 5!, Hold=30, ADC Residuals vs DAC

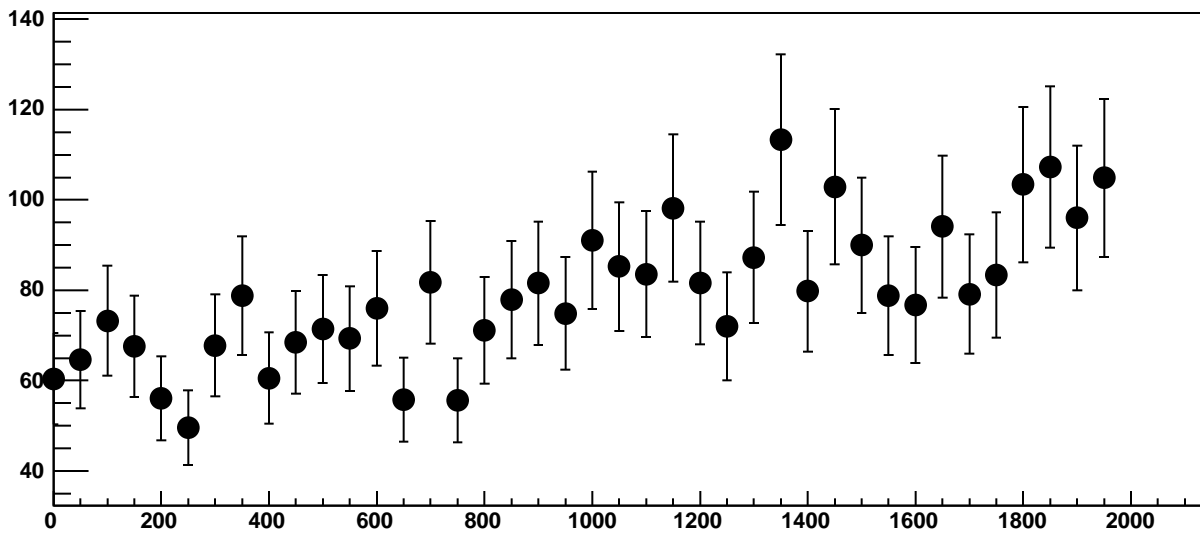




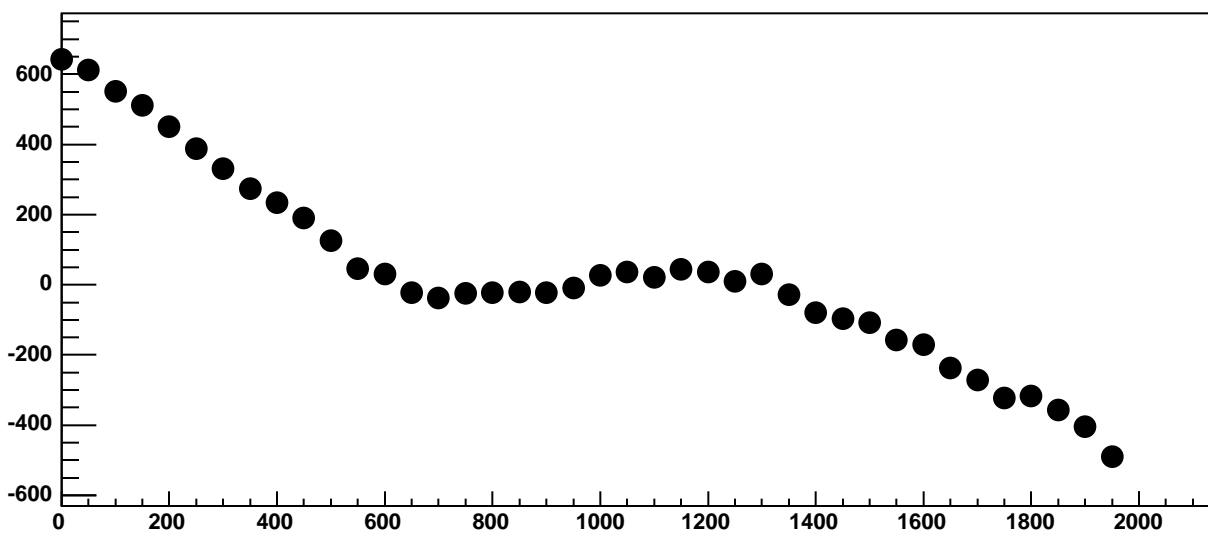
Chip 11, Channel 9, Enable 0, Hold=30, ADC Mean vs DAC



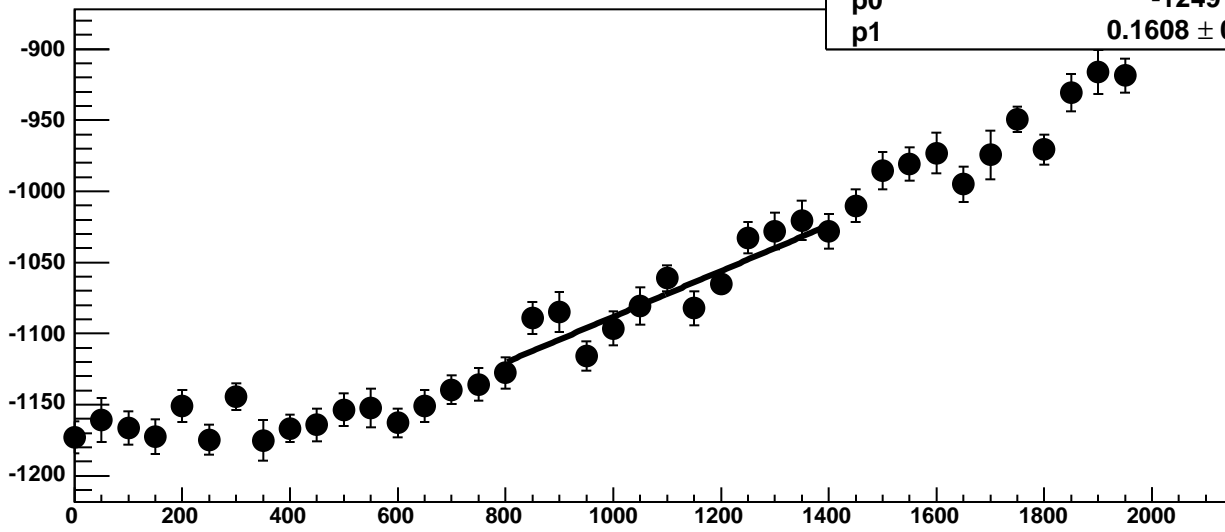
Chip 11, Channel 9, Enable 0, Hold=30, ADC Noise vs DAC



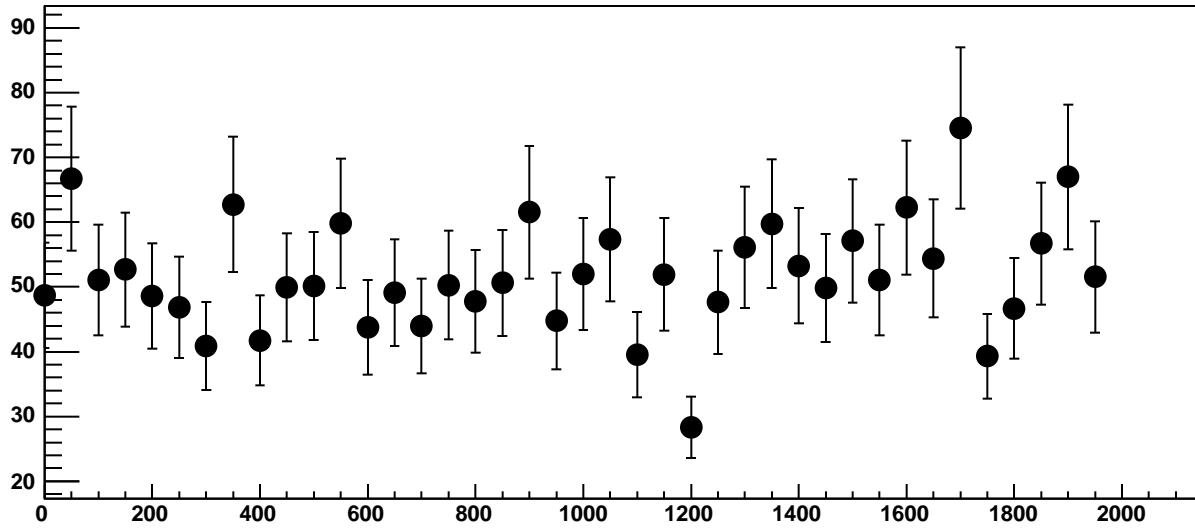
Chip 11, Channel 9, Enable 0, Hold=30, ADC Residuals vs DAC



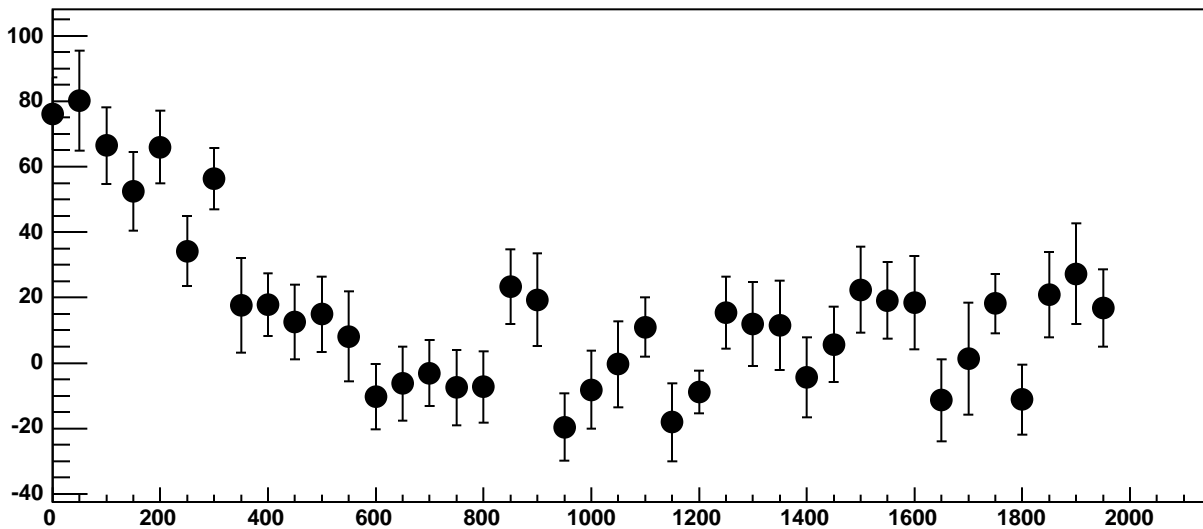
Chip 11, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC



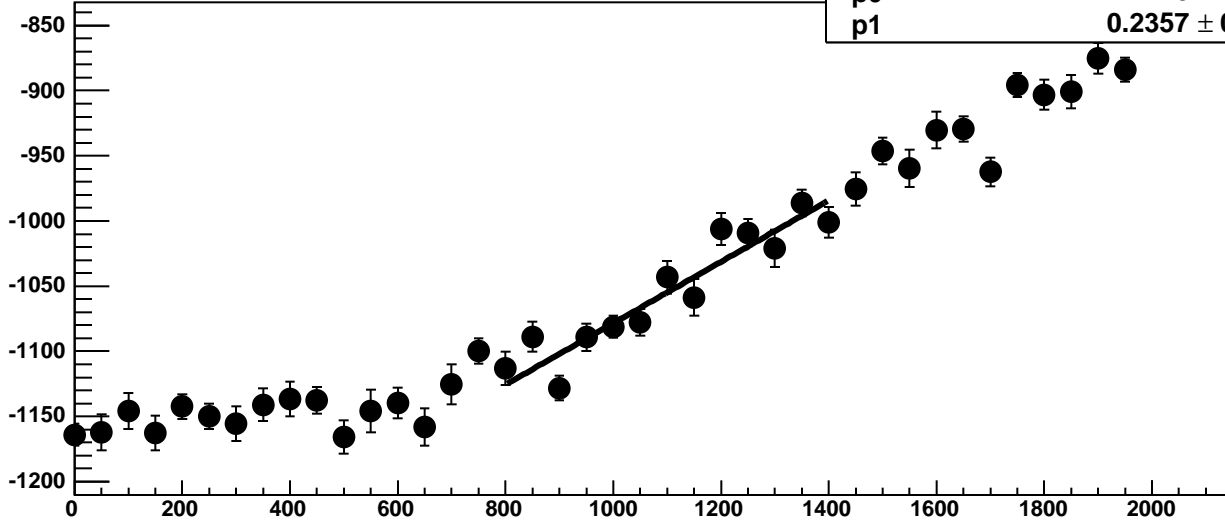
Chip 11, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC



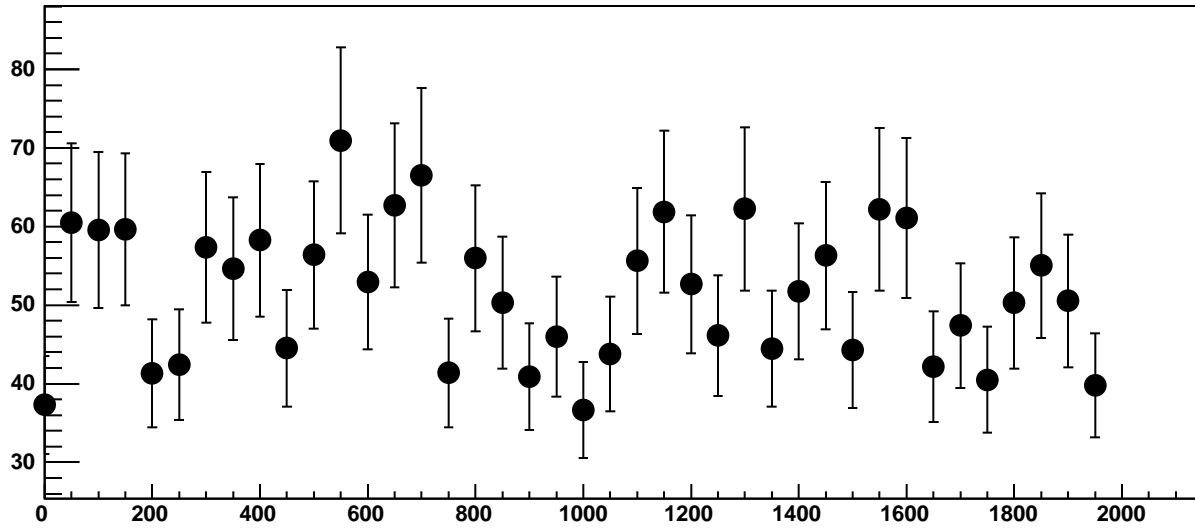
Chip 11, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC



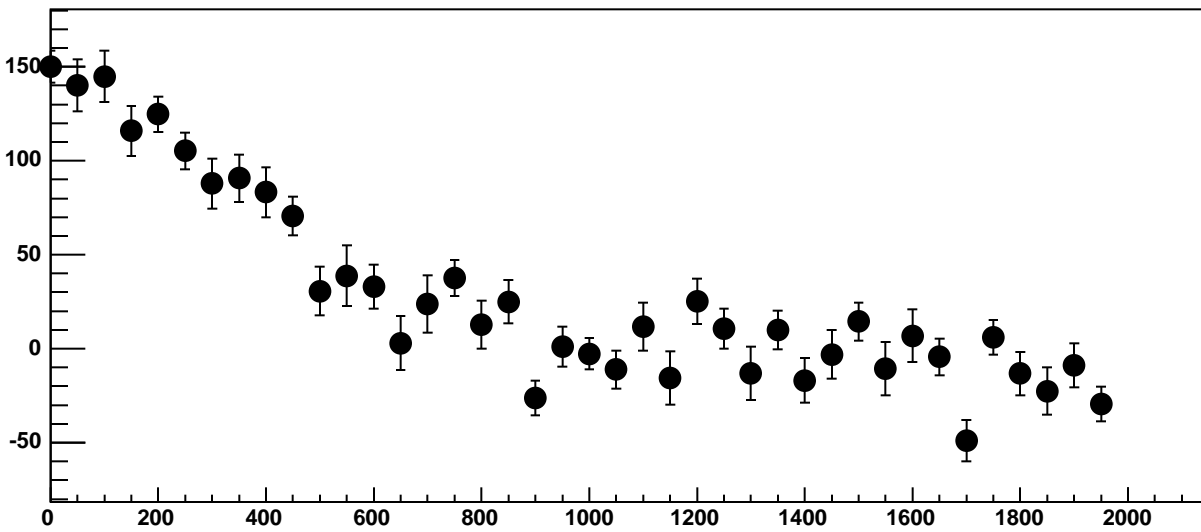
Chip 11, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC



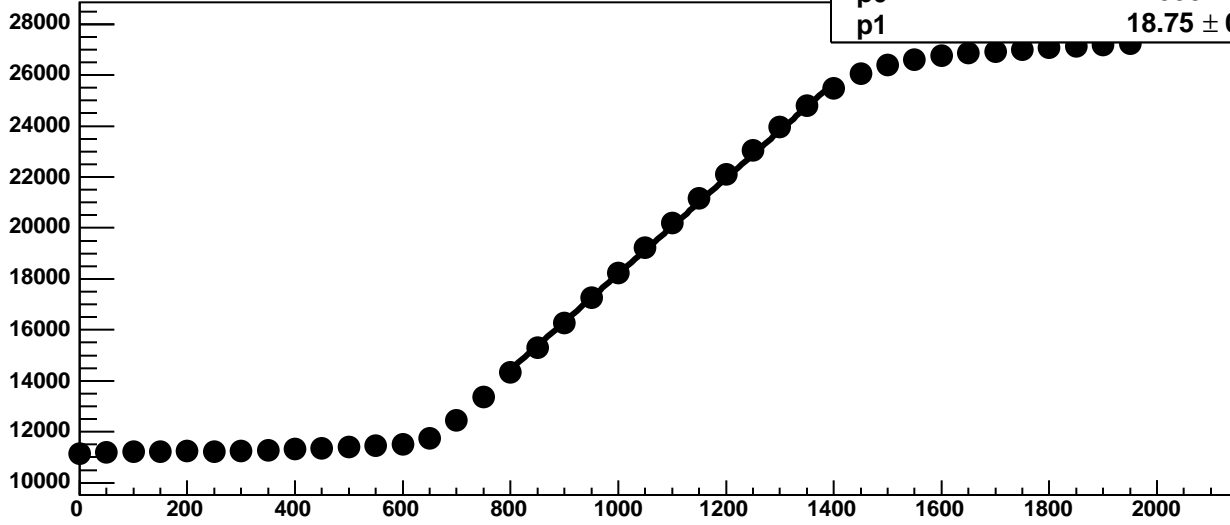
Chip 11, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC



Chip 11, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC

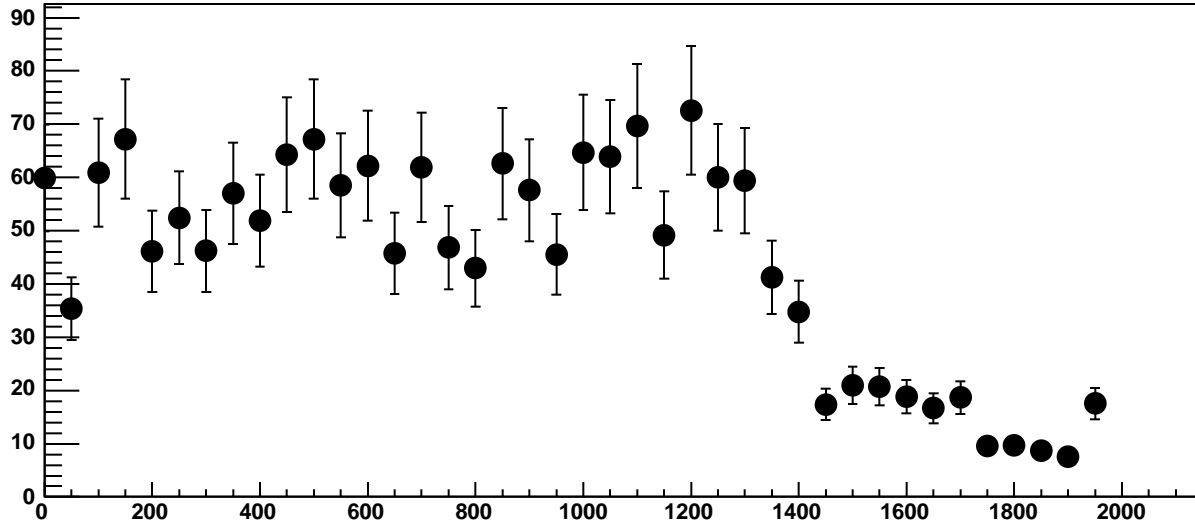


Chip 11, Channel 9, Enable 3!, Hold=30, ADC Mean vs DAC

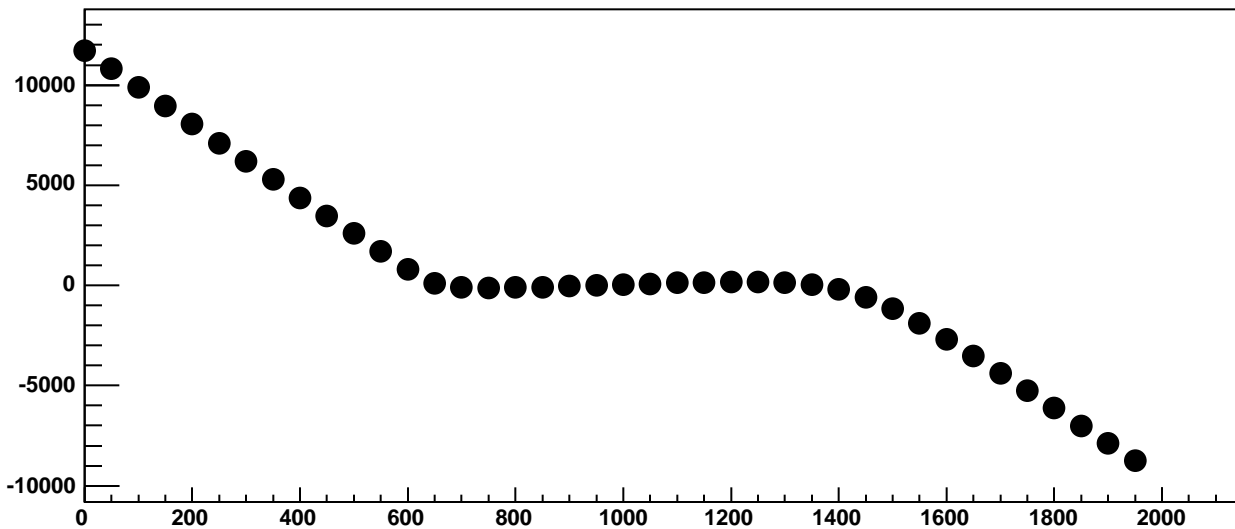


$\chi^2 / \text{ndf}$  1447 / 11  
p0  $-558.2 \pm 17.82$   
p1  $18.75 \pm 0.01552$

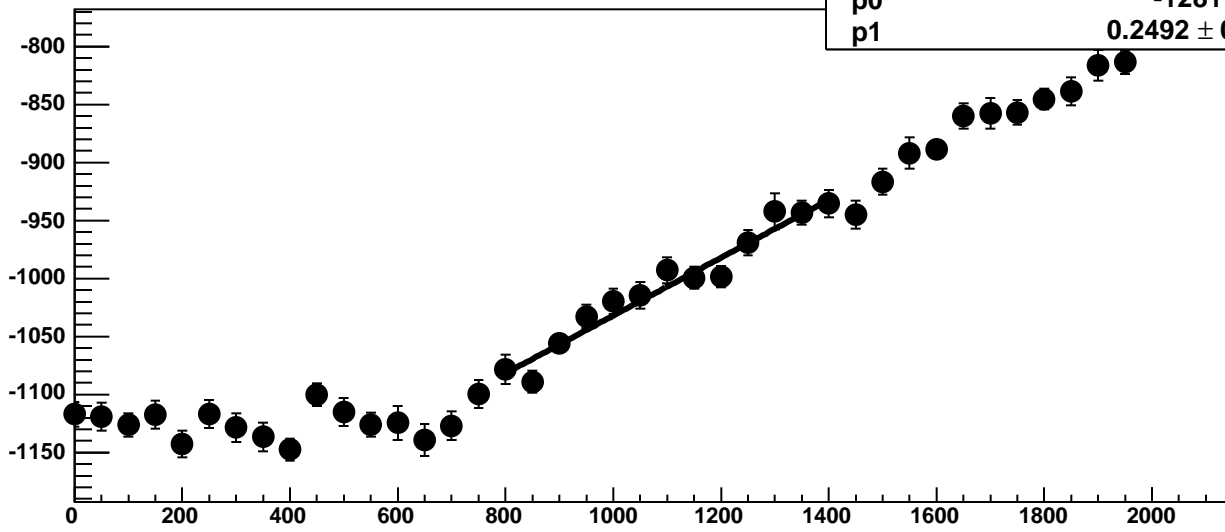
Chip 11, Channel 9, Enable 3!, Hold=30, ADC Noise vs DAC



Chip 11, Channel 9, Enable 3!, Hold=30, ADC Residuals vs DAC

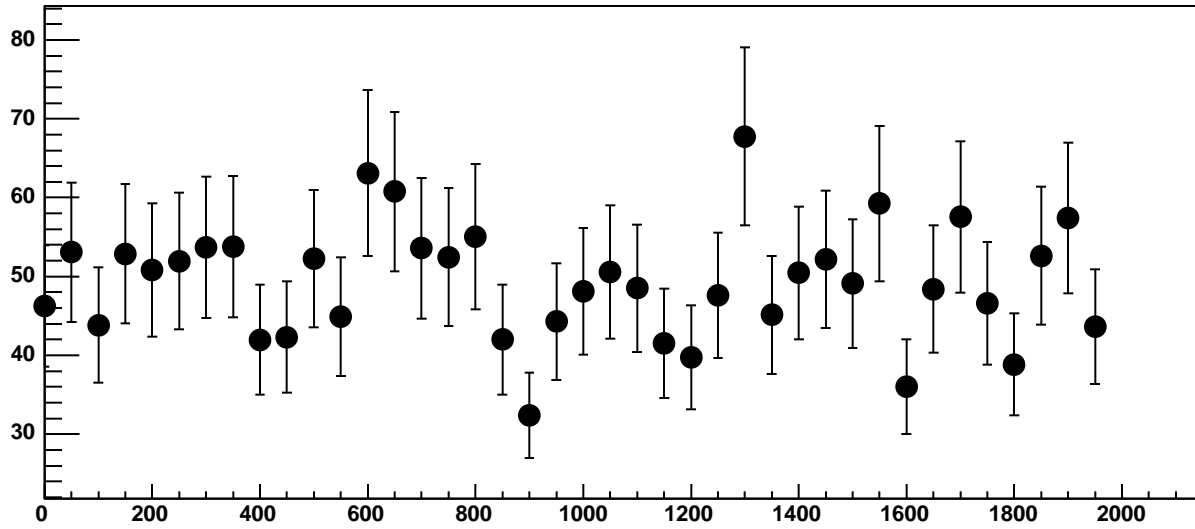


Chip 11, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC

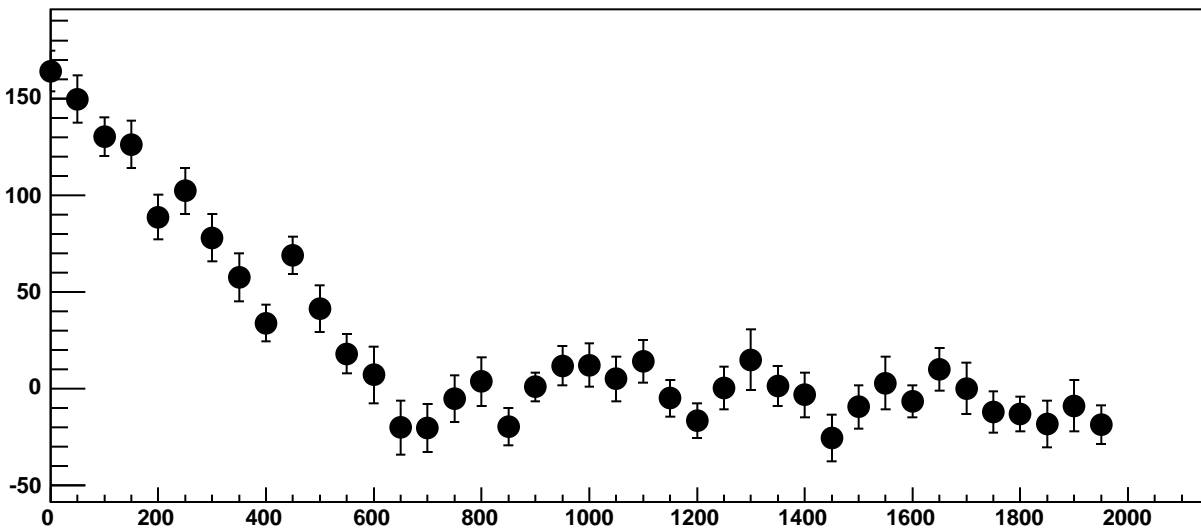


$\chi^2 / \text{ndf}$  13.18 / 11  
p0  $-1281 \pm 17.34$   
p1  $0.2492 \pm 0.01584$

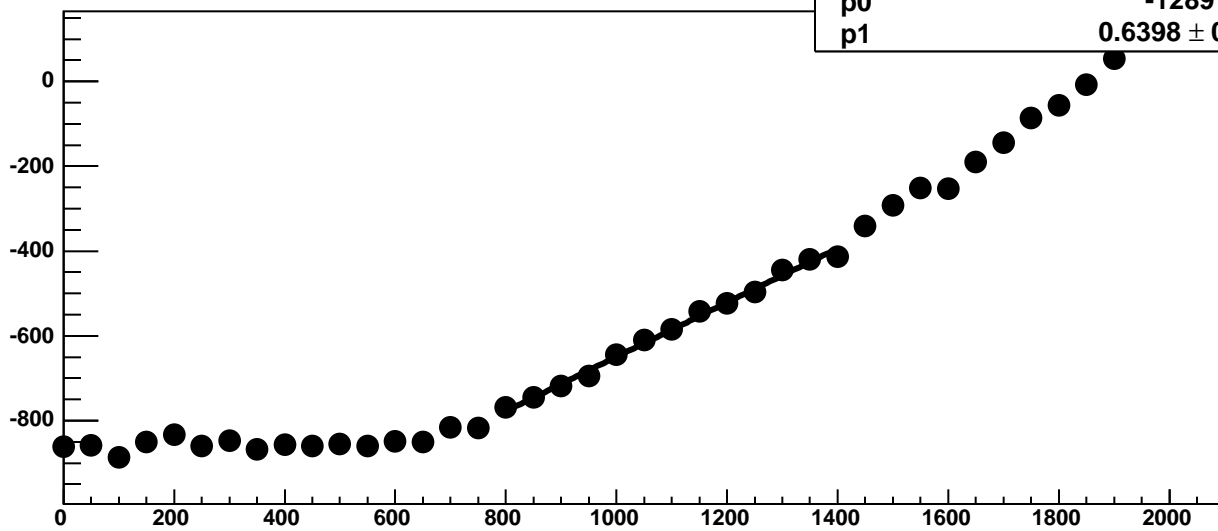
Chip 11, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC



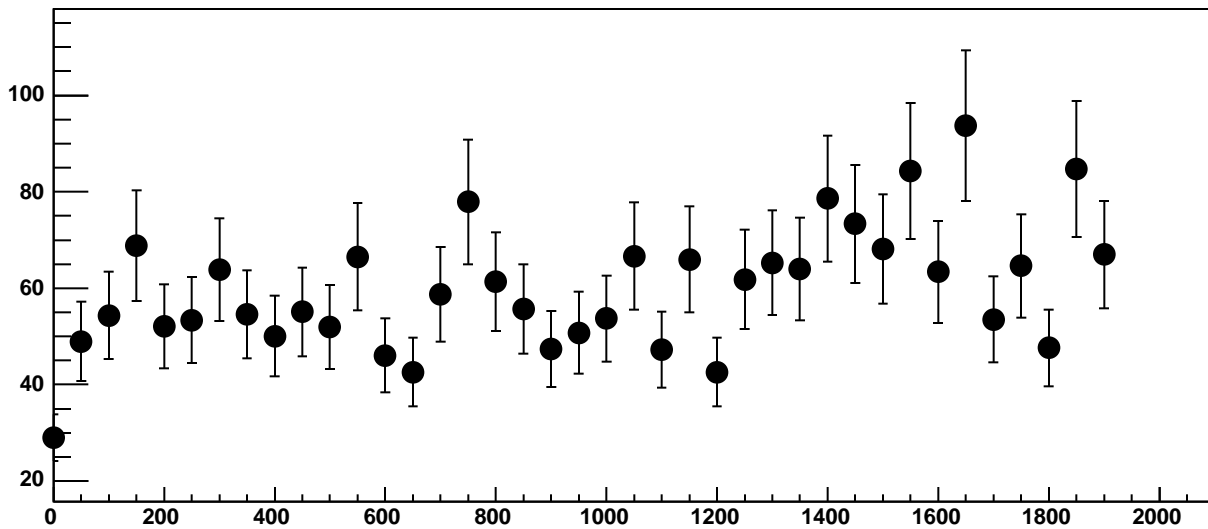
Chip 11, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC



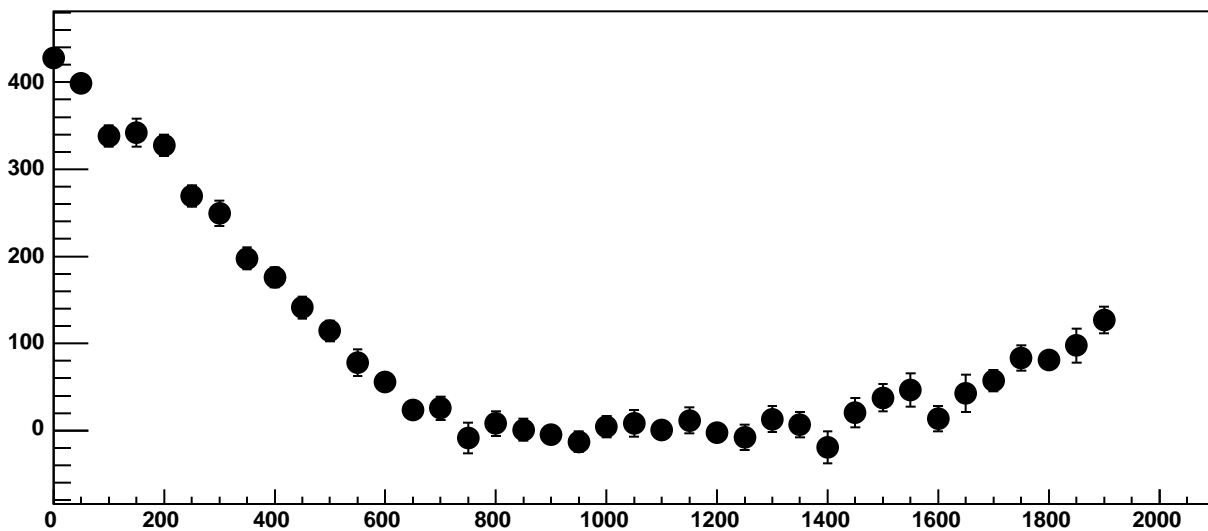
Chip 11, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC



Chip 11, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC

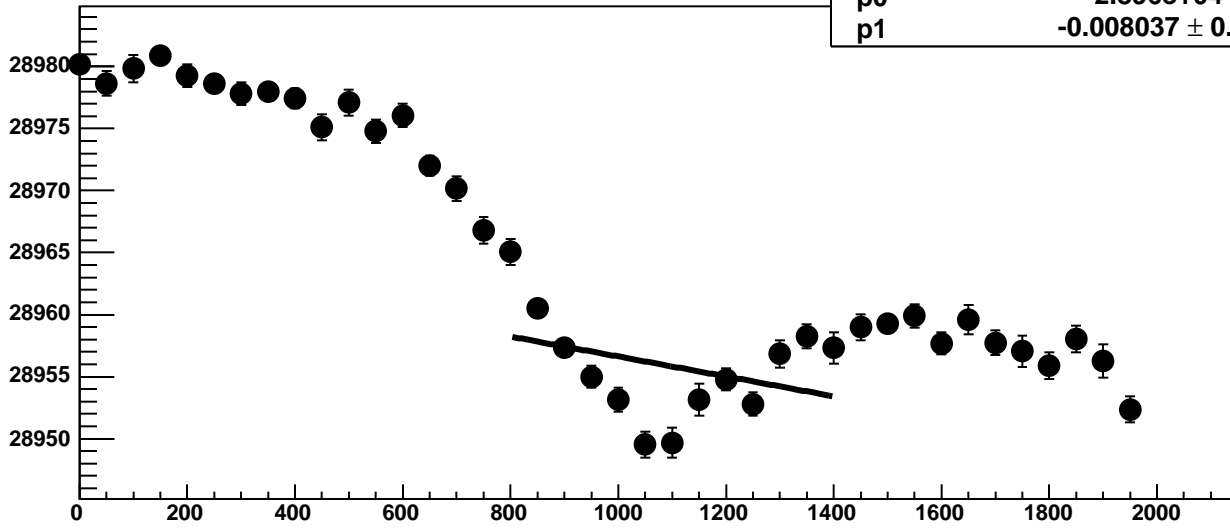


Chip 11, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC

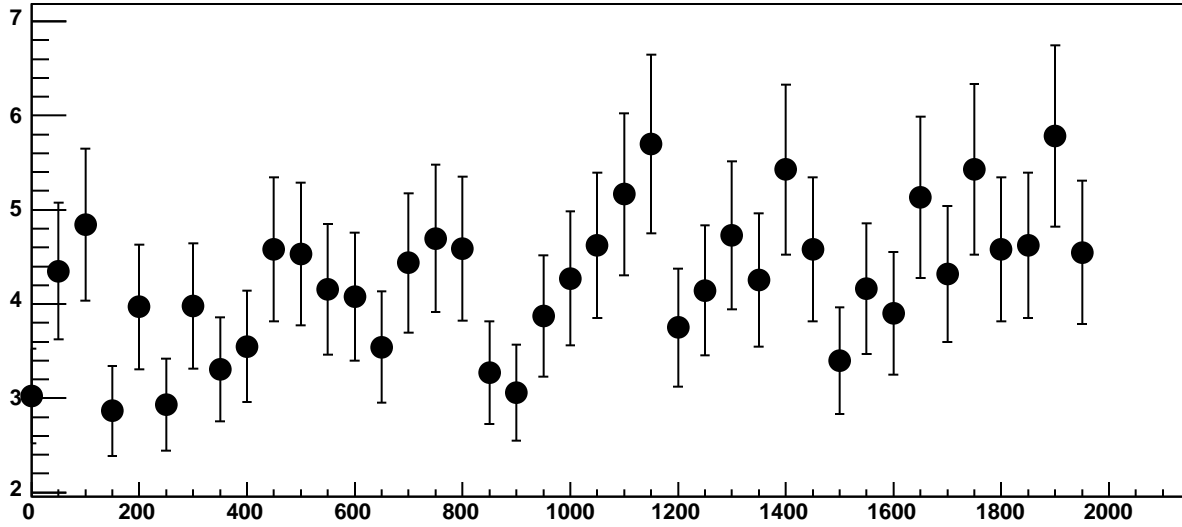


Chip 11, Channel 10, Enable 0!, Hold=30, ADC Mean vs DAC

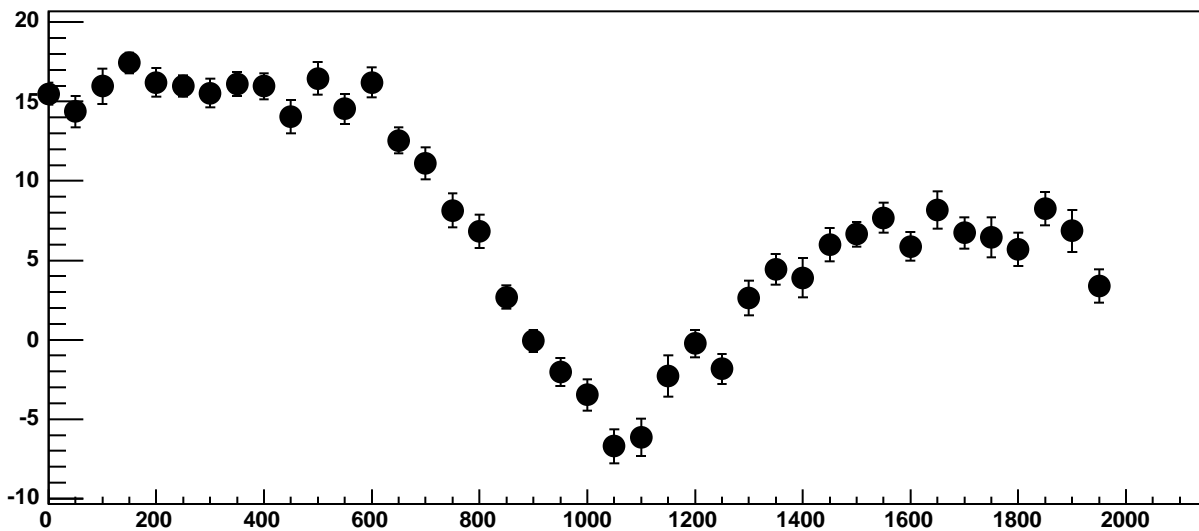
$\chi^2 / \text{ndf}$  182.6 / 11  
p0  $2.896\text{e}+04 \pm 1.537$   
p1  $-0.008037 \pm 0.001422$



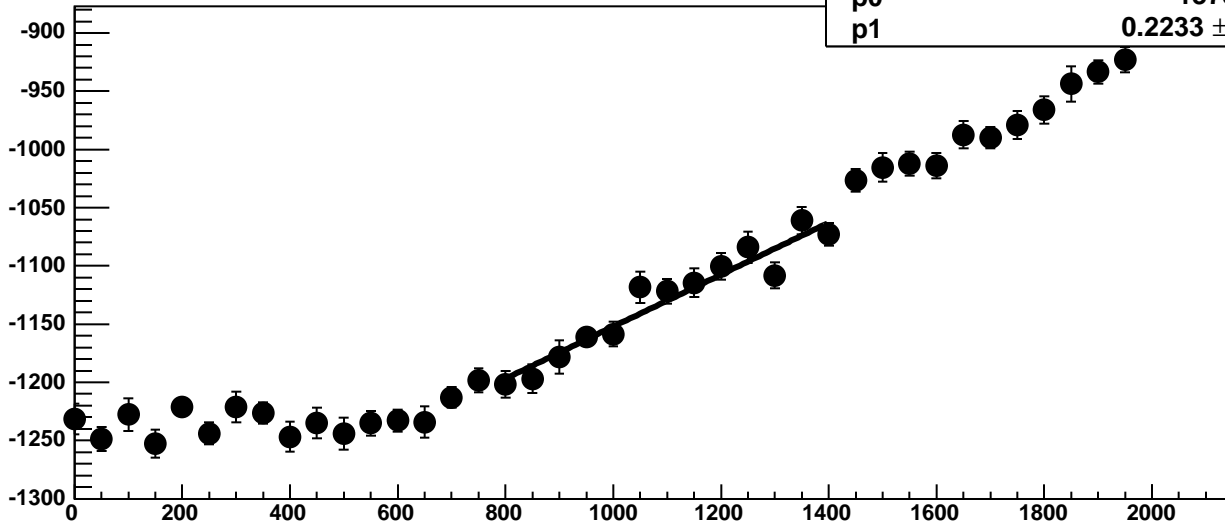
Chip 11, Channel 10, Enable 0!, Hold=30, ADC Noise vs DAC



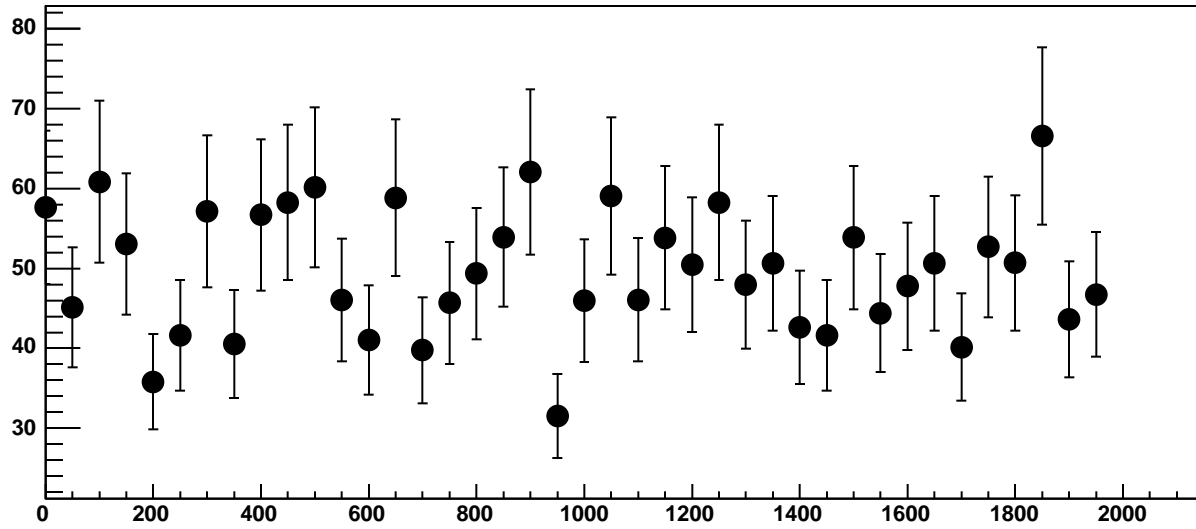
Chip 11, Channel 10, Enable 0!, Hold=30, ADC Residuals vs DAC



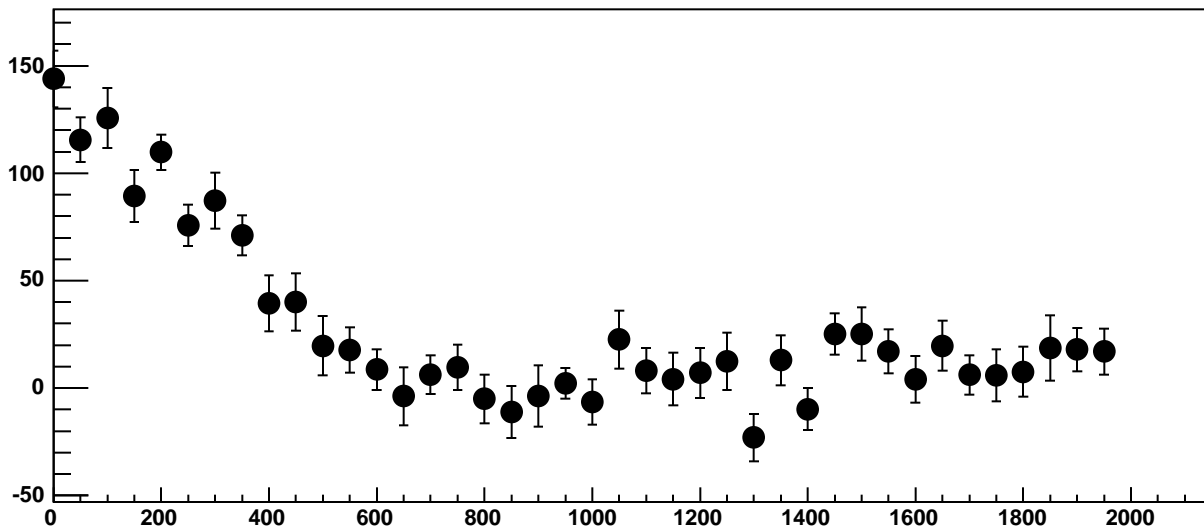
Chip 11, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC



Chip 11, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC

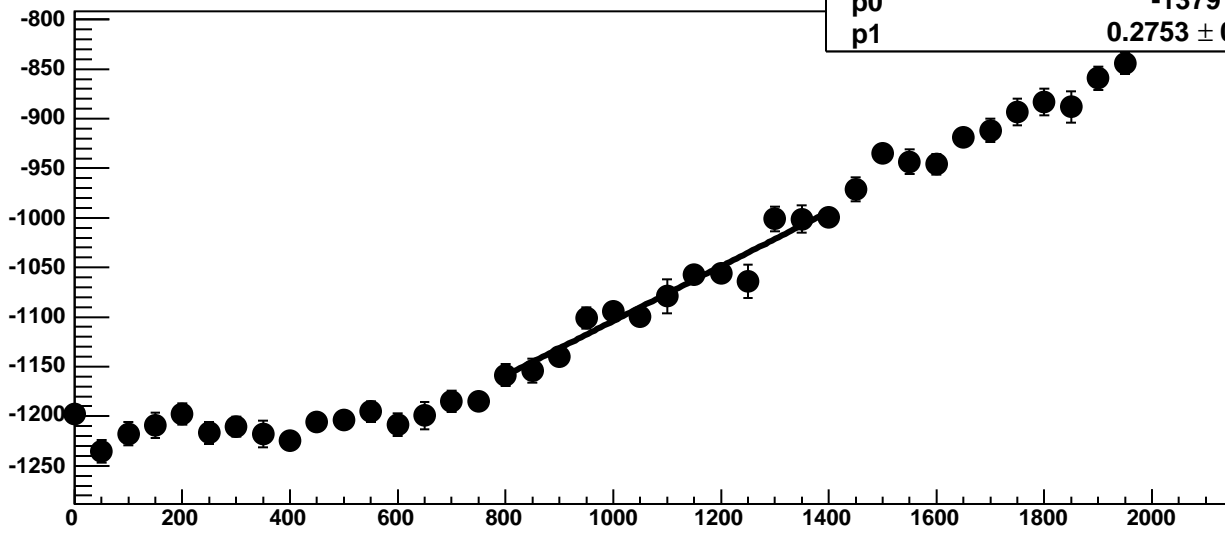


Chip 11, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC



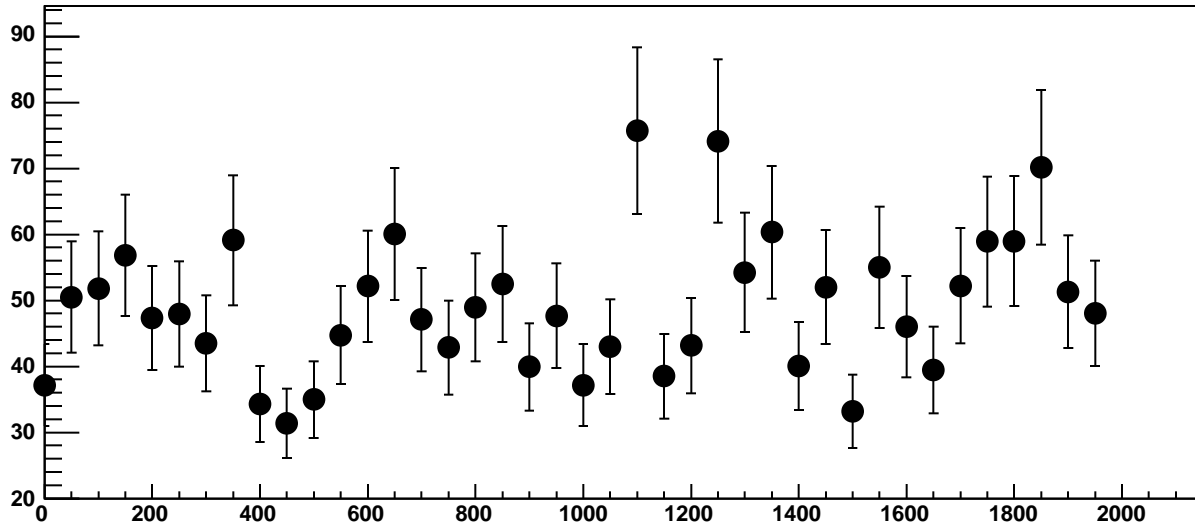


Chip 11, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC

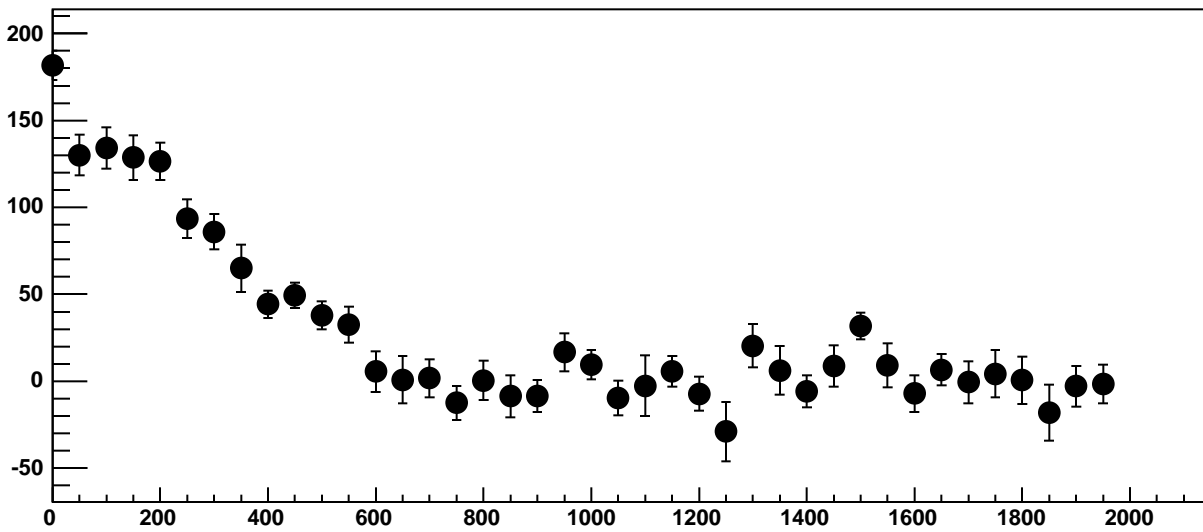


$\chi^2 / \text{ndf}$  13.06 / 11  
p0  $-1379 \pm 17.86$   
p1  $0.2753 \pm 0.01618$

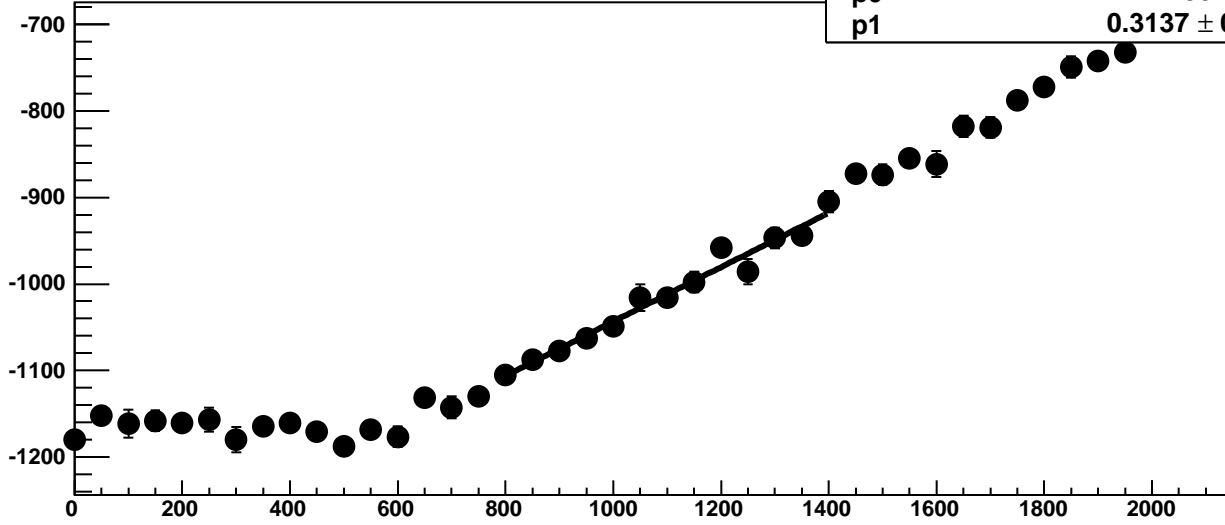
Chip 11, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC



Chip 11, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC

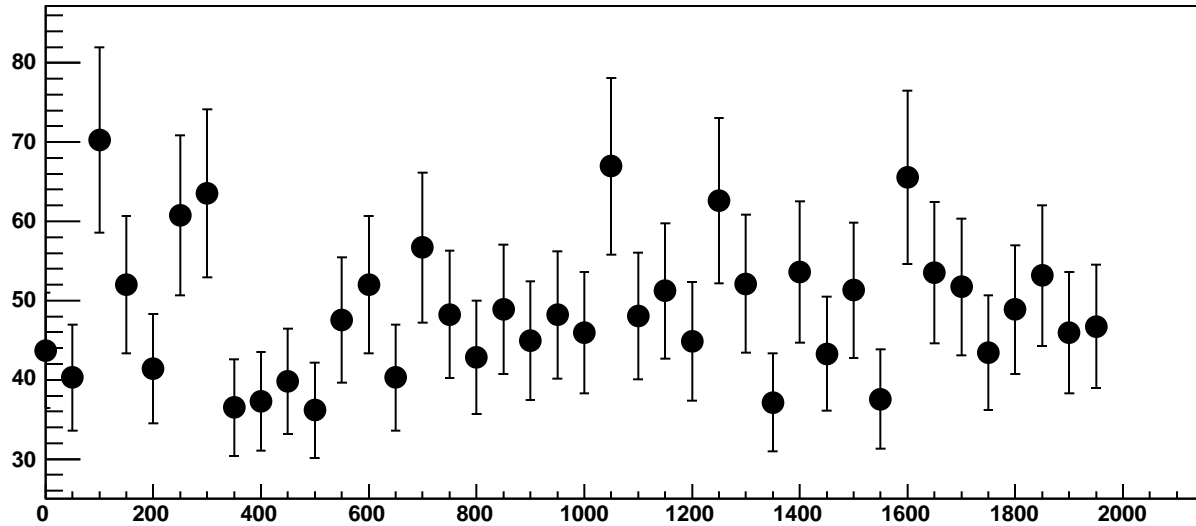


Chip 11, Channel 10, Enable 3, Hold=30, ADC Mean vs DAC

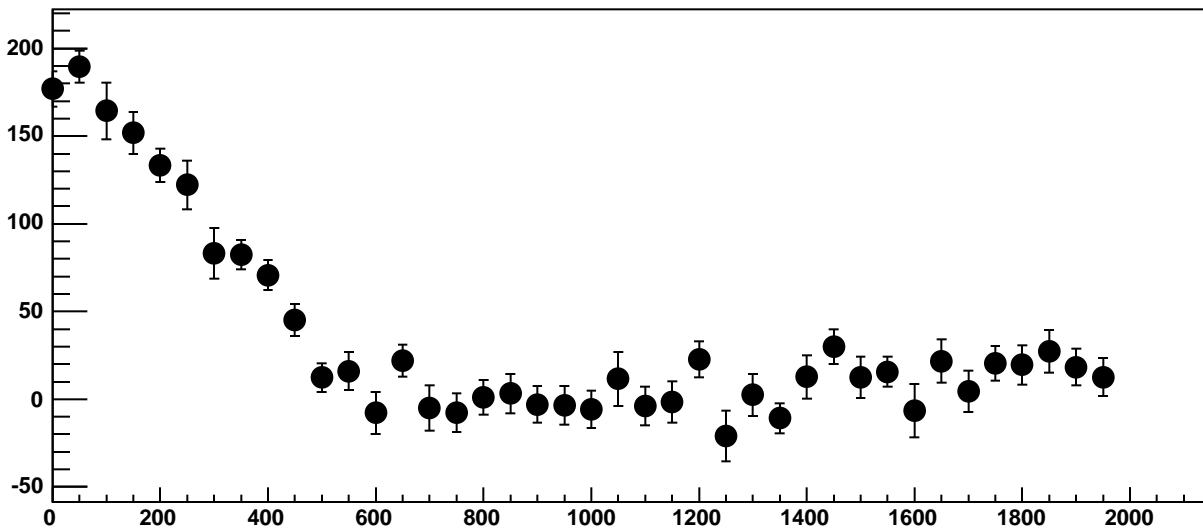


$\chi^2 / \text{ndf}$  10.97 / 11  
p0  $-1357 \pm 17.5$   
p1  $0.3137 \pm 0.01572$

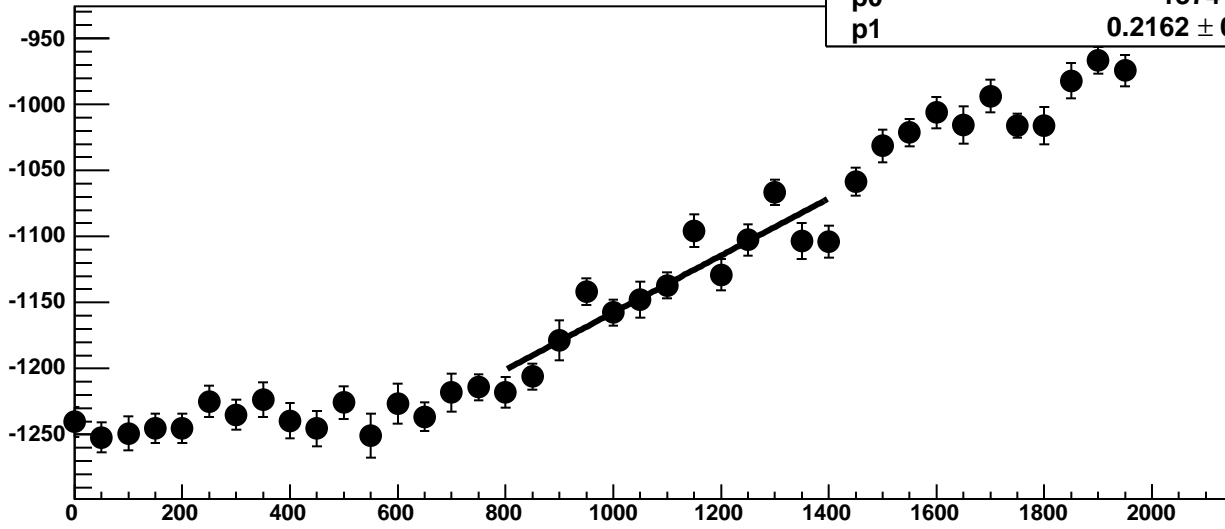
Chip 11, Channel 10, Enable 3, Hold=30, ADC Noise vs DAC



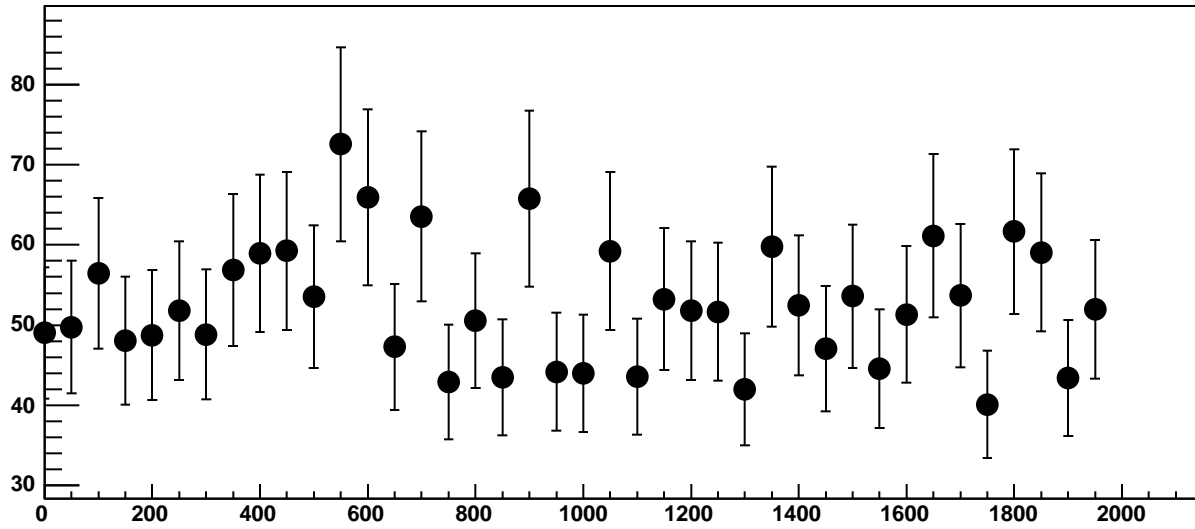
Chip 11, Channel 10, Enable 3, Hold=30, ADC Residuals vs DAC



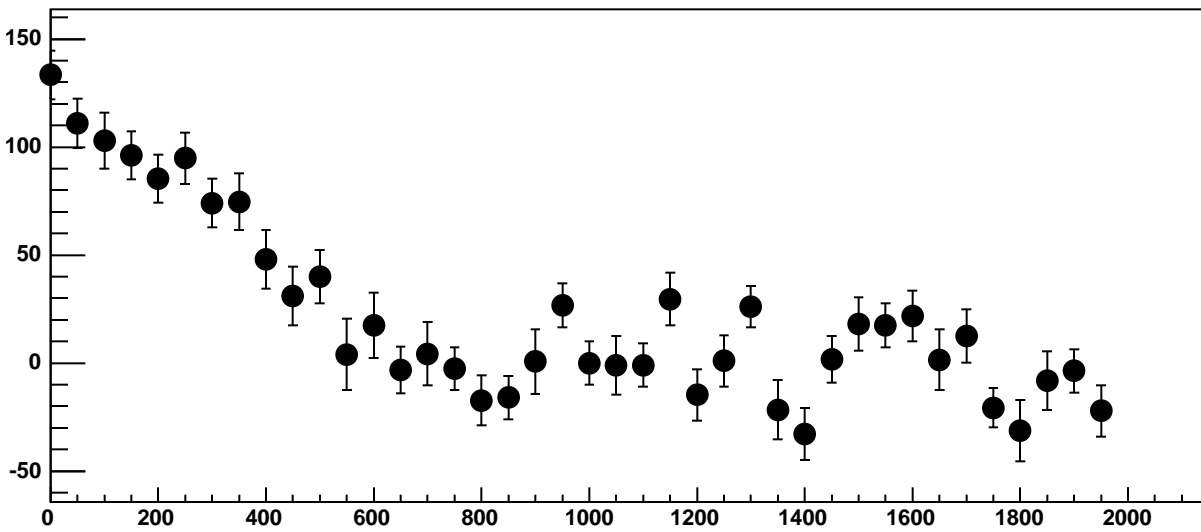
Chip 11, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC



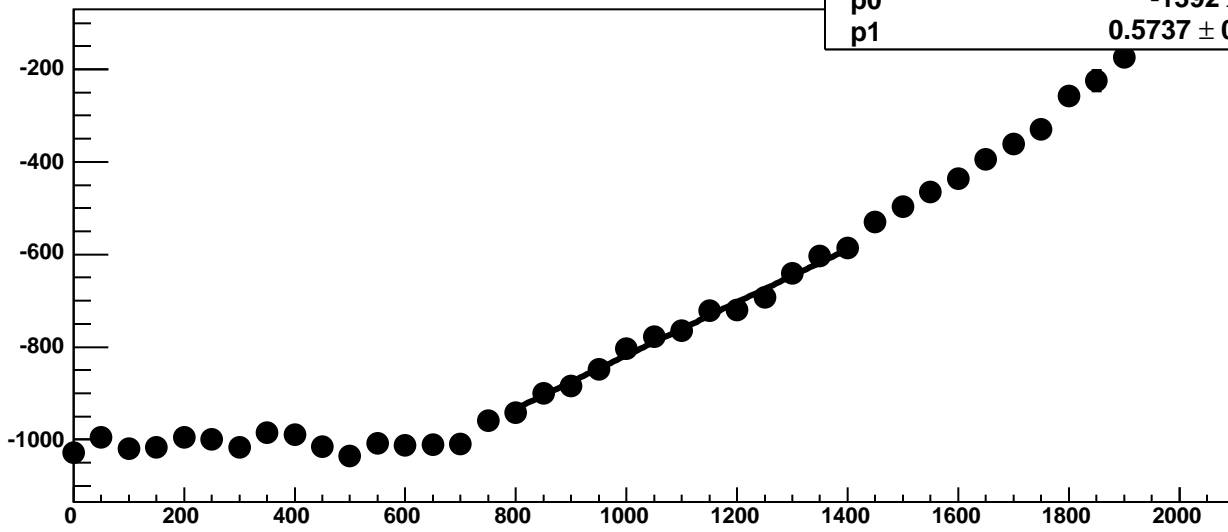
Chip 11, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC



Chip 11, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC

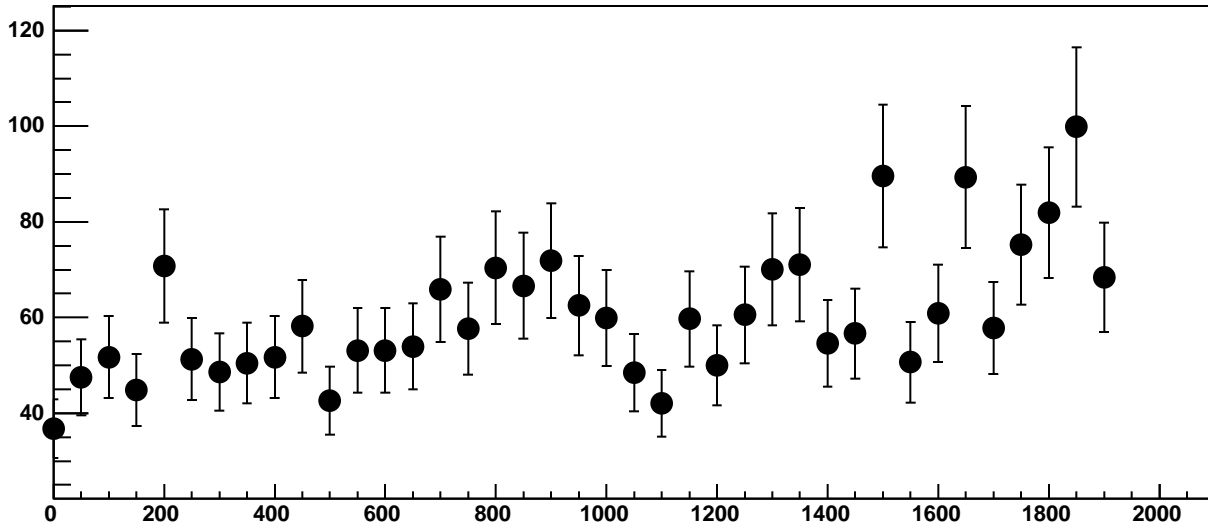


Chip 11, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC

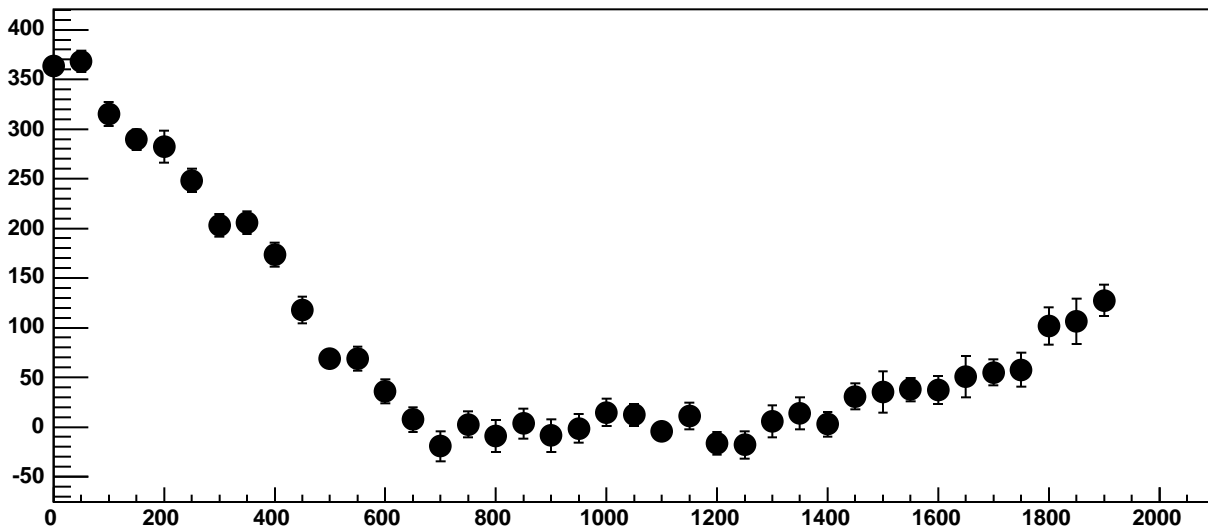


$\chi^2 / \text{ndf}$  8.503 / 11  
p0  $-1392 \pm 24.52$   
p1  $0.5737 \pm 0.02181$

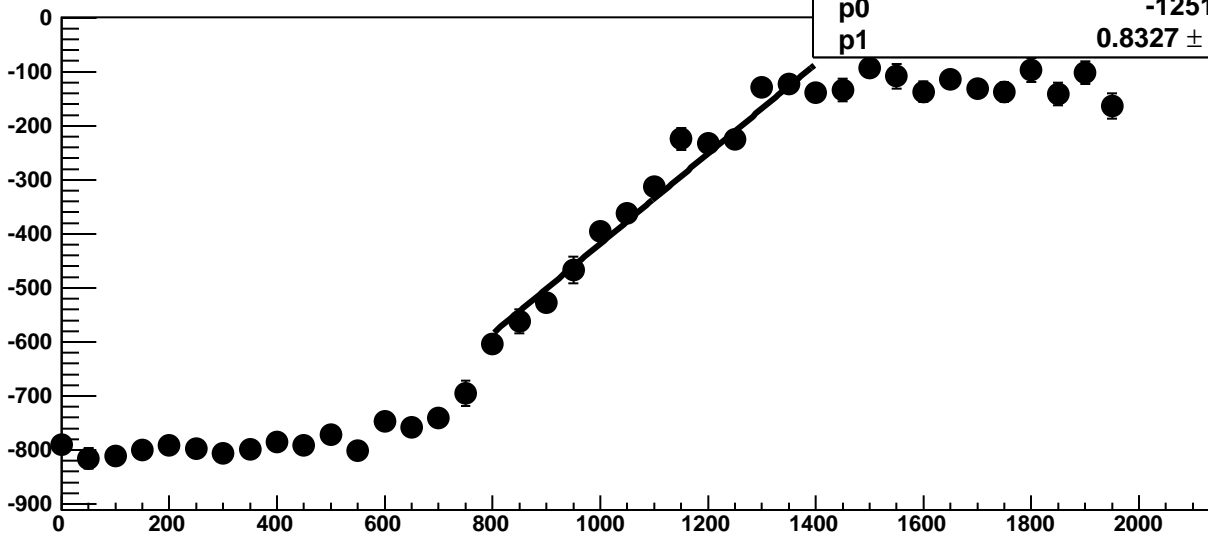
Chip 11, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC



Chip 11, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC

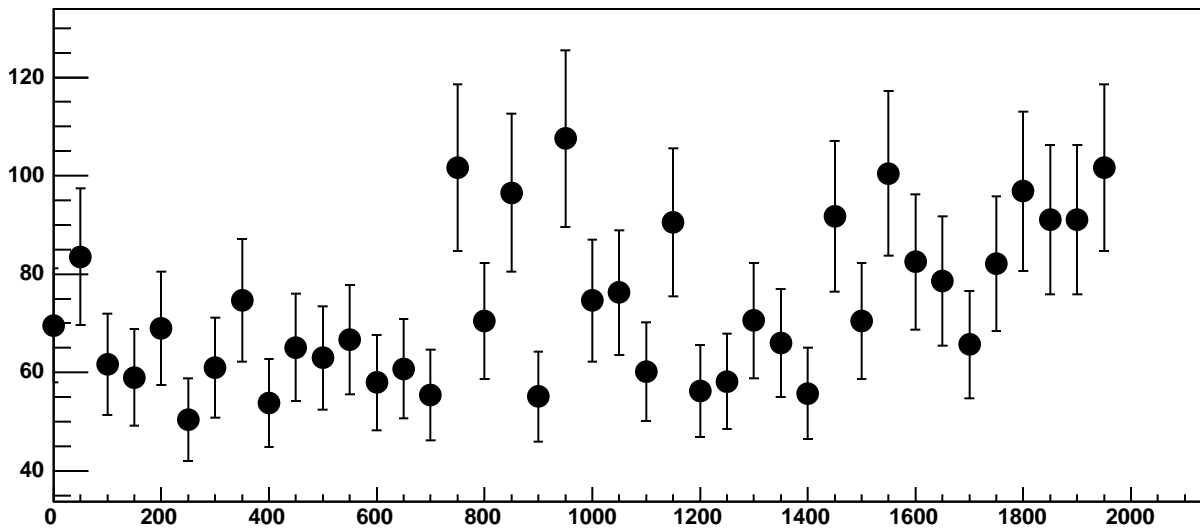


Chip 11, Channel 11, Enable 0, Hold=30, ADC Mean vs DAC

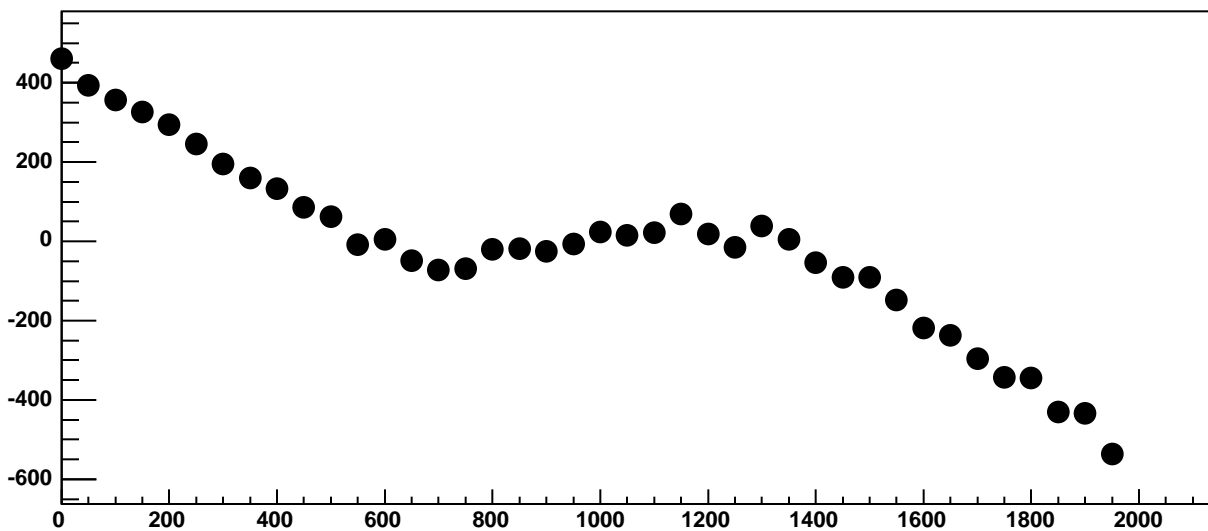


$\chi^2 / \text{ndf}$  49.04 / 11  
p0  $-1251 \pm 26.11$   
p1  $0.8327 \pm 0.02281$

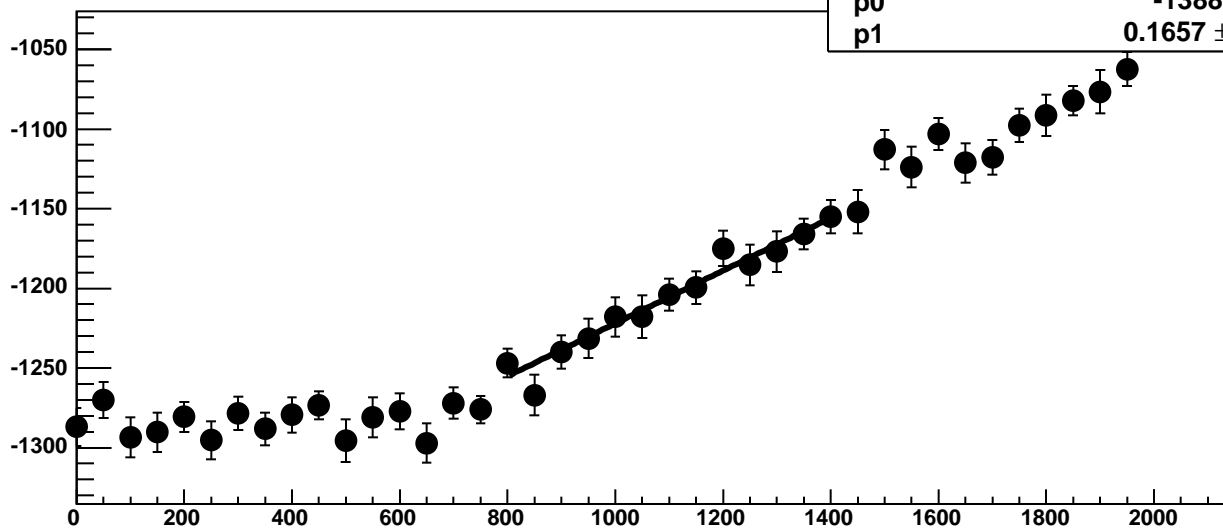
Chip 11, Channel 11, Enable 0, Hold=30, ADC Noise vs DAC



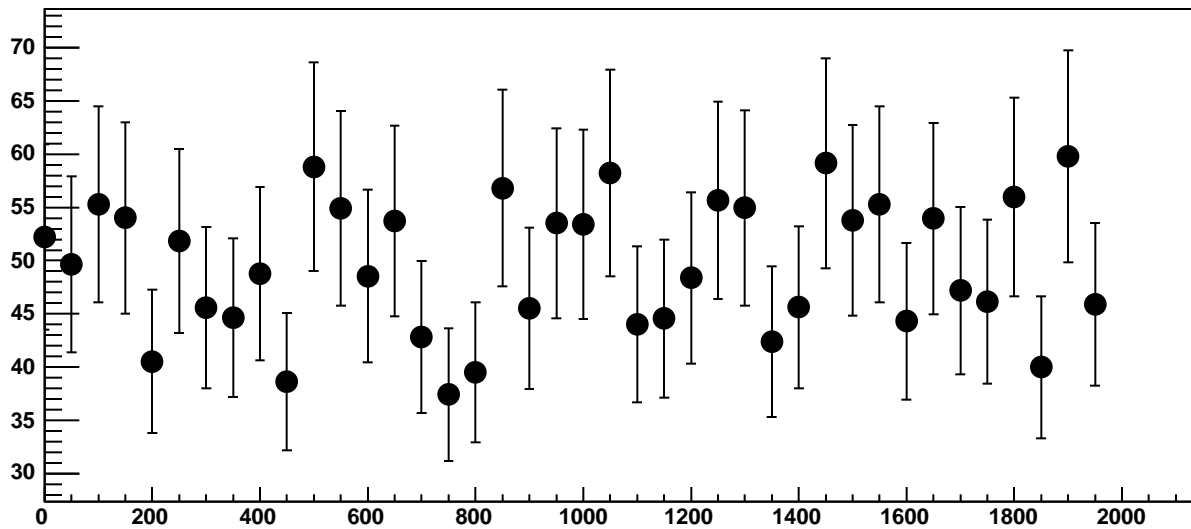
Chip 11, Channel 11, Enable 0, Hold=30, ADC Residuals vs DAC



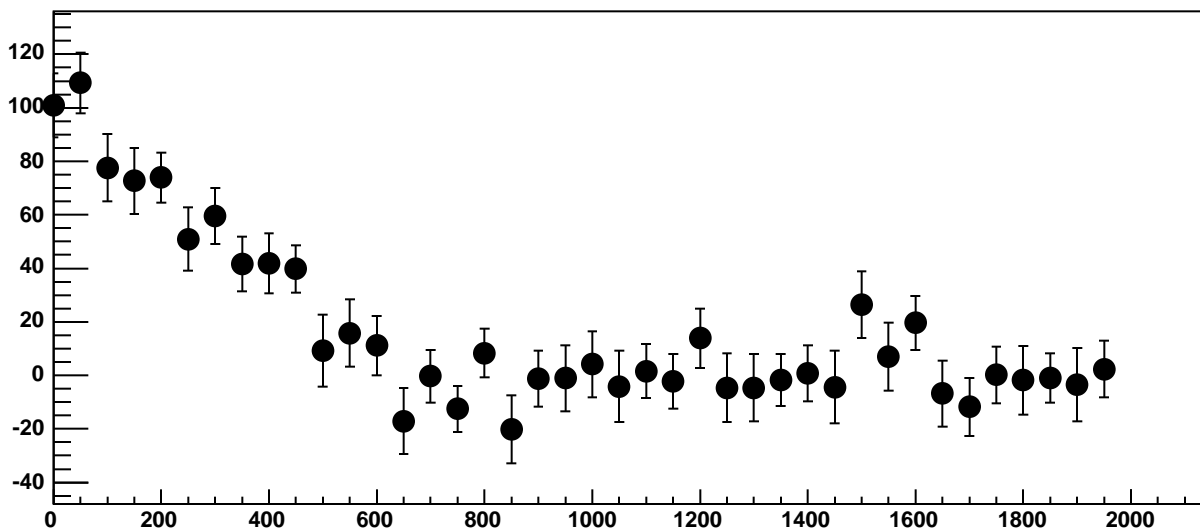
Chip 11, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC



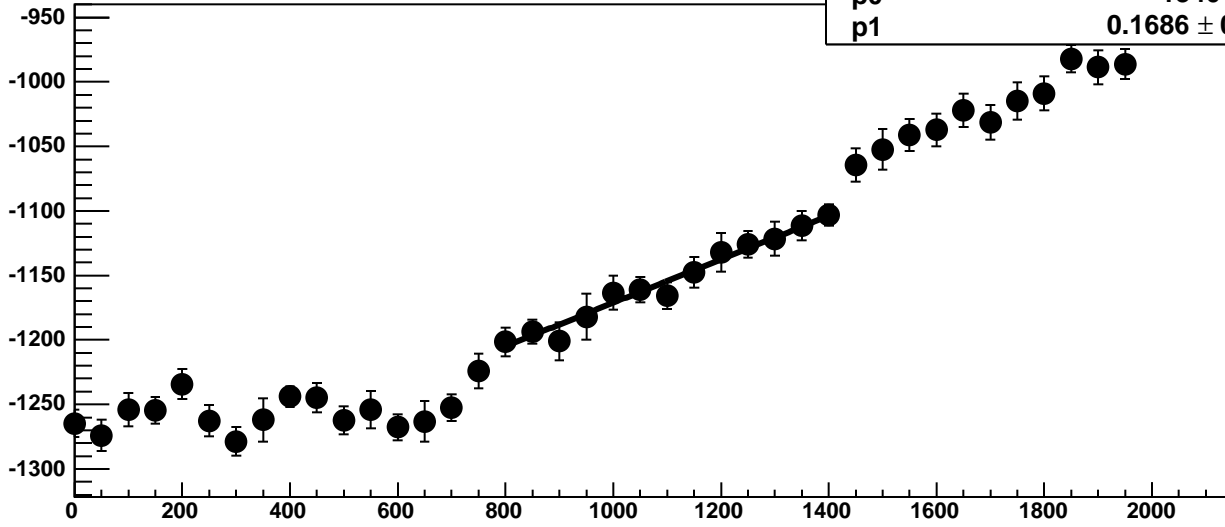
Chip 11, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC



Chip 11, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC

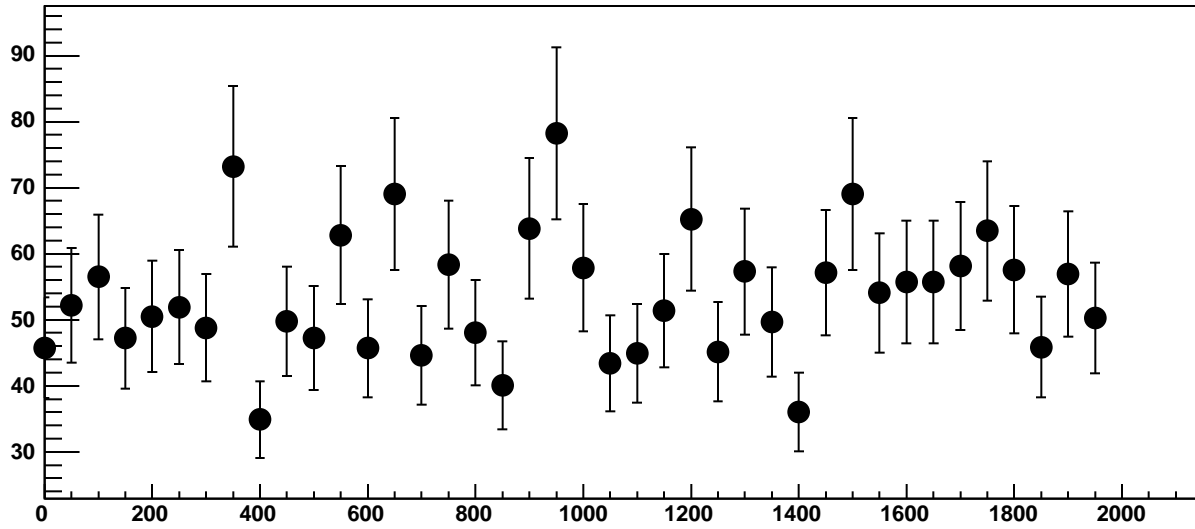


Chip 11, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC

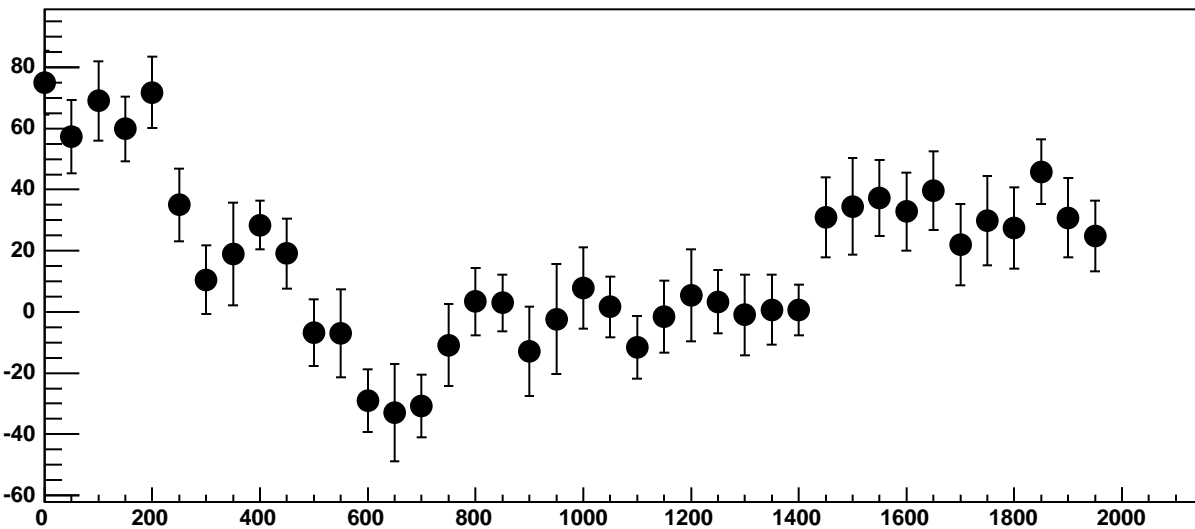


$\chi^2 / \text{ndf}$  2.882 / 11  
p0  $-1340 \pm 17.75$   
p1  $0.1686 \pm 0.01564$

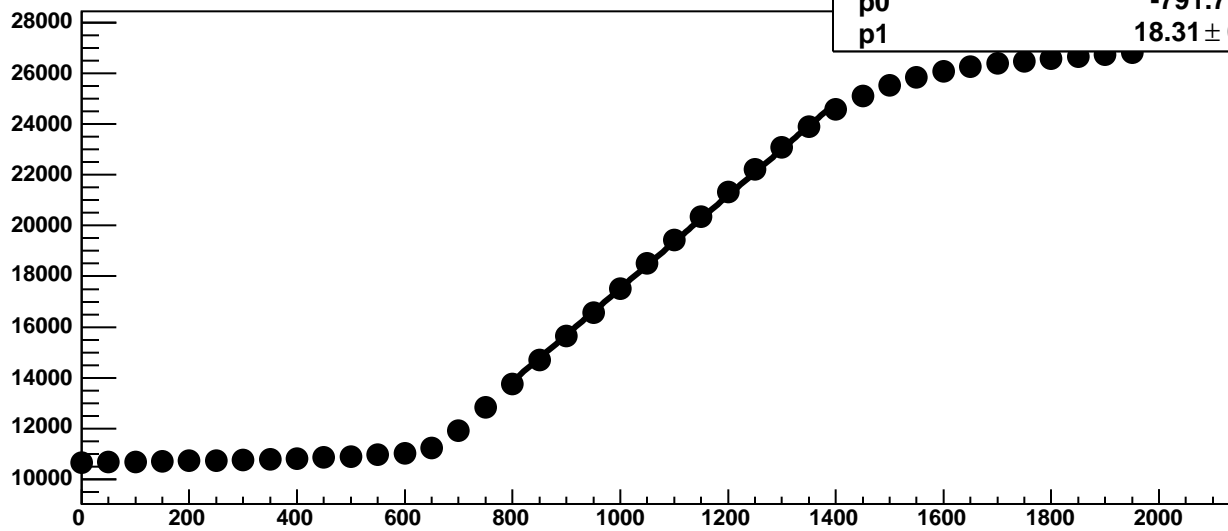
Chip 11, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC



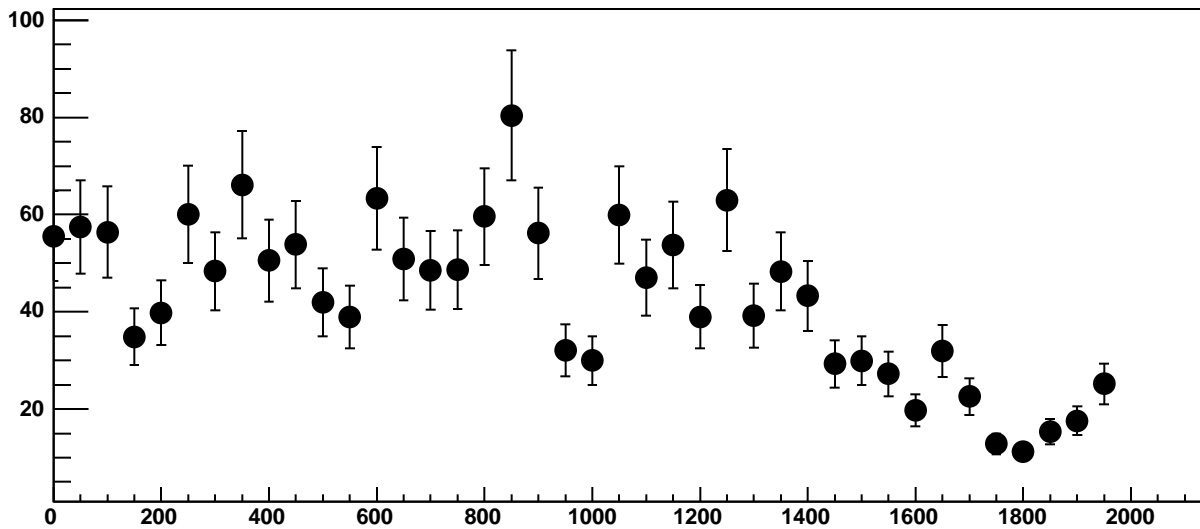
Chip 11, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC



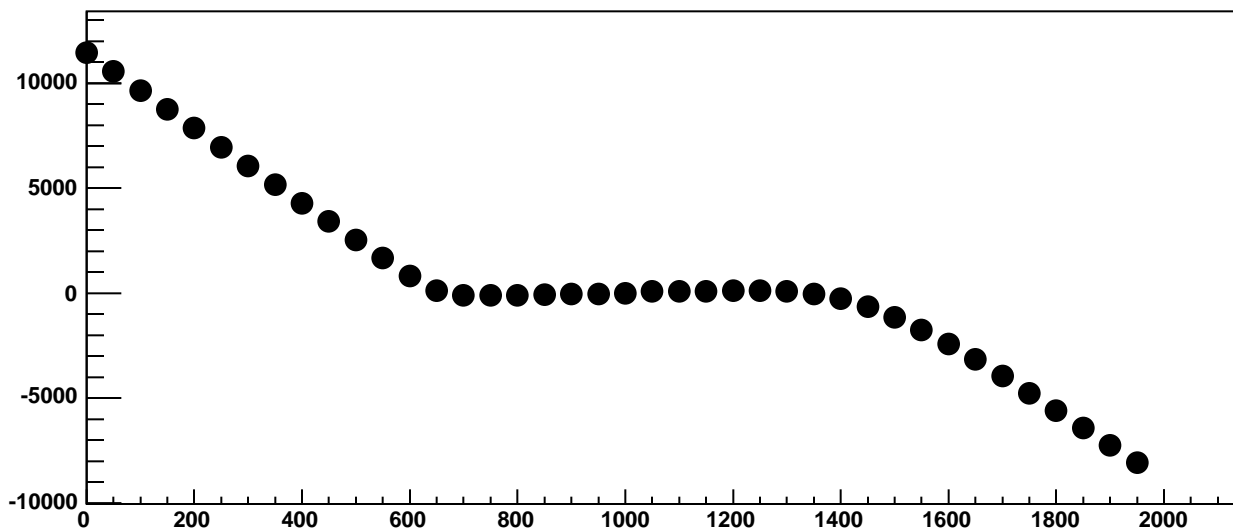
Chip 11, Channel 11, Enable 3!, Hold=30, ADC Mean vs DAC



Chip 11, Channel 11, Enable 3!, Hold=30, ADC Noise vs DAC

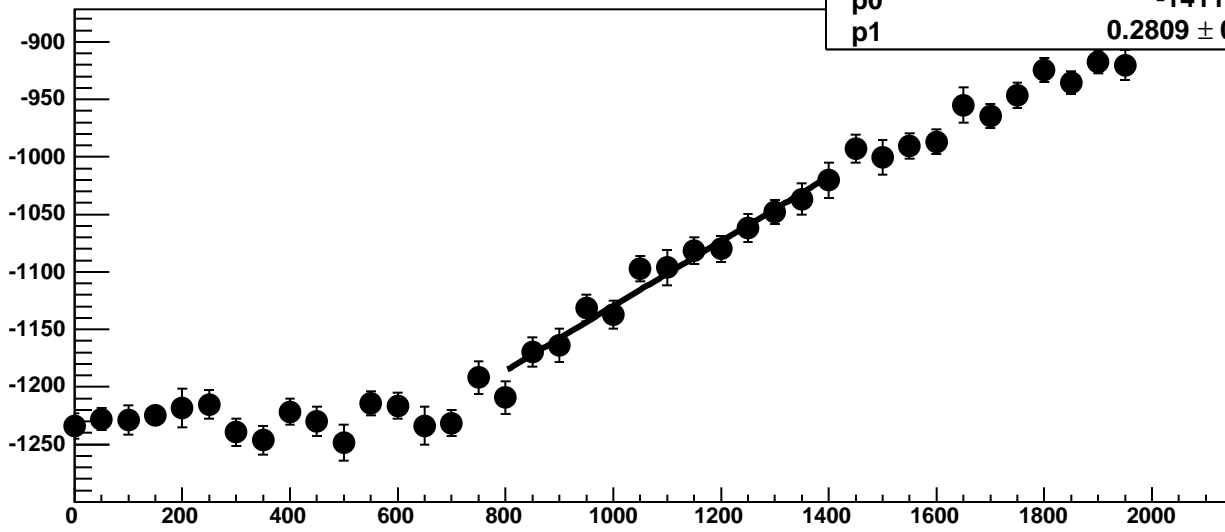


Chip 11, Channel 11, Enable 3!, Hold=30, ADC Residuals vs DAC

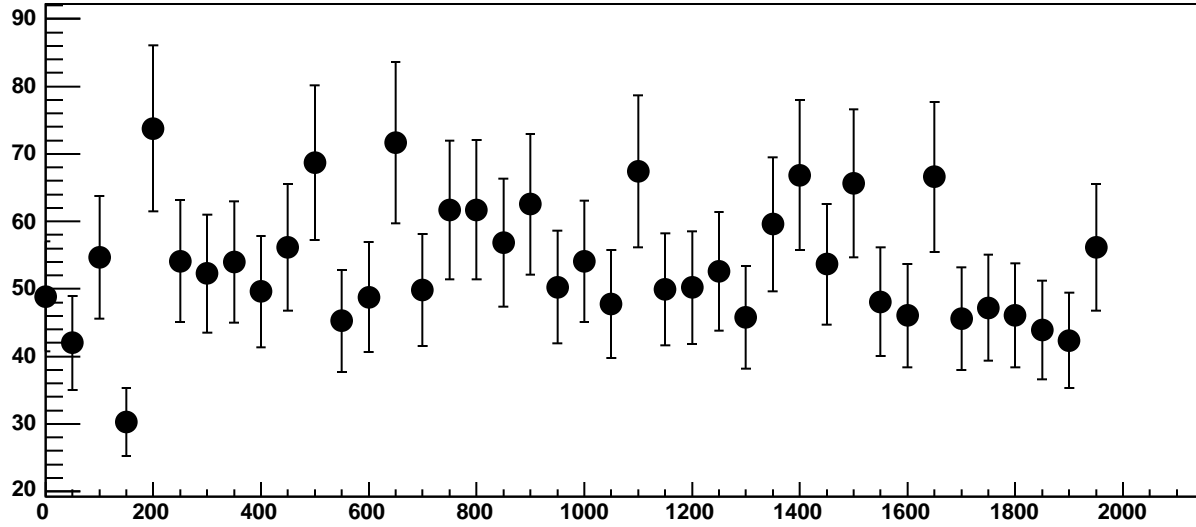




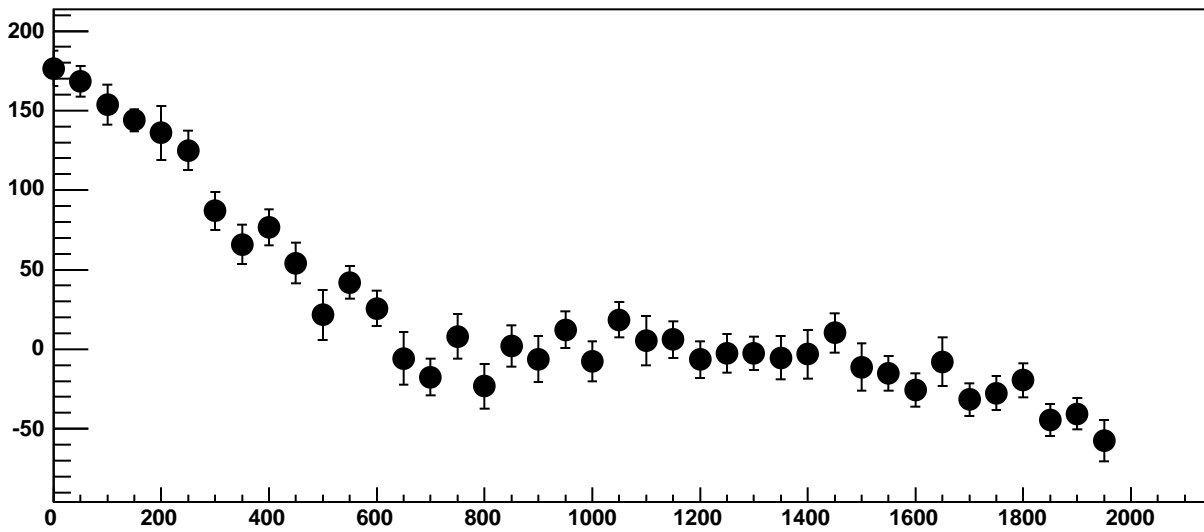
Chip 11, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC



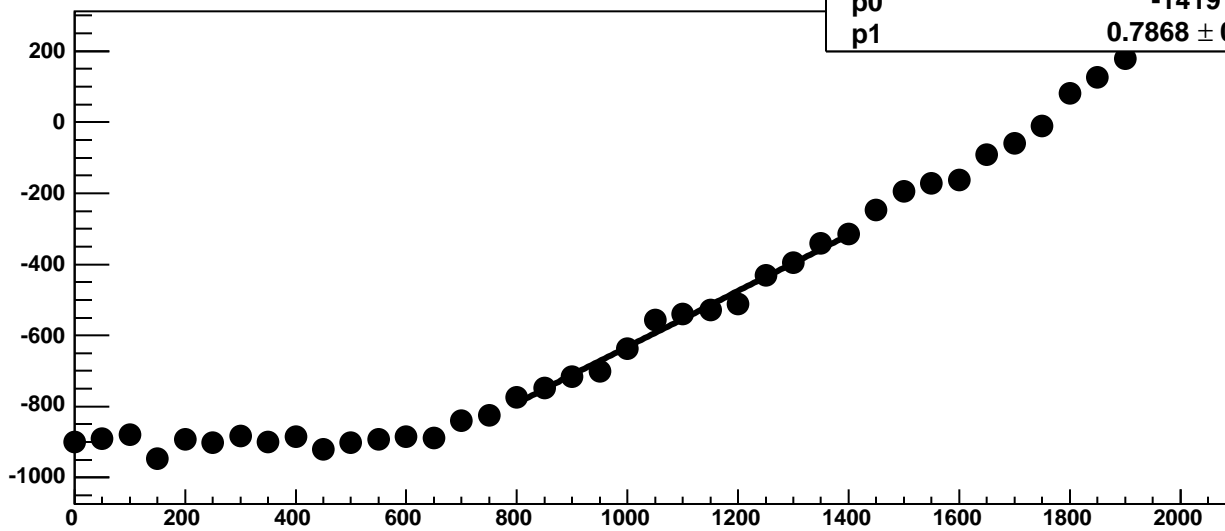
Chip 11, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC



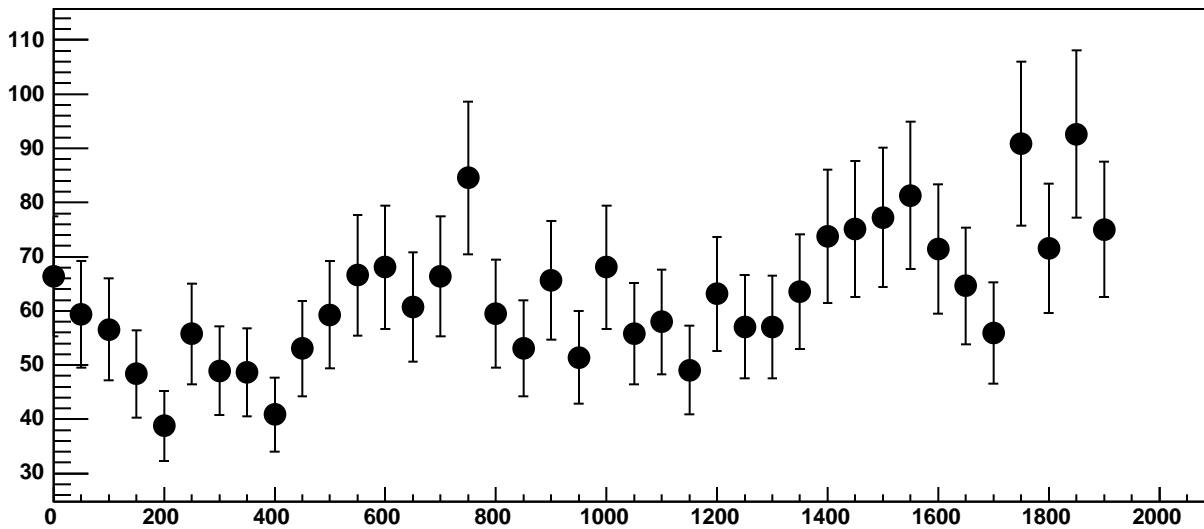
Chip 11, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC



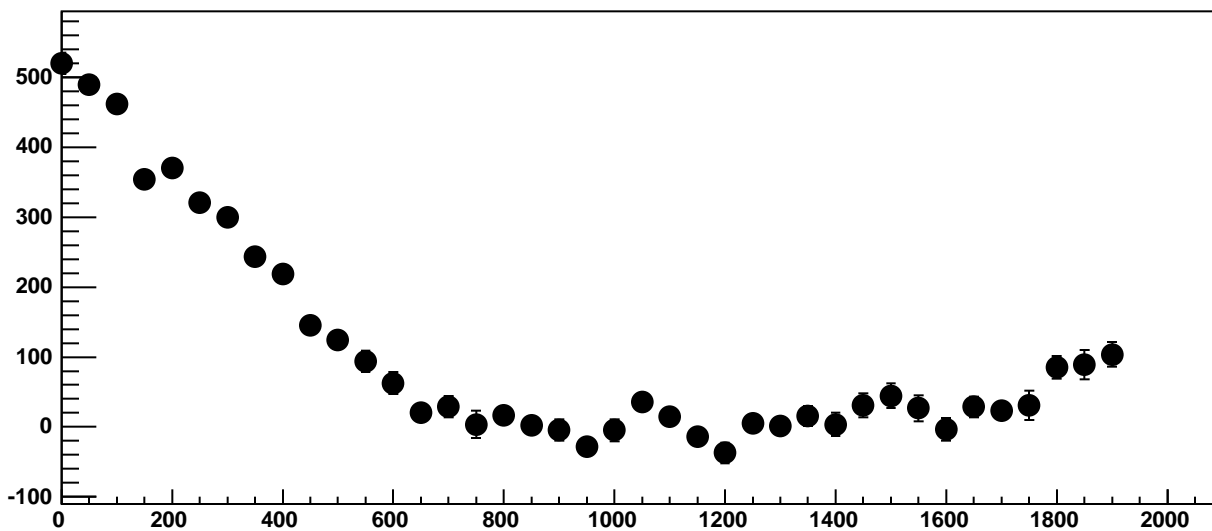
Chip 11, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC



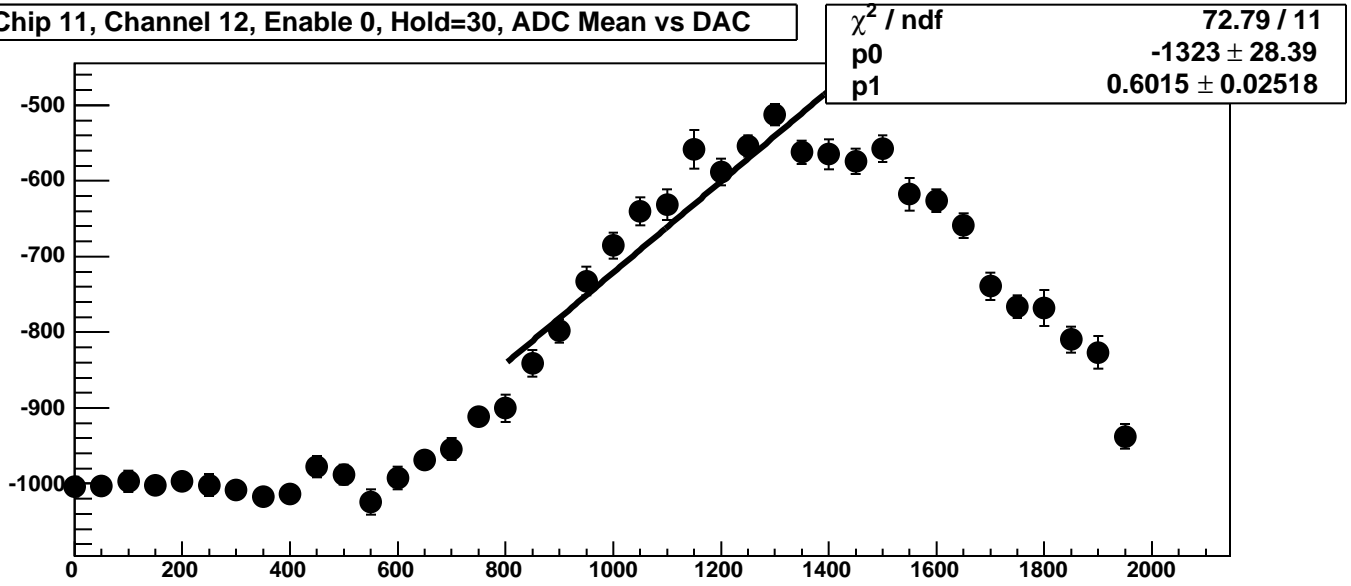
Chip 11, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC



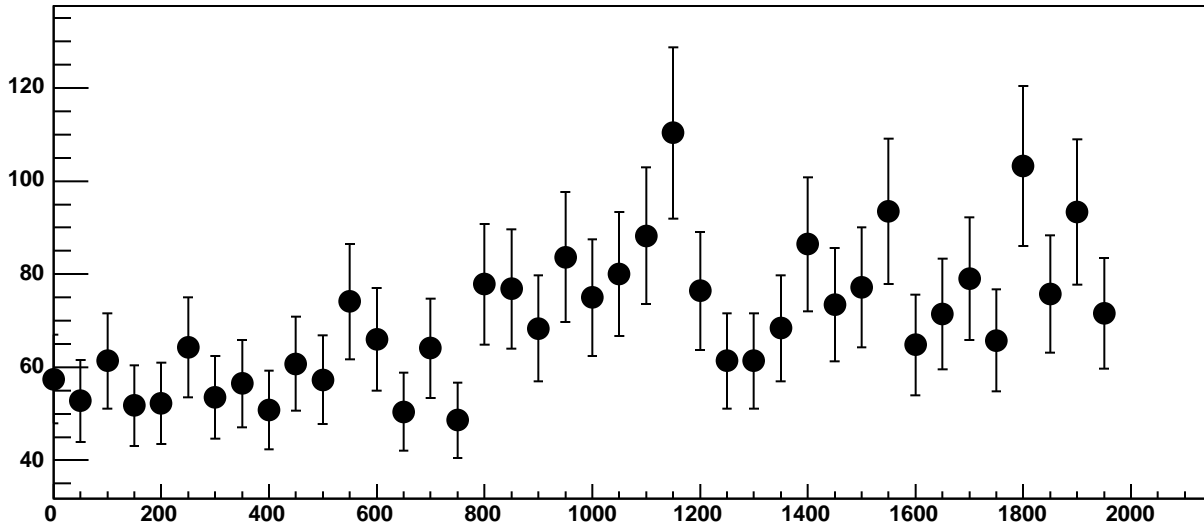
Chip 11, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC



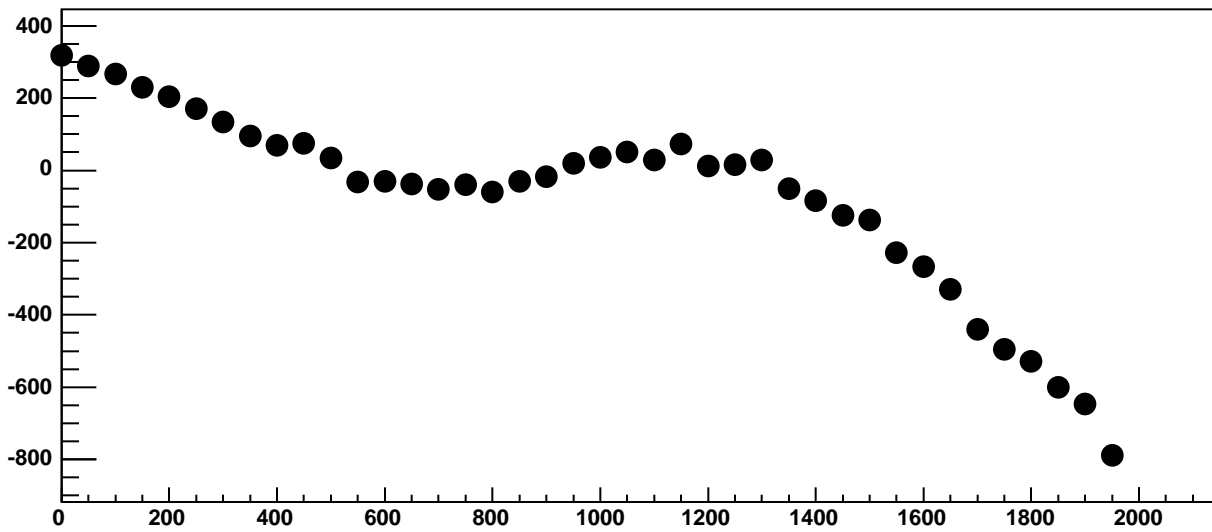
Chip 11, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC



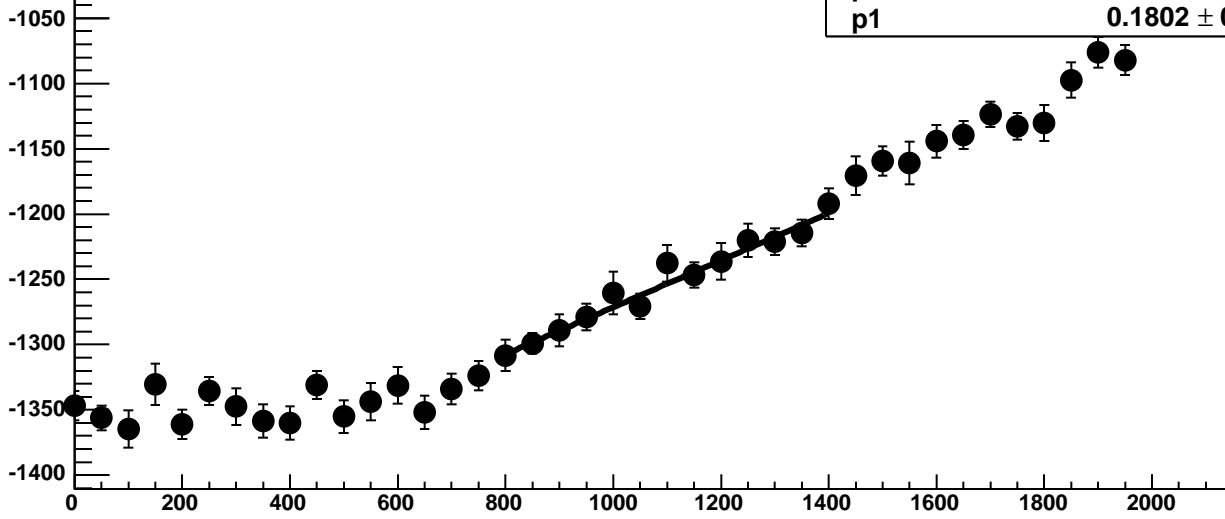
Chip 11, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC



Chip 11, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC

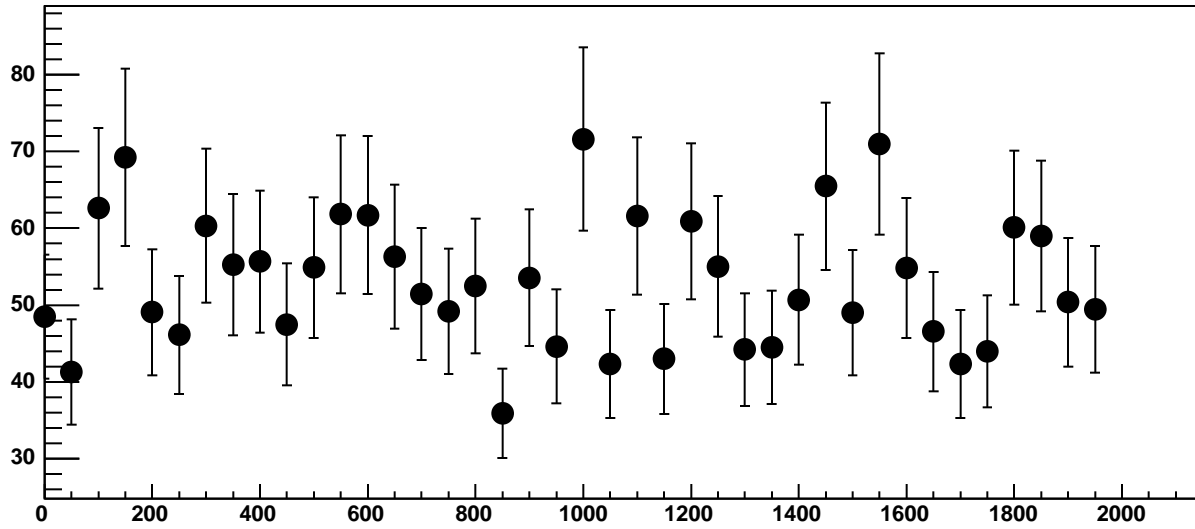


Chip 11, Channel 12, Enable 1, Hold=30, ADC Mean vs DAC

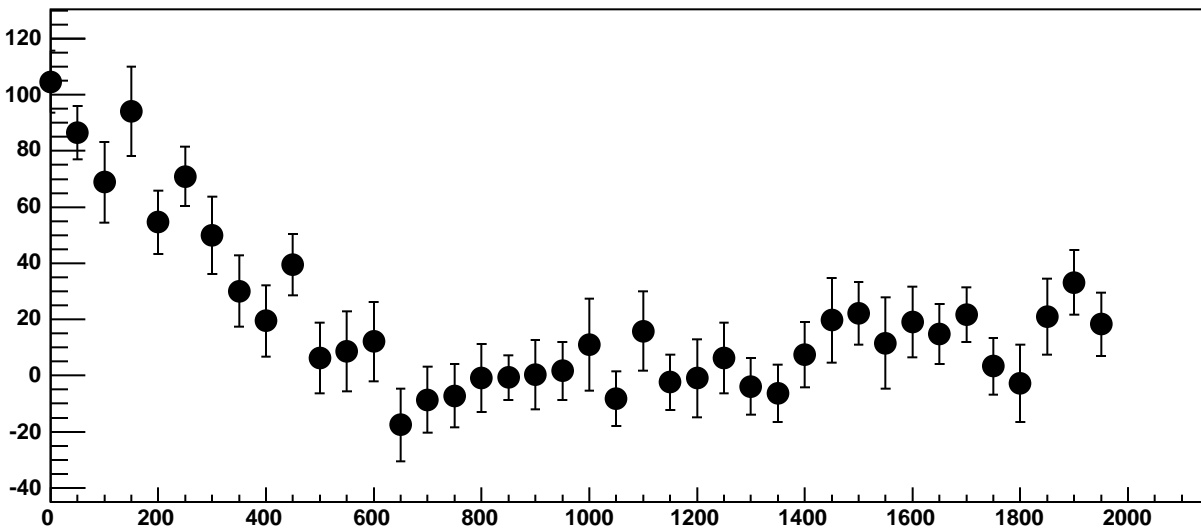


$\chi^2 / \text{ndf}$  3.684 / 11  
p0  $-1452 \pm 17.52$   
p1  $0.1802 \pm 0.01582$

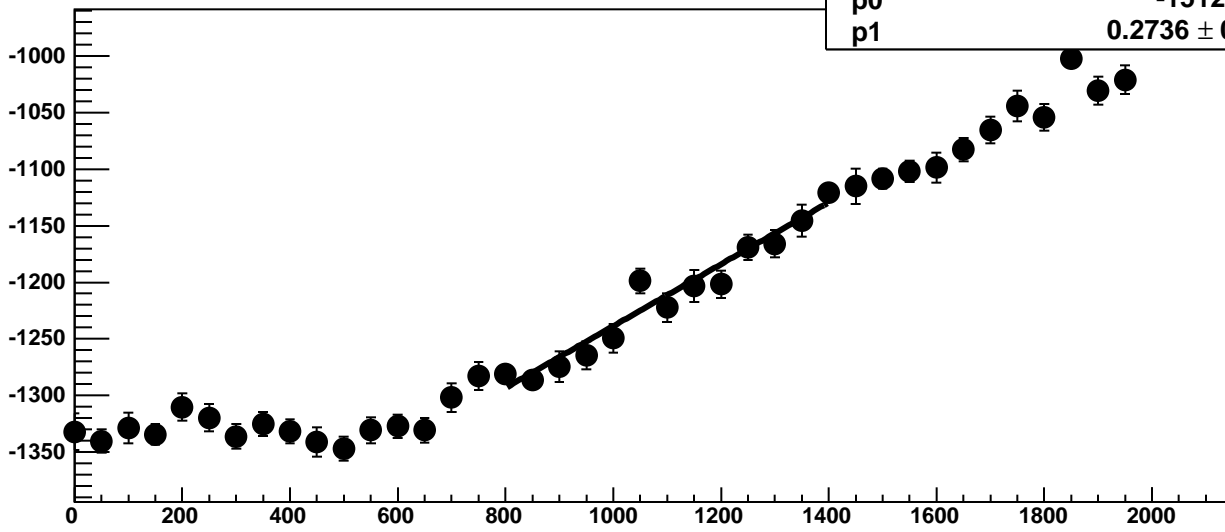
Chip 11, Channel 12, Enable 1, Hold=30, ADC Noise vs DAC



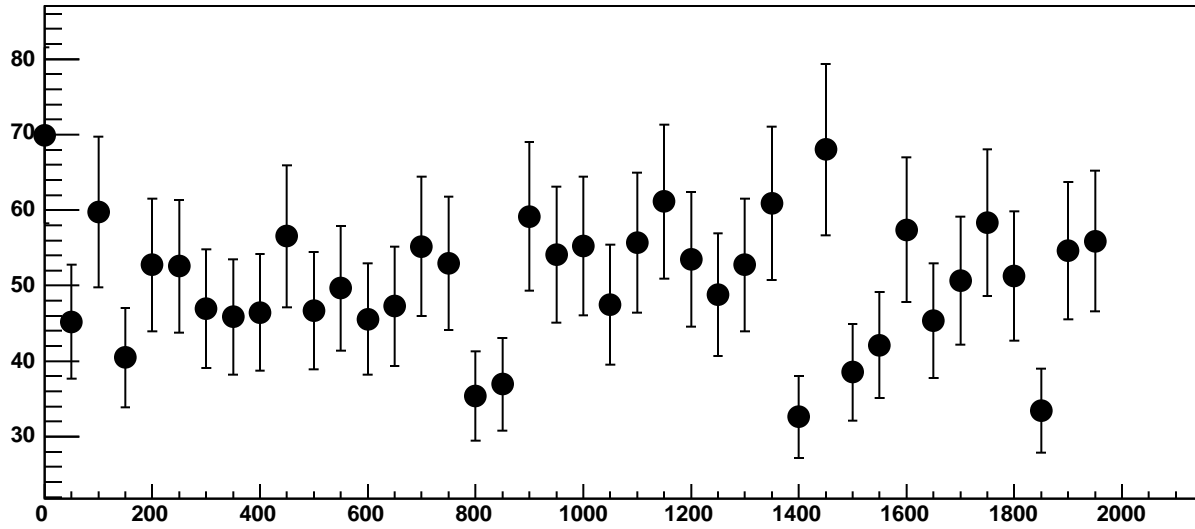
Chip 11, Channel 12, Enable 1, Hold=30, ADC Residuals vs DAC



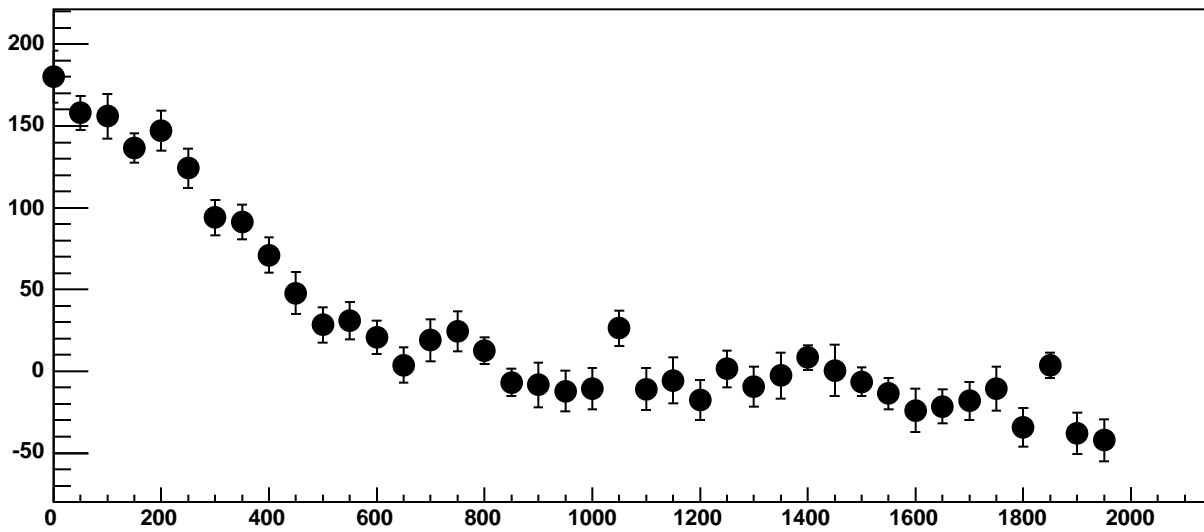
Chip 11, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC



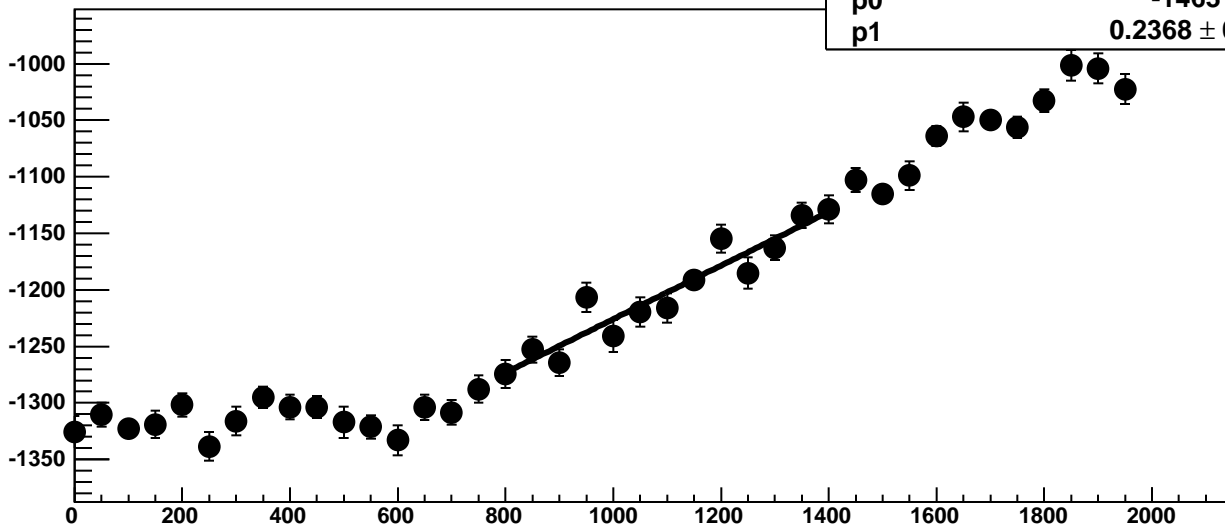
Chip 11, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC



Chip 11, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC

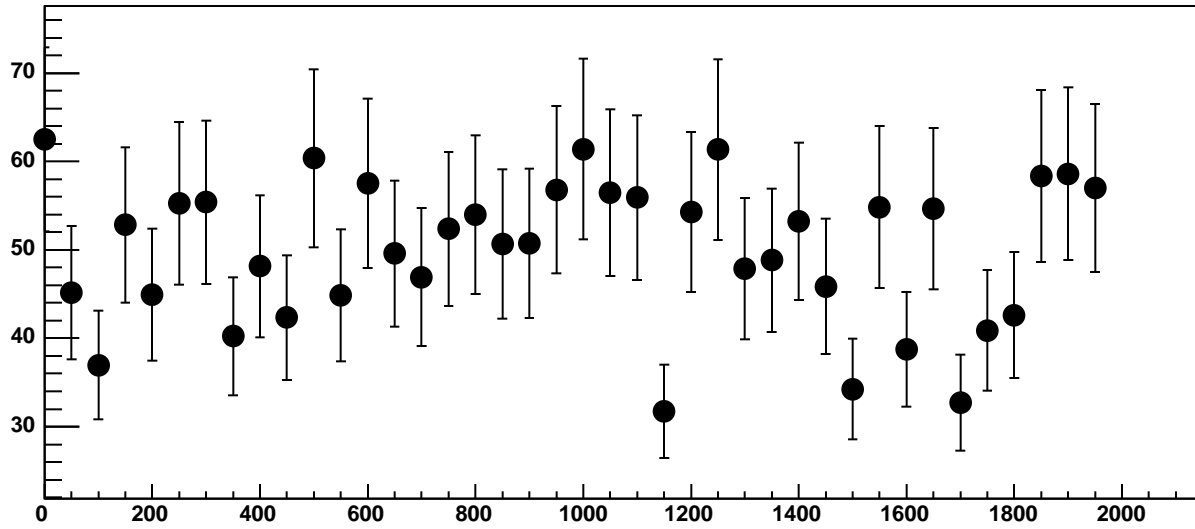


Chip 11, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC

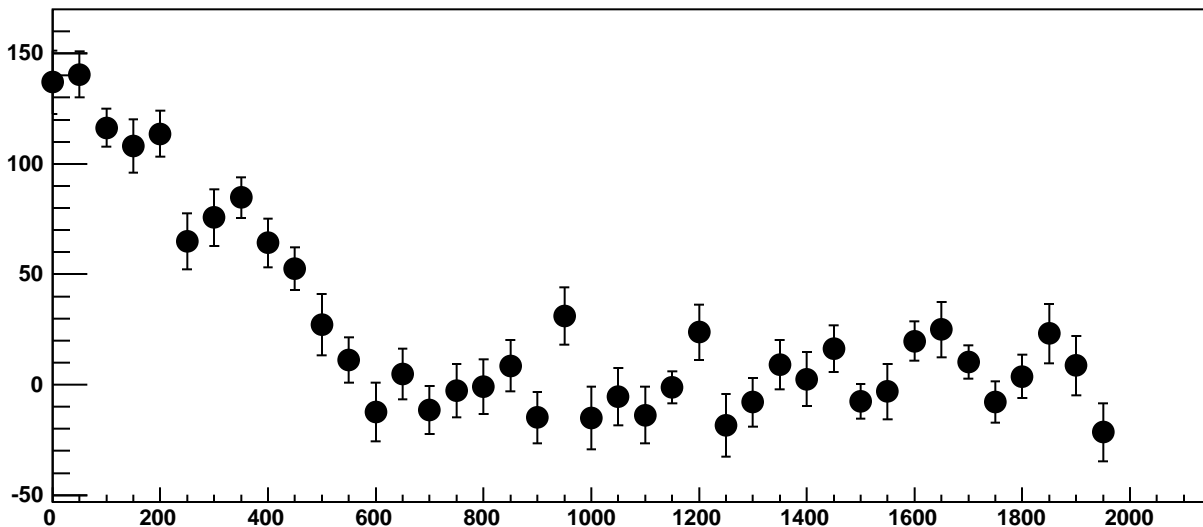


$\chi^2 / \text{ndf}$  16.9 / 11  
p0  $-1463 \pm 19.93$   
p1  $0.2368 \pm 0.01771$

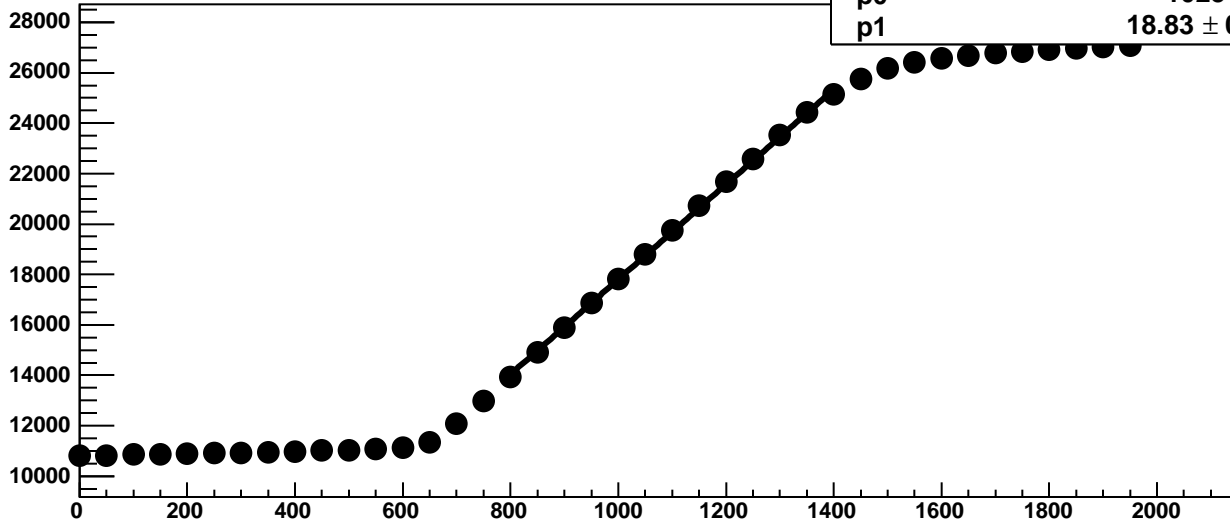
Chip 11, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC



Chip 11, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC

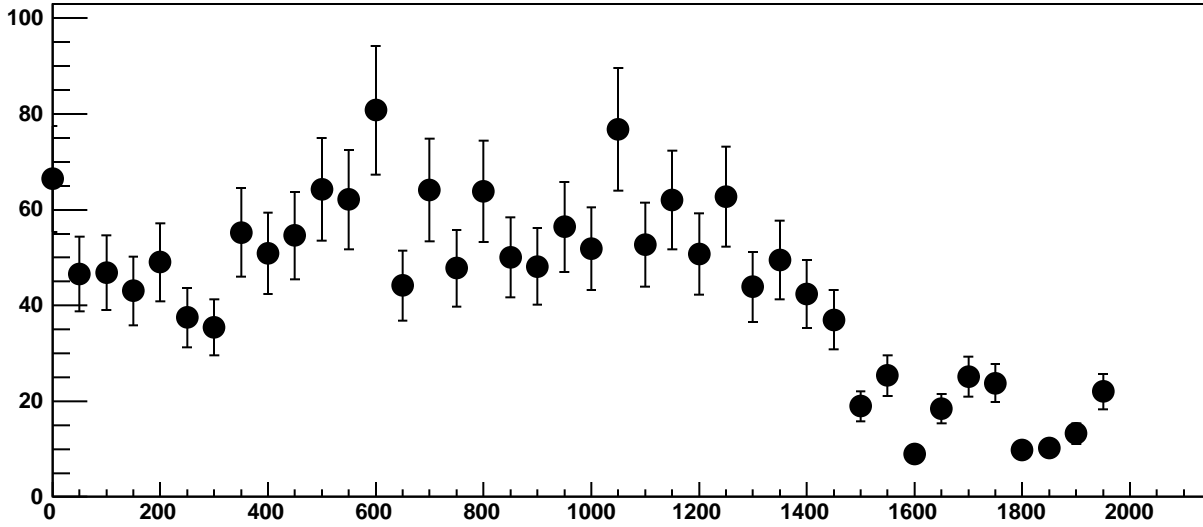


Chip 11, Channel 12, Enable 4!, Hold=30, ADC Mean vs DAC

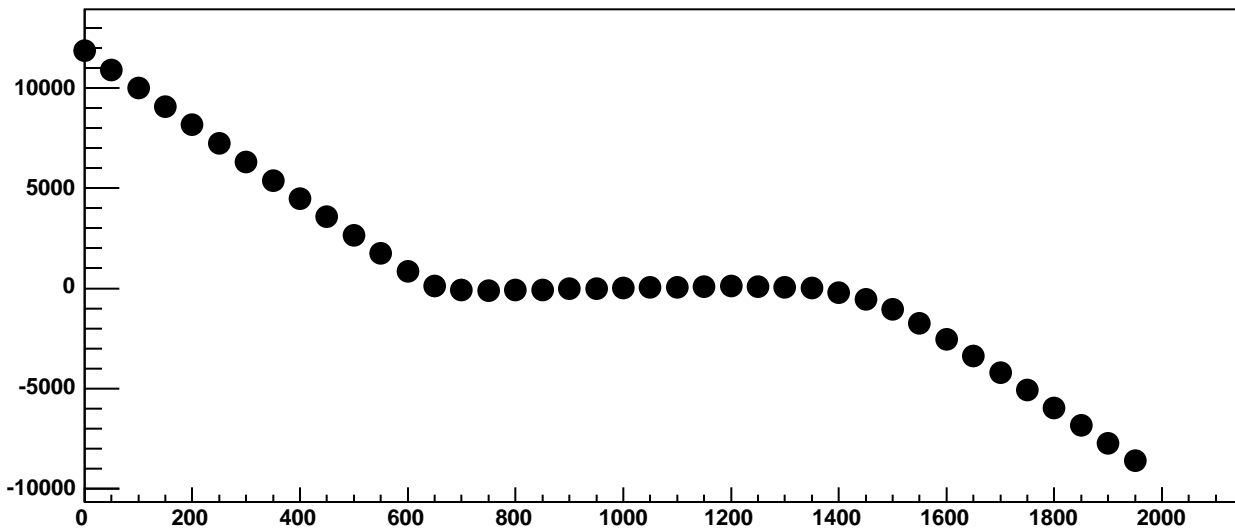


$\chi^2 / \text{ndf}$  774.6 / 11  
p0  $-1025 \pm 19.53$   
p1  $18.83 \pm 0.01712$

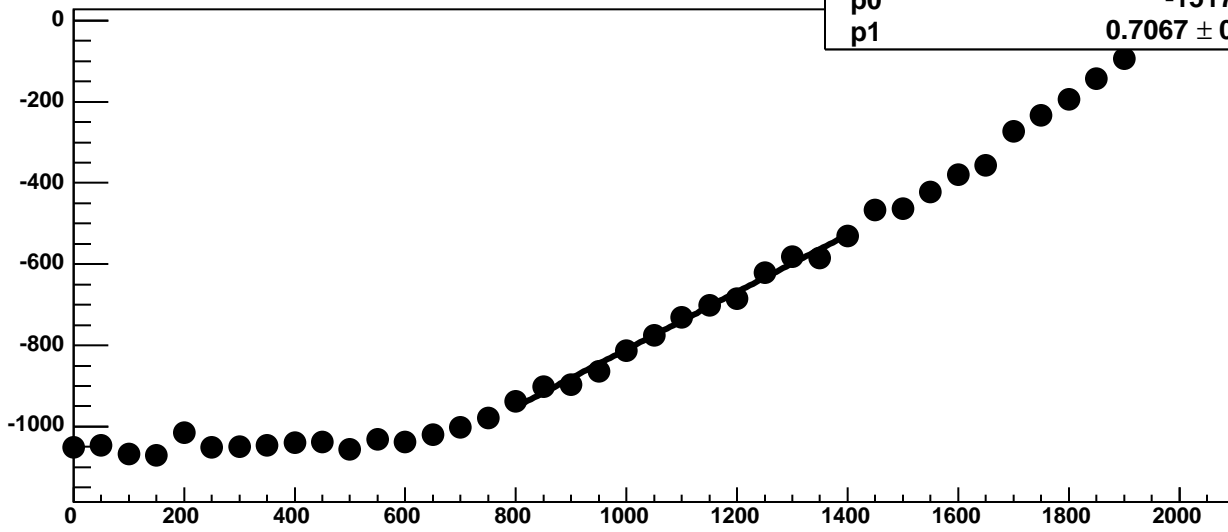
Chip 11, Channel 12, Enable 4!, Hold=30, ADC Noise vs DAC



Chip 11, Channel 12, Enable 4!, Hold=30, ADC Residuals vs DAC

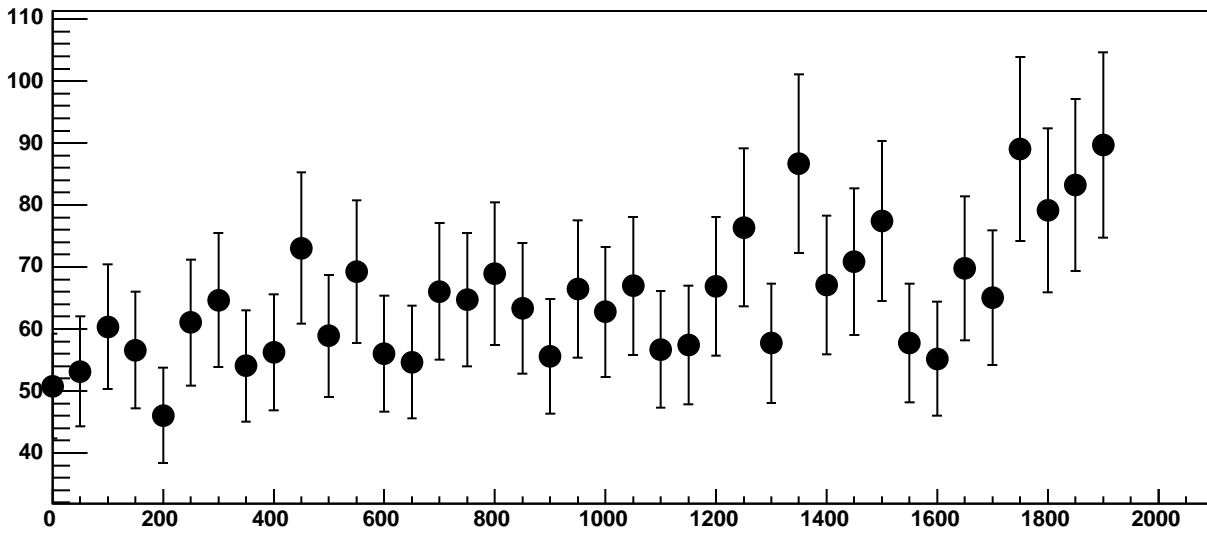


Chip 11, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC

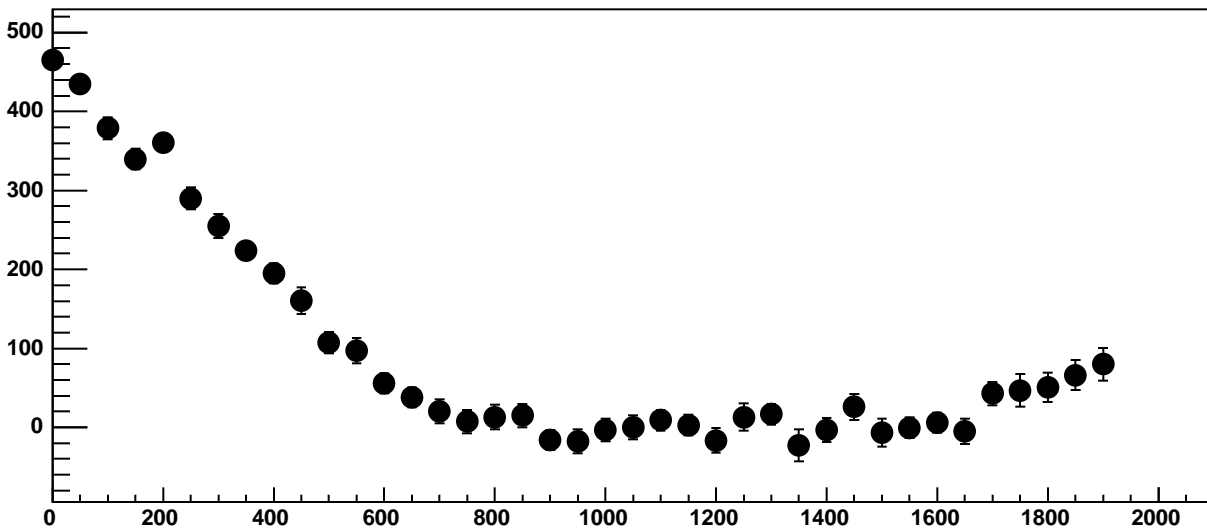


$\chi^2 / \text{ndf}$  9.927 / 11  
p0  $-1517 \pm 25$   
p1  $0.7067 \pm 0.02267$

Chip 11, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC

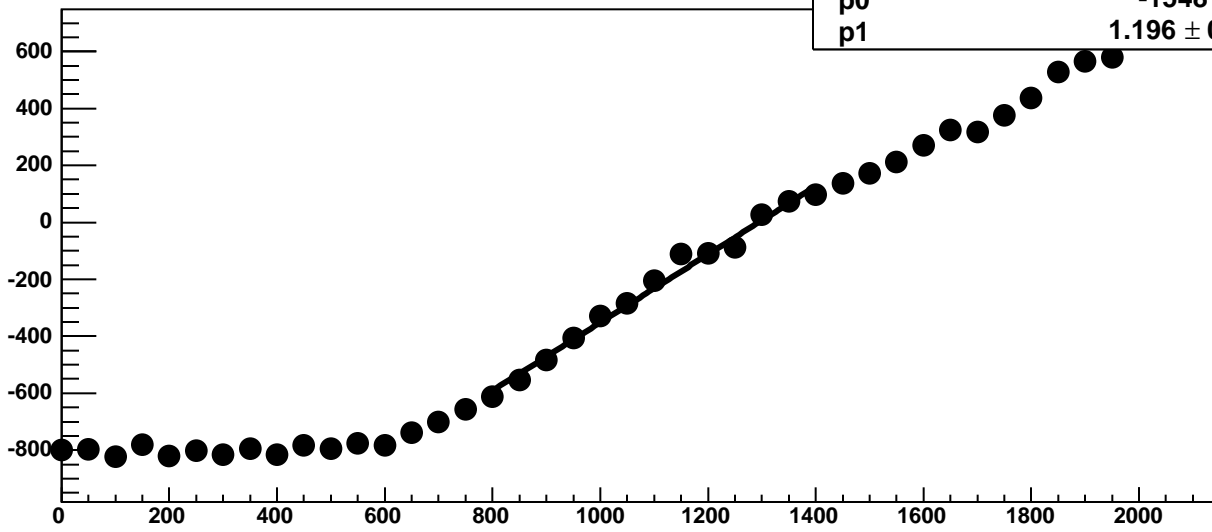


Chip 11, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC

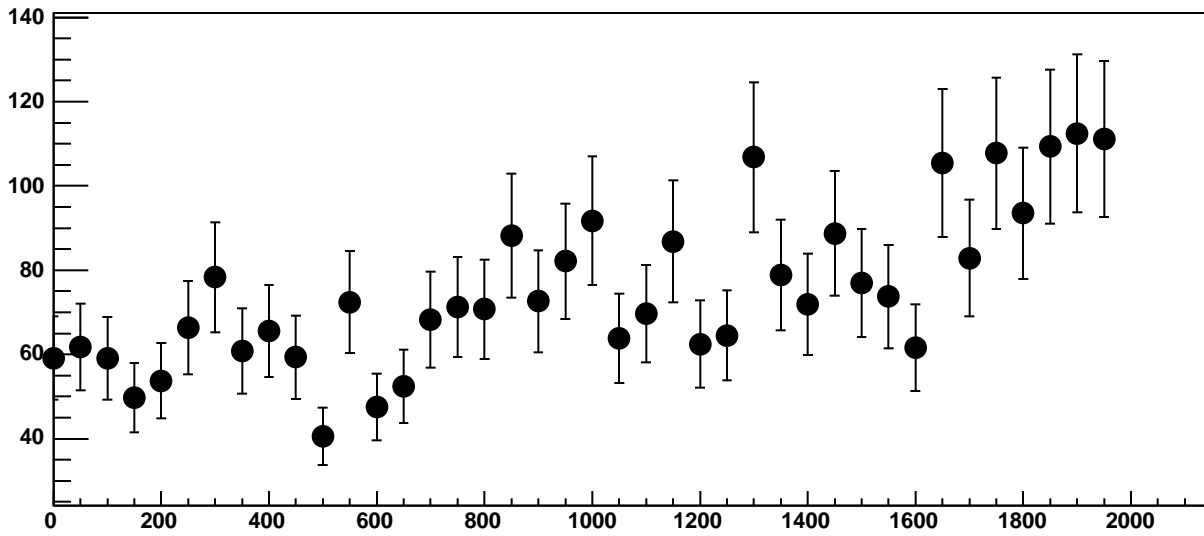




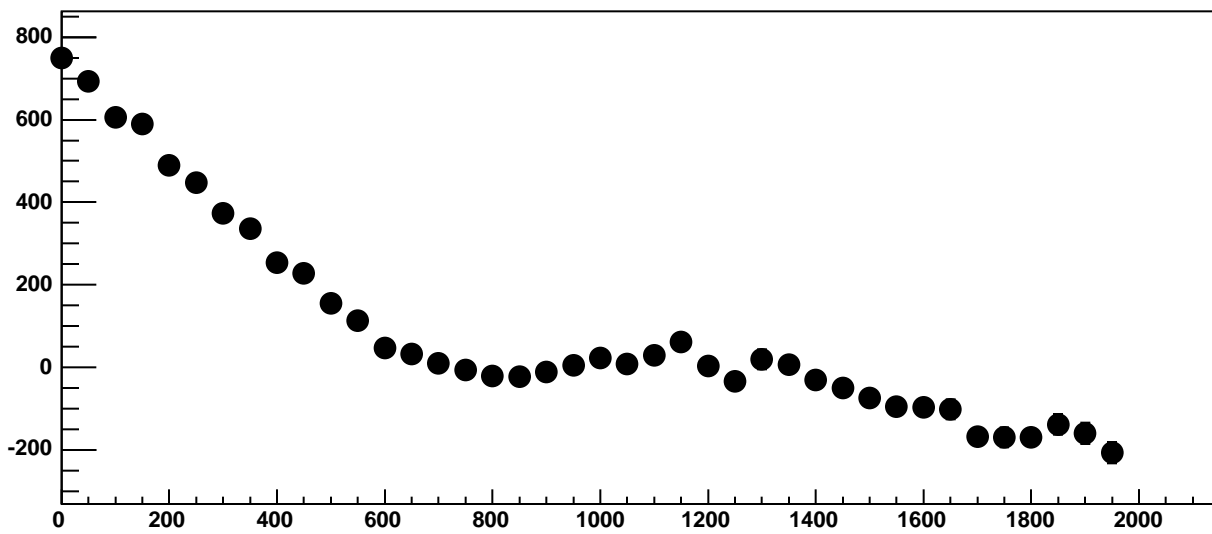
Chip 11, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC



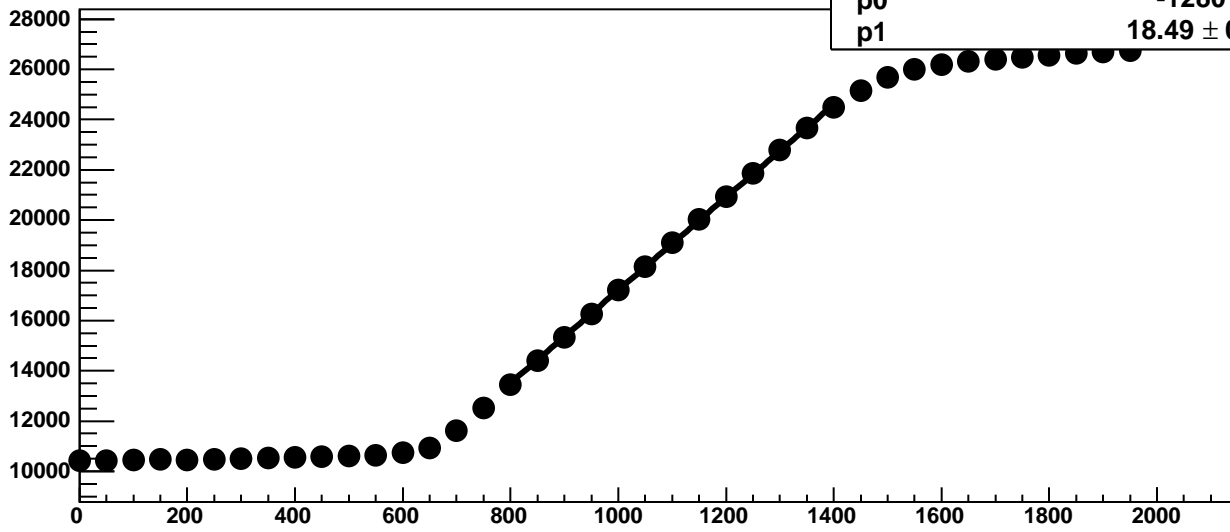
Chip 11, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC



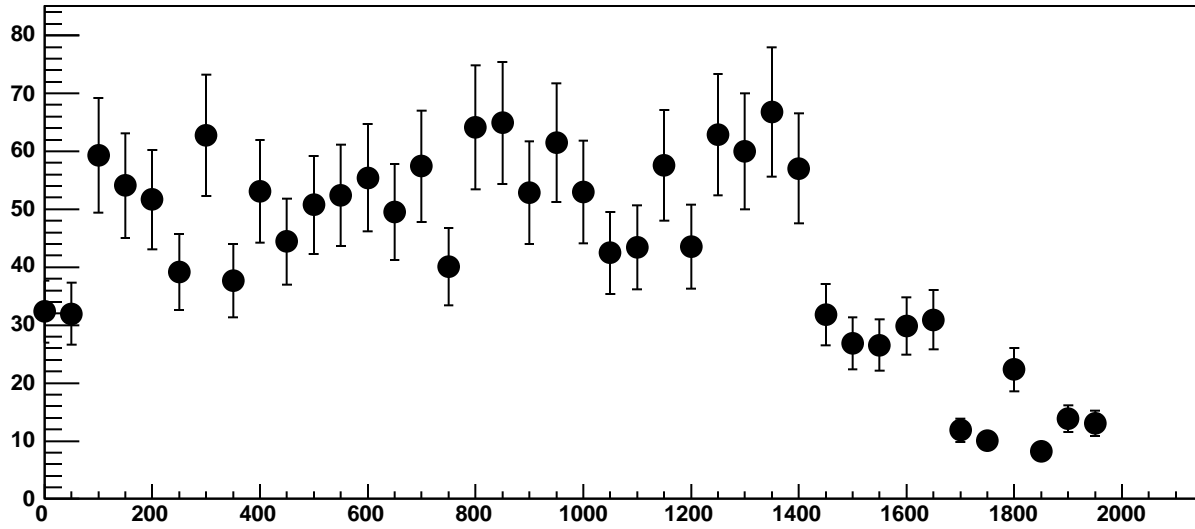
Chip 11, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC



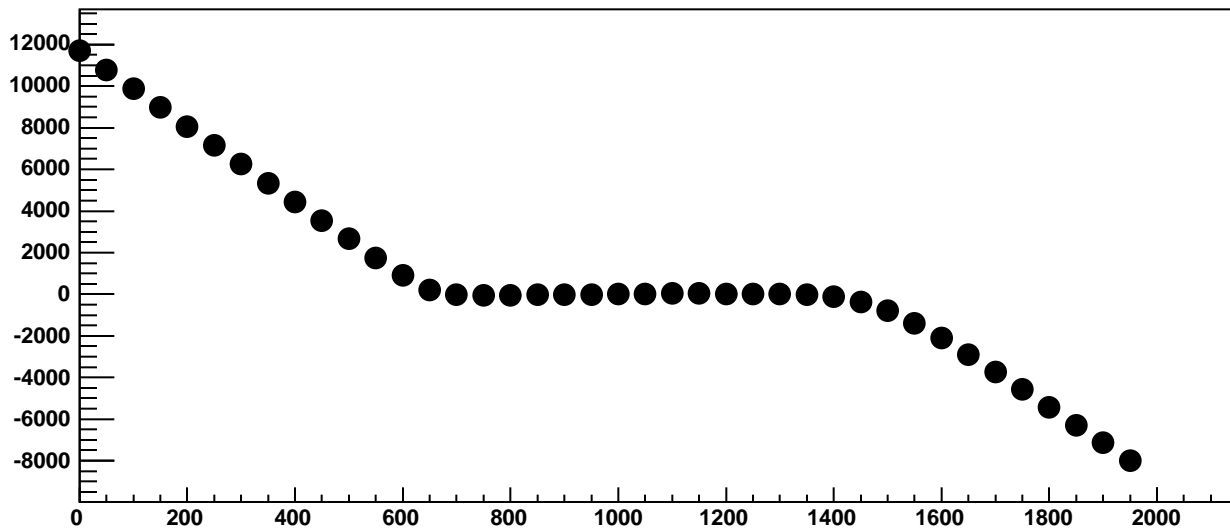
Chip 11, Channel 13, Enable 1!, Hold=30, ADC Mean vs DAC



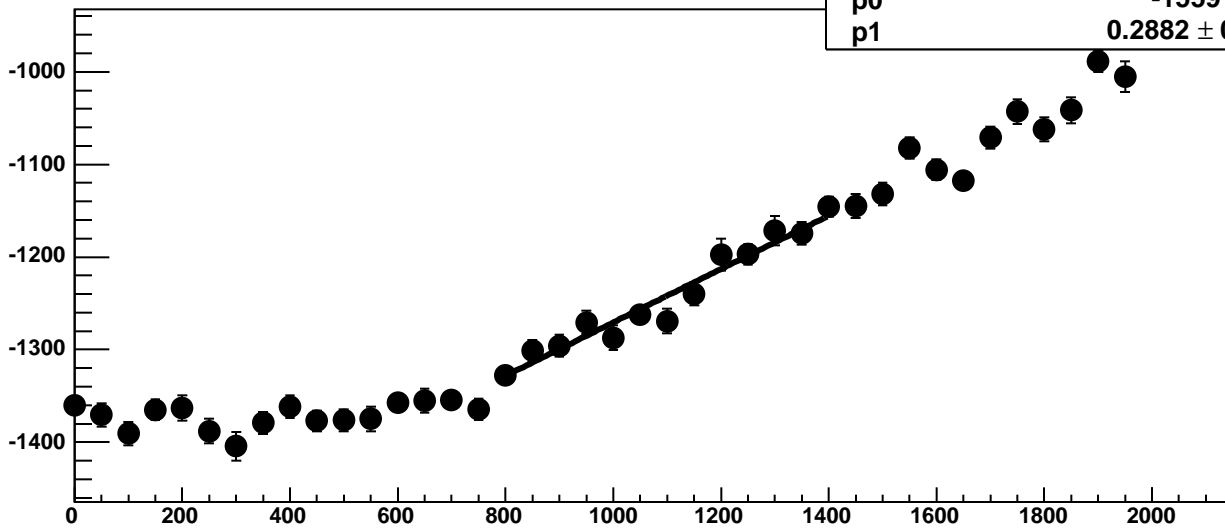
Chip 11, Channel 13, Enable 1!, Hold=30, ADC Noise vs DAC



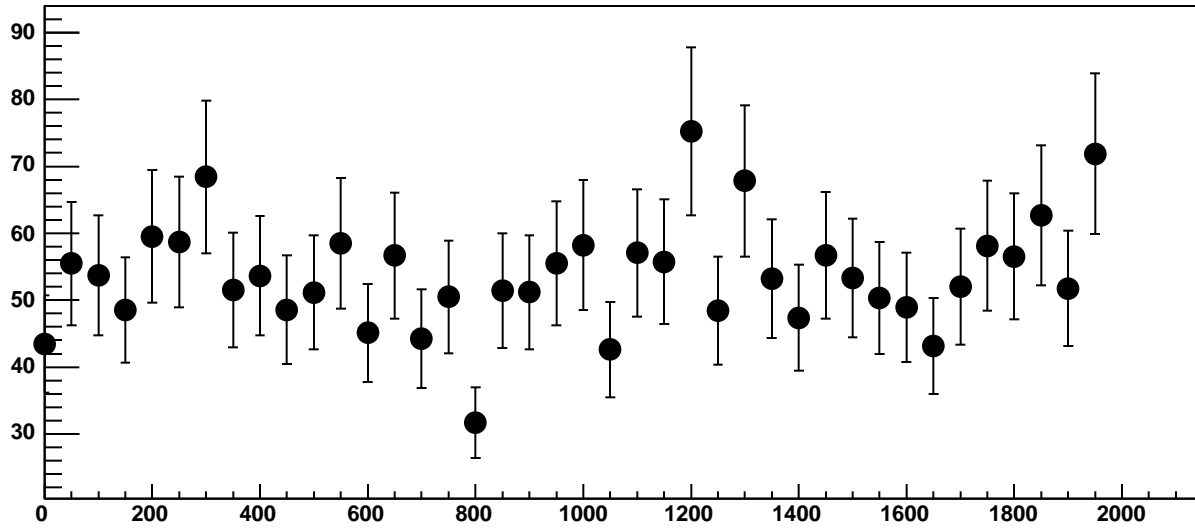
Chip 11, Channel 13, Enable 1!, Hold=30, ADC Residuals vs DAC



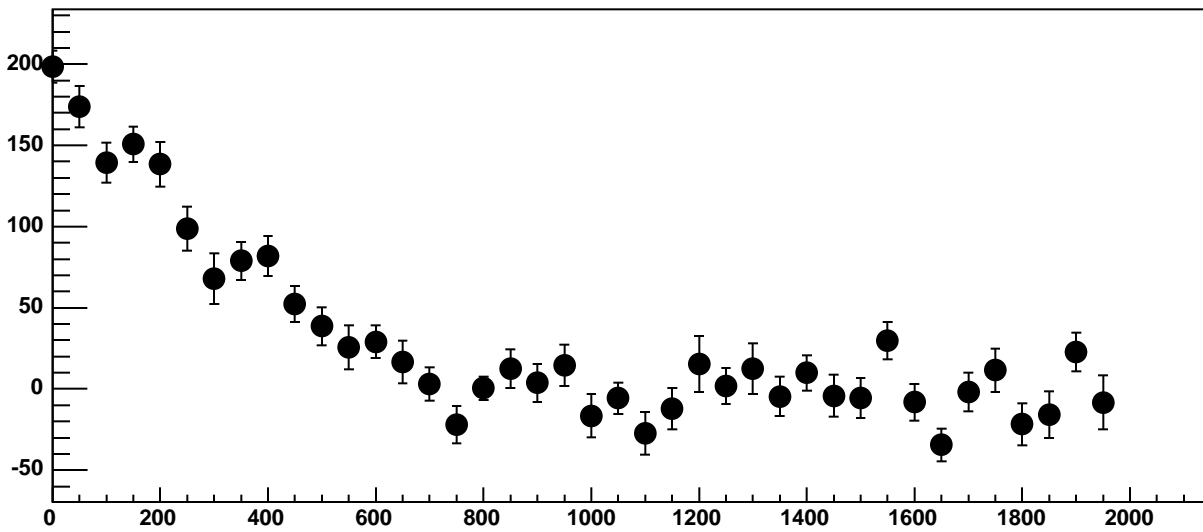
Chip 11, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC



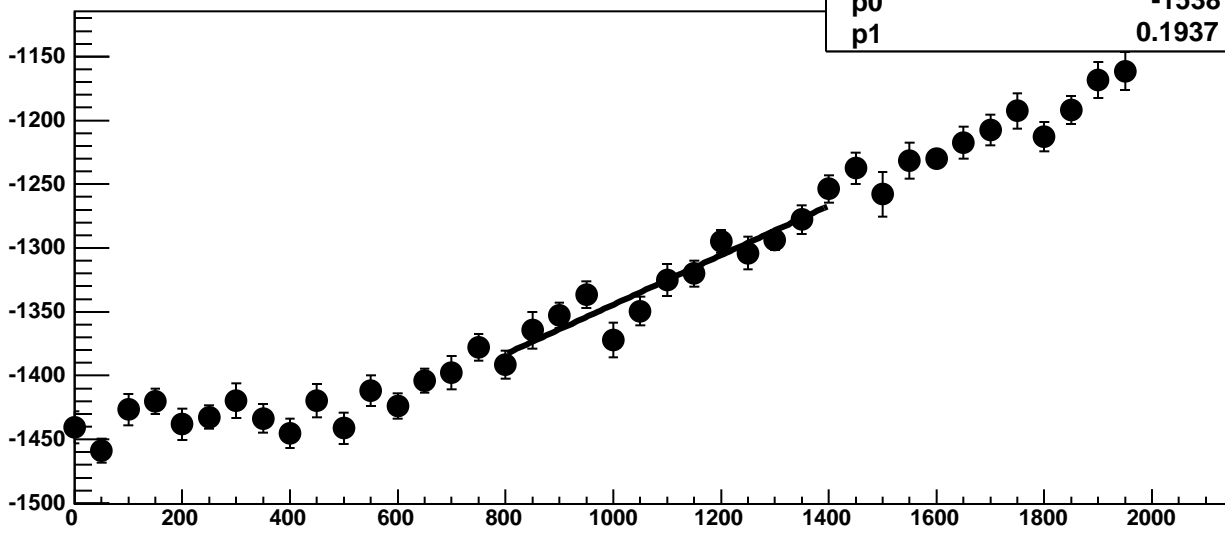
Chip 11, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC



Chip 11, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC

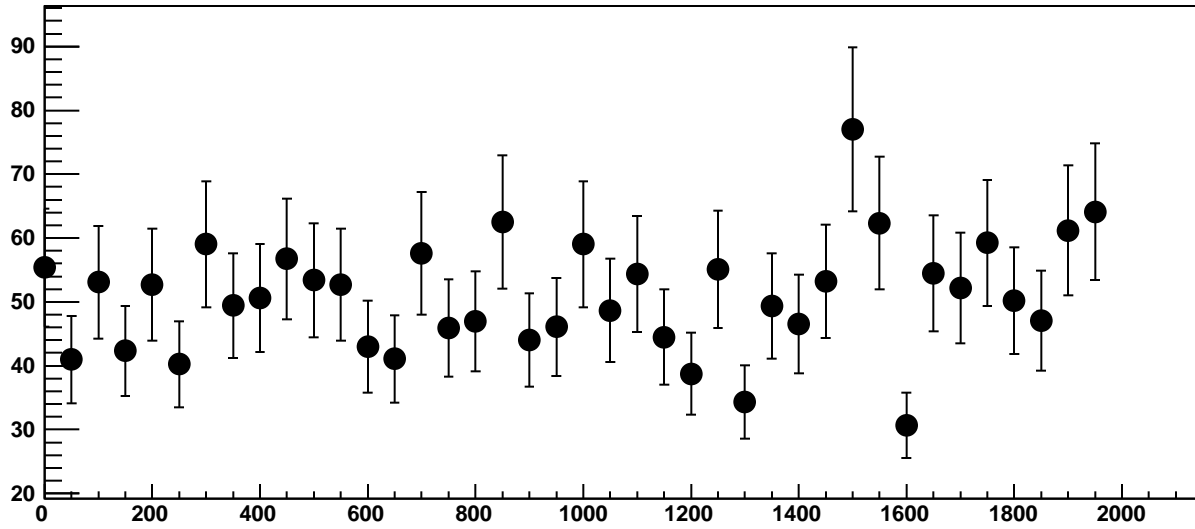


Chip 11, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC

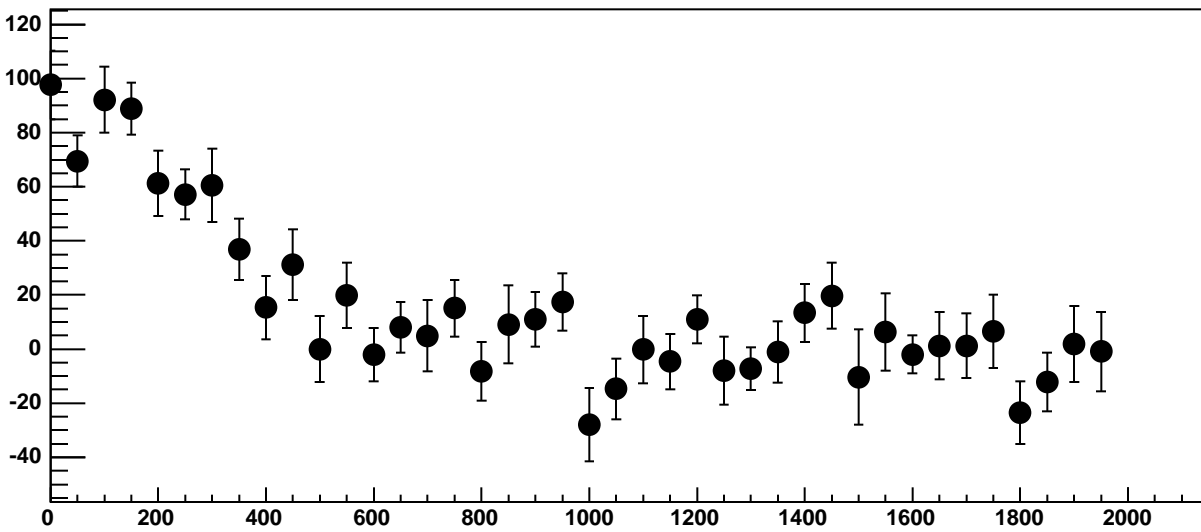


$\chi^2 / \text{ndf}$  15.43 / 11  
p0  $-1538 \pm 18.19$   
p1  $0.1937 \pm 0.016$

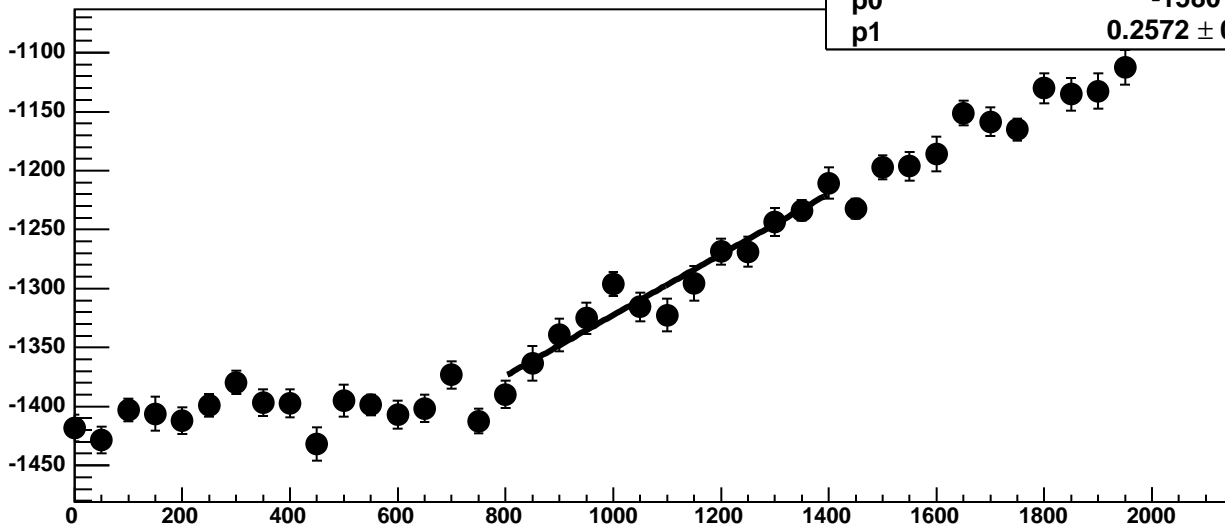
Chip 11, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC



Chip 11, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC

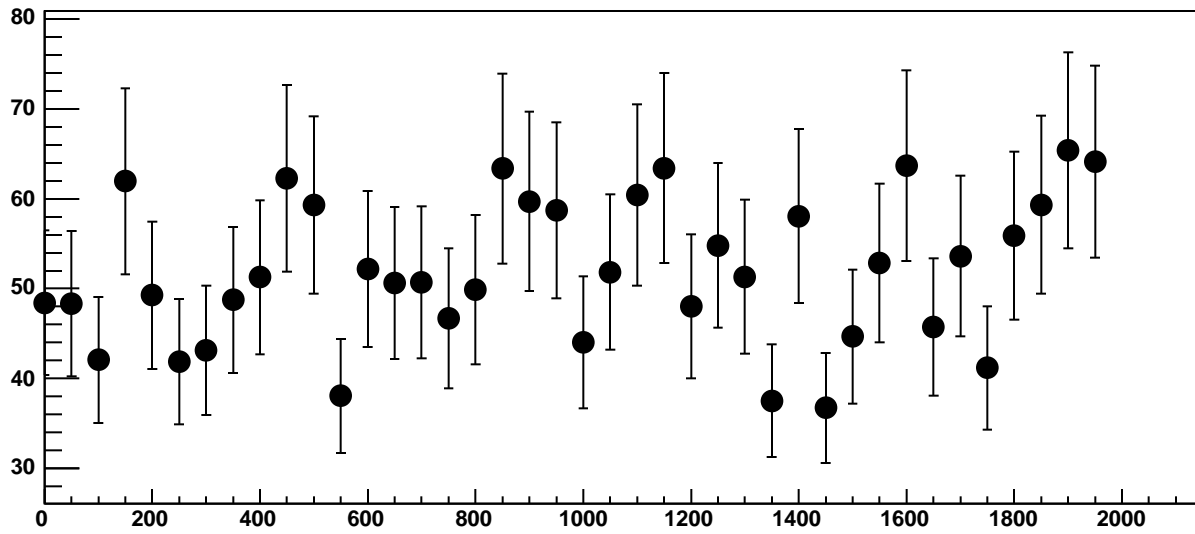


Chip 11, Channel 13, Enable 4, Hold=30, ADC Mean vs DAC

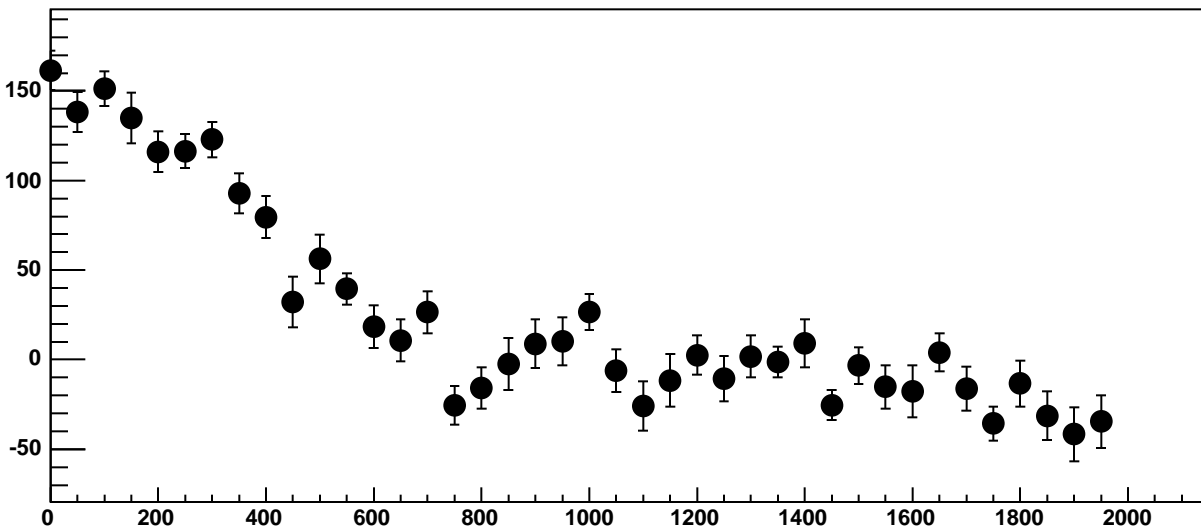


$\chi^2 / \text{ndf}$  15.43 / 11  
p0  $-1580 \pm 19.85$   
p1  $0.2572 \pm 0.01747$

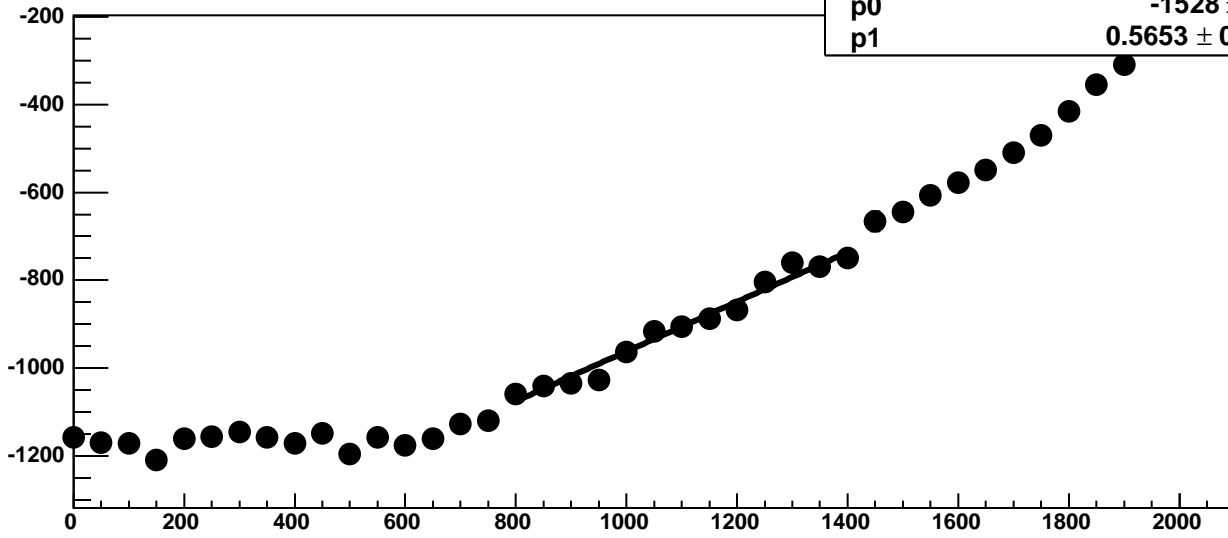
Chip 11, Channel 13, Enable 4, Hold=30, ADC Noise vs DAC



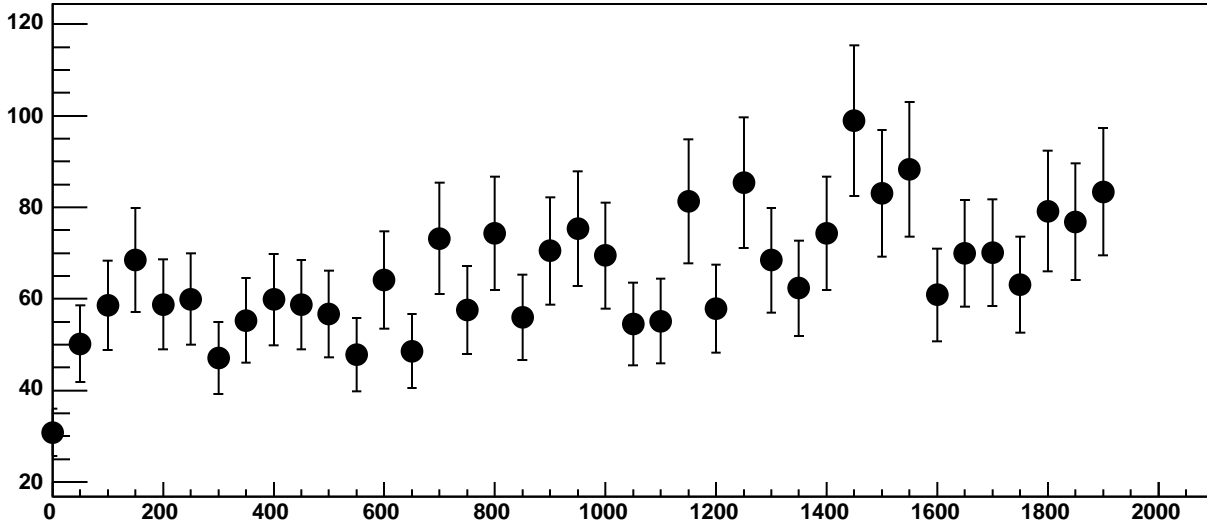
Chip 11, Channel 13, Enable 4, Hold=30, ADC Residuals vs DAC



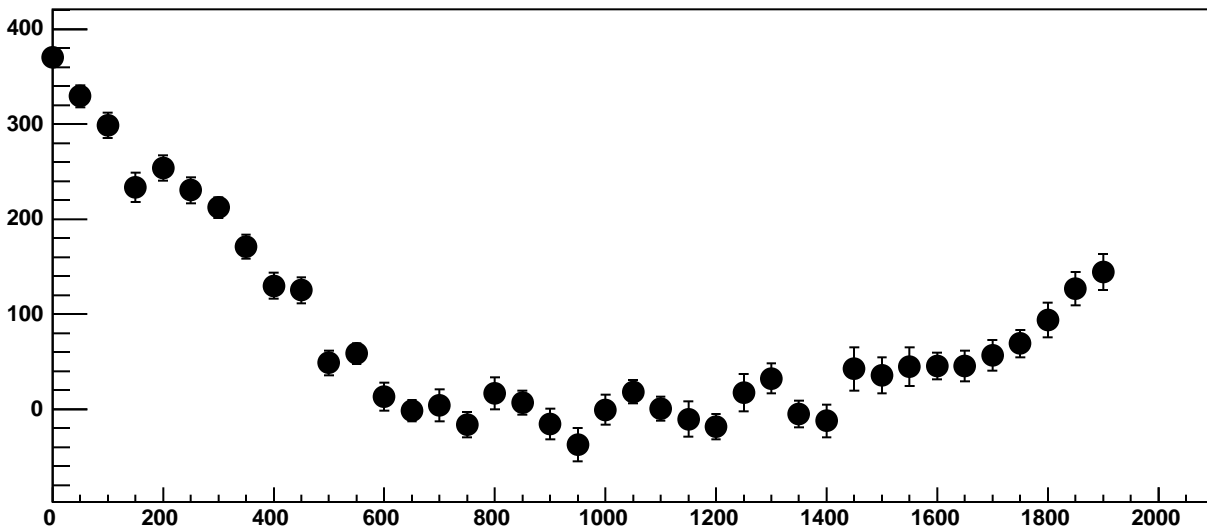
Chip 11, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC



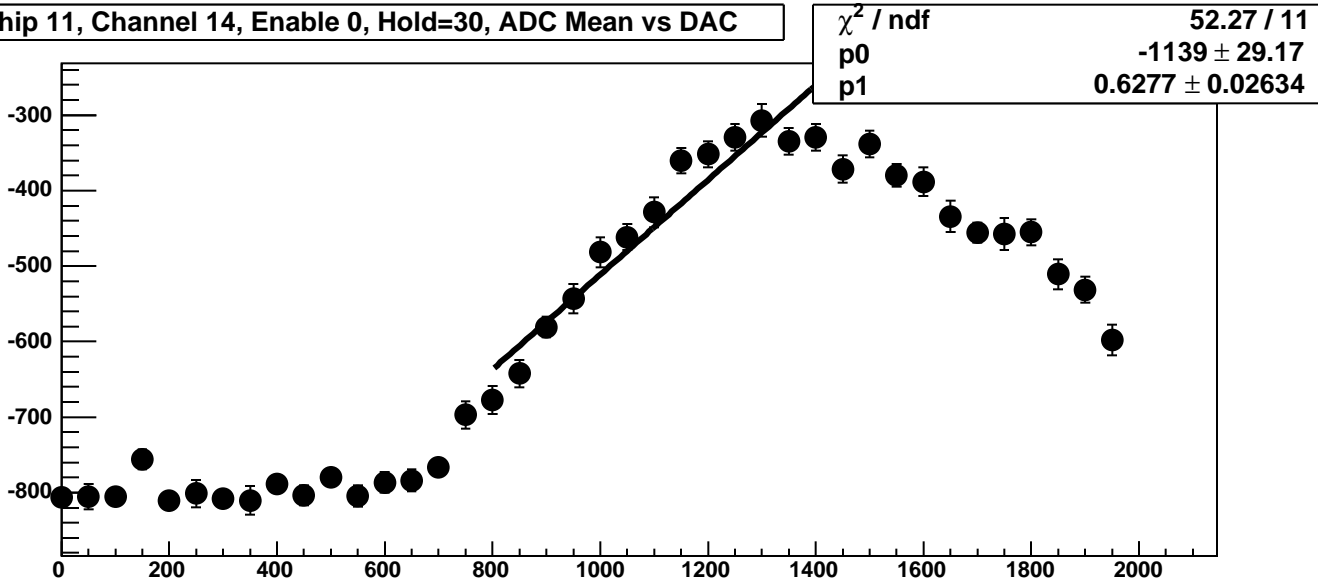
Chip 11, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC



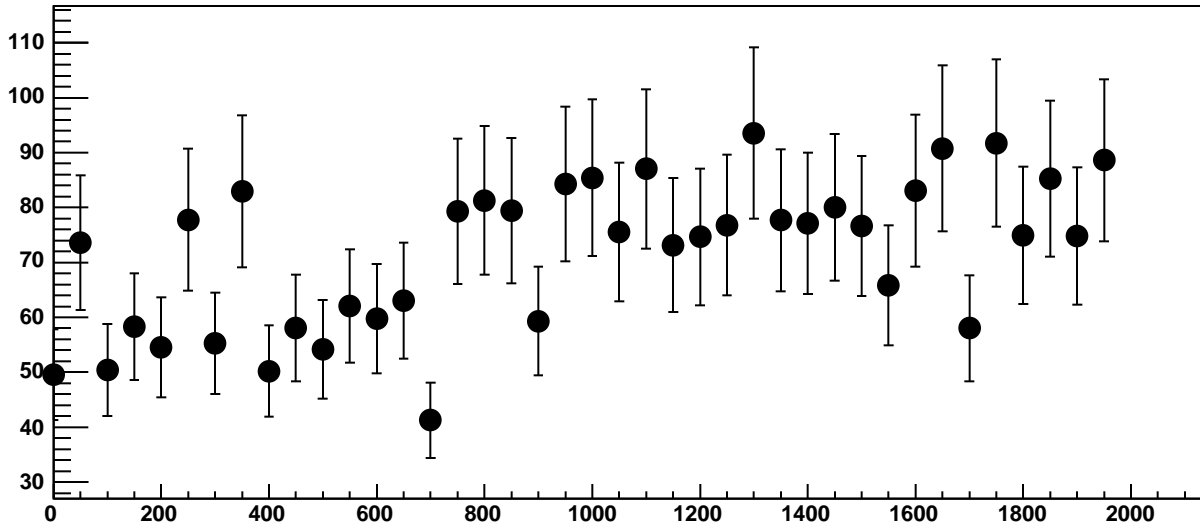
Chip 11, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC



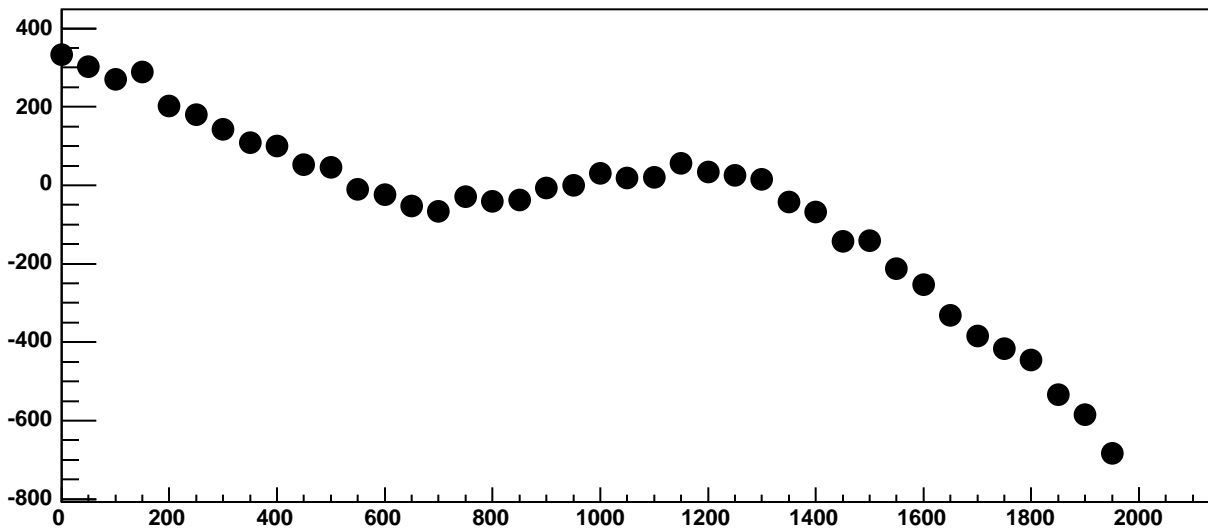
Chip 11, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC



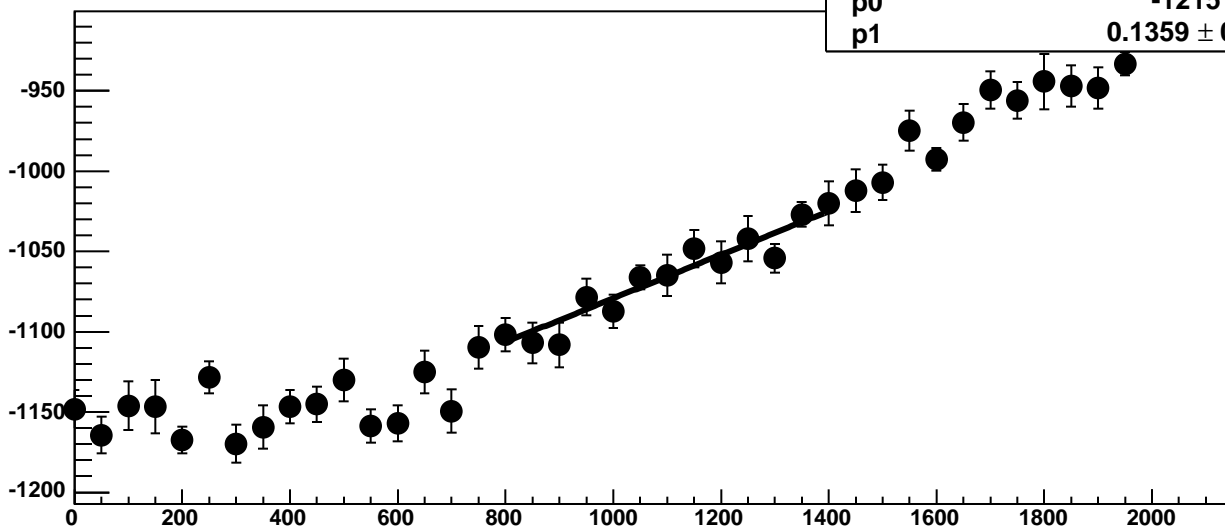
Chip 11, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC



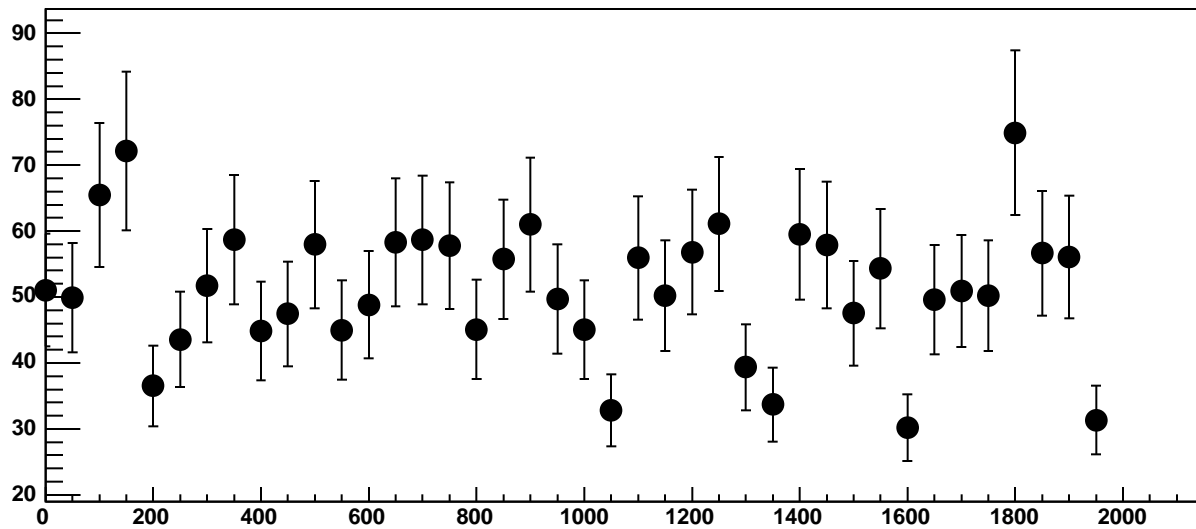
Chip 11, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC



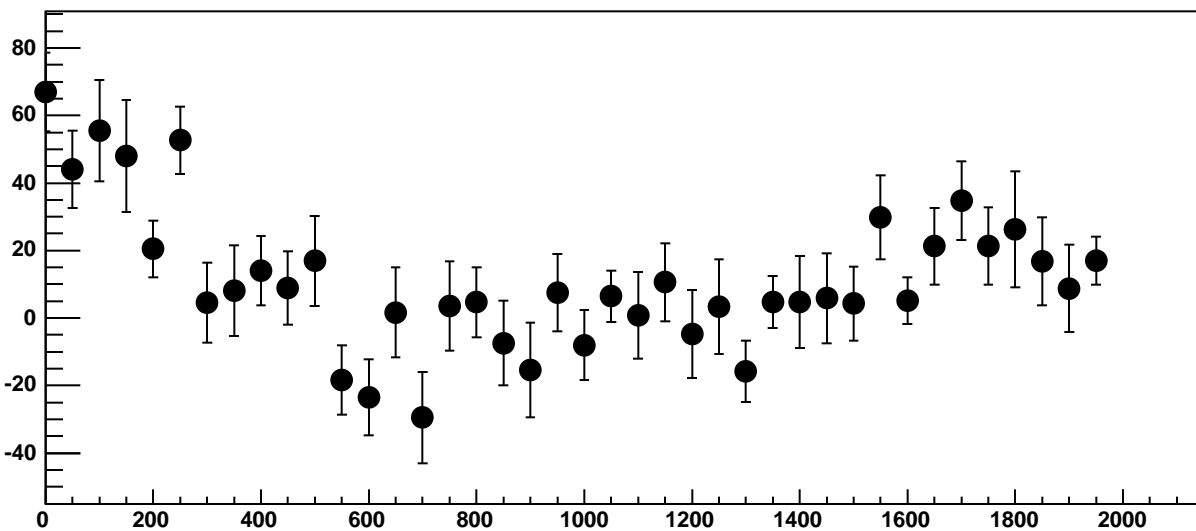
Chip 11, Channel 14, Enable 1, Hold=30, ADC Mean vs DAC



Chip 11, Channel 14, Enable 1, Hold=30, ADC Noise vs DAC

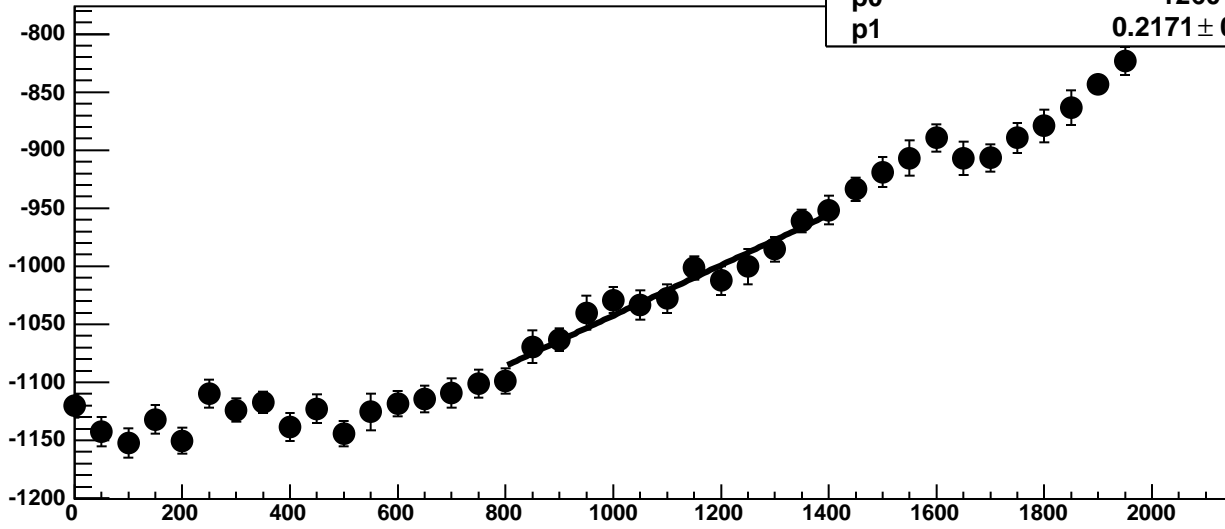


Chip 11, Channel 14, Enable 1, Hold=30, ADC Residuals vs DAC

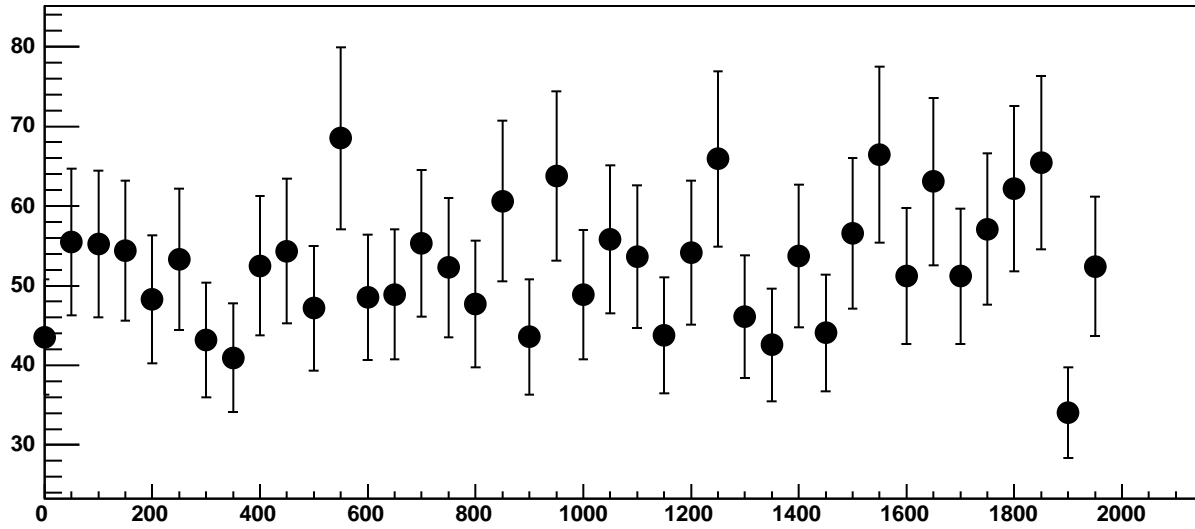




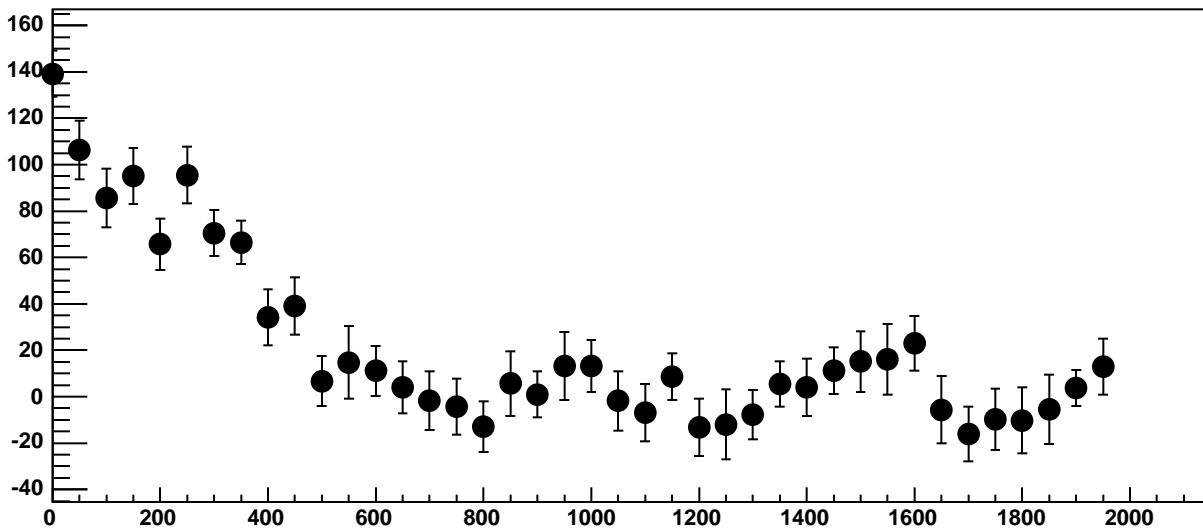
Chip 11, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC



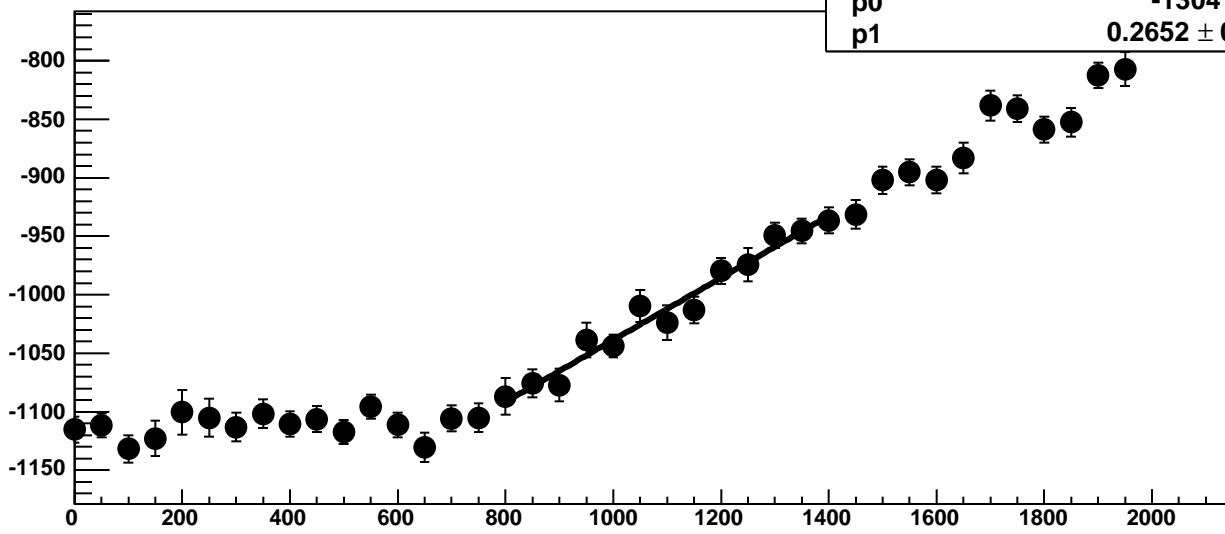
Chip 11, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC



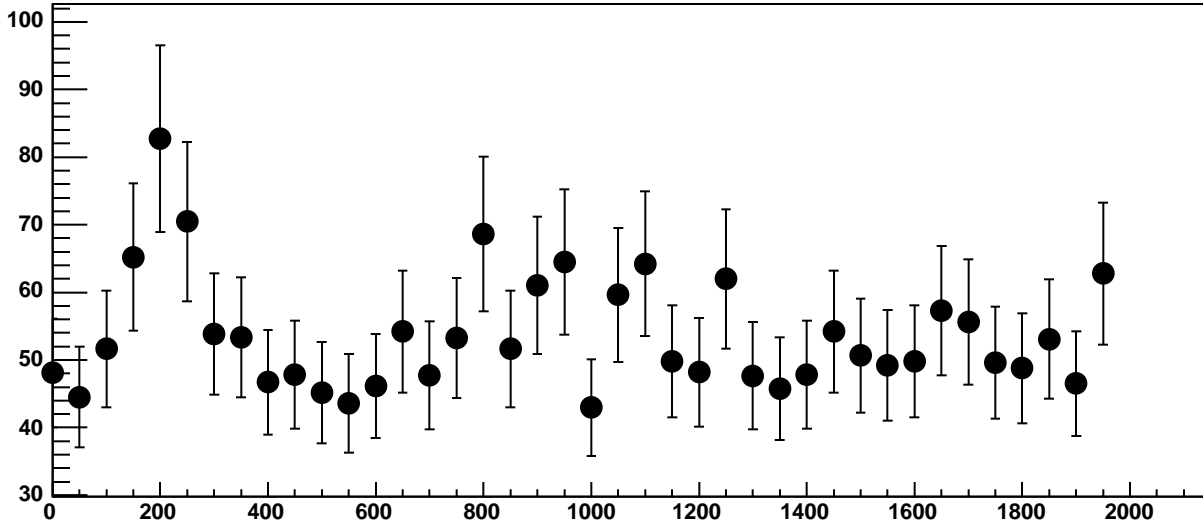
Chip 11, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC



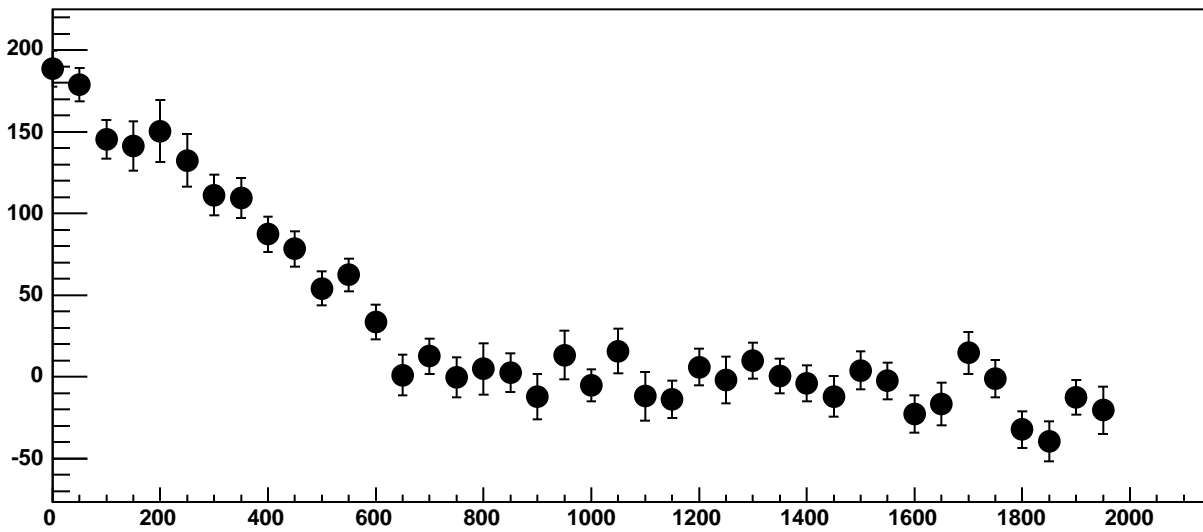
Chip 11, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC



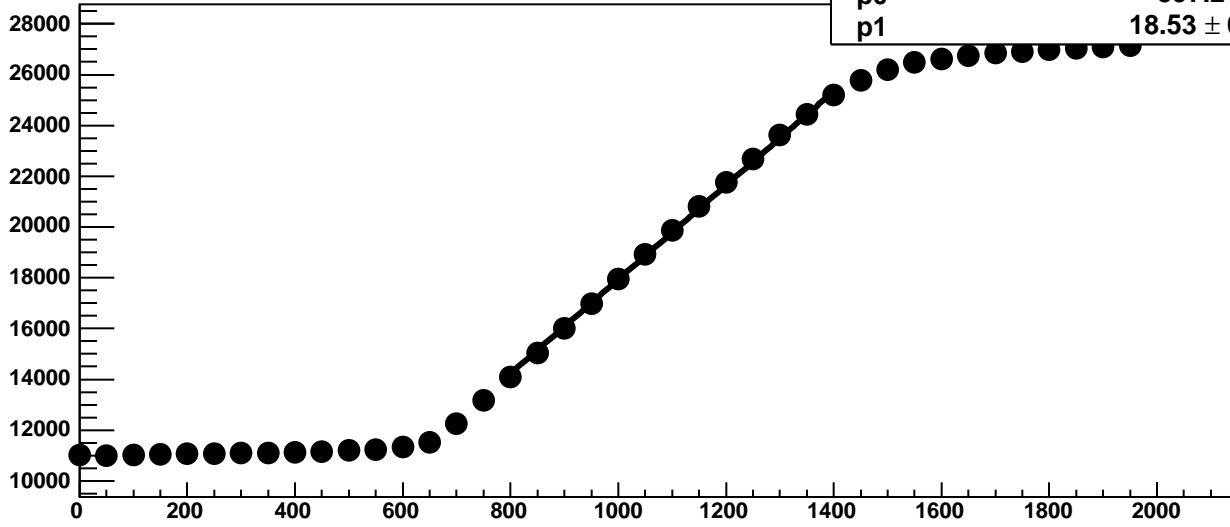
Chip 11, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC



Chip 11, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC

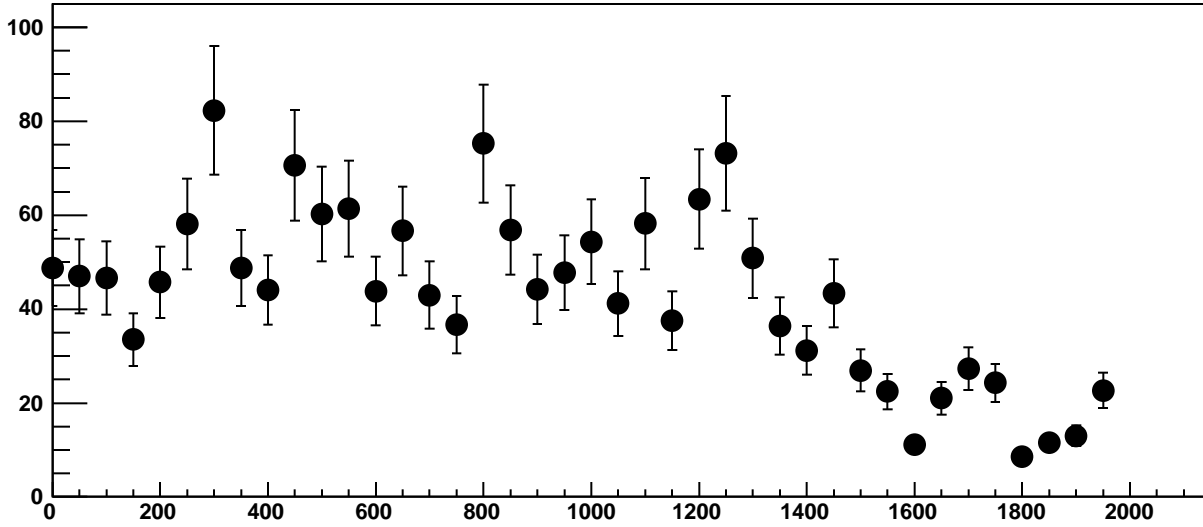


Chip 11, Channel 14, Enable 4!, Hold=30, ADC Mean vs DAC

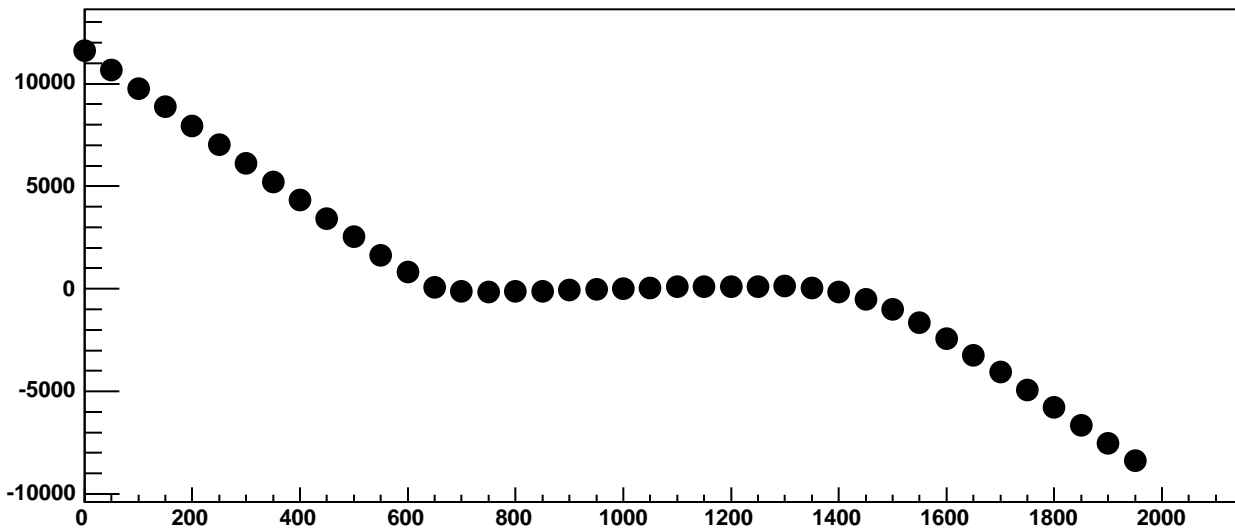


$\chi^2 / \text{ndf}$  1078 / 11  
p0  $-597.2 \pm 18.08$   
p1  $18.53 \pm 0.01551$

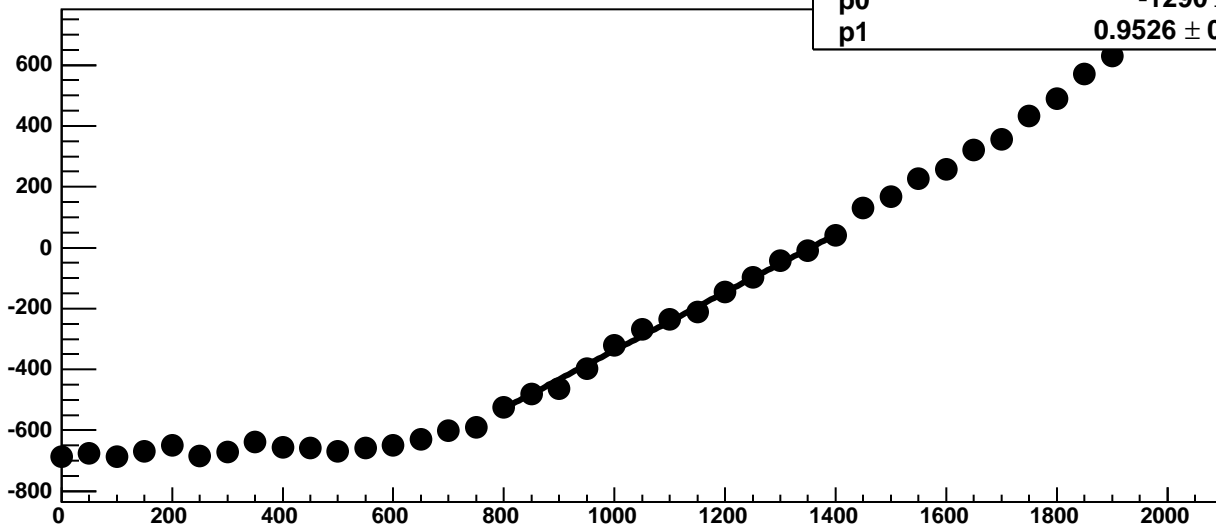
Chip 11, Channel 14, Enable 4!, Hold=30, ADC Noise vs DAC



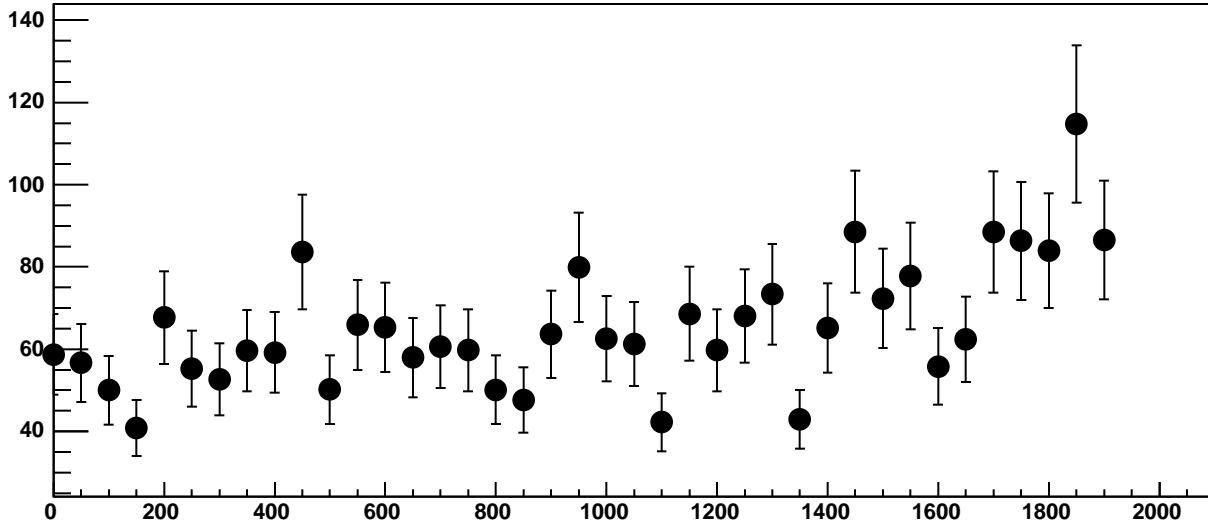
Chip 11, Channel 14, Enable 4!, Hold=30, ADC Residuals vs DAC



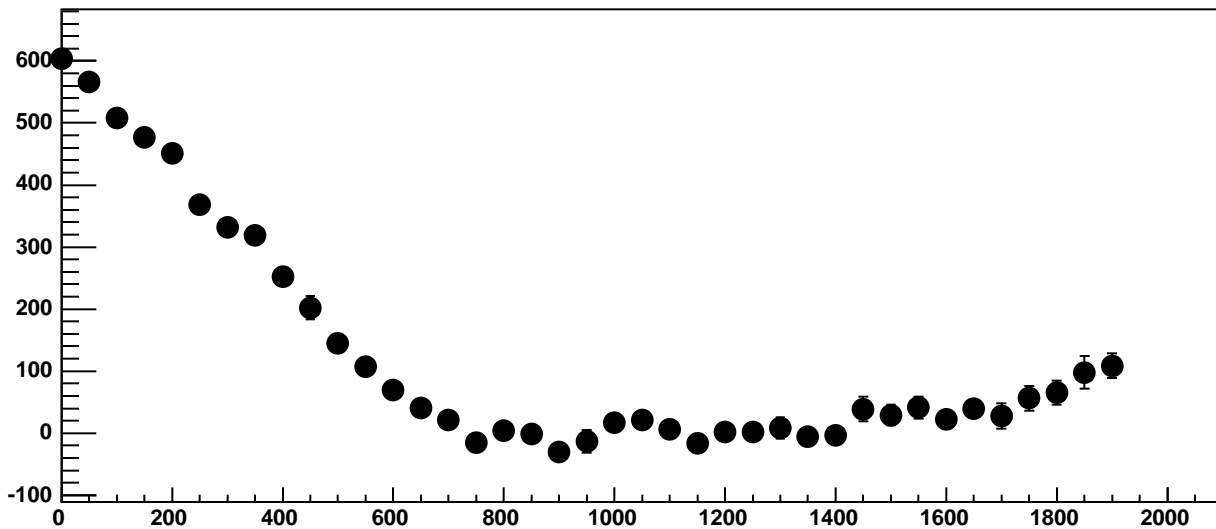
Chip 11, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC



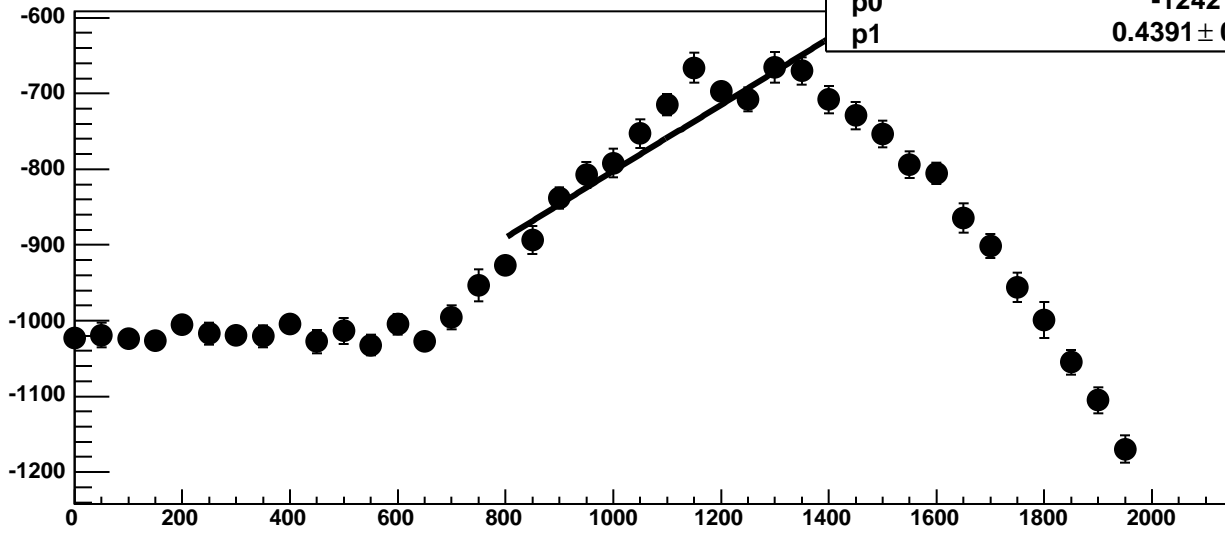
Chip 11, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC



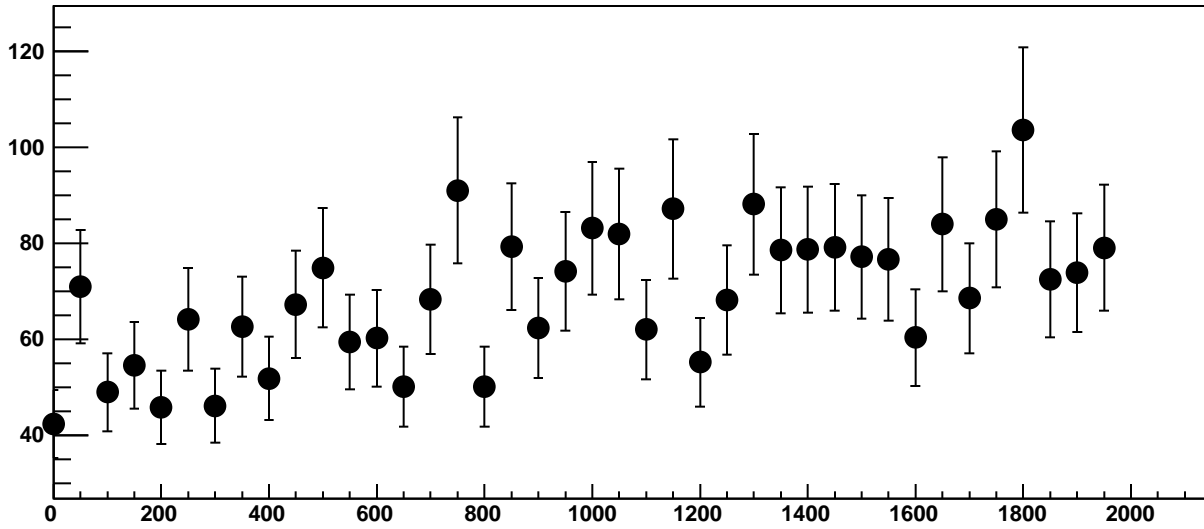
Chip 11, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC



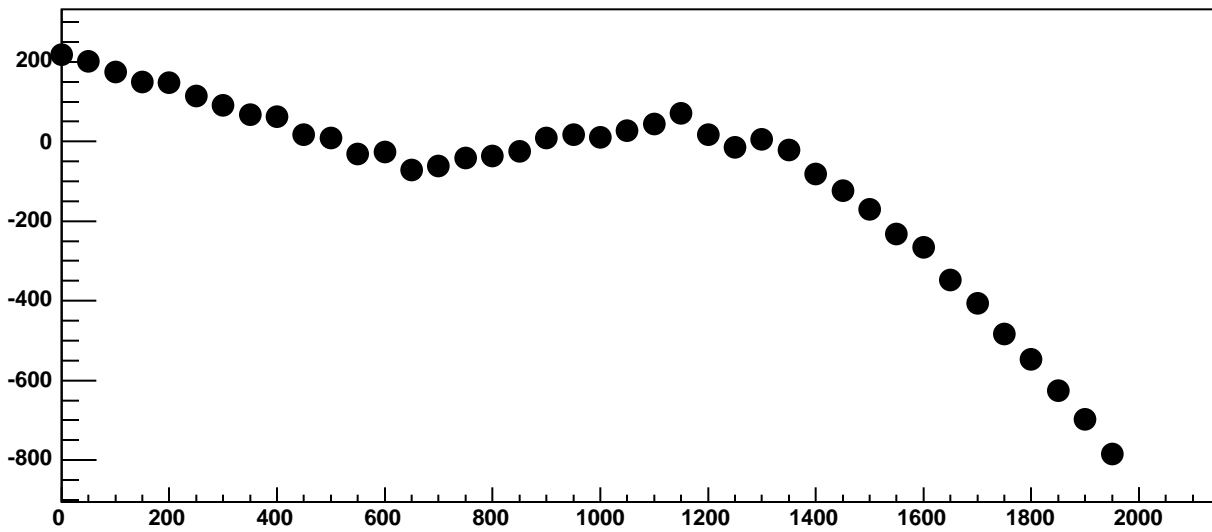
Chip 11, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC



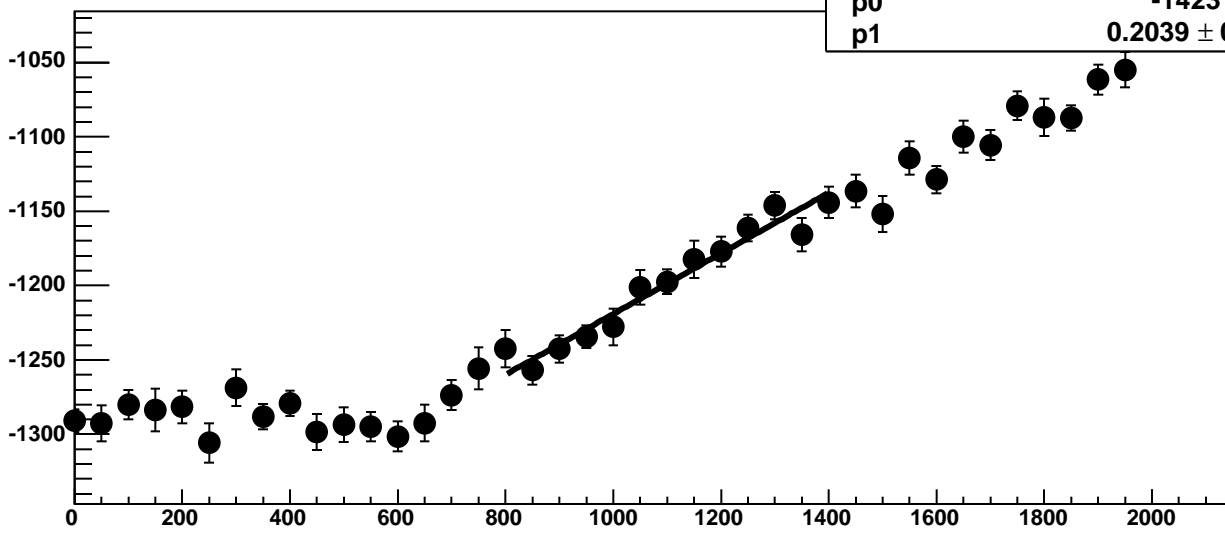
Chip 11, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC



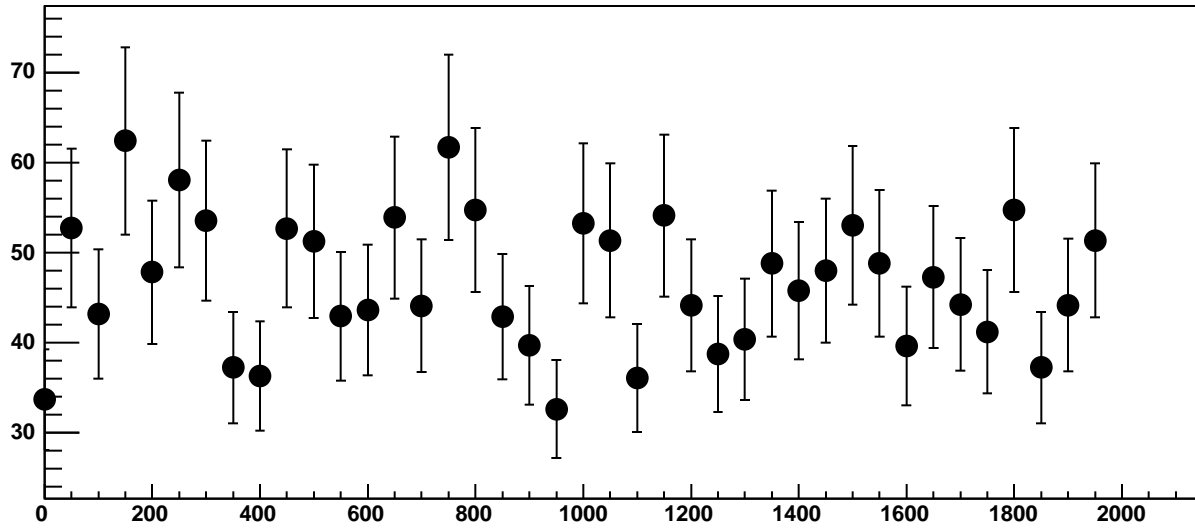
Chip 11, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC



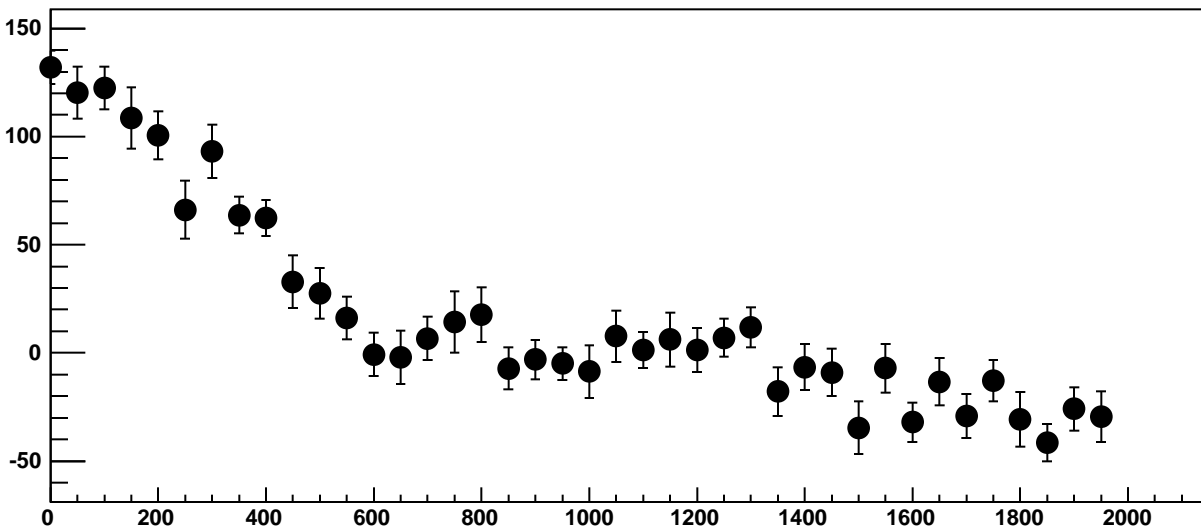
Chip 11, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC



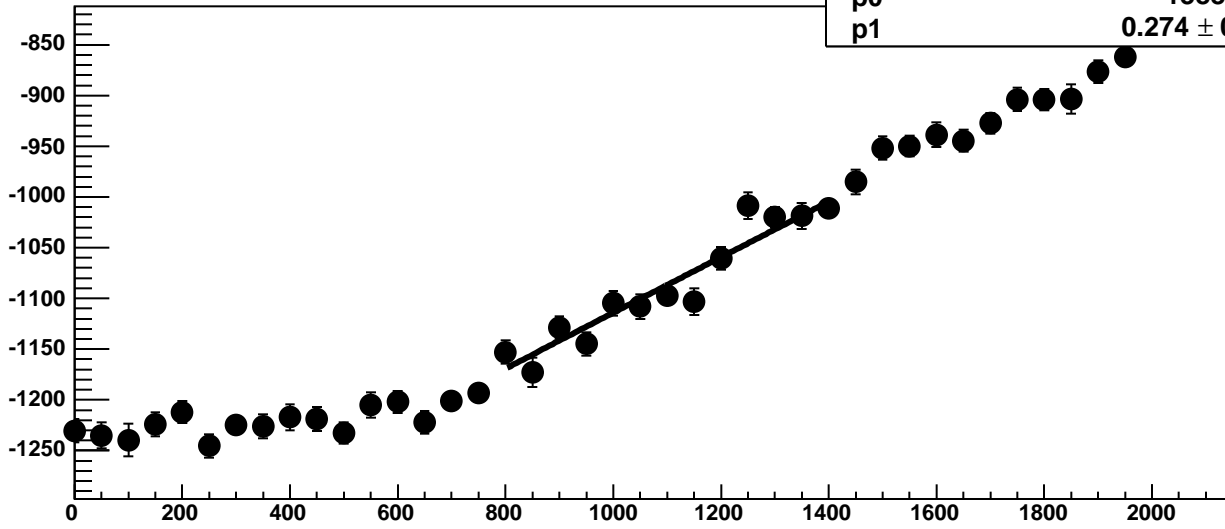
Chip 11, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC



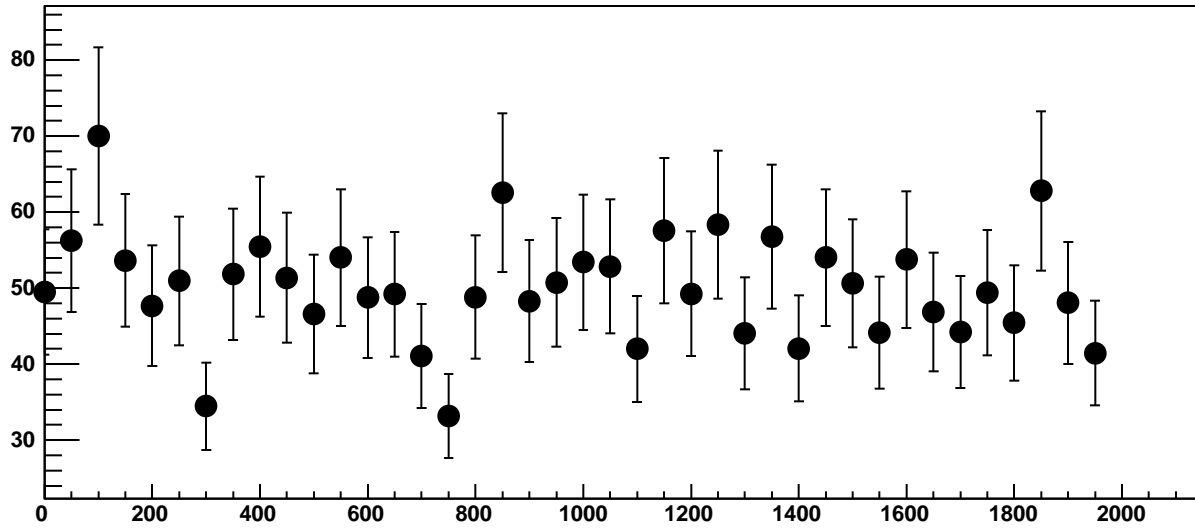
Chip 11, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC



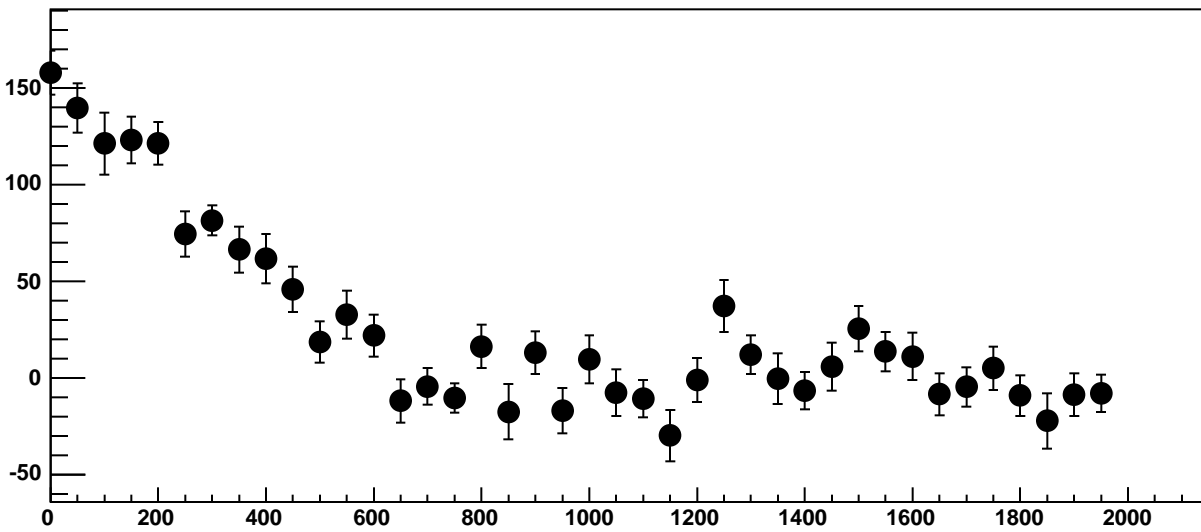
Chip 11, Channel 15, Enable 2, Hold=30, ADC Mean vs DAC



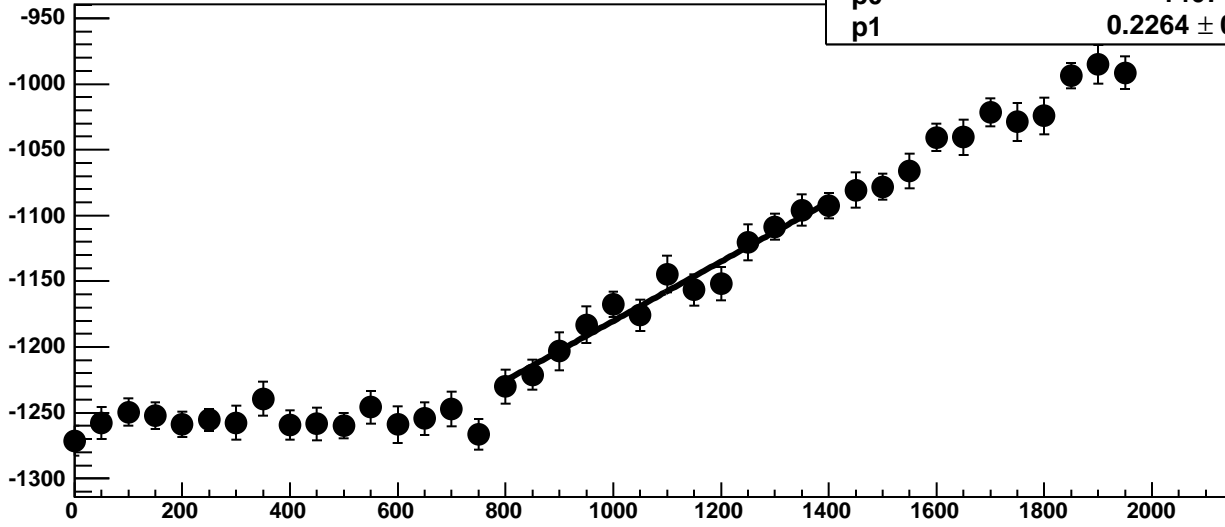
Chip 11, Channel 15, Enable 2, Hold=30, ADC Noise vs DAC



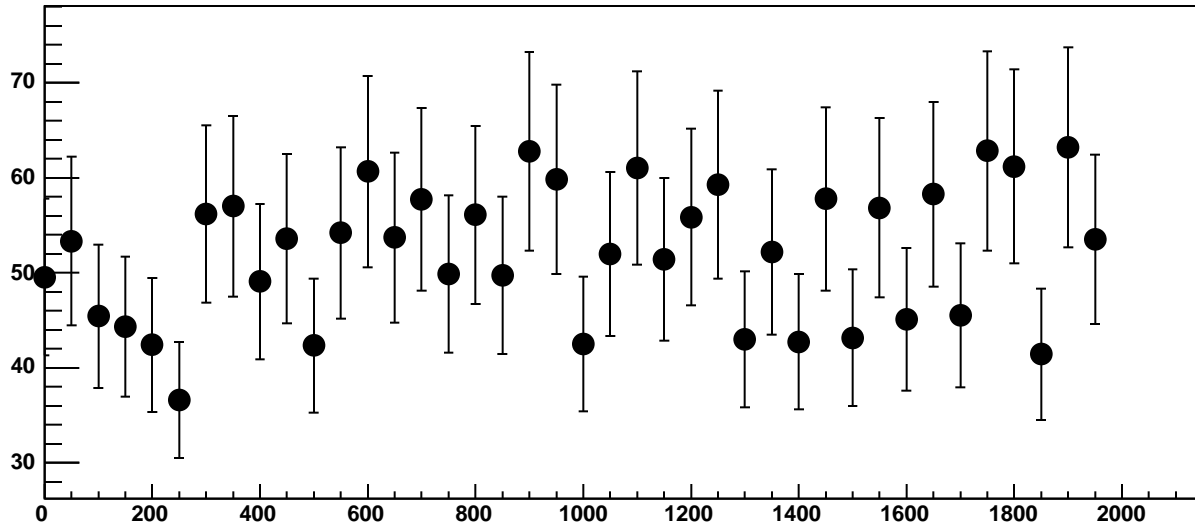
Chip 11, Channel 15, Enable 2, Hold=30, ADC Residuals vs DAC



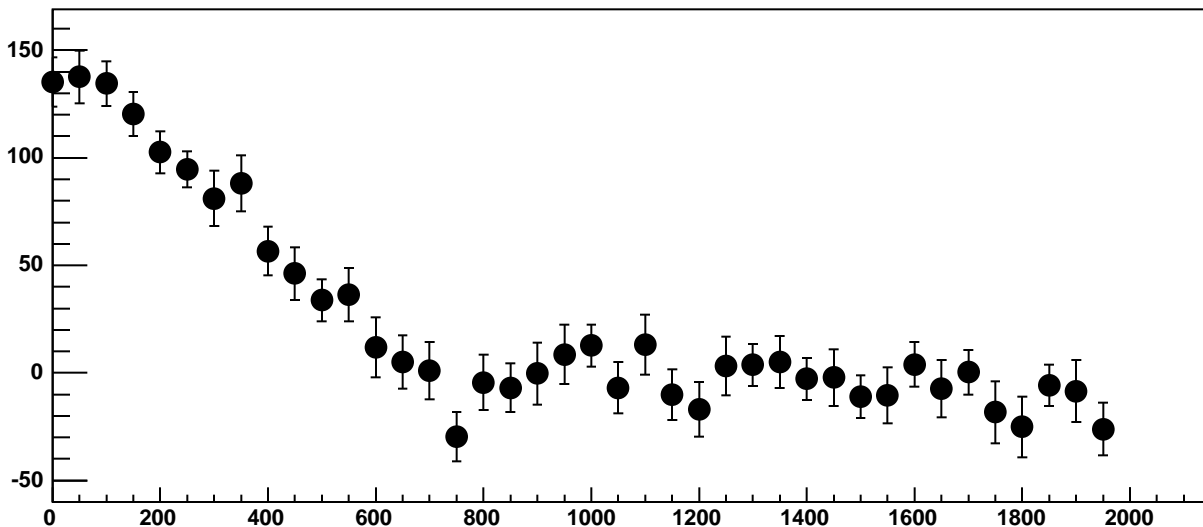
Chip 11, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC



Chip 11, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC

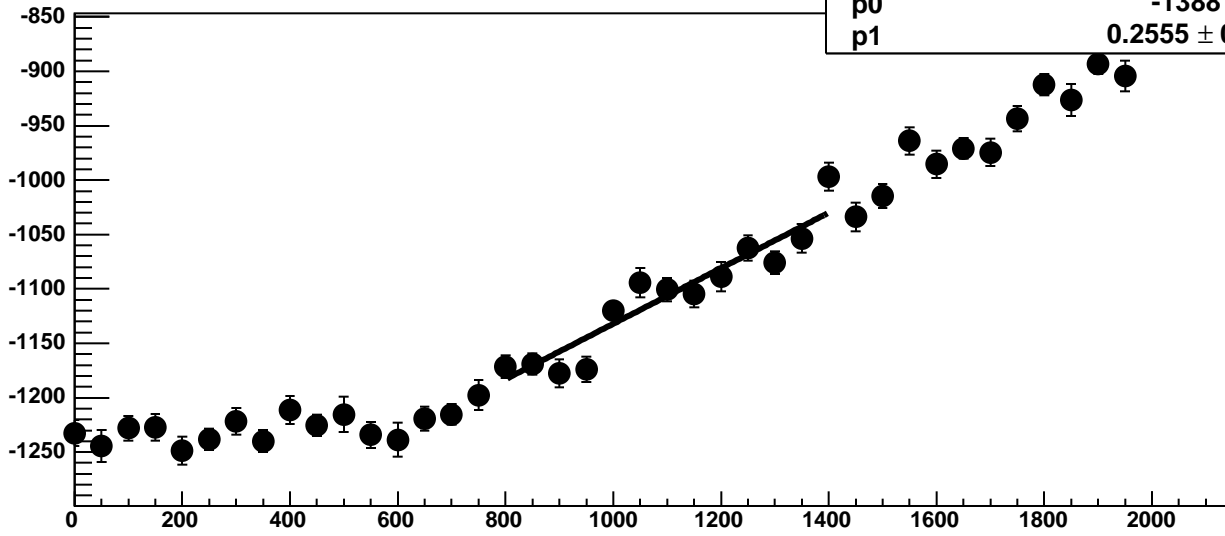


Chip 11, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC

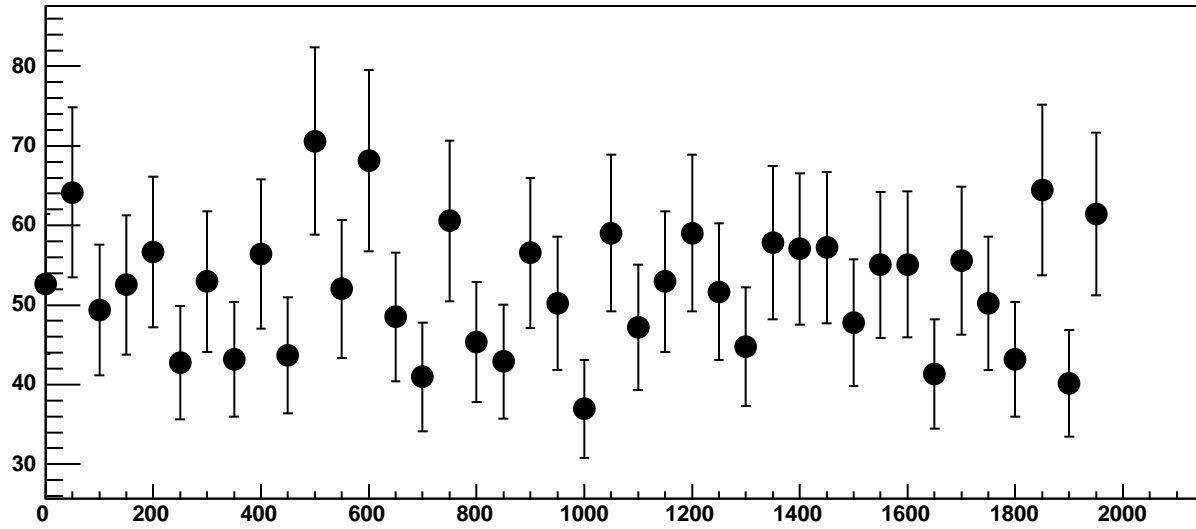




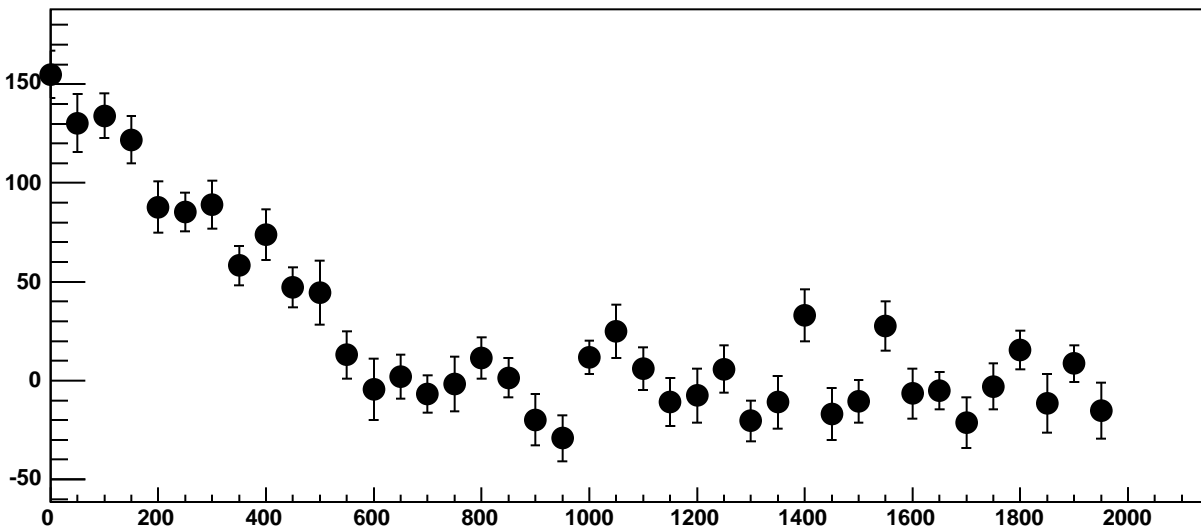
Chip 11, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC



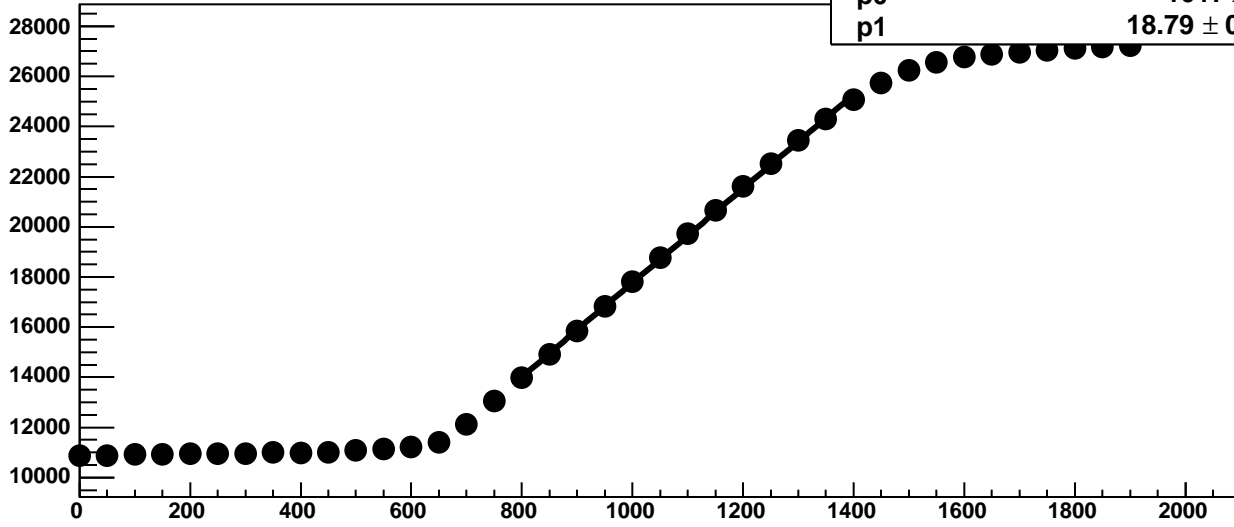
Chip 11, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC



Chip 11, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC

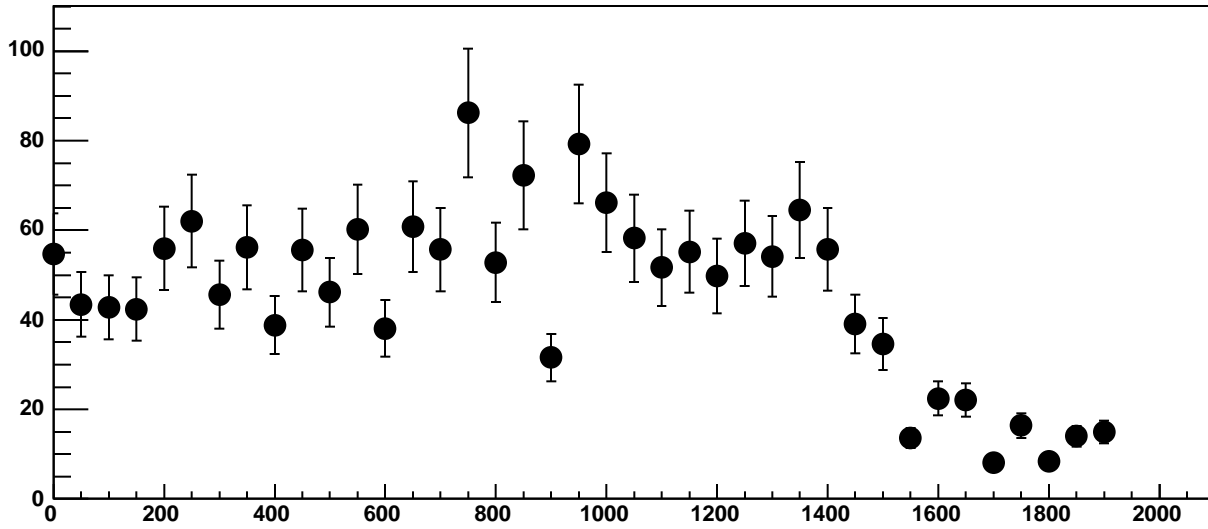


Chip 11, Channel 15, Enable 5!, Hold=30, ADC Mean vs DAC

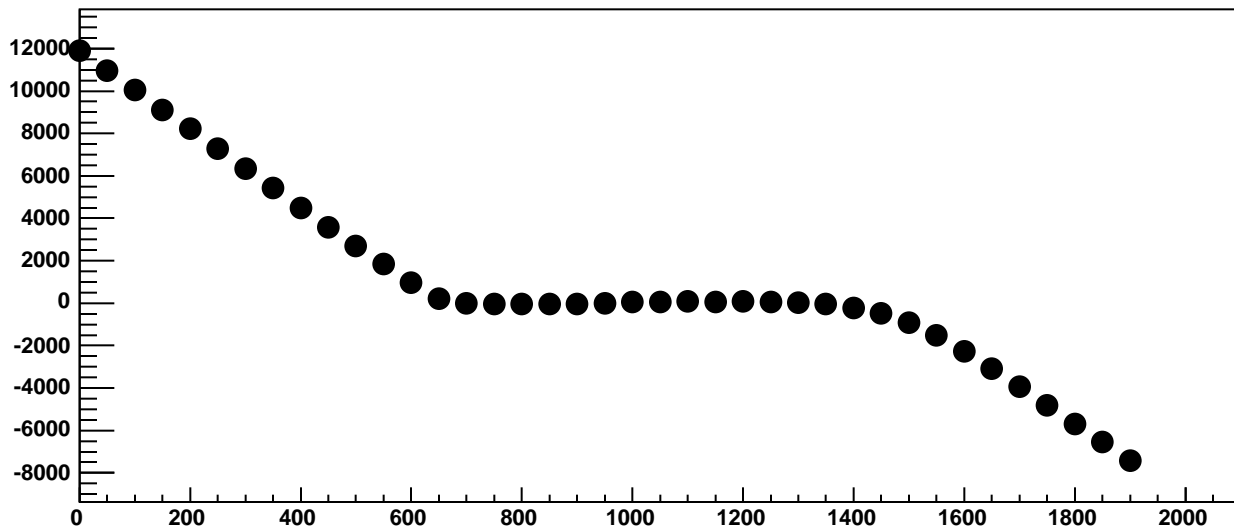


$\chi^2 / \text{ndf}$  544.1 / 11  
p0  $-1017 \pm 19.98$   
p1  $18.79 \pm 0.01822$

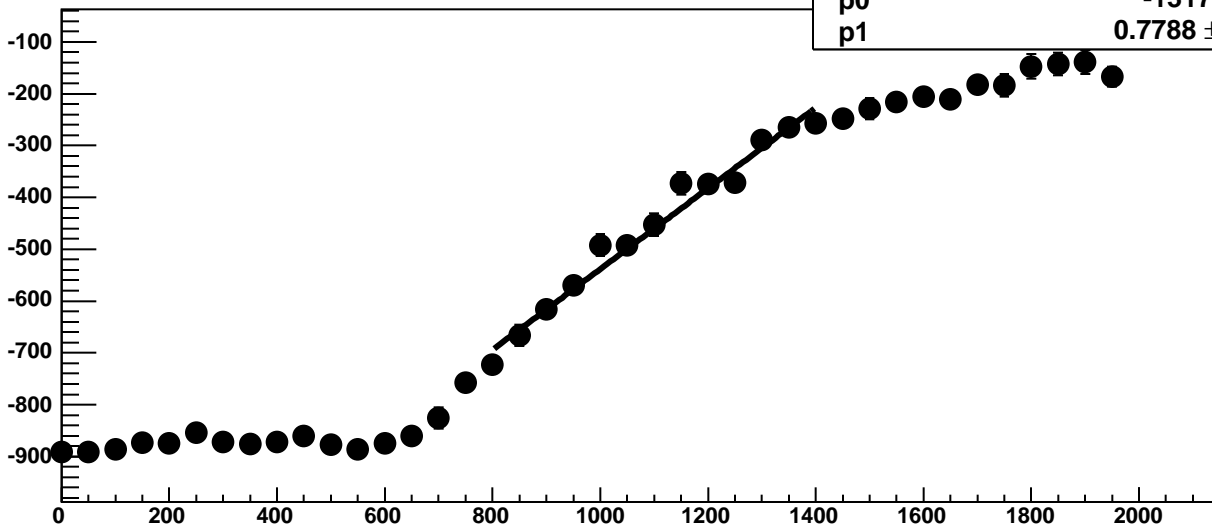
Chip 11, Channel 15, Enable 5!, Hold=30, ADC Noise vs DAC



Chip 11, Channel 15, Enable 5!, Hold=30, ADC Residuals vs DAC

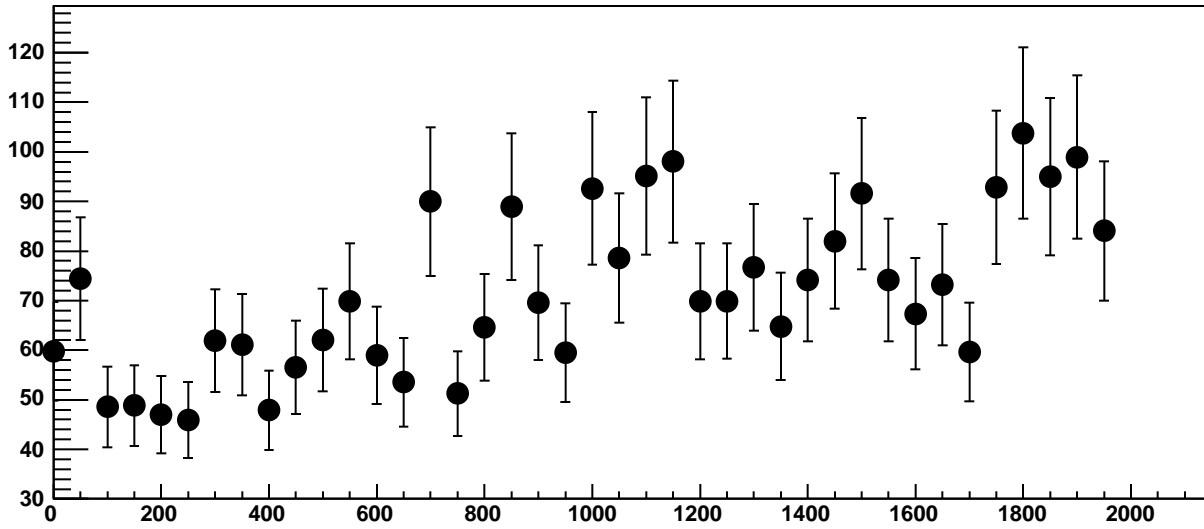


Chip 11, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC

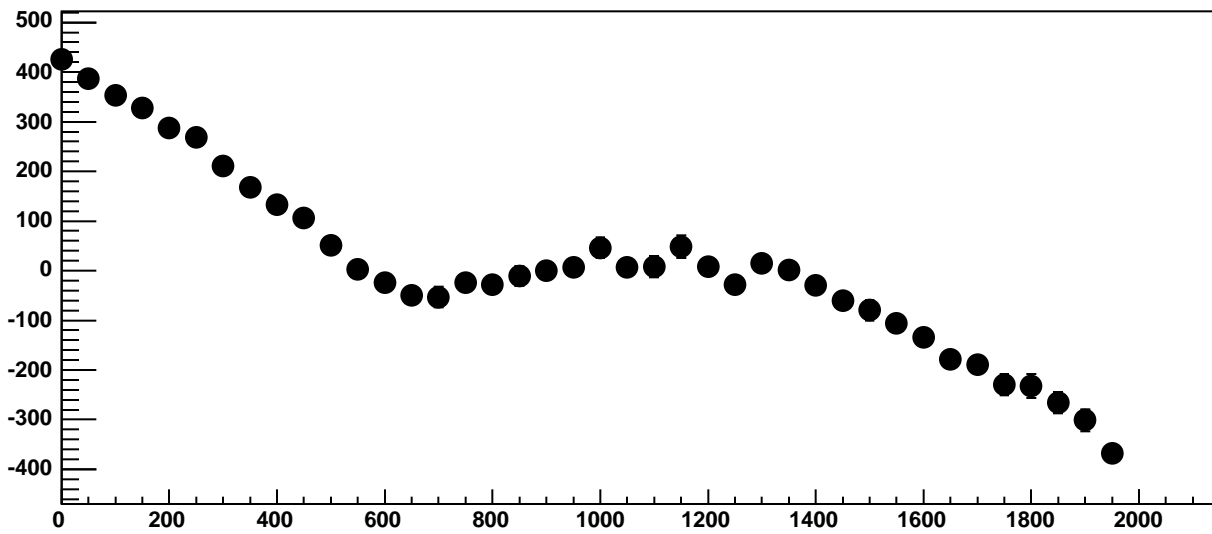


$\chi^2 / \text{ndf}$  21.03 / 11  
p0  $-1317 \pm 26.91$   
p1  $0.7788 \pm 0.0241$

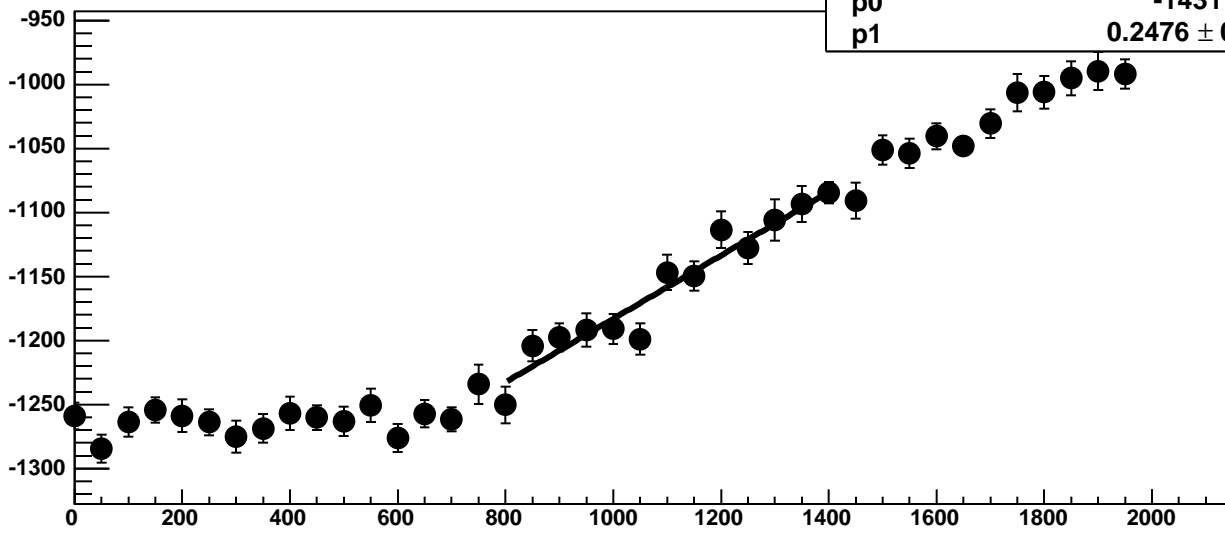
Chip 11, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC



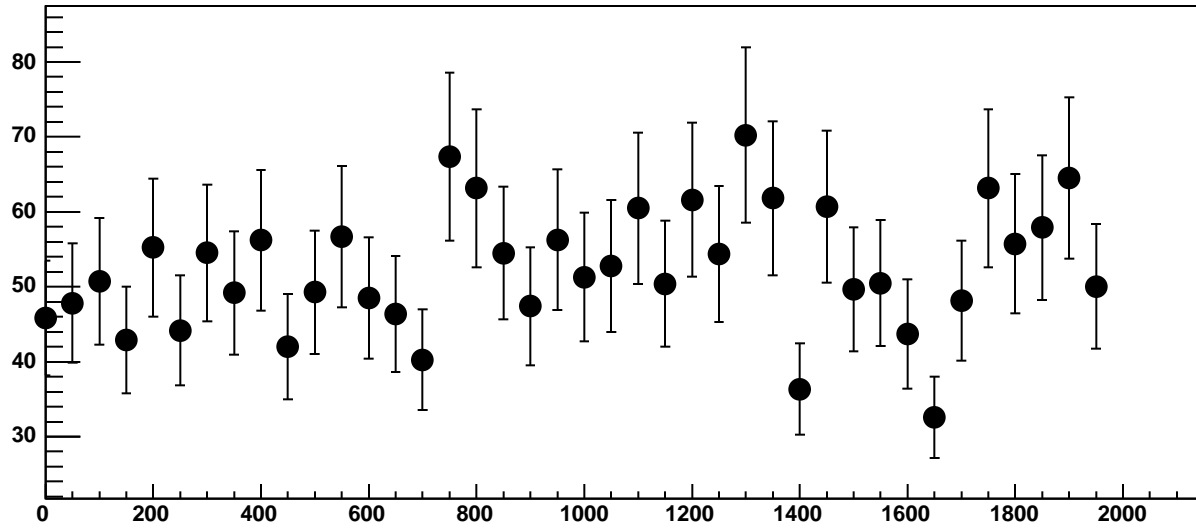
Chip 11, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC



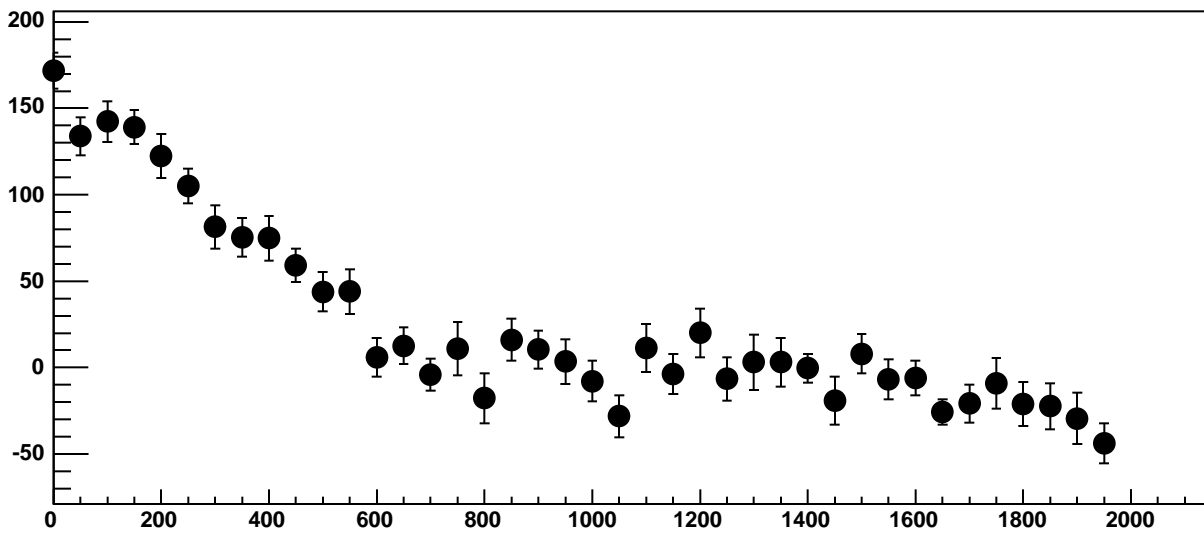
Chip 11, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC



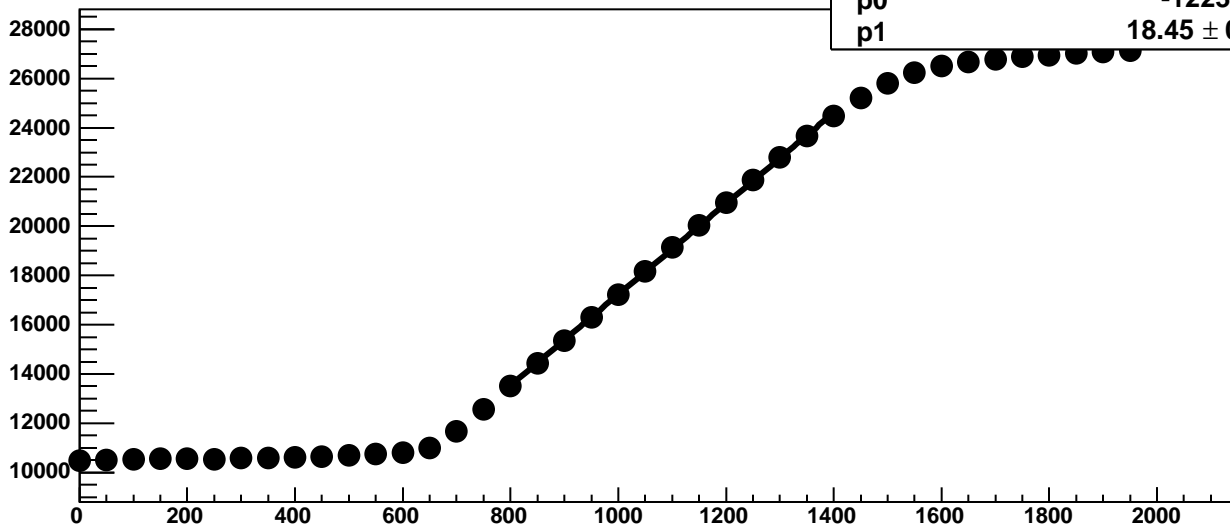
Chip 11, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC



Chip 11, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC

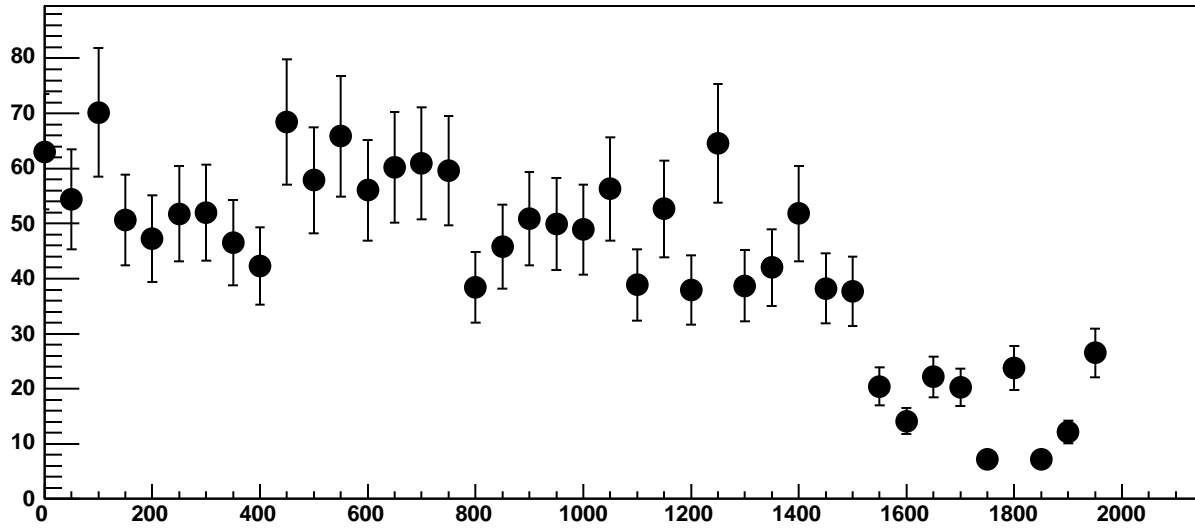


Chip 11, Channel 16, Enable 2!, Hold=30, ADC Mean vs DAC

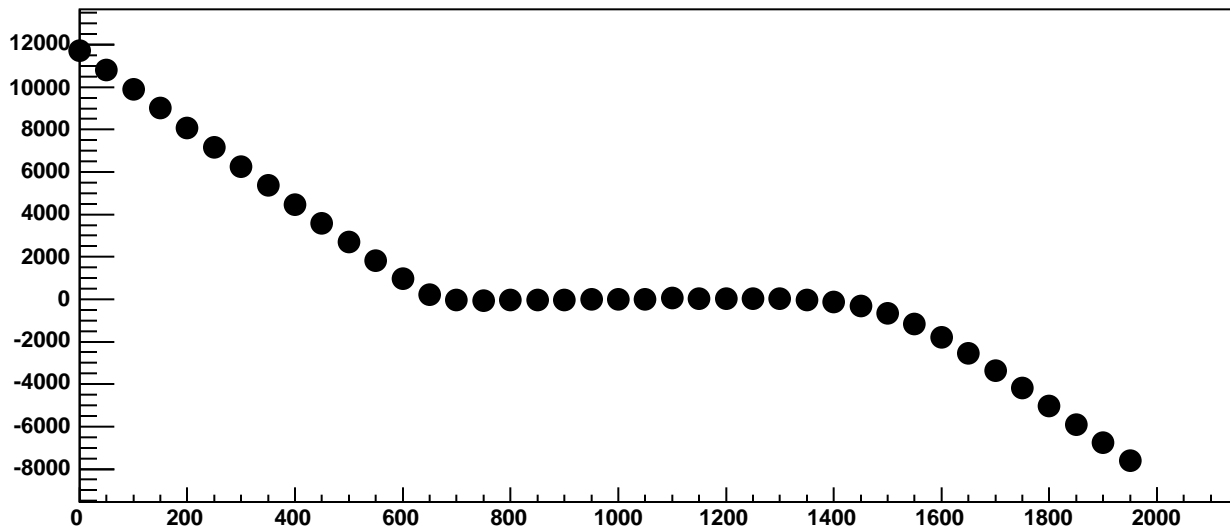


$\chi^2 / \text{ndf}$  214.1 / 11  
p0 -1223 ± 16.91  
p1 18.45 ± 0.01516

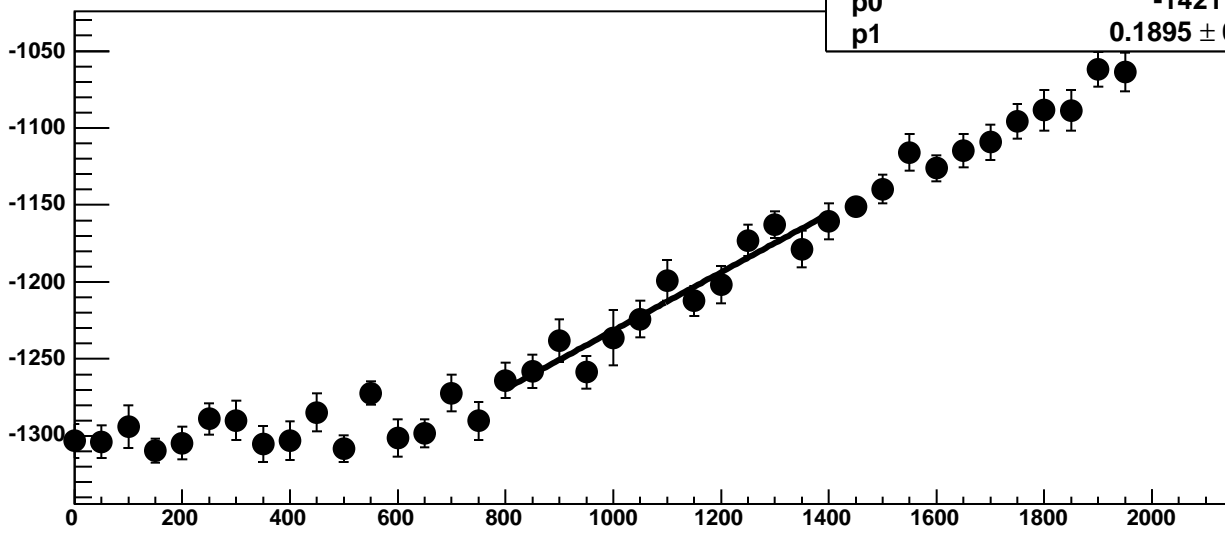
Chip 11, Channel 16, Enable 2!, Hold=30, ADC Noise vs DAC



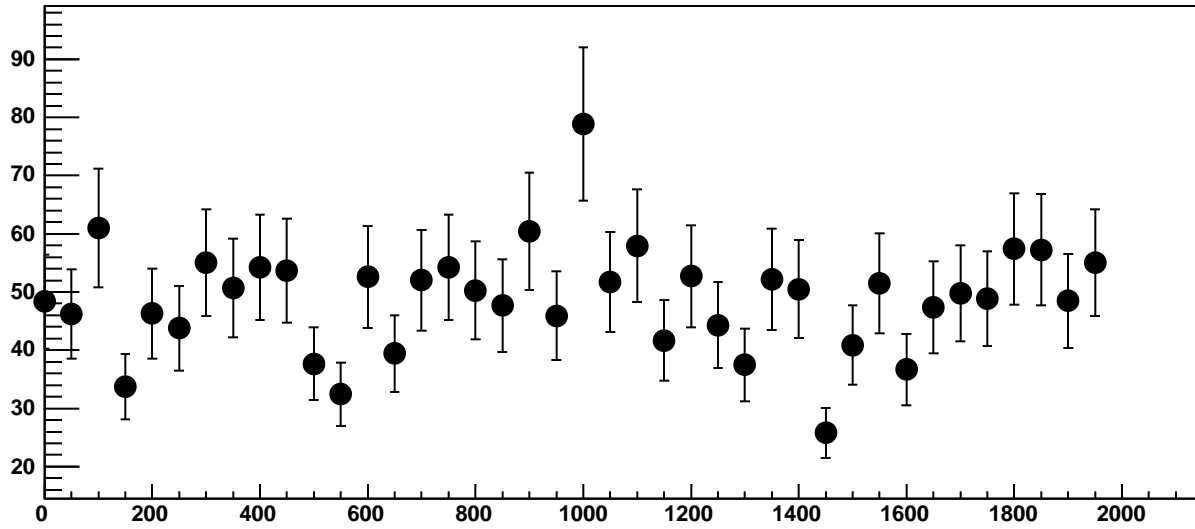
Chip 11, Channel 16, Enable 2!, Hold=30, ADC Residuals vs DAC



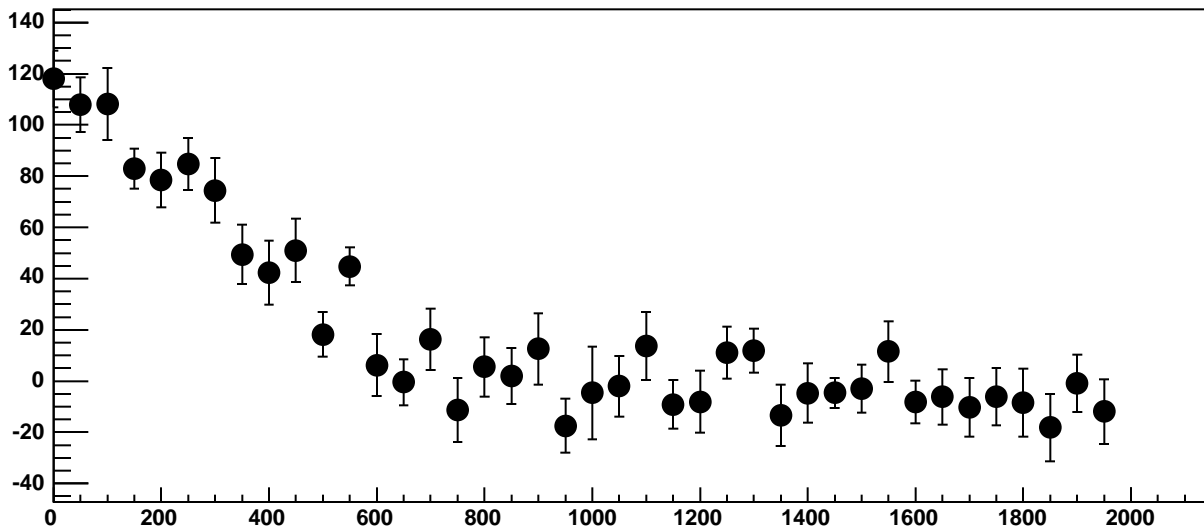
Chip 11, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC



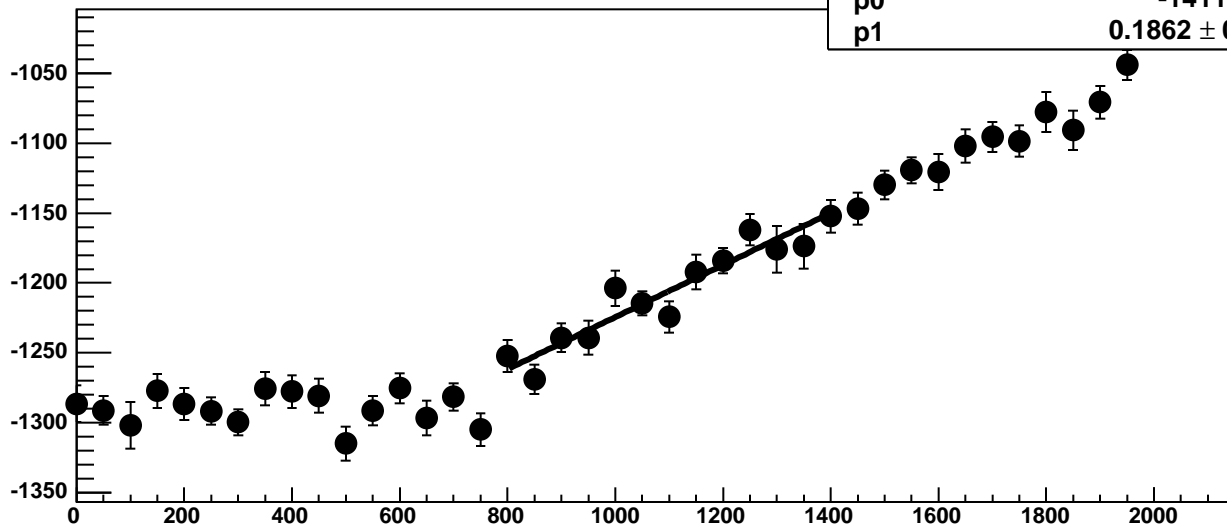
Chip 11, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC



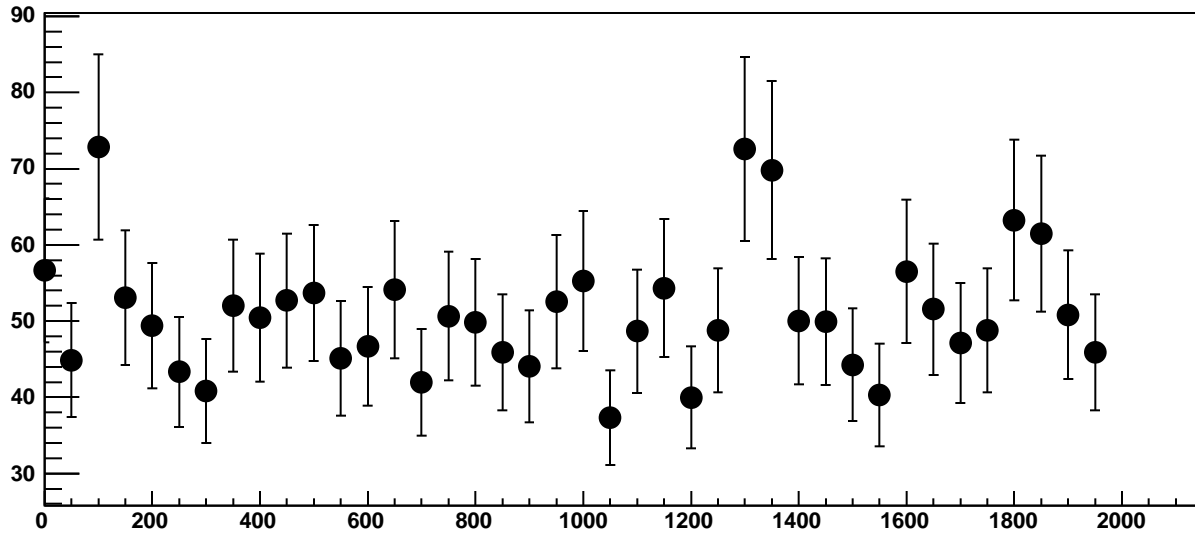
Chip 11, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC



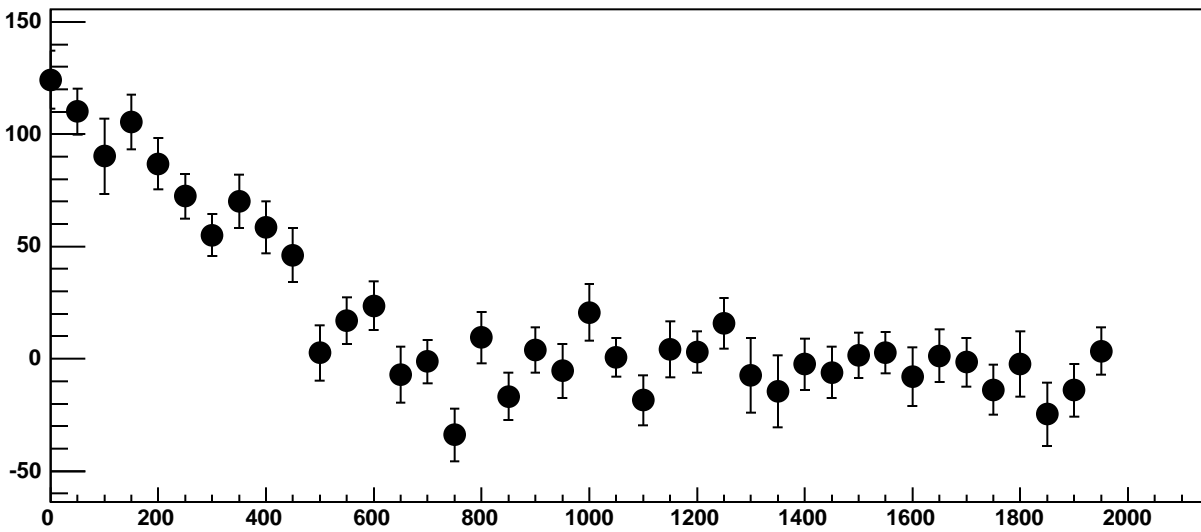
Chip 11, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC



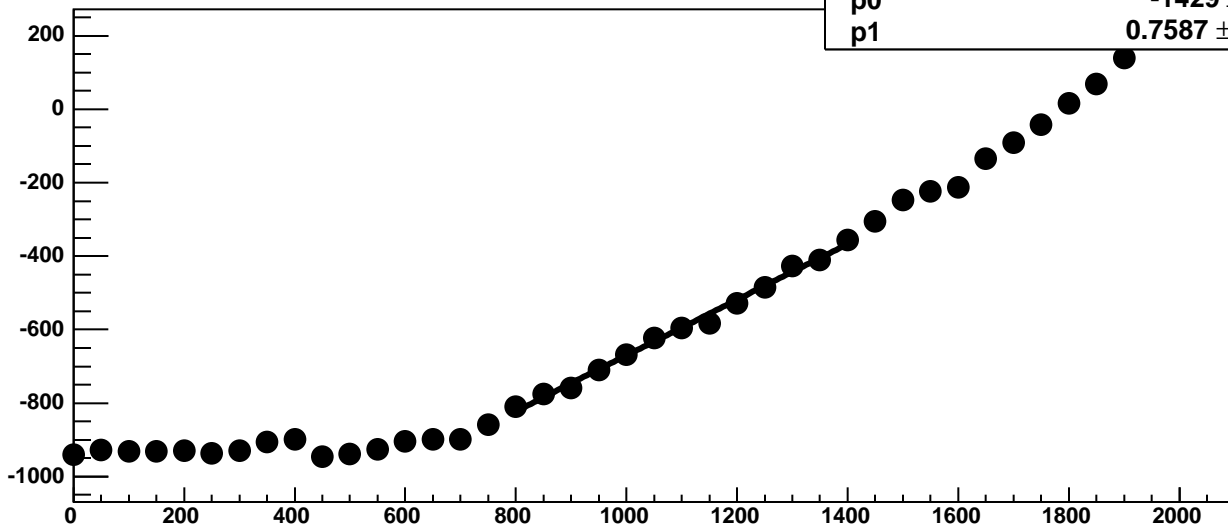
Chip 11, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC



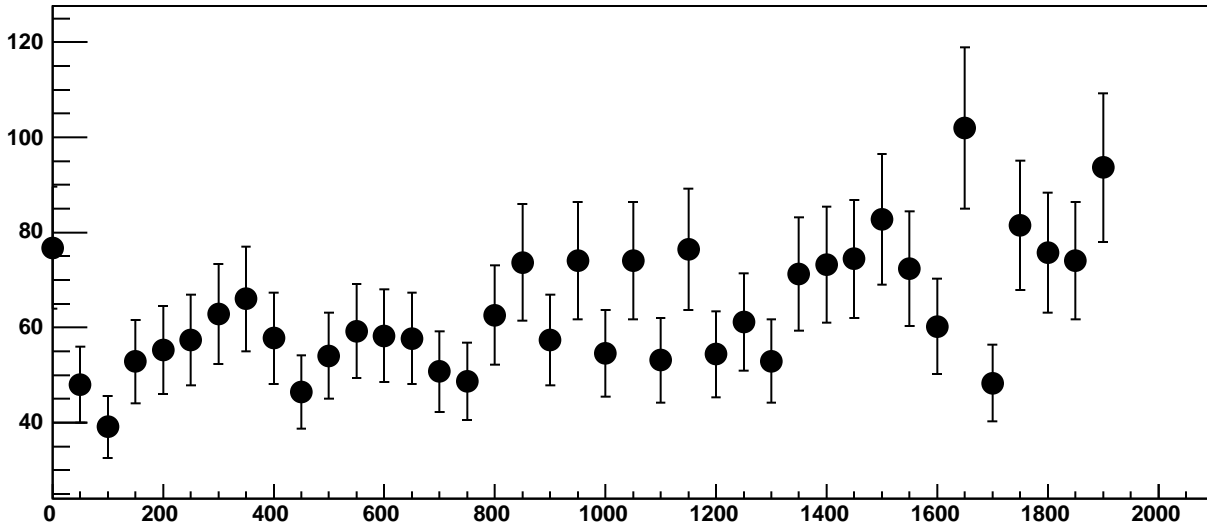
Chip 11, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC



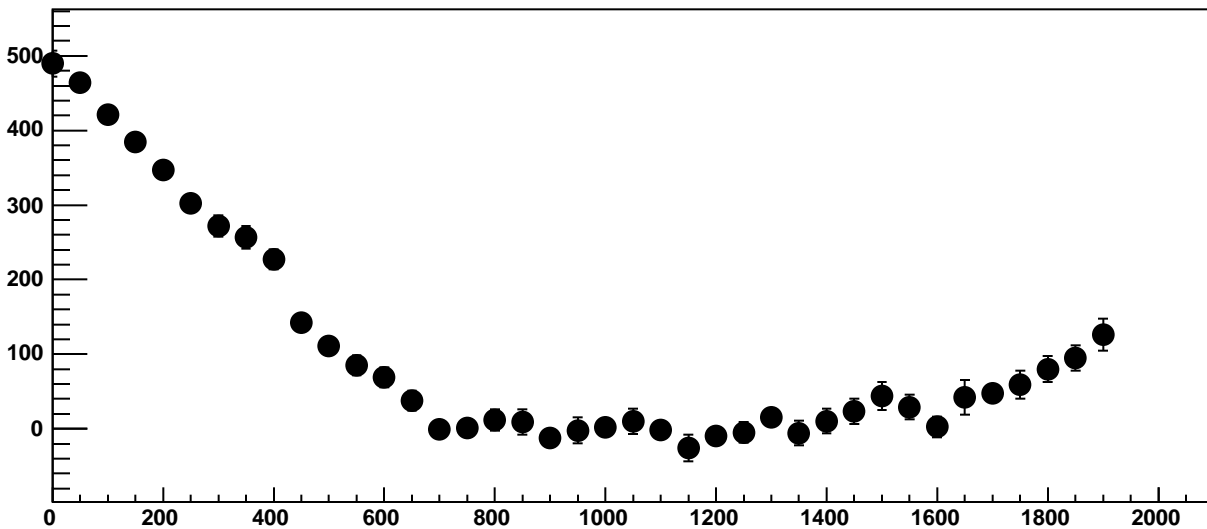
Chip 11, Channel 16, Enable 5, Hold=30, ADC Mean vs DAC



Chip 11, Channel 16, Enable 5, Hold=30, ADC Noise vs DAC

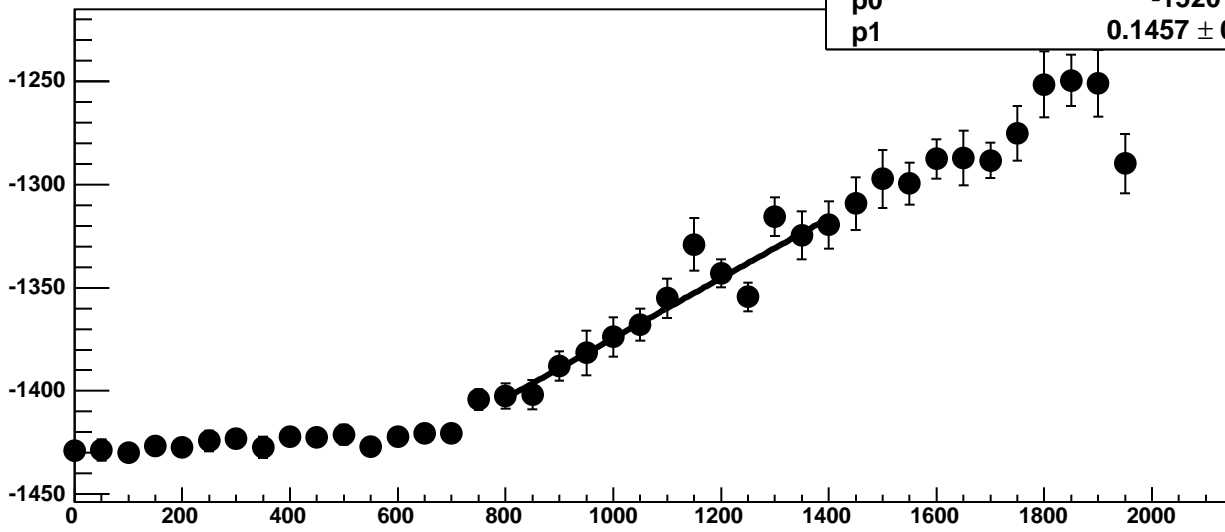


Chip 11, Channel 16, Enable 5, Hold=30, ADC Residuals vs DAC

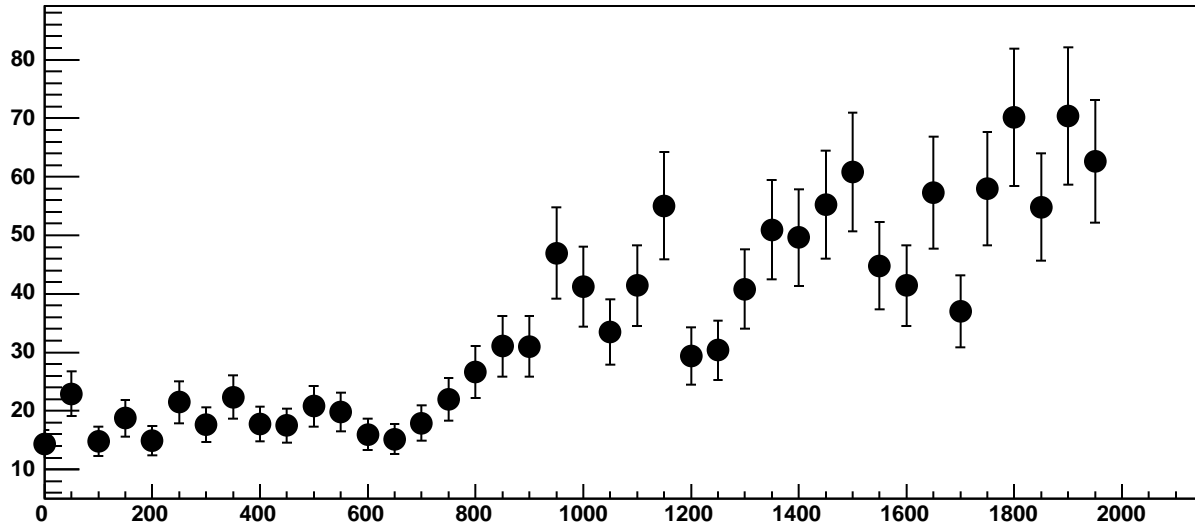




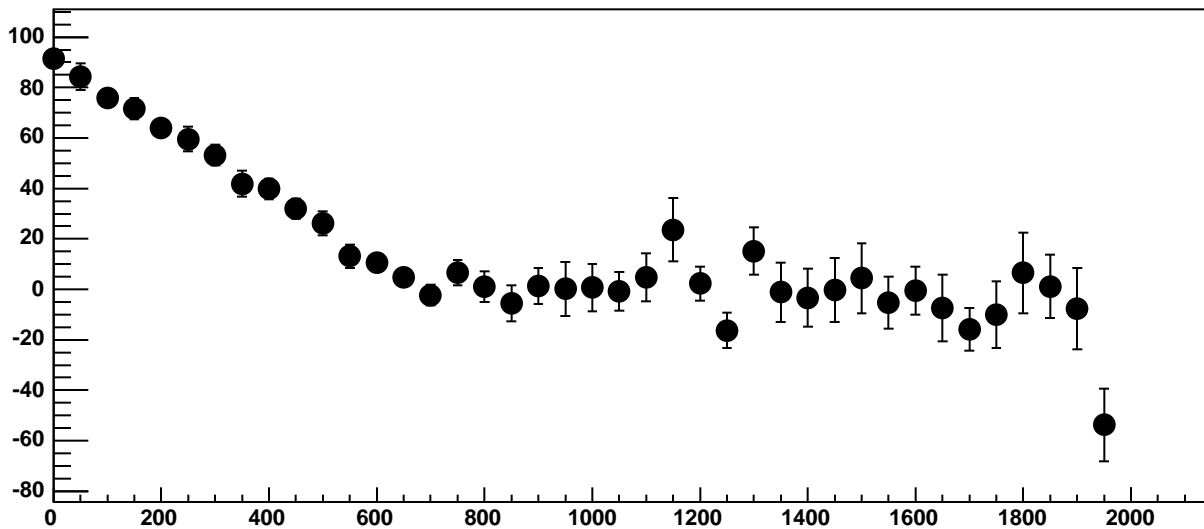
Chip 11, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC



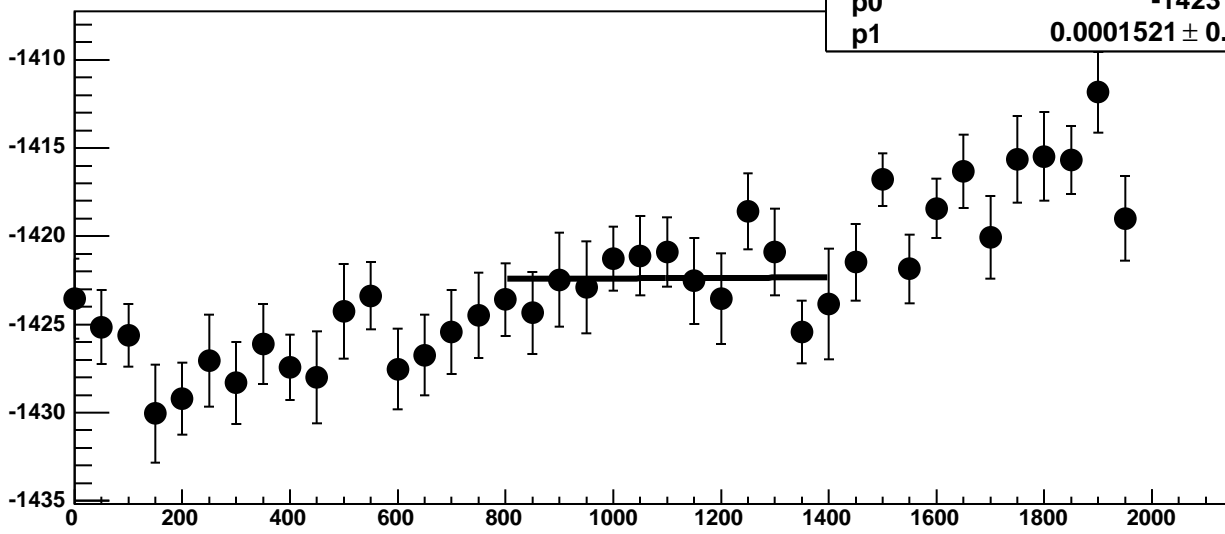
Chip 11, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC



Chip 11, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC

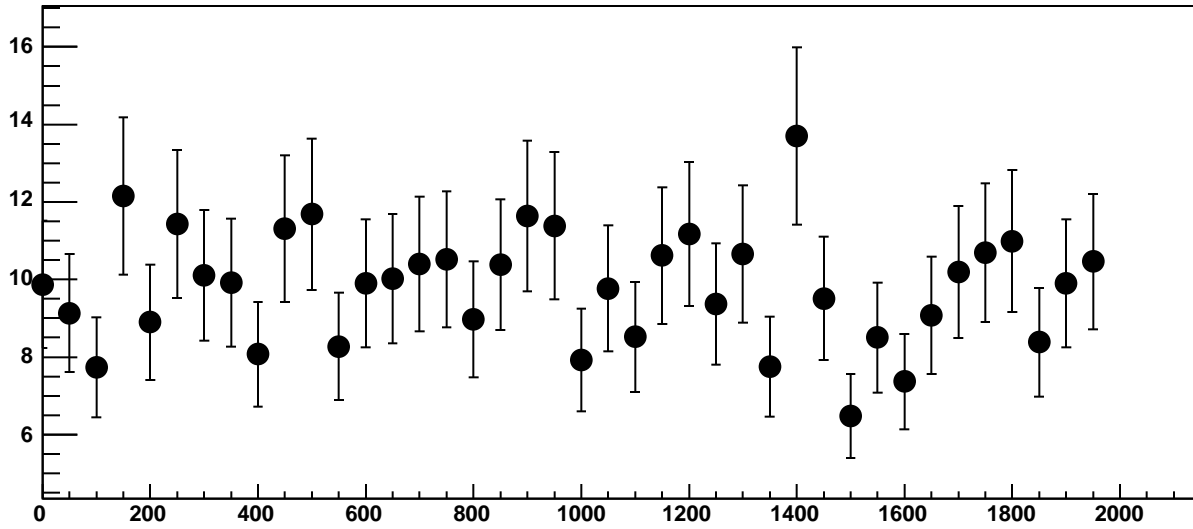


Chip 11, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC

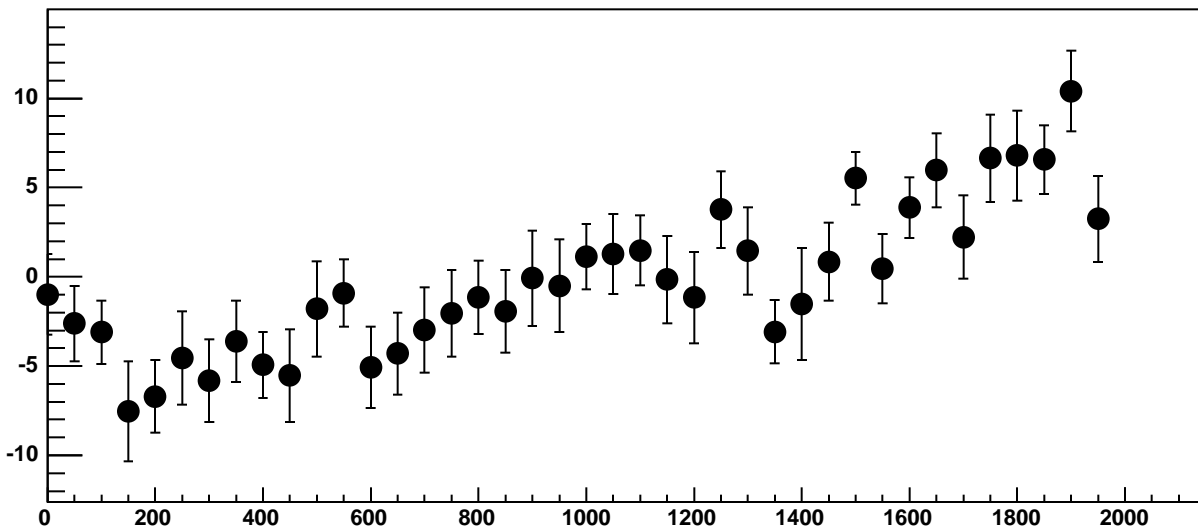


$\chi^2 / \text{ndf}$  9.222 / 11  
p0  $-1423 \pm 3.757$   
p1  $0.0001521 \pm 0.003378$

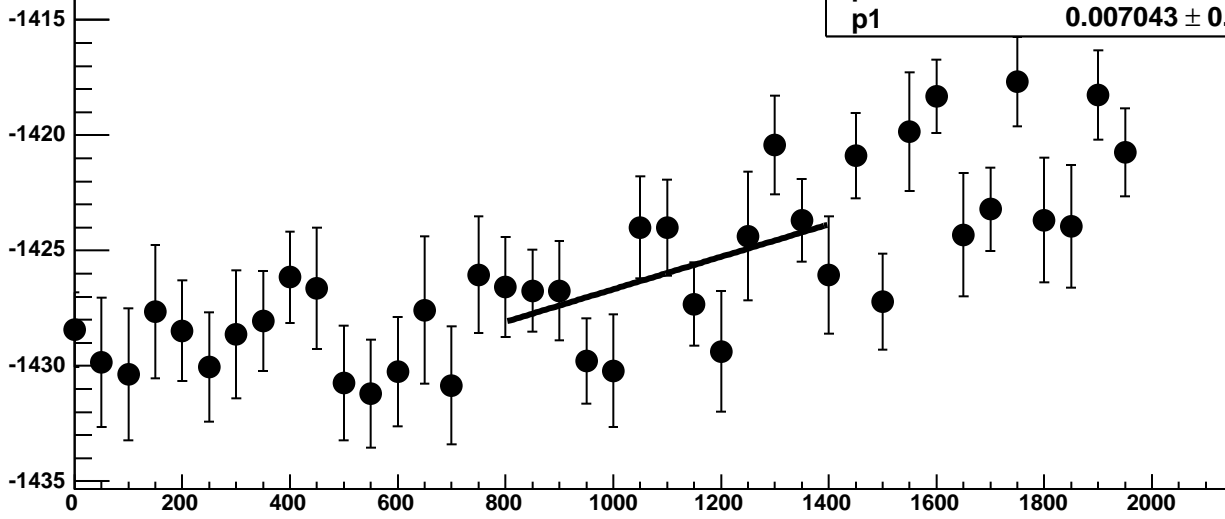
Chip 11, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC



Chip 11, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC

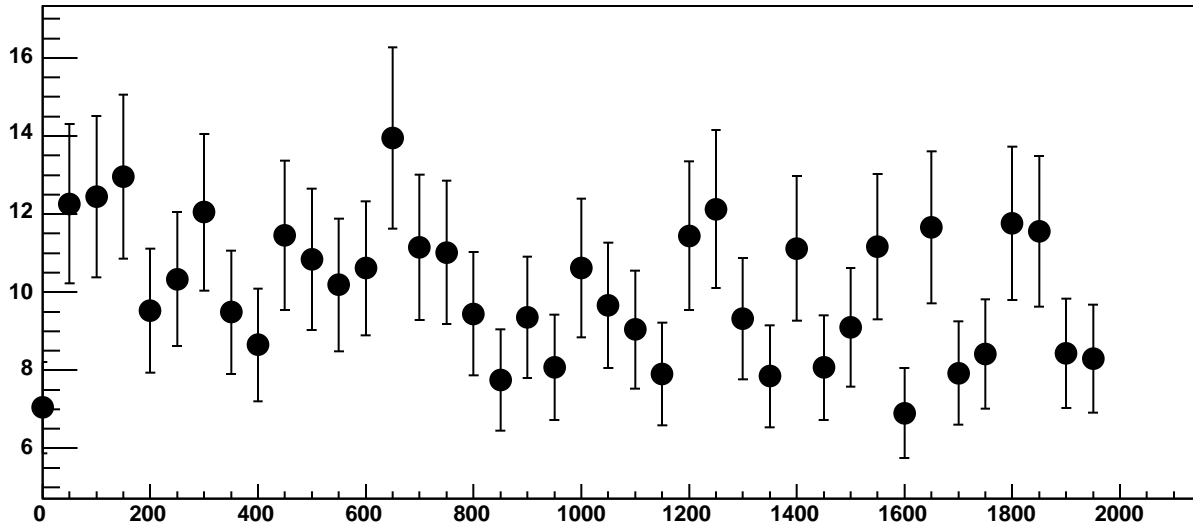


Chip 11, Channel 17, Enable 2, Hold=30, ADC Mean vs DAC

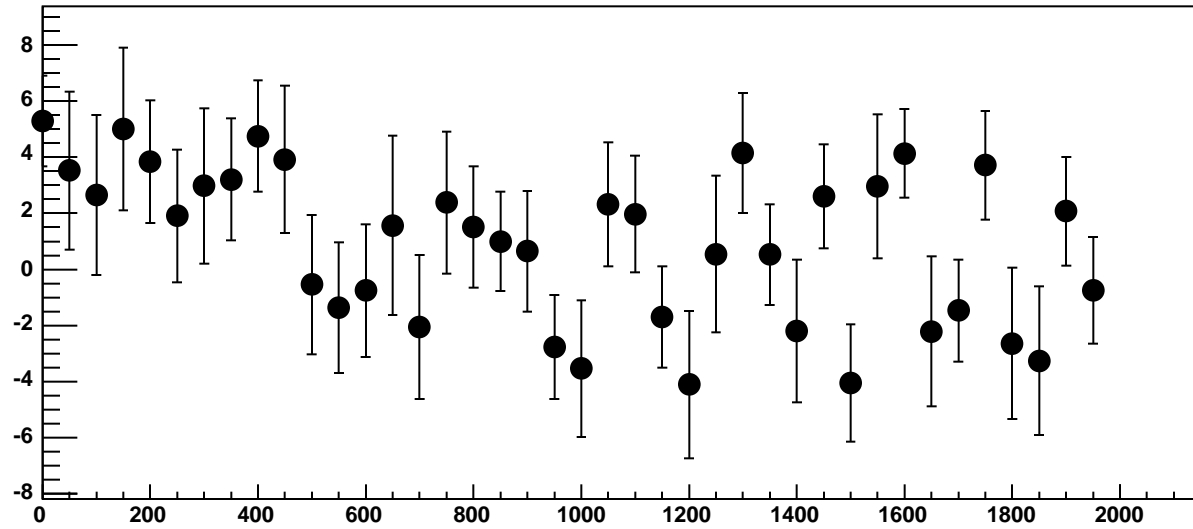


$\chi^2 / \text{ndf}$  15.15 / 11  
p0  $-1434 \pm 3.438$   
p1  $0.007043 \pm 0.003119$

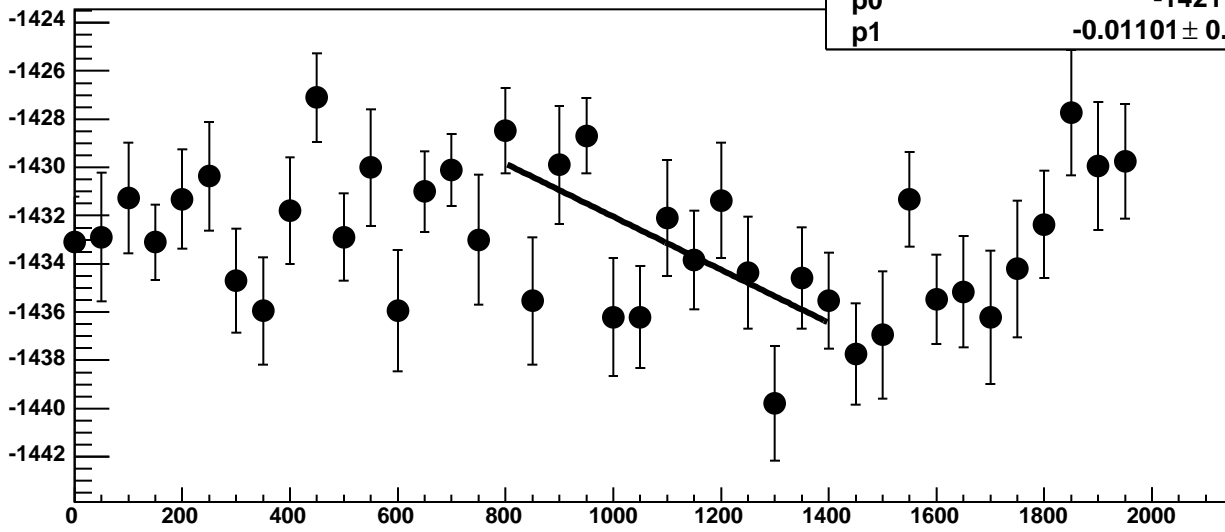
Chip 11, Channel 17, Enable 2, Hold=30, ADC Noise vs DAC



Chip 11, Channel 17, Enable 2, Hold=30, ADC Residuals vs DAC



Chip 11, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

19.43 / 11

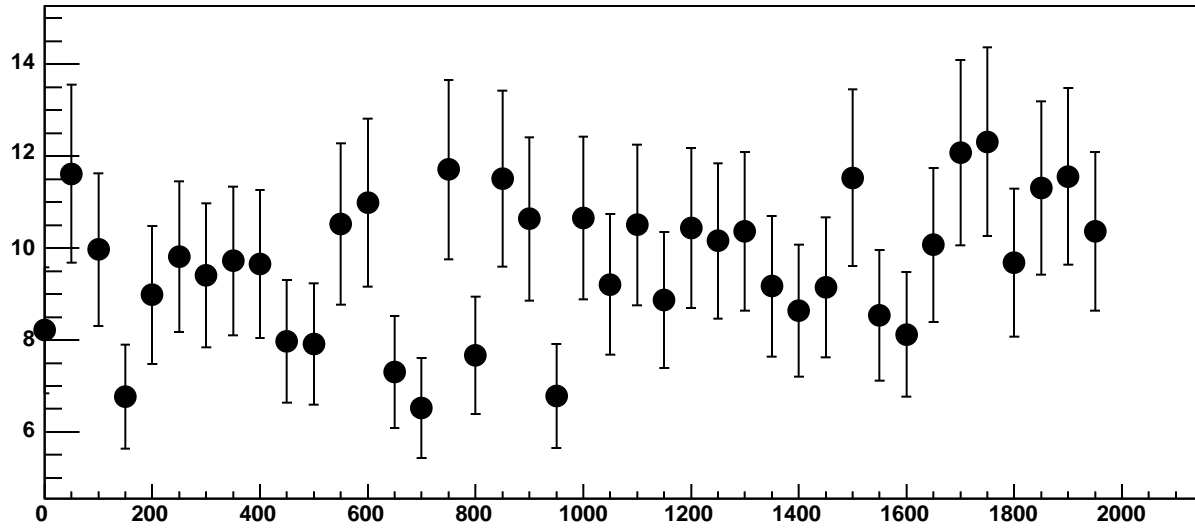
p0

$-1421 \pm 3.389$

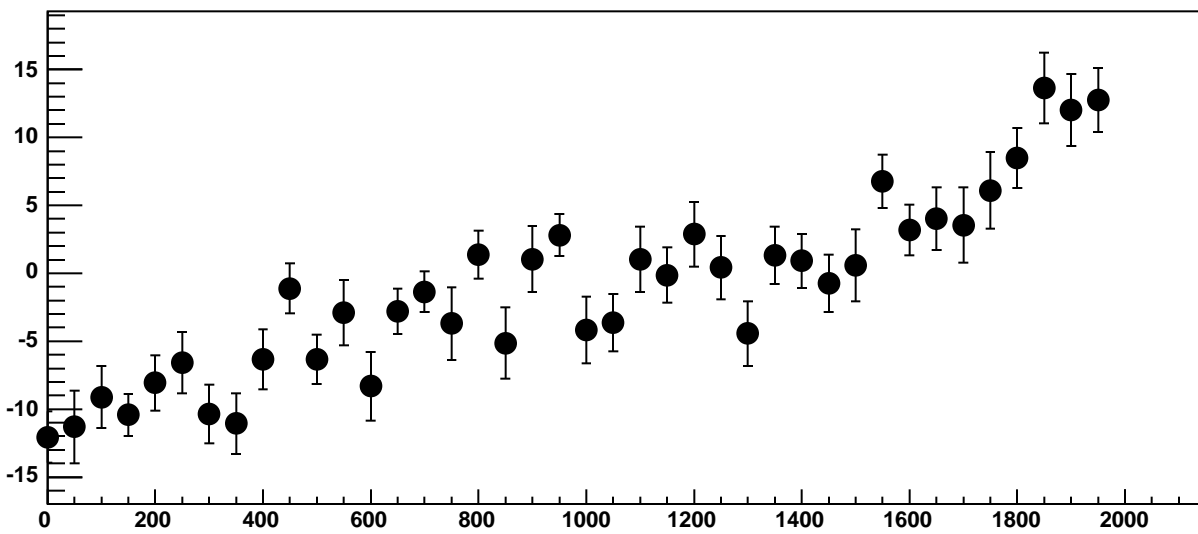
p1

$-0.01101 \pm 0.003063$

Chip 11, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC

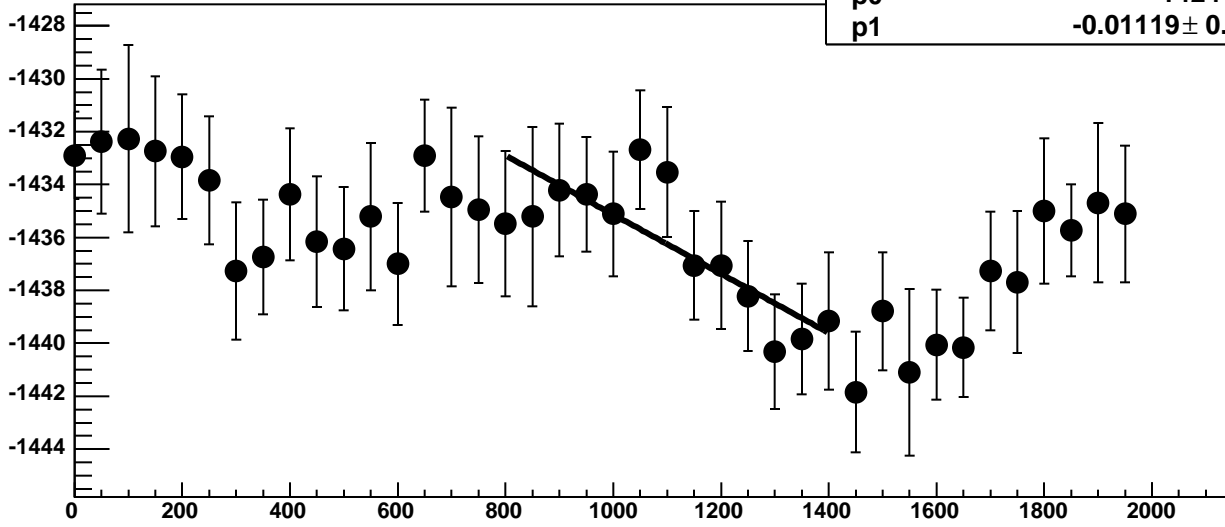


Chip 11, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC

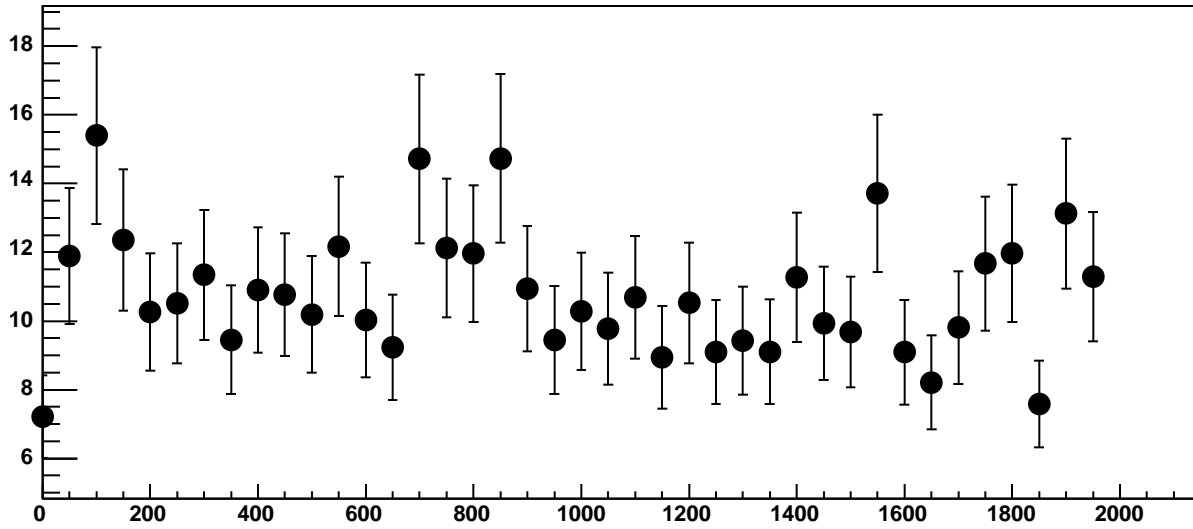


Chip 11, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC

$\chi^2 / \text{ndf}$  5.137 / 11  
p0  $-1424 \pm 4.198$   
p1  $-0.01119 \pm 0.003692$



Chip 11, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC



Chip 11, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC

