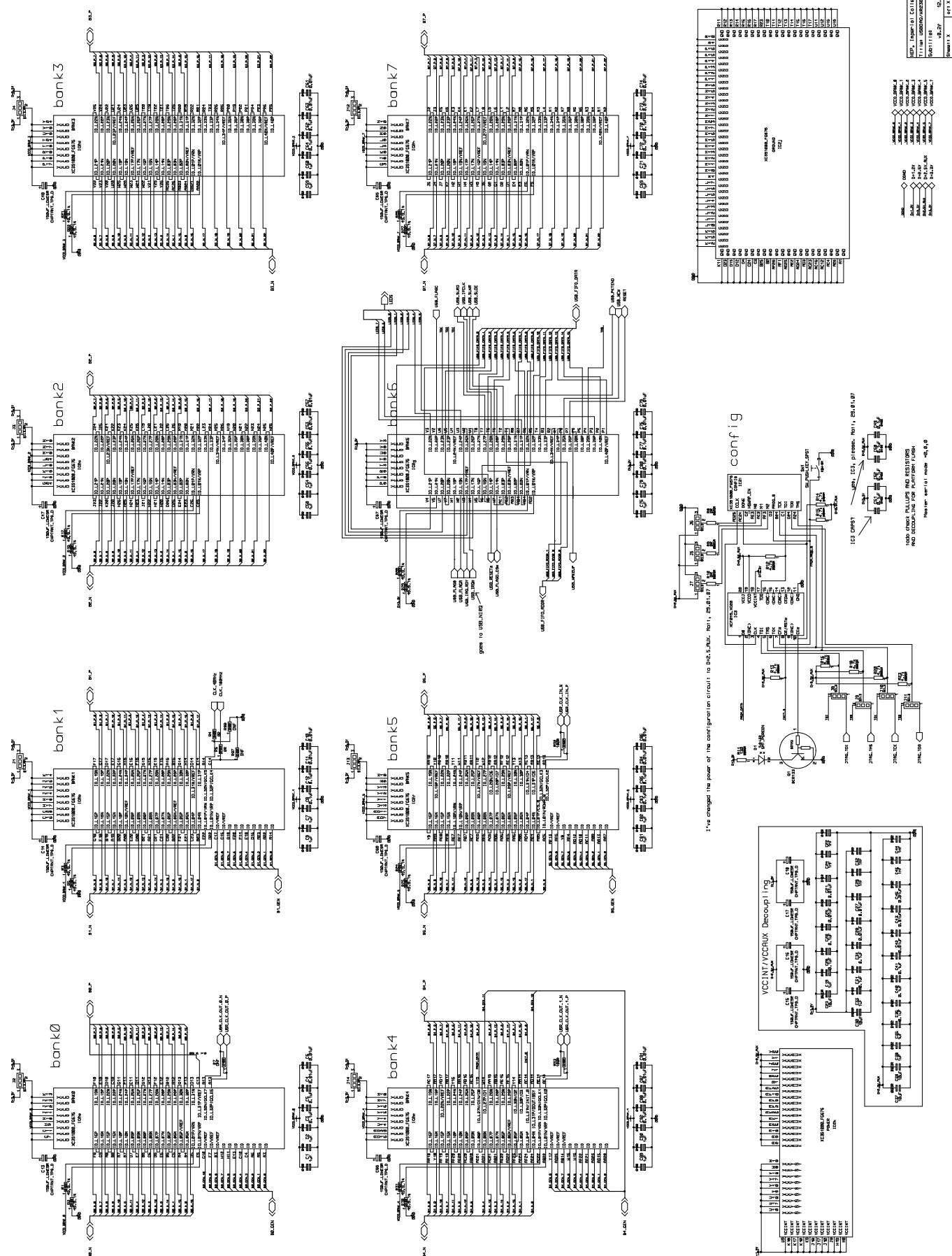


A SYSTEM_FPGA

SPARTAN3_1MGATE A



1. We changed the power of the configuration circuit to DPL5.0Vx (Ref. 1, Ref. 3, Ref. 4).
2. Input in Configuration, London
3. 1.1 was 1.0V, now -0.2V
4. Sop11111111
5. 12.12.05 Year 1
6. Sop11111111
7. 12.12.05 Year 1
8. Sop11111111
9. 12.12.05 Year 1
10. Sop11111111
11. 12.12.05 Year 1
12. Sop11111111
13. 12.12.05 Year 1
14. Sop11111111
15. 12.12.05 Year 1
16. Sop11111111
17. 12.12.05 Year 1
18. Sop11111111
19. 12.12.05 Year 1
20. Sop11111111
21. 12.12.05 Year 1
22. Sop11111111
23. 12.12.05 Year 1
24. Sop11111111
25. 12.12.05 Year 1
26. Sop11111111
27. 12.12.05 Year 1
28. Sop11111111
29. 12.12.05 Year 1
30. Sop11111111
31. 12.12.05 Year 1
32. Sop11111111
33. 12.12.05 Year 1
34. Sop11111111
35. 12.12.05 Year 1
36. Sop11111111
37. 12.12.05 Year 1
38. Sop11111111
39. 12.12.05 Year 1
40. Sop11111111
41. 12.12.05 Year 1
42. Sop11111111
43. 12.12.05 Year 1
44. Sop11111111
45. 12.12.05 Year 1
46. Sop11111111
47. 12.12.05 Year 1