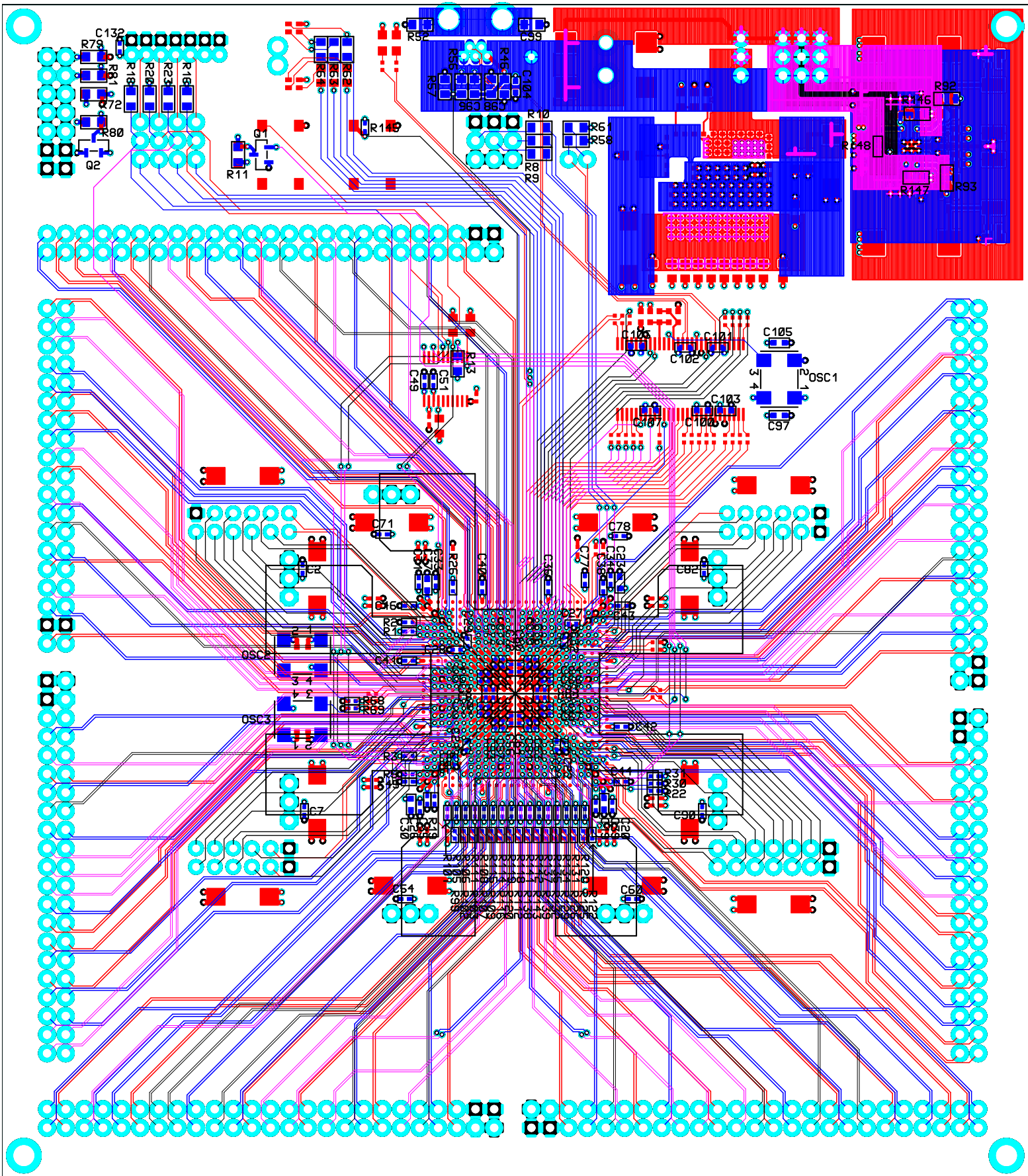


USB_DAQ VIA LAYER



USB_DAQ BOTTOM LAYER

USB_DAQ RLV PLOT WITH TOP LAYER