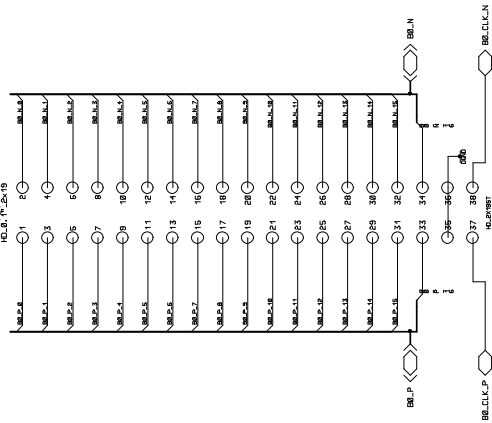
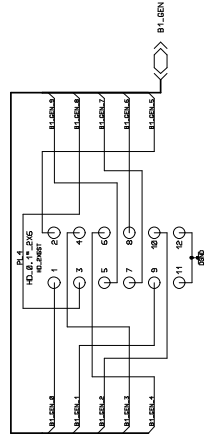
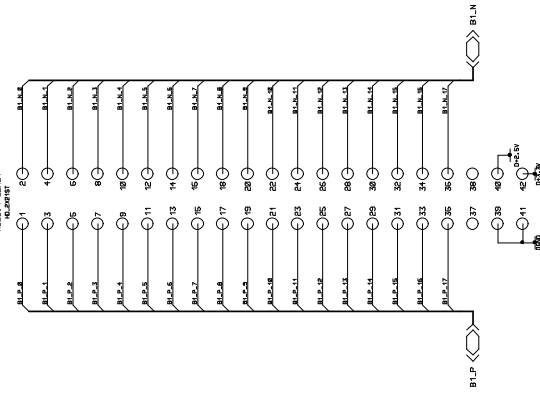


change symbol sometime...still thinks its 48way

BANK 0

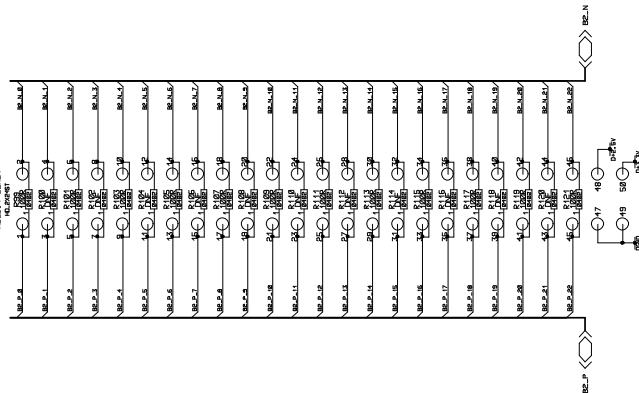


BANK 1

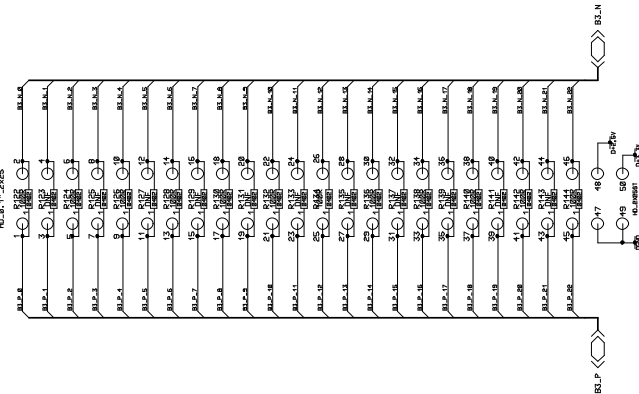


RESISTORS PLACE NEAR FPGAR WITHIN 2CM MAX  
CAPACITORS PLACE NEAR FPGAR WITHIN 2CM MAX

BANK 2



BANK 3



- ⊗ VCC3\_3V
- ⊗ VCC2\_2V
- ⊗ VCC1\_1V
- ⊗ VCC0\_0V
- ⊗ VCC0\_BANK\_0
- ⊗ VCC0\_BANK\_1
- ⊗ VCC0\_BANK\_2
- ⊗ VCC0\_BANK\_3

REP: Imperial College London
Title: USB0/AE28
Author: [Name]
Date: 12.12.06
Sheet: X of X
Date: 11.06