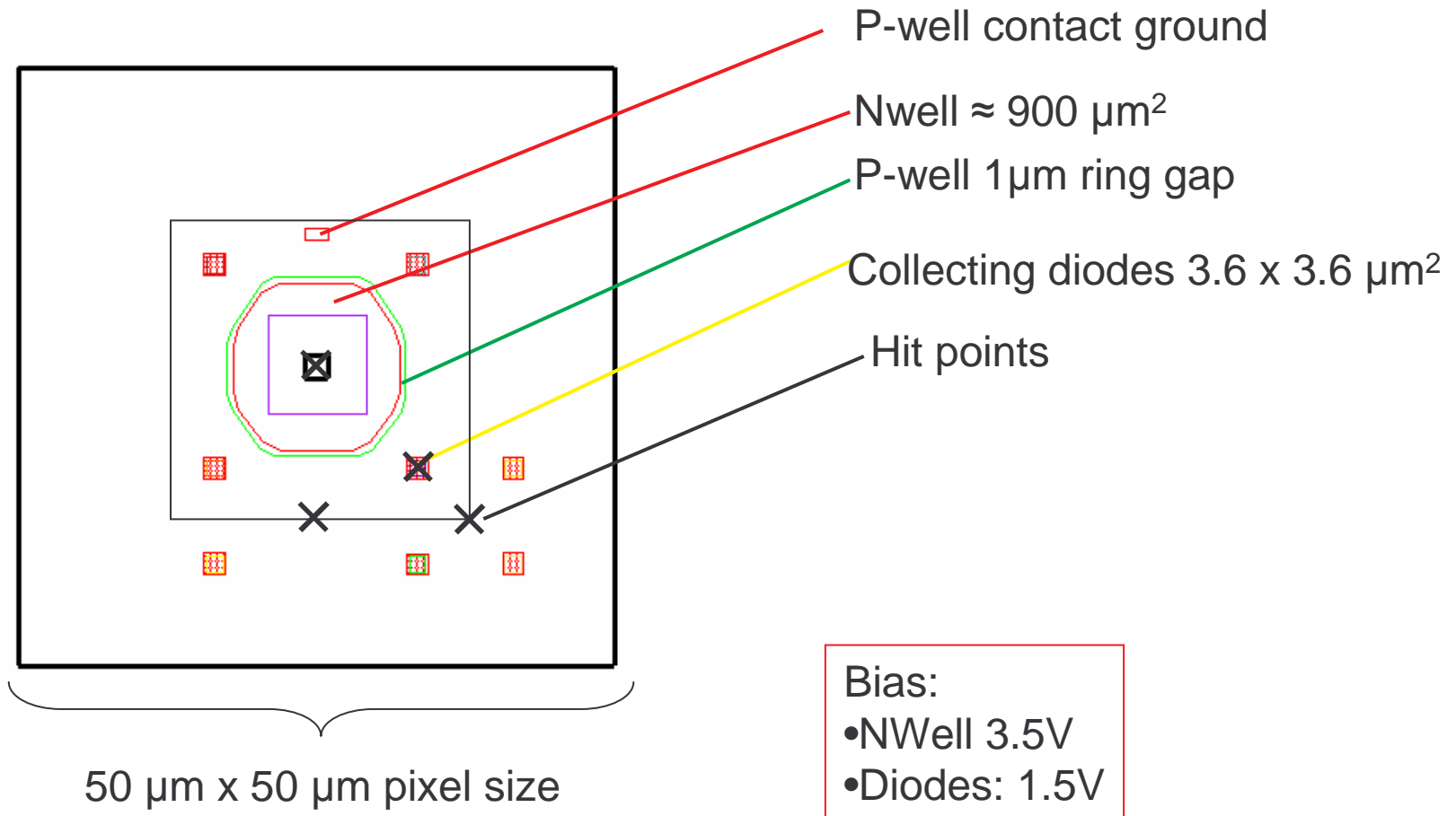
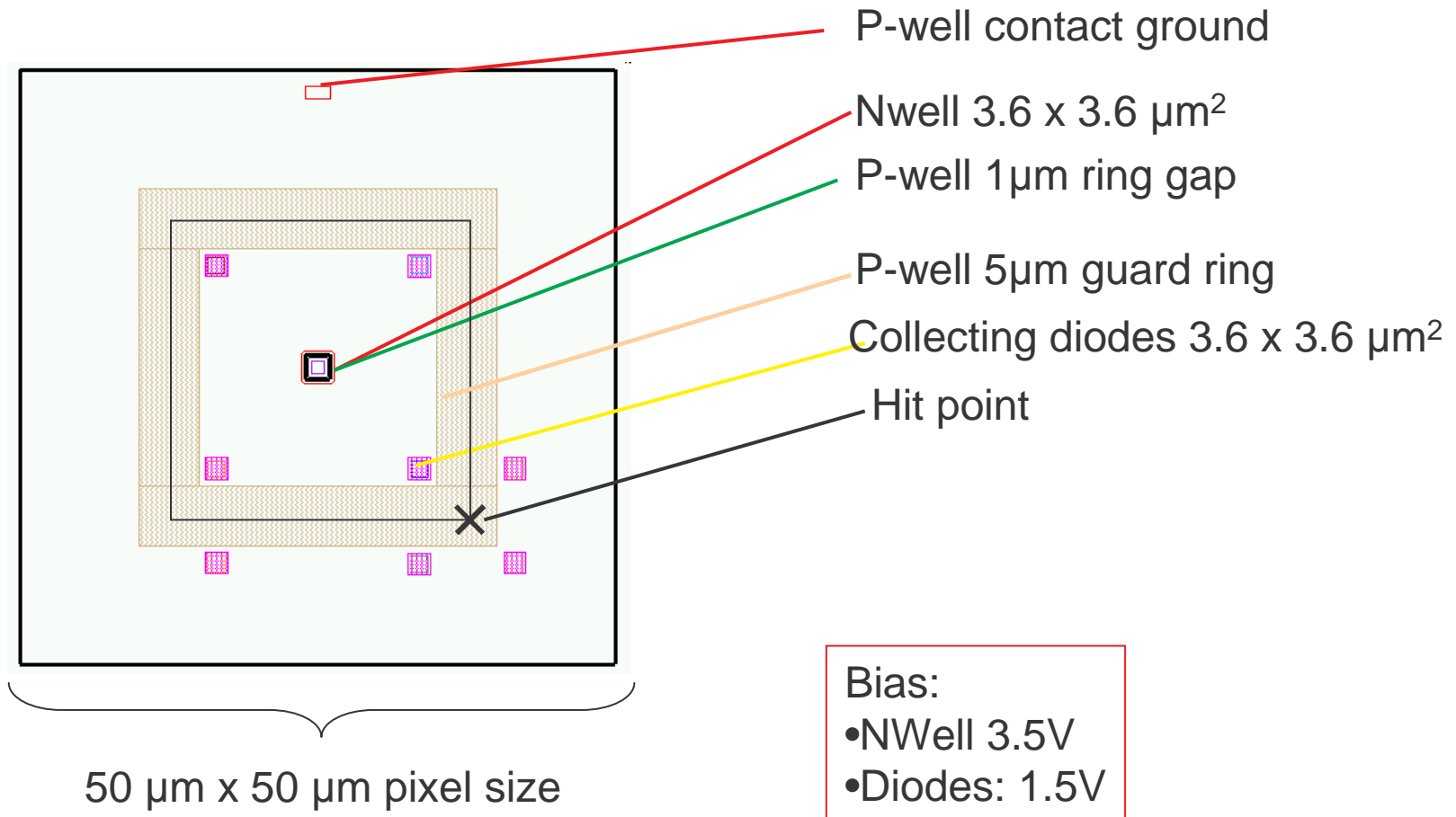


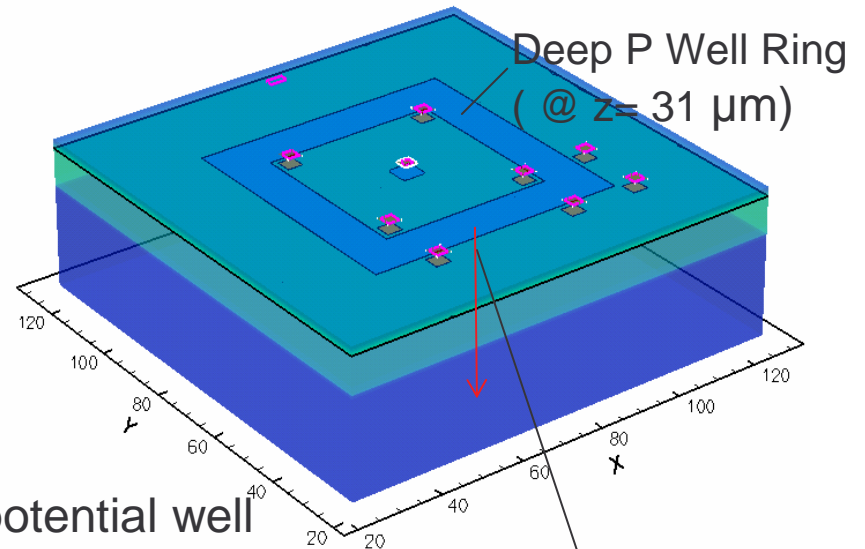
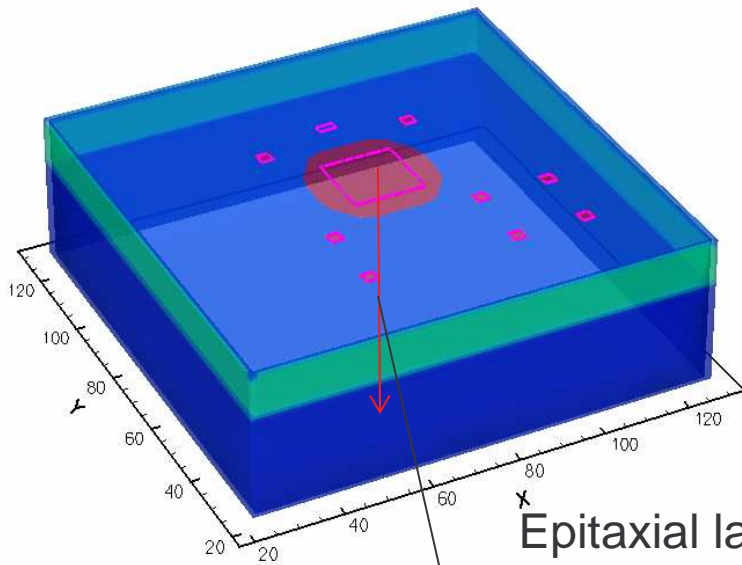
CALICE pixel Deep P-Well results



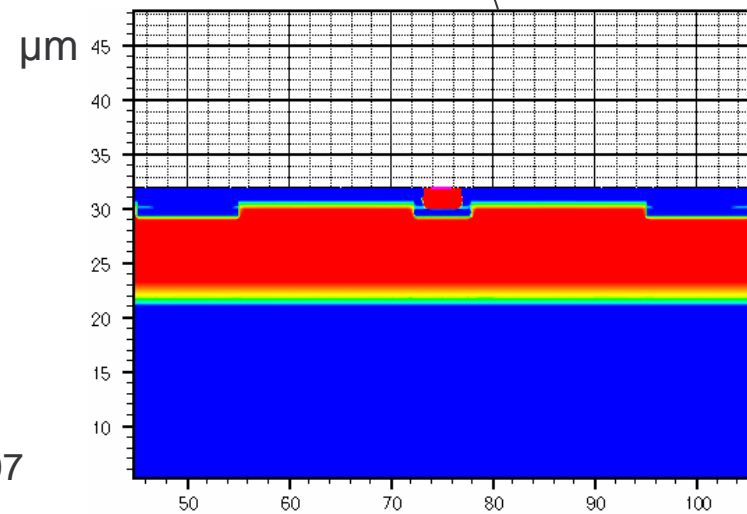
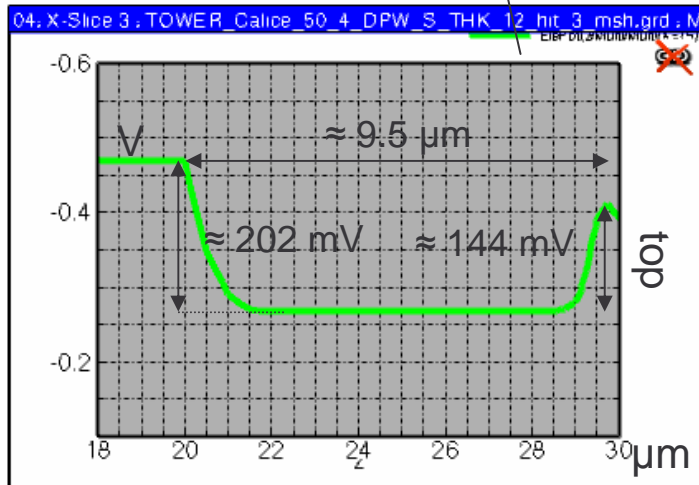
CALICE pixel Deep P-Well results



CALICE pixel Deep P-Well results



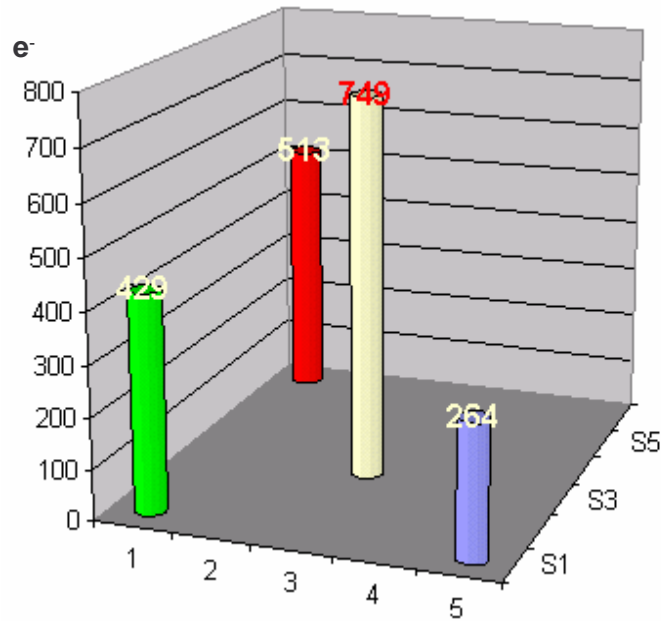
Epitaxial layer potential well



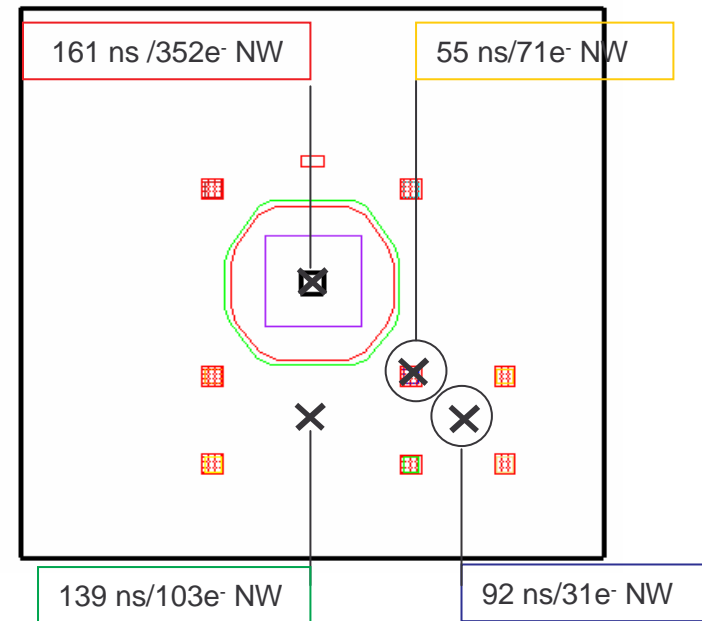
G.Villani jan. 07

CALICE pixel Deep P-Well results

Single pixel results No Pwell Guard ring



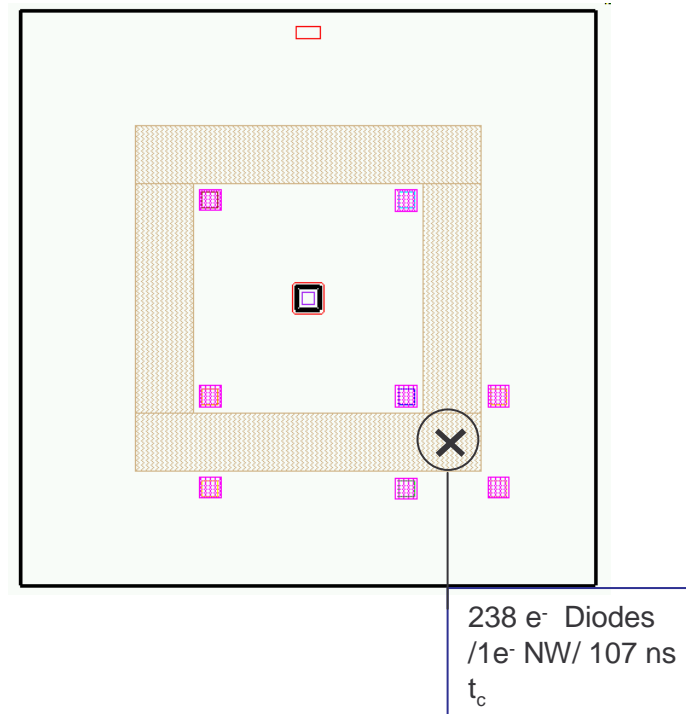
Diodes Charge collected (e⁻)



D t_c / NW Charge collected

CALICE pixel Deep P-Well results

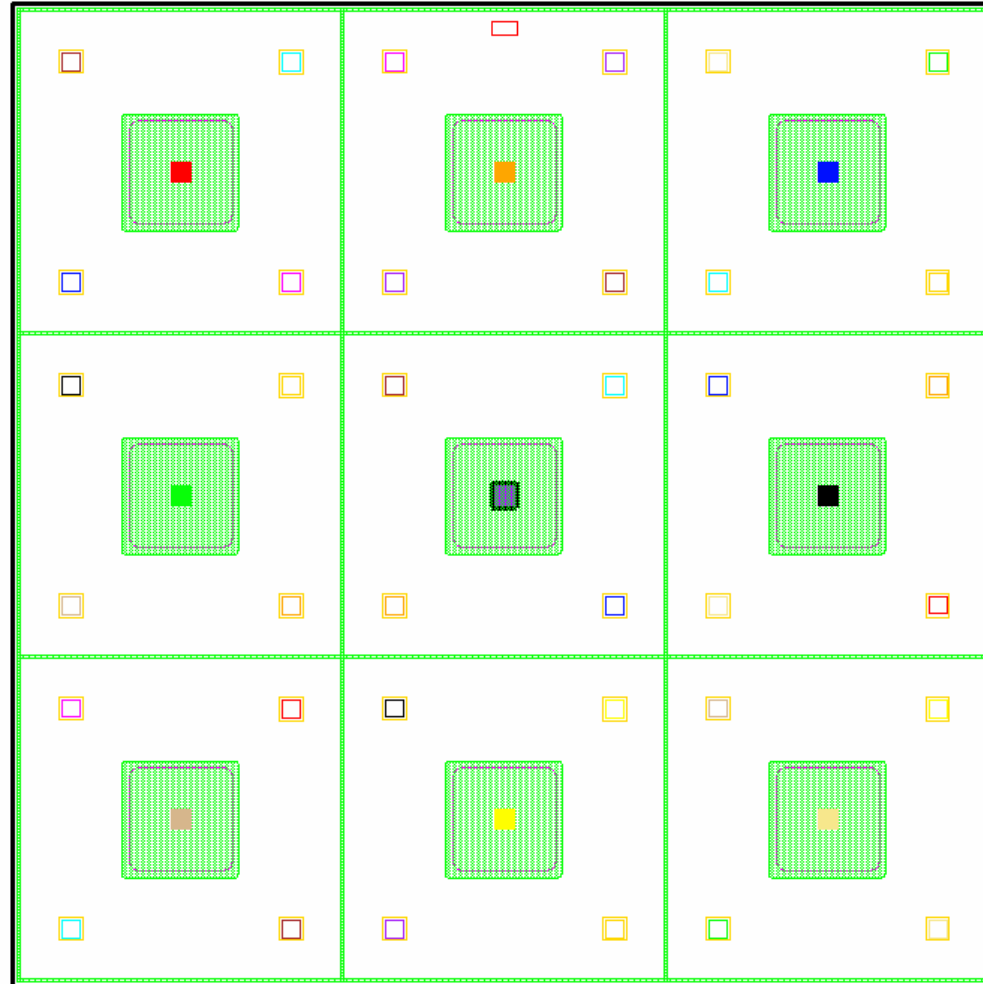
Single pixel results 5 μm Pwell Guard ring



CALICE pixel Deep P-Well results

- **Conclusions**
- The layout with increased central well size shows a decrease in efficiency of charge collection: however, a S/N_{\min} should still exceed 10
- Collection time still well below 200 ns.
- P Well Guard ring of 5 μm makes worst hit (21) lower but still acceptable with 3.6 μm central N Well
- Next step:
- Different size diodes simulations (7.6 / 1.8 μm)
- Final layout simulation

CALICE pixel Deep P-Well results



Narrow (3 μm ?) P-Well guard ring around each pixel

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