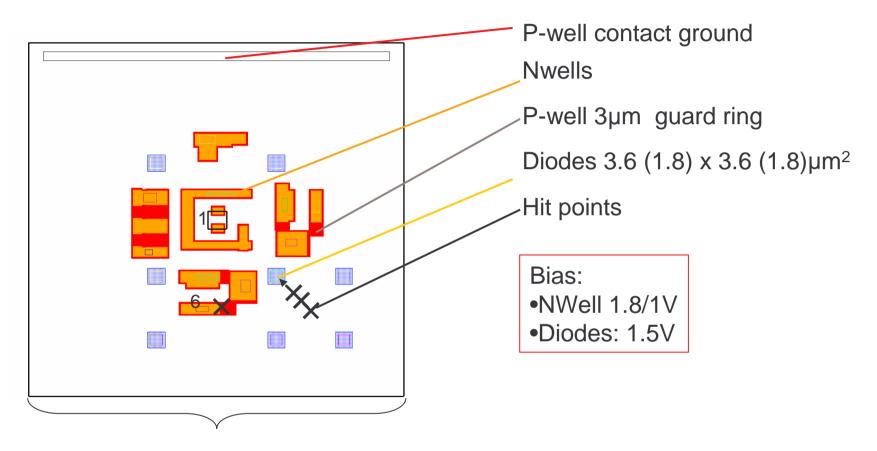
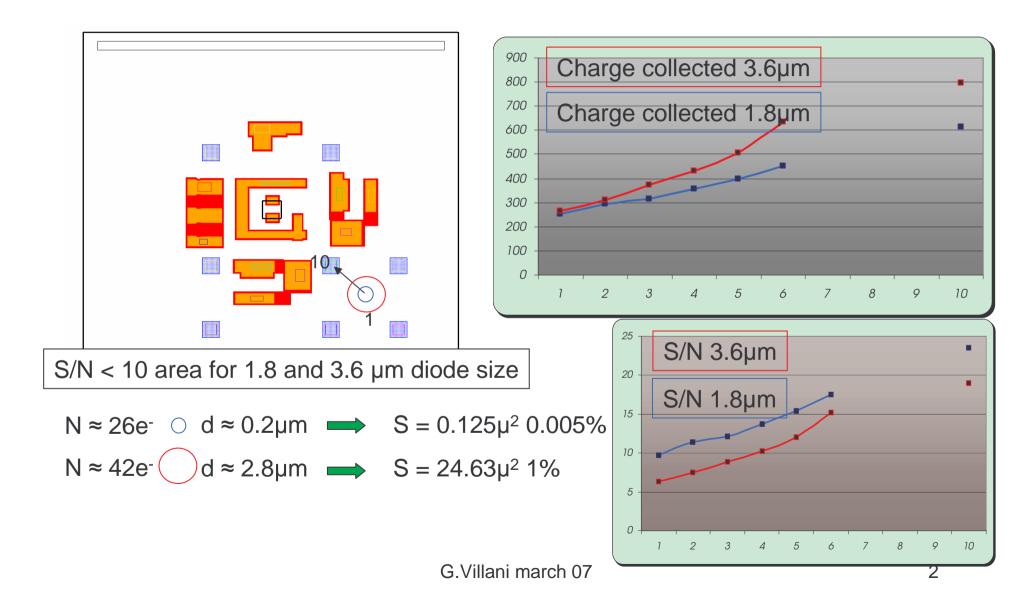
CALICE pixel Deep P-Well results



50 µm x 50 µm pixel size

CALICE pixel Deep P-Well results



CALICE pixel Deep P-Well results

Conclusions

- The final layout shows good performances in terms of charge collection, with smaller diode size 1.8µm giving better S/N
- Area of S/N < 10 around 1% with 3.6µm diode size, negligible with 1.8µm
- Collection time still well below 200 ns in both cases
- Next step:
- Full layout simulation 3x3 pixels if possible