ASIC 1.1 Specification (?)

- All pixels → shapers
  - Which sub-variant? (→ Keep both)
- Test pixels → shapers
  - Add nearby nwell diodes for more realistic charge collection
  - Share existing shaper bias lines *(some unused sampler biases)*
- Bug fixes
  - SRAM write (insert level-shifting logic)
  - Biases
    - Monostables (limbias)
    - Sense amp (mismatched transistors)
- TRIM resolution extension?
  - Feasibility study: Add trim bits to pixel?
  - Pixel layout modifications

JC

1mo

+1

1mo