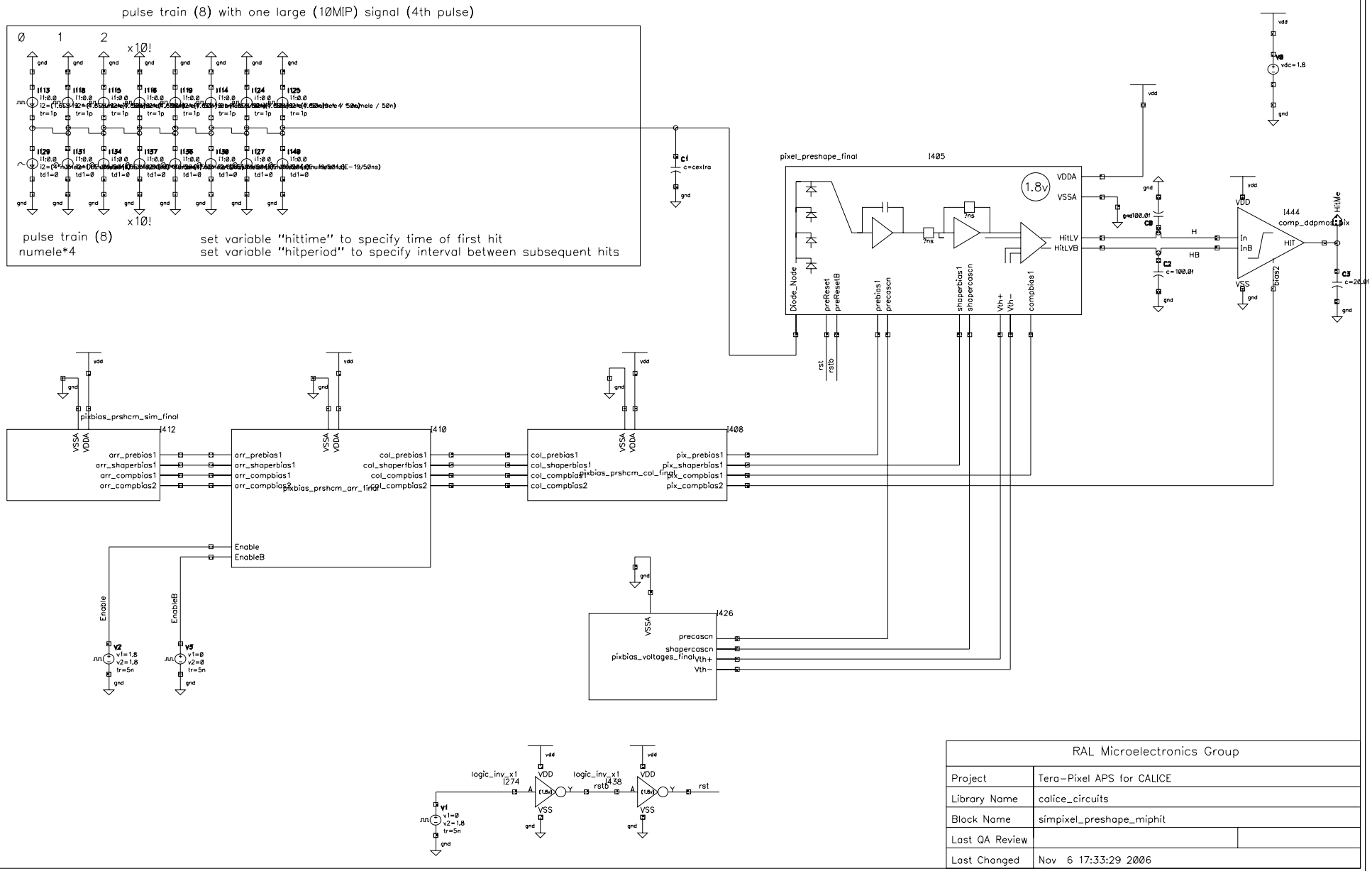
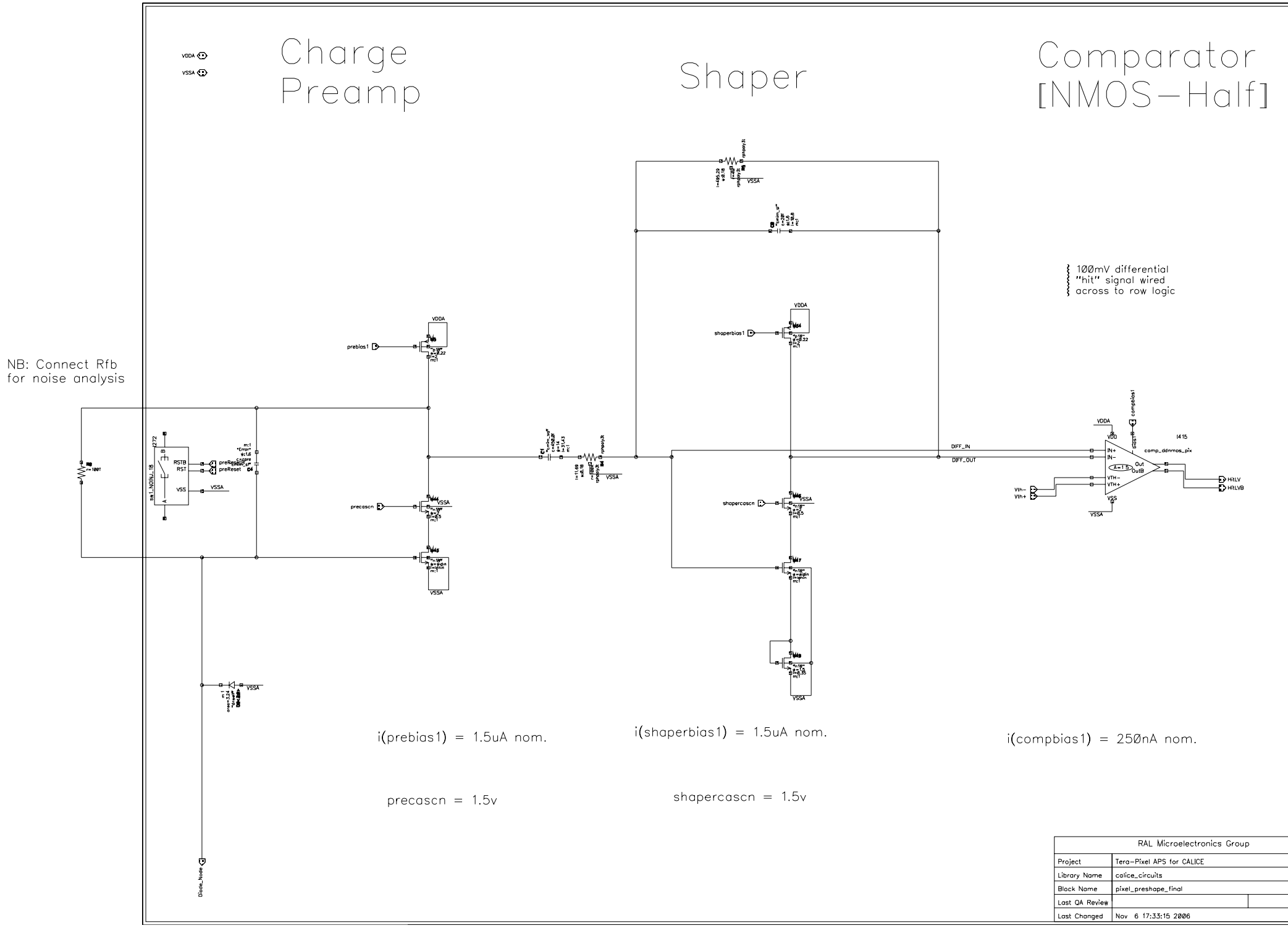
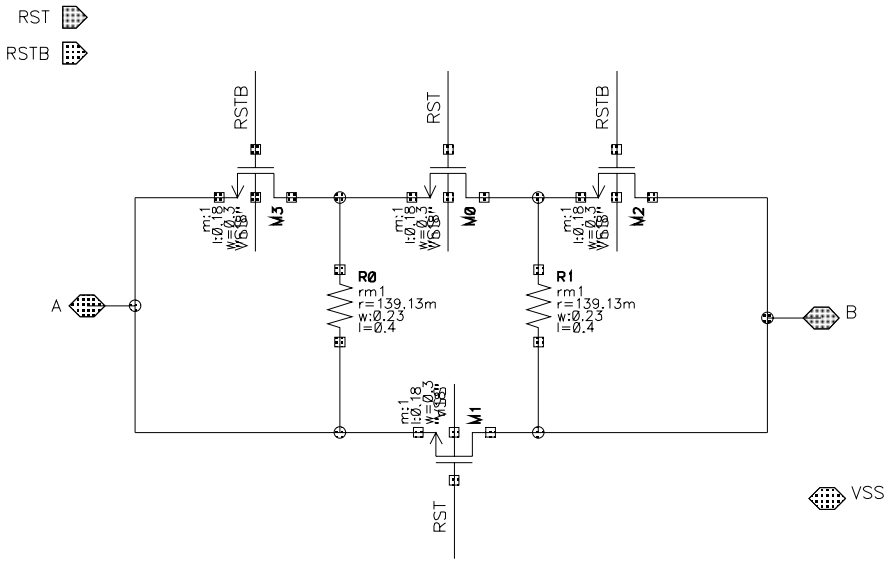


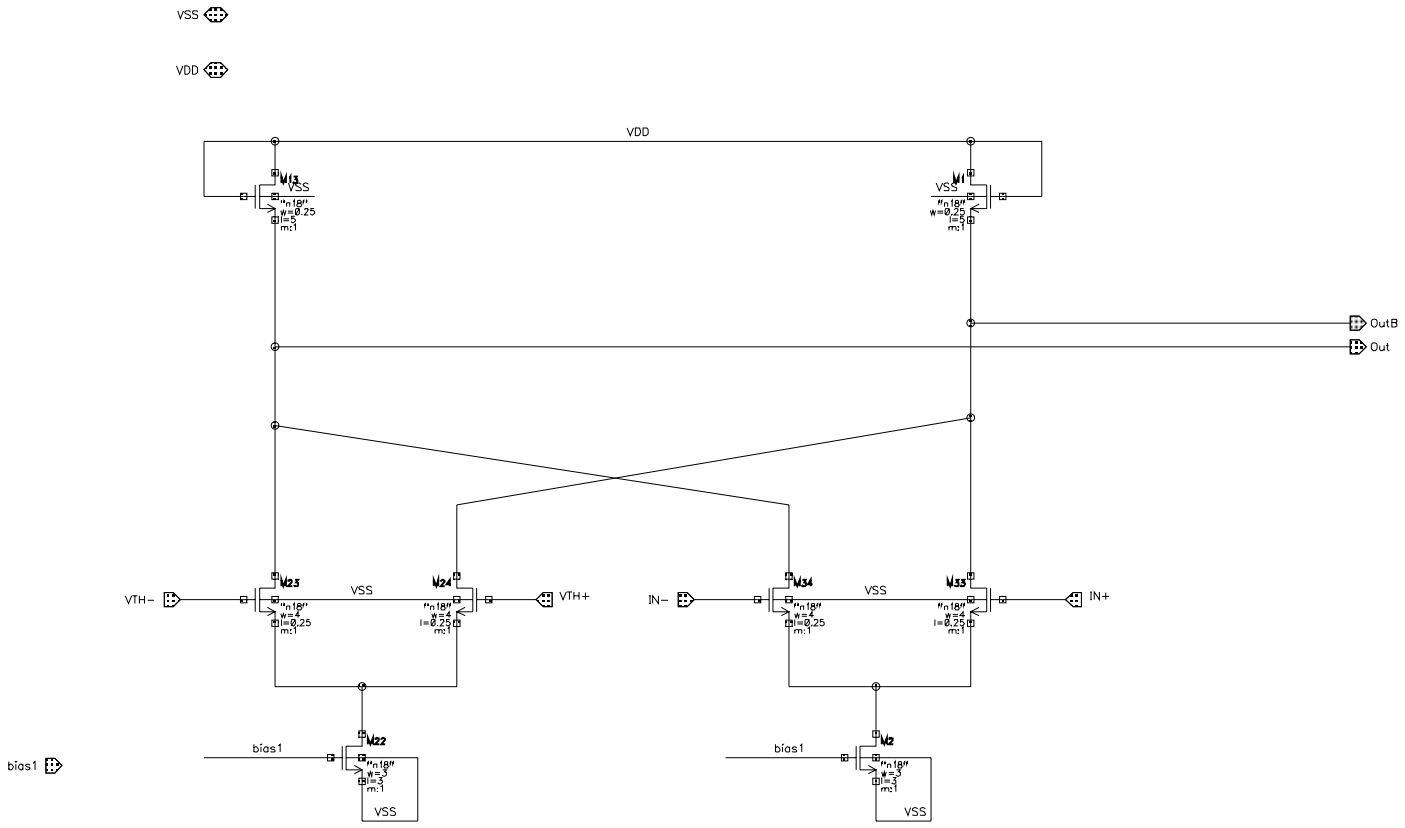
RAL Microelectronics Group	
Project	Tera-Pixel APS for CALICE
Library Name	calice_circuits
Block Name	simpixel_preshape_miphit
Last QA Review	
Last Changed	Nov 6 17:33:29 2006



RAL Microelectronics Group	
Project	Tera-Pixel APS for CALICE
Library Name	calice_circuits
Block Name	pixel_preshape_final
Last QA Review	
Last Changed	Nov 6 17:33:15 2006



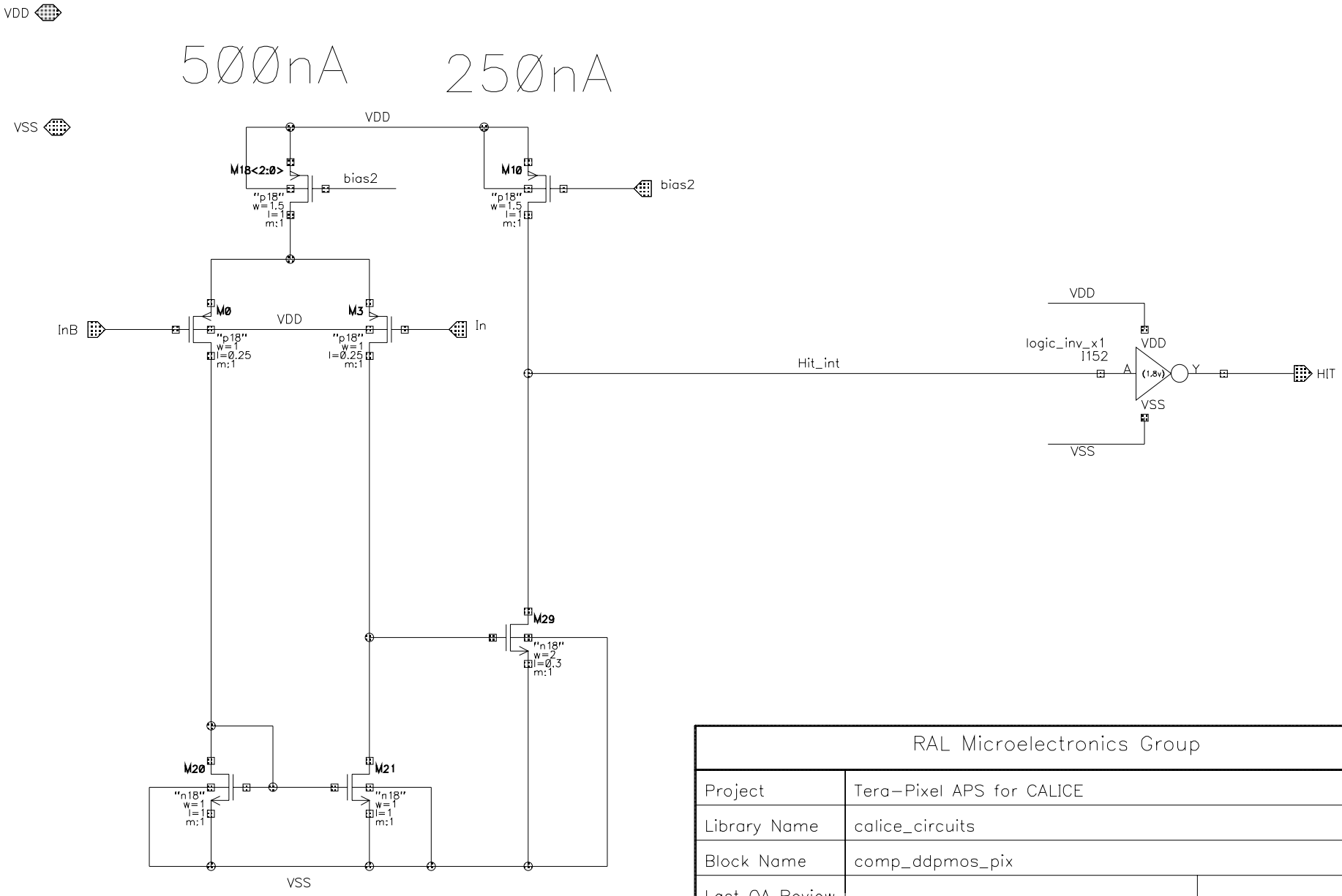
RAL Microelectronics Group	
Project	Tera-Pixel APS for CALICE
Library Name	calice_circuits
Block Name	sw1_NOINJ_18
Last QA Review	
Last Changed	Oct 13 10:05:15 2006



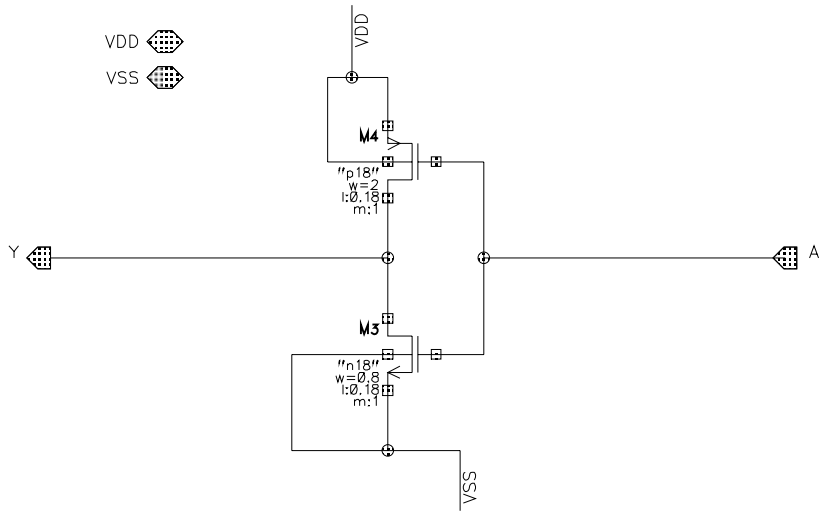
differential (10s of mV)
hit signal wired across
to pmos comparator at ro
logic

RAL Microelectronics Group	
Project	Tera−Pixel APS for CALICE
Library Name	calice_circuits
Block Name	comp_ddnmos_pix
Last QA Review	
Last Changed	Oct 17 09:27:22 2006

<<<<< AT ROW LOGIC >>>>>

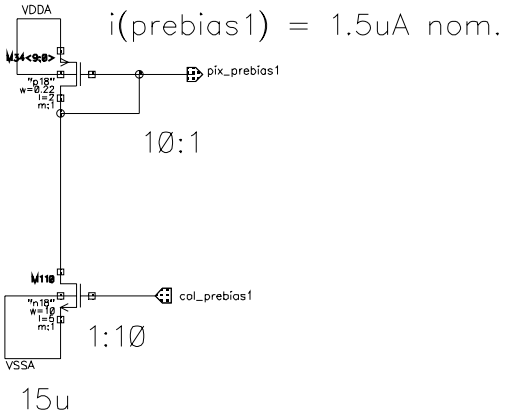


RAL Microelectronics Group	
Project	Tera-Pixel APS for CALICE
Library Name	calice_circuits
Block Name	comp_ddpmos_pix
Last QA Review	
Last Changed	Oct 16 18:10:12 2006

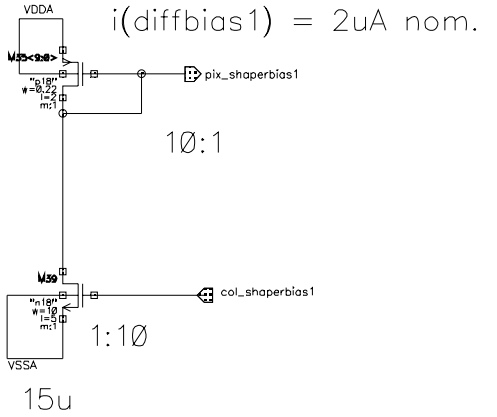


RAL Microelectronics Group	
Project	Tera-Pixel APS for CALICE
Library Name	calice_circuits
Block Name	logic_inv_x1
Last QA Review	
Last Changed	Sep 28 11:46:11 2006

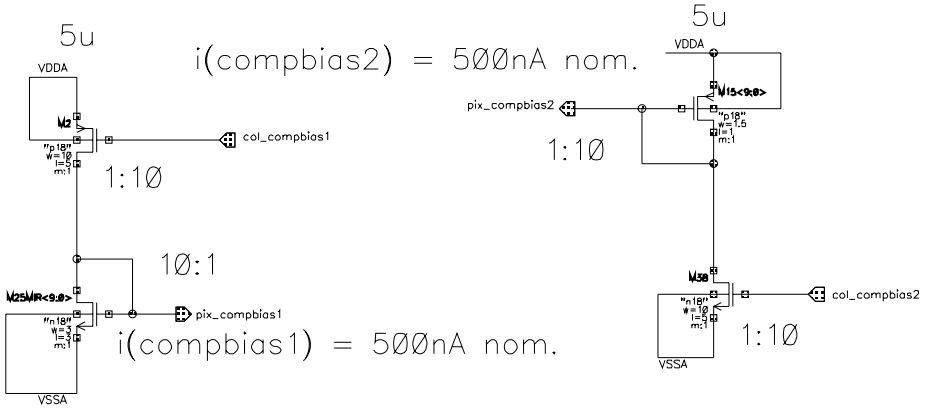
Preamp Bias



Shaper Bias



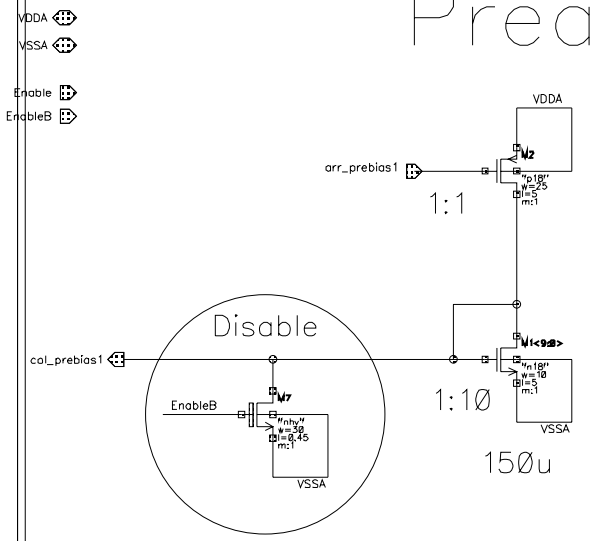
Comparator Bias



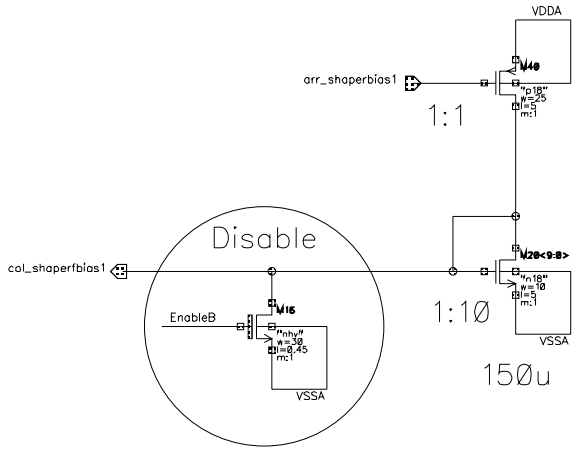
COLUMN CIRCUITS

RAL Microelectronics Group	
Project	Tera-Pixel APS for CALICE
Library Name	calice_circuits
Block Name	pixbias_prshcm_col_final
Last QA Review	
Last Changed	Oct 27 14:03:52 2006

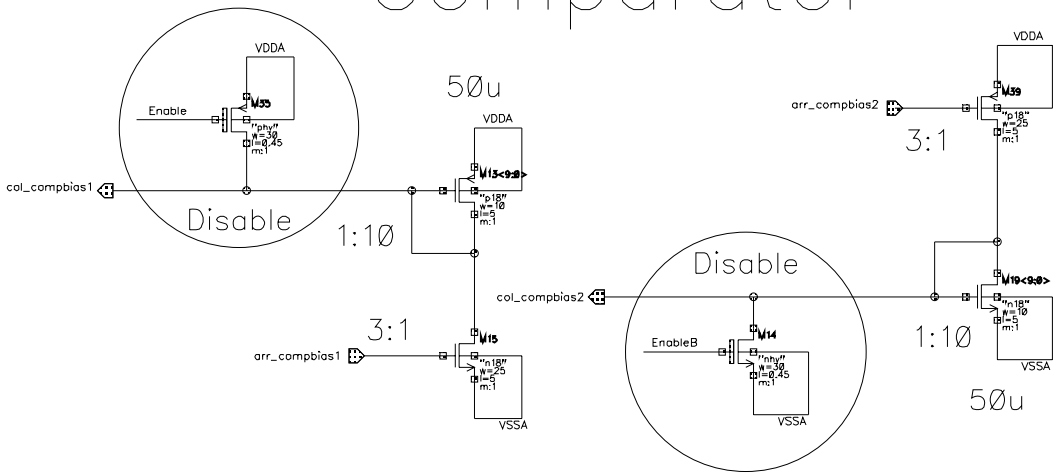
Preamp Bias



Shaper Bias



Comparator



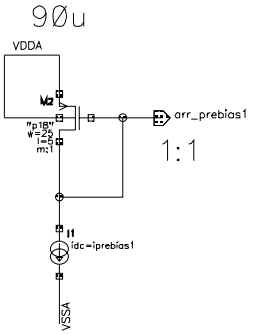
ARRAY CIRCUITS

RAL Microelectronics Group	
Project	Tera-Pixel APS for CALICE
Library Name	calice_circuits
Block Name	pixbias_prshcm_arr_final
Last QA Review	
Last Changed	Oct 27 14:17:16 2006

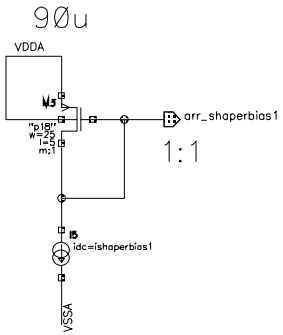
Preamp

VDDA

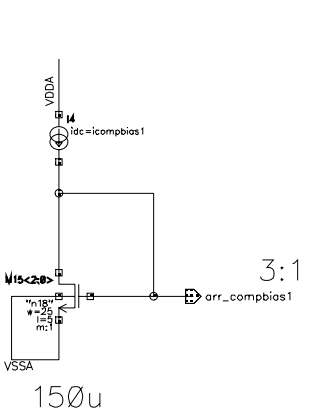
VSSA

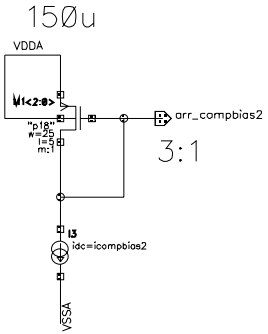


Shaper Bias



Comparator Bias

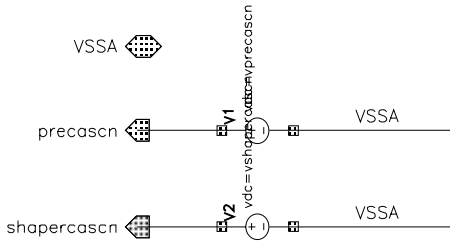




RAL Microelectronics Group	
Project	Tera-Pixel APS for CALICE
Library Name	calice_circuits
Block Name	pixbias_prshcm_sim_final
Last QA Review	
Last Changed	Oct 27 14:25:26 2006

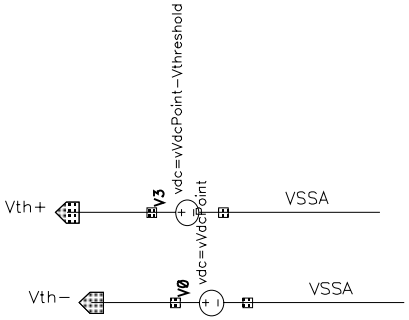
preamp cascode

shaper cascode



Vth+ = Vdcpoint + Vth

Vth− = Vdcpoint = 1.Øv nom.



RAL Microelectronics Group	
Project	Tera−Pixel APS for CALICE
Library Name	calice_circuits
Block Name	pixbias_voltages_final
Last QA Review	
Last Changed	Oct 27 14:12:14 2ØØ6